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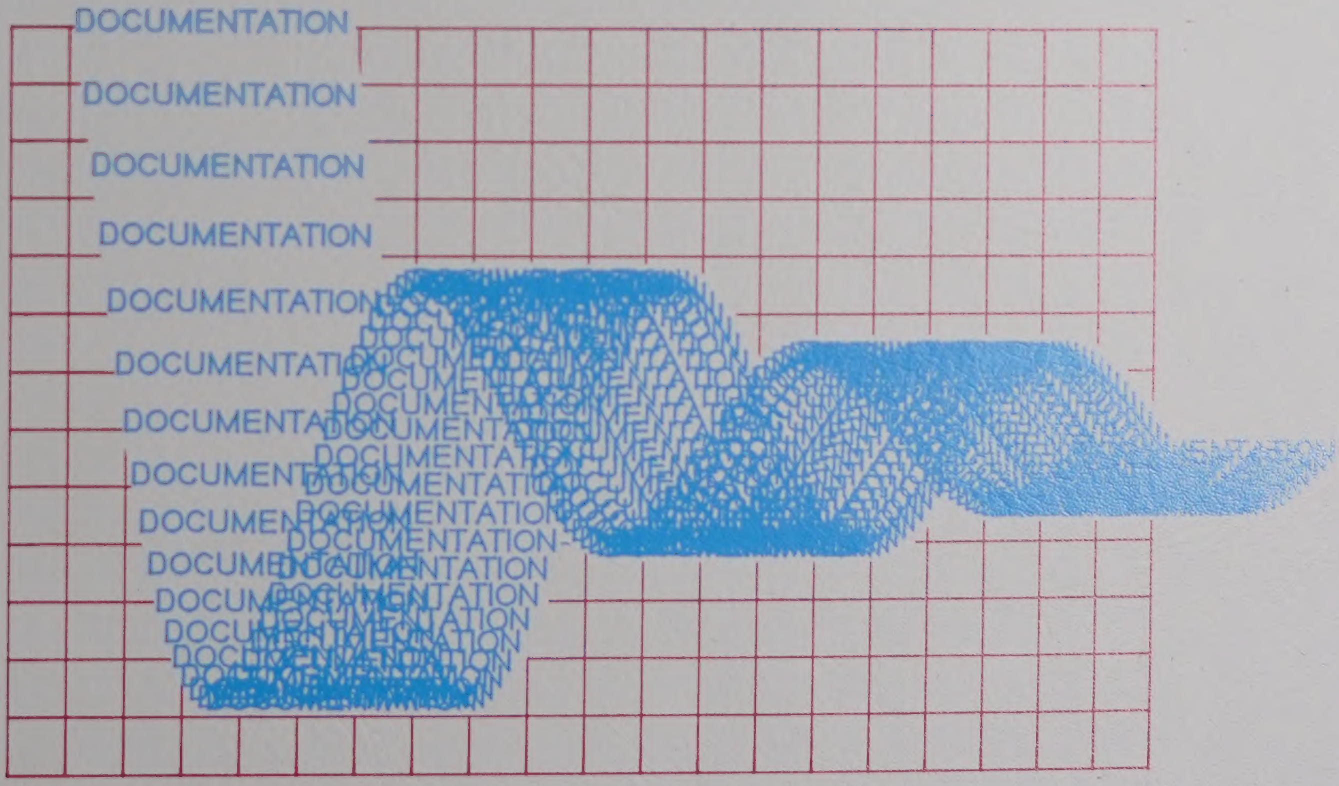

DOCUMENTATION

ROHDE&SCHWARZ
Test and Measurement
Division

Operating Manual

VECTOR SIGNAL GENERATOR

SMIQ02B
1125.5555.02

SMIQ03B
1125.5555.03

SMIQ03HD
1125.5555.33

SMIQ04B
1125.5555.04

SMIQ06B
1125.5555.06

SMIQ06ATE
1125.5555.26

Volume 1
This Operating Manual consists of 2 volumes

Printed in the Federal
Republic of Germany

Supplement to Manual
SMIQ06ATE

The functionality of model SMIQ06ATE and its compliance with specifications correspond to model SMIQ06B (see Data sheet SMIQB06B).
Model SMIQ06ATE differs from model SMIQ06B as follows:

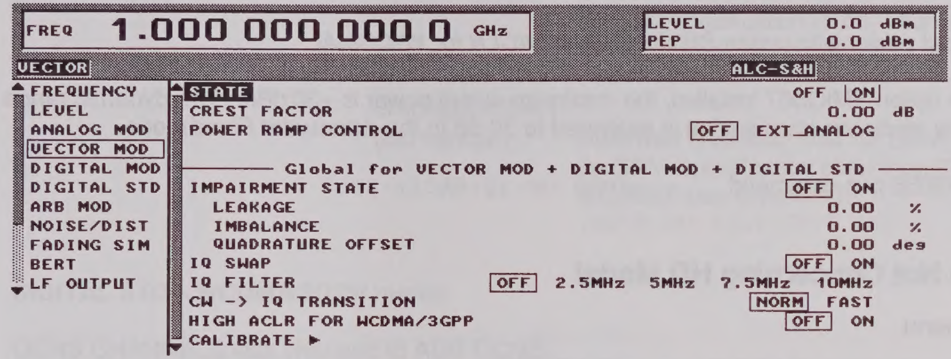
- The instrument has no display (item 1 in front panel view)
- The connectors on the front panel (item 4 in front panel view) are provided on the rear panel.
- The following connectors are not fitted:
  - I Faded,
  - Q Faded,
  - I/Q AUX,
  - LF,
  - EXT 1,
  - EXT 2,
  - SYM CLK,
  - X-AXIS,
  - MARKER,
  - SER DATA,
  - PULS,
  - EXTTUNE
  - BER
  - DATA
  - BITCLOCK and
  - PAR DATA

For manual operation of the SMIQ06ATE please download SMIQ-TV from the www.rohde-schwarz.com website. SMIQ-TV runs on any external Windows™ PC including a GPIB controller and features a full display of the SMIQ.

Supplement to Manual SMIQ

Special Features of HD Model

VECTOR MOD menu with model SMIQ03HD:



```
FREQ    1.000 000 000 0 GHz                              LEVEL       0.0 dBm
                                                         PEP         0.0 dBm
VECTOR                                                          ALC-S&H
  FREQUENCY    STATE                                                 OFF  ON
  LEVEL        CREST FACTOR                                          0.00 dB
  ANALOG MOD   POWER RAMP CONTROL                            OFF  EXT_ANALOG
  VECTOR MOD
  DIGITAL MOD  ——— Global for VECTOR MOD + DIGITAL MOD + DIGITAL STD ———
  DIGITAL STD  IMPAIRMENT STATE                                      OFF  ON
  ARB MOD        LEAKAGE                                             0.00  %
  NOISE/DIST     IMBALANCE                                           0.00  %
  FADING SIM     QUADRATURE OFFSET                                   0.00  deg
  BERT         IQ SWAP                                               OFF  ON
  LF OUTPUT    IQ FILTER                 OFF  2.5MHz  5MHz  7.5MHz  10MHz
               CW -> IQ TRANSITION                                  NORM  FAST
               HIGH ACLR FOR WCDMA/3GPP                              OFF  ON
               CALIBRATE ▶
```

IQ FILTER Selection between filter off and a 2.5 MHz, 5 MHz, 7.5 MHz or 10 MHz lowpass filter in the baseband. The filters suppress noise in the baseband, which improves adjacent channel power (ACP) with W-CDMA. The filter frequency response is automatically compensated with DIGITAL MOD and DIGITAL STD in the MCOD mode, so that the error vector magnitude (EVM) deteriorates only slightly. If the VECTOR MOD mode is active and AMIQ with WinIQSIM used as a source, the frequency response is compensated by WinIQSIM. For applications requiring a very low EVM the I/Q filter should always be switched OFF.

OFF	filter off
2.5 MHz	2.5 MHz lowpass filter
5 MHz	5 MHz lowpass filter
7.5 MHz	7.5 MHz lowpass filter
10 MHz	10 MHz lowpass filter

IEC/IEEE-bus commands `:SOUR:DM:IQ:FILT:STAT ON|OFF`
`:SOUR:DM:IQ:FILT:FREQ 2.5MHZ`

HIGH ACLR W-CDMA signals with a very high adjacent channel power ratio (ACPR) can be generated in the 3GPP downlink band by means of option SMIQB57 (*High ACLR for W-CDMA*) in conjunction with option SMIQB20 (*Modulation Coder*). Moreover, RF peak levels up to +30 dBm can be produced thanks to the extremely linear power amplifier.

Note: *Option SMIQB57 can be used only in the 2110 MHz to 2170 MHz 3GPP downlink band. Although carrier frequencies outside this band can be set, no specified values exist for such applications.*

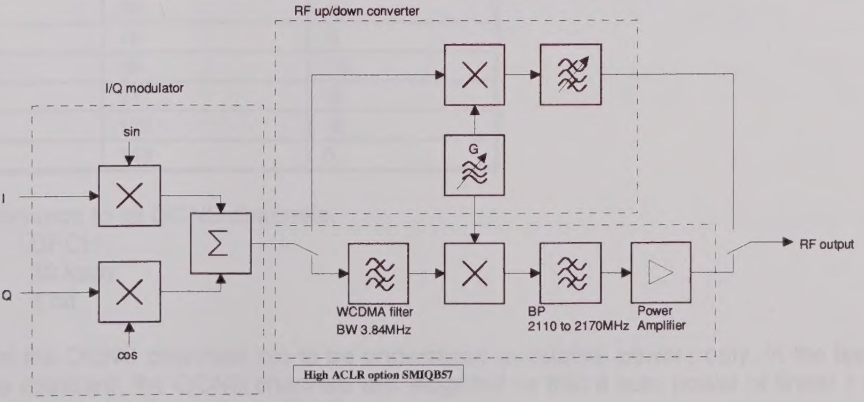


Fig. 1 High ACLR for W-CDMA

Option SMIQB57 incorporates a filter tailored to the channel bandwidth of a 3GPP W-CDMA signal (3.84 MHz) for suppressing unwanted signal components outside the useful band. SMIQB57 can for this reason be used effectively only with this particular type of digital modulation. Despite this, the option can be used with other digital modulation modes provided the occupied bandwidth is smaller than 3.84 MHz and the channel frequency is in the range of 2110 MHz to 2170 MHz. This may be expedient for applications requiring very high RF levels.

Output power with option SMIQB57 (*High ACLR for W-CDMA*):

With option SMIQB57 installed, the maximum output power is +30 dBm. The dynamic range of the electronic level setting is extended to 30 dB in the *Attenuator Fixed* mode.

IEC/IEEE-bus command `:SOUR:DM:IQ:HACLr:STAT`

New Features Not Concerning HD Model

VECTOR MOD menu

IQ FILTER Only with option SMIQB47; not on SMIQ03HD.
Selection between filter off and a 850 kHz, 2.5 MHz or 5 MHz lowpass filter in the baseband. The filters suppress noise in the baseband from 900 kHz, 3 MHz or 6 MHz, which improves adjacent channel power (ACP) with IS-95 and W-CDMA. The filter frequency response is automatically compensated with DIGITAL MOD and DIGITAL STD in the MCOD mode, so that the error vector magnitude (EVM) deteriorates only slightly. If the VECTOR MOD mode is active and AMIQ with WinIQSIM used as a source, the frequency response is compensated by WinIQSIM. For applications requiring a very low EVM the I/Q filter should always be switched OFF.

OFF	filter off
850 kHz	850 kHz lowpass filter
2.5 MHz	2.5 MHz lowpass filter
5 MHz	5 MHz lowpass filter

IEC/IEEE-bus commands

```
:SOUR:DM:IQ:FILT:STAT ON|OFF
:SOUR:DM:IQ:FILT:FREQ 2.5MHZ
```

New Features on All Models

DIGITAL MOD – FILTER menu:

A new filter was added:

(FILTER...)	**FILTER TYPE**	GAUSS LINEAR	linearized Gaussian filter for GSM_EDGE (to GSM Specification 05.04, Change Request A010) IEC/IEEE-bus command: `:SOUR:DM:FILT:TYPE LGA`
		GAUSS LINEAR (old version)	linearized Gaussian filter for GSM_EDGE (to GSM Specification older than 05.04) IEC/IEEE-bus command: `:SOUR:DM:FILT:TYPE OLG`

DIGITAL STD – WCDMA/3GPP menu:

OCNS CHANNELS was changed to ADD OCNS.

ADD OCNS Simulation of orthogonal channel noise. This menu item is available only in the downlink and if option SMIQB48 is installed. For more information refer to section 2.15.4.

Section 2.15.4 ADD OCNS:

Generation of orthogonal channel noise (OCNS) to TS 25.101

The OCNS scenario is defined as follows:

Table C.6: DPCH spreading code, timing offsets and relative level settings for OCNS signal

Channelization code	Timing offset ($x256T_{chip}$)	Level setting (dB)
2	86	-1
11	134	-3
17	52	-3
23	45	-5
31	143	-2
38	112	-4
47	59	-8
55	23	-7
62	1	-4
69	88	-6
78	30	-5
85	18	-9
94	30	-10
125	61	-8
113	128	-6
119	143	0

Parameters common to all OCNS channels:

Type:	DPCH
Symbol rate:	30 ksps
Pilot length:	8 bit

The powers of the OCNS channels are to be understood as relative powers only. In the test scenarios defined by the standard, the OCNS channels are weighted so that a sum power of linear 1 (or 0 dB) is obtained. This is done automatically in SMIQ.

If ADD OCNS is set to ON,

- channels 15 to 30 of base station 1 are automatically coded and set as shown in the table above and
- the power of the OCNS component is adjusted automatically so that the powers of the OCNS channels and the powers of the non-OCNS channels of base station 1 add up to yield a sum power of linear 1.

The settings for the 16 OCNS channels are READ ONLY settings, i.e. they cannot be modified. In the channel table, the OCNS channels are marked by an "O" after the channel number.
If the setting of a non-OCNS channel is modified, the power of the OCNS channels is adjusted immediately. If adjustment is not possible because the powers of the non-OCNS channels already add up to 0 dB or higher, the OCNS channels are set to -60 dB. In this case an error message is output when WCDMA/3GPP is switched on.

Further Settings of ENHANCED CHANNELS Menu:

SEQUENCE LENGTH The maximum sequence length not only depends on free DGEN list memory space but also decisively on the ENHANCED P-CCPCH/BCH status and, where applicable, on the number of DPCHs:

ENHANCED P-CCPCH/BCH STATE = OFF

1 DPCH:	1022 frames
2 DPCHs:	511 frames
3 DPCHs:	341 frames
4 DPCHs:	255 frames

ENHANCED P-CCPCH/BCH STATE = ON

SYMBOL RATE DPCH ≤ 30 ksps:	2044 frames
SYMBOL RATE DPCH > 30 ksps:	500 frames

New IEC/IEEE-Bus Commands

[:SOURce]:DM:IQ:FILTer:FREQuency 850 kHz, 2.5 MHz, 5 MHz, 7.5 MHz, 10 MHz

With this command, an IQ filter with the required cutoff frequency is selected. The command is available, with option SMIQB47 (*LOW ACP for IS-95 CDMA and W-CDMA*) installed, for cutoff frequencies 850 kHz, 2.5 MHz and 5 MHz. With model SMIQ03HD, cutoff frequencies 2.5 MHz, 5 MHz, 7.5 MHz and 10 MHz are available.

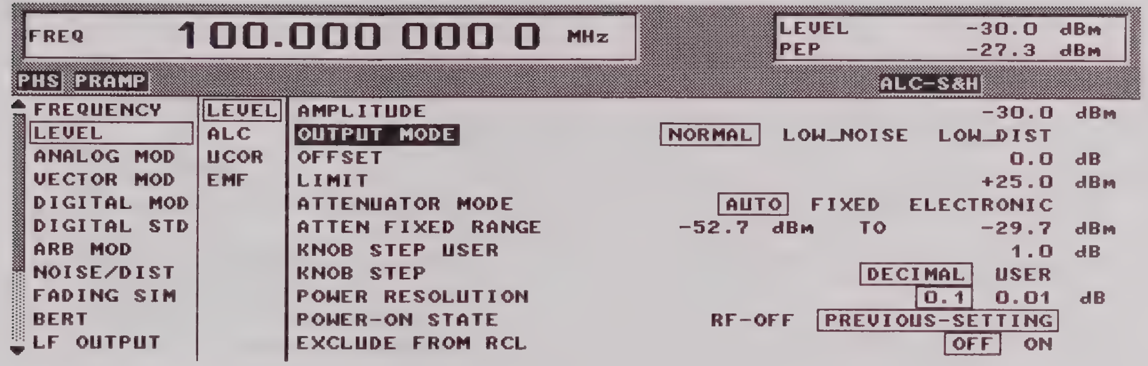
Example: `:SOUR:DM:IQ:FILT:FREQ 2.5MHZ` *RST value is 2.5 MHz

[:SOURce]:DM:IQ:HACLr:STATe ON | OFF

This command switches the option SMIQB57 on or off.

Example: `:SOUR:DM:IQ:HACL:STAT ON` *RST value is OFF

Supplement to Manual SMIQ
Output Mode



```
FREQ        100.000 000 0  MHz             LEVEL            -30.0  dBm
                                           PEP              -27.3  dBm
PHS PRAMP                                                   ALC-S&H
FREQUENCY     LEVEL  AMPLITUDE                                -30.0  dBm
LEVEL         ALC    OUTPUT MODE          NORMAL  LOW_NOISE  LOW_DIST
ANALOG MOD    UCOR   OFFSET                                    0.0  dB
VECTOR MOD    EMF    LIMIT                                   +25.0  dBm
DIGITAL MOD          ATTENUATOR MODE         AUTO  FIXED  ELECTRONIC
DIGITAL STD          ATTEN FIXED RANGE   -52.7 dBm   TO      -29.7  dBm
ARB MOD              KNOB STEP USER                            1.0  dB
NOISE/DIST           KNOB STEP                         DECIMAL  USER
FADING SIM           POWER RESOLUTION                     0.1   0.01  dB
BERT                 POWER-ON STATE          RF-OFF  PREVIOUS-SETTING
LF OUTPUT            EXCLUDE FROM RCL                        OFF   ON
```

NORMAL	This mode corresponds to that of the previous SMIQ.
LOW NOISE:	The modulation in the SMIQ is set so that the power of the second and following adjacent channels (ALT1...) is minimal.
LOW DIST:	The modulation in the SMIQ is set so that the power of the first adjacent channel (ADJ) is minimal. This optimization takes into consideration the modulation signal and the version-dependent parameters of the module at 2 GHz.

Hints for the individual modes:

CW:	The output mode has no effect in this mode.
VECTOR MOD:	With the LOW NOISE output mode, you should additionally raise the IQ level applied (max. 1 V p). The optimal IQ level with LOW DIST depends on the modulation signal and must be ascertained through measurements. The adjacent channel power for symbol rates <200 kHz is essentially determined through multiplicative phase noise. The IQ level can be set to 250 mV p independent of the modulation signal. The level error caused by a change in the IQ level can be corrected with the LEVEL OFFSET.
ARB MOD:	With the LOW NOISE / LOW DIST output mode, the modulation in the SMIQ only changes by a fixed amount. The user can vary the IQ level in order to achieve the best results with LOW DIST as a function of the modulation signal.
FADSIM/NDSIM:	If these modules are activated, the output mode has no effect. If FSIM is activated, the adjacent channel power can be optimized by the parameter "Insertion Loss Setting Mode" in the FADSIM menu.

Additional hints:
The specifications for the level accuracy and harmonic ratio are only valid for the NORMAL output mode; in the other modes they can change.

IEC/IEEE-bus command:
```
SOURce:POWer[:LEVel]:OMODe NORMal
                          |LDIStortion
                          |LNOise
```

Tabbed Divider Overview

Volume 1

How to Use this Manual
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Data Sheet
Supplement to Data Sheet

Safety Instructions
Certificate of quality
EC Certificate of Conformity
List of R & S Representatives

Tabbed Divider

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How to Use this Manual
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Introduction on how to use the manual

This operating manual contains essential information on commissioning, manual control, remote control, maintenance and checking the rated specifications of SMIQ as well as all specifications of the unit and available options.

The following models and options are described in this manual:

- SMIQ02B – Vector Signal Generator 300 kHz to 2.2 GHz
- SMIQ03B – Vector Signal Generator 300 kHz to 3.3 GHz
- SMIQ04B – Vector Signal Generator 300 kHz to 4.4 GHz
- SMIQ06B – Vector Signal Generator 300 kHz to 6.4 GHz

- Option SM-B1 – Reference Oscillator OCXO
- Option SM-B5 – FM/PM Modulator
- Option SMIQB11 – Data Generator
- Option SMIQB12 – Memory Extension to Data Generator
- Option SMIQB14 – Fading Simulator FSIM1
- Option SMIQB15 – Second Fading Simulator FSIM2
- Option SMIQB17 – Noise Generator and Distortion Simulator
- Option SMIQB19 – Rear Panel Connections for RF and LF
- Option SMIQB20 – Modulation Coder
- Option SMIQB21 – Bit Error Rate Test
- Option SMIQB42 – Digital Standard IS-95 CDMA
- Option SMIQB43 – Digital Standard W-CDMA
- Option SMIQB45 – Digital Standard 3 GPP W-CDMA
- Option SMIQB47 – LOW ACP Filter
- Option SMIQB48 – Enhanced Functions für 3GPP W-CDMA
- Option SMIQB49 – Dynamic Fading
- Option SMIQB60 – Arbitrary Waveform Generator

The chapters with associated contents are as follows:

Data sheets	list guaranteed specifications for the functions and characteristics of the unit and its options.
Chapter 1	provides information on putting the unit into operation (AC supply connection, switch-on/off), functional test, preset settings, fitting the options and mounting the unit into a 19" rack.
Chapter 2	explains the **manual control** of SMIQ. It contains front and rear panel views, describes the control elements as well as connectors, provides a short introduction with sample settings for first-time users. It also explains how to change parameters and the use of the list editor and gives an overview of the menus for the functions covered by the unit and its options. It also presents the functions and menus of the unit and its options (frequency and level setting, analog and digital modulation, ARB, external modulation source AMIQ, fading simulation, noise generation and distortion simulation, BERT, sweep, LIST mode, memory sequence and general functions not relating to signal generation).

Chapter 3	provides information on **remote control** of SMIQ. It informs about basics like IEC/IEEE bus, RS-232C interface, interface and device-dependent messages, command processing, status reporting system etc. It also includes an overview of each command system and describes all commands available in the unit and its options.
Chapter 4	informs about preventive maintenance and functional tests.
Chapter 5	contains information on how to check the rated specifications (required test equipment, test setup, test procedure) and on the performance test report.

Annex A	provides information on interfaces.
Annex B	contains a list of SCPI- and SMIQ-specific error messages displayed by the unit.
Annex C	provides an alphabetical list of commands.
Annex D	gives programming examples for remote control.
Index	provides the index with entries in alphabetical order.

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READY FOR
WCDMA

ROHDE & SCHWARZ
SIGNAL GENERATOR
SMIQ 03B
1125 5555 03
FREQ
2.000 000 000 0 GHz
LEVEL
PEP
WCDMA/3GPP
(peak/avg Power)[dB]
DATA INPUT

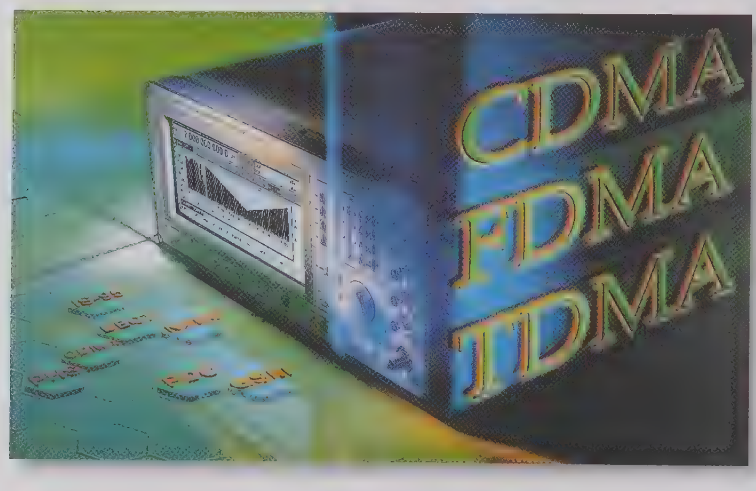
Vector Signal Generator R&S SMIQ

Digital signals of your choice

- Frequency range 300 kHz to 2.2 GHz/3.3 GHz/4.4 GHz/ 6.4 GHz
- Analog and digital modulation
- Versatile and broadband generation of digitally modulated signals up to 18 Msymbol/s
- Generation of TDMA, CDMA, WCDMA and CDMA2000 standard signals to all main mobile radio standards
- Broadband I/Q modulator with outstanding vector accuracy
- Optional internal fading simulator to test specifications of mobile radio standards
- Optional internal noise generator and distortion simulator
- Optional BER measurement
- Optional arbitrary waveform generator
- Low ACP for IS-95 CDMA and WCDMA (option)
- Low cost of ownership due to three-year calibration intervals
- Future-oriented platform concept

ROHDE & SCHWARZ

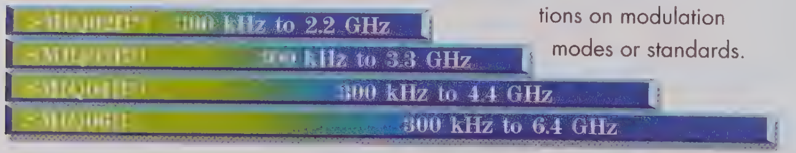
A safe investment for the future ...



The B series of Signal Generator Family R&S SMIQ for analog and digital modulation from Rohde&Schwarz is offering solutions for today and tomorrow. This series particularly takes into account future developments in the field of 3rd-generation digital mobile radio.

The R&S SMIQ family comprises four models which differ in their upper frequency limits. These feature a hitherto unrivalled versatility regarding signal generation and signal quality and are therefore ideal for use in development and type-approval testing.

With their outstanding price/performance ratio, these signal generators are also economically attractive for applications in production.



The wide frequency range from 300 kHz to 6.4 GHz covers all main radio bands including their IF ranges.

The high-grade I/Q modulator fitted as standard ensures minimum error vector magnitude and high intermodulation suppression.

Using modern digital signal processor (DSP) technology, the versatile concept allows the generation of high-precision digital modulation signals with high bit rates without any limitations on modulation modes or standards.

In addition to digital modulation, the signal generators provide the full range of analog modulation modes as well as simultaneous modulation capability.

*) Every model upgradable up to 6.4 GHz

The right option for every application

APPLICATION[1]	**R&S SM-B1** Reference Oscillator OCXO	**R&S SM-B5** FM/φM Modulator	**R&S SMIQB11** [2] Data Generator (15 Mbit RAM)	**R&S SMIQB12** Memory Extension, 32 Mbit	**R&S SMIQB14** Fading Simulator (6 paths)	**R&S SMIQB15** 2nd Fading Simulator (6 paths)	**R&S SMIQB17** Noise Generator and Distortion Simulator	**R&S SMIQB20** Digital Modulation Coder	**R&S SMIQB21** [2] BER measurement	**R&S SMIQB42** [3] Digital Standard IS-95 CDMA	**R&S SMIQB43** [3] Digital Standard WCDMA (NTT DoCoMo 1.0, ARIB 0.0)	**R&S SMIQB45** [3] Digital Standard WCDMA according to 3GPP (FDD)	**R&S SMIQB47** Low ACP for IS-95 CDMA and WCDMA	**R&S SMIQB48** Extended Functions for WCDMA 3GPP	**R&S SMIQB60** Arbitrary Waveform Generator	R&S SMIQK11 Digital Standard IS-95 CDMA (with ARB R&S SMIQB60)	[illegible]
TDMA																	
To standard	○		●	○	○			●									
Non-standard	○	○	●	○	○		○	●	○								
CDMA IS-95																	
To standard	○	○	●	○	○		○	●		●					○	○	
WCDMA																	
To standard	○	○	●	○	○		○	●			●	●	●	○	○		
CDMA2000																	
To standard	○	○	●	○	○		○	●							●		●
Fading																	
					●	○											
Vector modulation																	
	○	○	○	○	○	○	○	○		○	○	○	○				
Analog modulation (AM, FM, φM)																	
	○	●															
Fast setting time																	
	○	○	○	○	○	○	○	○		○	○	○					

[1] R&S SMIQ02B/03B (R&S SMIQ04B/06B) can be equipped with up to three (two) of the following options: R&S SM-B5, R&S SMIQB14, R&S SMIQB15 or R&S SMIQB17

[2] Option R&S SMIQB20 required

[3] Options R&S SMIQB20 and R&S SMIQB11 required

● = required ○ = optional

R&S SMIQ rear pa

I FADER
Q FADER
I/Q AUX
PAR DATA
LF
EXT 1
EXT 2
DATA
BIT CLK
SYM CLK
X AXIS
BLANK
MARKER
SER DATA
PULS
TRIGGER
EXTTUNE
BER
RS 232

R&S SMIQ – a signal generator family ...

Digital modulation

Any digital modulation modes (with option R&S SMIQB20)

- Free choice of modulation mode from ASK through to 256QAM
- Any kind of baseband filtering with variable filter parameters
- Symbol rate adjustable up to 18 Msymbol/s
- Realtime coding of internal and external data
- Internal PRBS generators

Convenient burst generation for TDMA standards (with option R&S SMIQB20/ R&S SMIQB11)

- TDMA mobile radio standards provided as standard GSM, GSM-EDGE, DECT, NADC (IS-54C/IS-136), PDC, PHS
- Versatile external synchronization capabilities
- Realtime processing of external and internal data
- Generation of TDMA frames with versatile timeslot configuration
- Continuous PRBS sequences
- Optimization of burst shaping to reduce spectra due to switching
- Realtime processing with external data for BER tests
- Slot-by-slot modulation change for TDMA
- Signals with preprogrammed frame structure

Up to 79 Mbit internal data memory (with 2 x option R&S SMIQB12)

Optional multichannel WCDMA signals for 3GPP (FDD) systems (R&S SMIQB45)

Optional cdma2000 standard (R&S SMIQB60 + R&S SMIQK12)

Optional WCDMA standard to ARIB0.0 standard and NTT DoCoMo1.0 (R&S SMIQB43)

Optional CDMA standard to IS-95 (R&S SMIQB42 or R&S SMIQB60 + R&S SMIQK11)

Special options

Fading simulation (options R&S SMIQB14 and R&S SMIQB15)

- Fading of internal or external I/Q signals conforming to mobile radio standards
- 6-path simulation can be enhanced to 12-path simulation (2-channel fading also possible with second vector signal generator)
- Selectable path attenuation and delay
- Simulation of high speeds
- Preprogrammed fading profiles for mobile radio standards GSM, NADC, IS-95 CDMA and TETRA

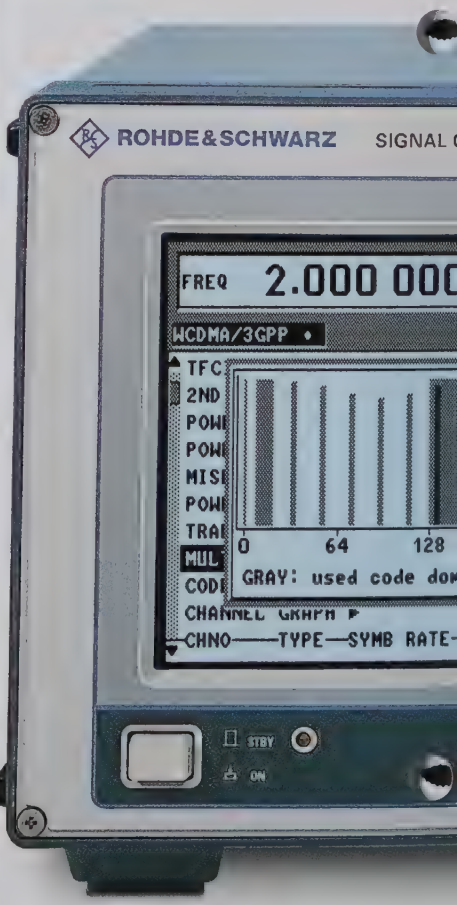
Analog modulation

- Broadband AM with up to 30 MHz modulation frequency
- I/Q modulation with 30 MHz modulation bandwidth (3 dB), 60 MHz RF bandwidth
- Unprecedented vector accuracy and high intermodulation suppression
- Amplitude modulation
- Pulse modulation
- Optional frequency and phase modulation (R&S SM-B5)

RF characteristics

- Wide output frequency range from 300 kHz to 6.4 GHz
- High (up to 16 dBm) and precise output level (<0.5 dB)
- Fast setting time for frequency (<3 ms) and level (<2.5 ms) *)
- Frequency hopping (500 µs)
- High spectral purity (typ. –130 dBc (1 Hz) at 1 GHz and 20 kHz carrier offset)
- RF, AF and level sweep (user-programmable)

*) Without switching the mechanical attenuators



... for all requirements

- Frequency range of basic unit can be fully utilized
- Calibrated RF level in range from –140 dBm to –5 dBm
- Unrivalled price/performance ratio

Noise generator and distortion simulator (option R&S SMIQB17)

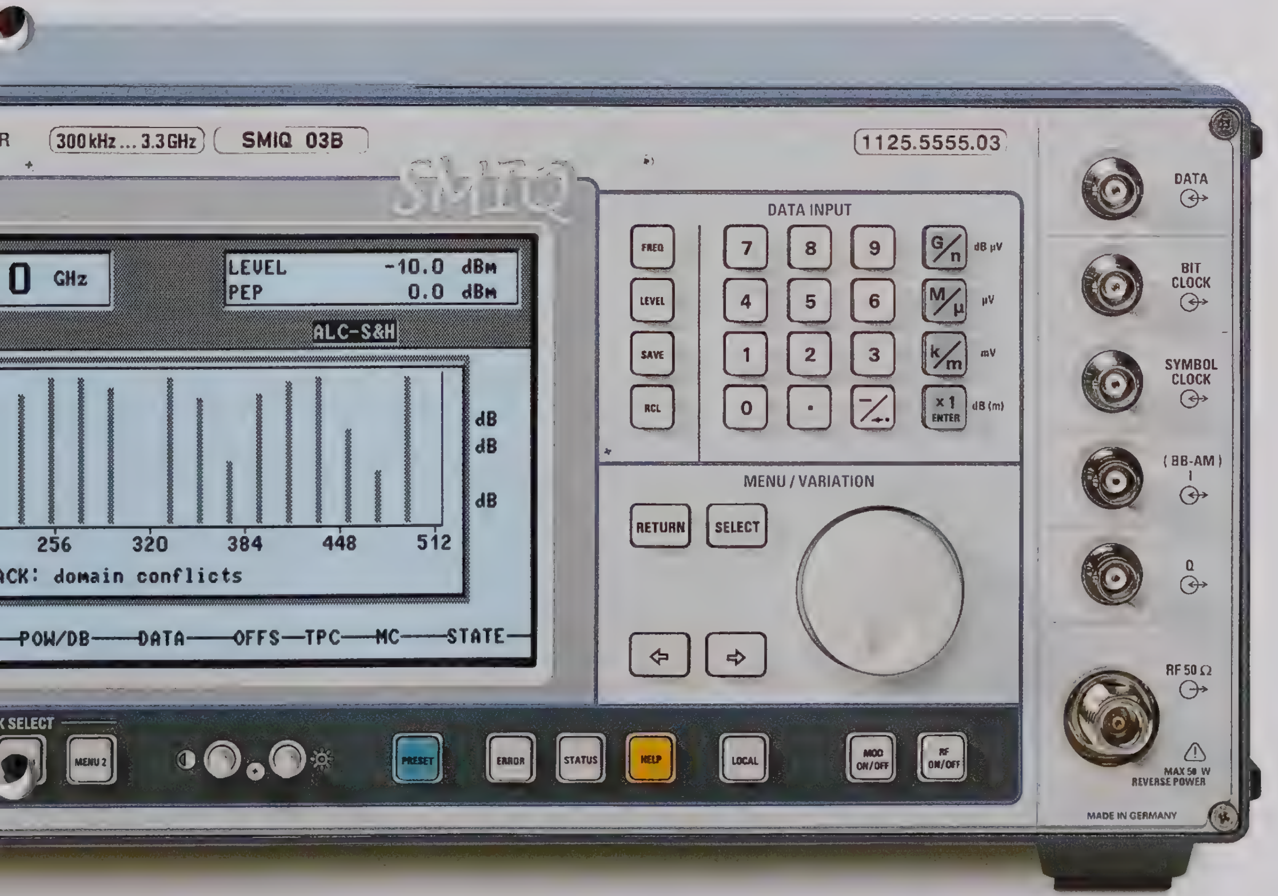
- Simulation of amplitude and phase distortion (AM/AM and AM/φM characteristics)
- Distortion characteristics programmable from up to 30 input values
- Superimposed noise signals (AWGN)
- C/N ratio variable with high resolution over a wide range
- Broad noise bandwidth (10 kHz to 10 MHz)

Bit error rate measurements (option R&S SMIQB21)

- Up to 30 MHz clock rate

Low ACP for IS-95 CDMA and WCDMA (option R&S SMIQB47)

- Specially designed for 1.2288 Mcps (cdmaOne/cdma2000), 3.84 Mcps and 2 channels 3.84 Mcps/5 MHz offset (3GPP)
- Can be used with internal (option R&S SMIQB42/43/45) or external CDMA/WCDMA signals
- Typical WCDMA adjacent-channel power ratio (5 MHz offset, 3.84 Mcps): –67 dBc (1 DPCH)
- Typical IS-95 CDMA adjacent-channel power ratio (885 kHz offset): –78 dBc (9 code channels)


300 kHz ... 3.3 GHz
SMIQ 03B
1125.5555.03
SMIQ
LEVEL -10.0 dBm
PEP 0.0 dBm
ALC-S&H
256 320 384 448 512
domain conflicts
POW/DB DATA OFFS TPC MC STATE
DATA INPUT
FREQ
LEVEL
SAVE
RCL
MENU / VARIATION
RETURN
SELECT
MENU 2
PRESET
ERROR
STATUS
HELP
LOCAL
MOD ON/OFF
RF ON/OFF
DATA
BIT CLOCK
SYMBOL CLOCK
(BB-AM) I
Q
RF 50 Ω
MAX 50 W REVERSE POWER
MADE IN GERMANY

Outstanding RF characteristics

From 300 kHz to 6.4 GHz

With its wide frequency range, the R&S SMIQ family has the right model for every application. The uppermost frequency limit of 6.4 GHz leaves sufficient margin even for WLL (wireless local loop) systems. Frequency extension options allow upgrading to higher frequency limits.

Level – high and precise

With a maximum output level of +13 dBm (+16 dBm in overrange) insertion losses caused by cables or switching matrixes can easily be compensated. For driving components with high input level the use of an external amplifier is not necessary.

A level accuracy of <0.5 dB allows high-precision measurements even on highly sensitive analog and digital receivers.

Excellent spectral purity

R&S SMIQ provides output signals of excellent spectral purity. Low-noise frequency synthesis ensures modulation of highest quality for reliable test signals.

Fast setting times

Fast setting times are among the most important criteria when it comes to choosing the right signal generator, especially in production.

The synthesizers of the R&S SMIQ family excel in this respect: with a frequency setting time of less than 3 ms they allow extremely fast measurements.

Besides standard sweep functions for RF, AF and level, R&S SMIQ features an extremely versatile and fast sweep mode for frequency and level settings to be carried out with the aid of stored lists. This mode with a setting time of less than 500 µs is ideal for frequency hopping applications.

Low cost of ownership

The use of high-precision reference elements with long-term stability ensures reliable operation over a long period of time. The three-year calibration intervals cut costs and increase availability.

Designed for the future

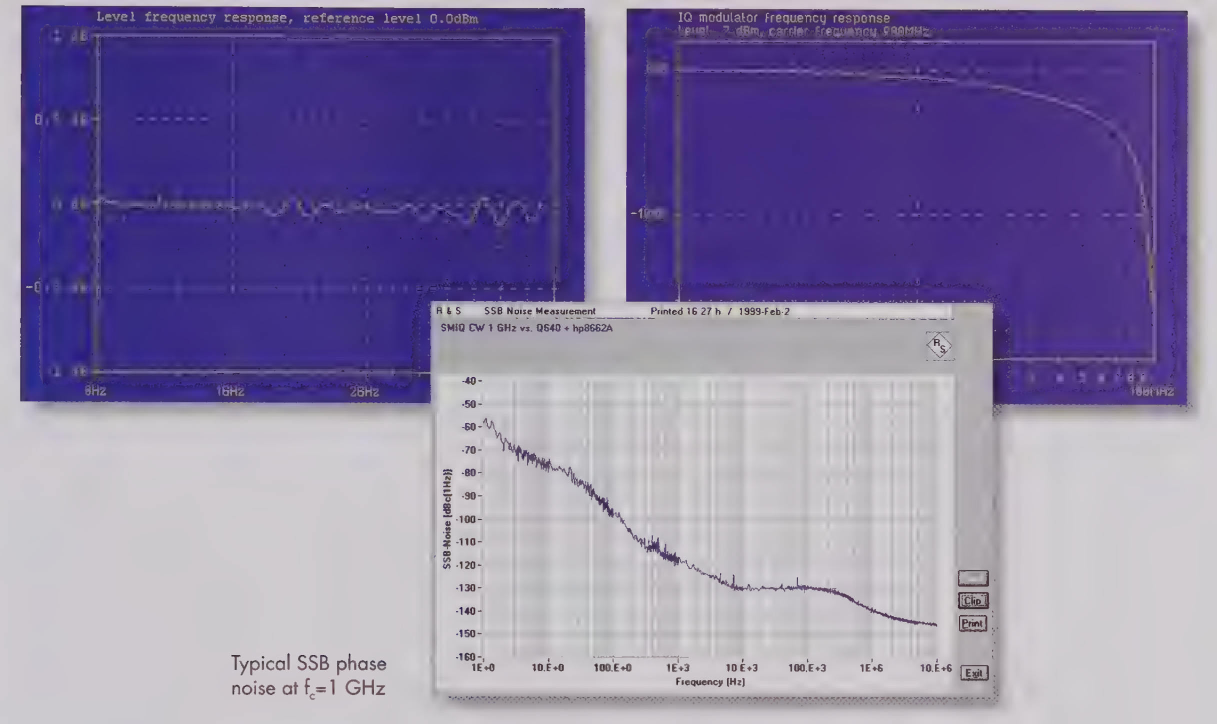
The open concept of R&S SMIQ allows the functionality of the signal generator to be adapted to future requirements in a simple and cost-effective way.

Since R&S SMIQ uses programmable digital signal processing chips throughout, its capabilities are not limited by the hardware used.

New functions can be downloaded very simply via the serial interface of the generators.

Typical level accuracy at 0 dBm

Typical I/Q frequency response



Typical SSB phase noise at f_c=1 GHz

Analog modulation

Excellent analog characteristics

R&S SMIQ sets standards in the field of digital modulation – without any restrictions on the analog side.

Amplitude modulation
The modulation frequency range is DC to 50 kHz. Particularly noteworthy is the extremely low incidental phase modulation with AM, which plays an important role in AM sensitivity tests on FM receivers.

Frequency modulation (option R&S SM-B5)
The modulation frequency range is DC to 2 MHz. In the FM DC mode, extremely high carrier frequency accuracy is ensured through the use of a novel control circuit. There is virtually no drift. This characteristic allows the digital signalling of receivers also by means of analog frequency modulation.

Phase modulation
Phase modulation ranges from DC to 100 kHz. This wide span opens up fields of application for which most signal generators do not qualify, for instance tests on phase-sensitive circuits or the generation of PSK modulation with freely selectable phase deviation.

IQ modulation at the highest level
The precision I/Q modulator of R&S SMIQ is the basis for the excellent modulation characteristics with spurious suppression of more than 70 dB. In addition to a bandwidth that is designed for the needs of future broadband systems, the modulator features high intermodulation suppression which with digital modulation yields excellent characteristics regarding adjacent-channel power.

Broadband amplitude modulation
Broadband amplitude modulation (BB-AM) is provided as standard and allows accurate envelope control (eg for pulse shaping) or generation of analog video signals. It features high modulation quality and low incidental FM at modulation frequencies of up to 30 MHz (3 dB) and is generated via the I input of the I/Q modulator.

Digital modulation

Fit for every requirement

The rapidly changing digital communications market makes great demands on measurement technology: for one thing measurements need to be done fast in an uncomplicated way, for another investments made today should cover the requirements of tomorrow. R&S SMIQ is setting standards. It provides convenient generation of high-precision signals in line with today's digital standards and in addition allows free variation of all digital modulation parameters.

TDMA, CDMA or WCDMA?

R&S SMIQ is at home in all access methods. It is just as good in generating versatile frame structures of all main TDMA systems as it is in CDMA and WCDMA applications.

Universal modulation coder

The universal modulation coder (option R&S SMIQB20) is the core of complex digital modulation generation. From the digital input signals it derives the analog signals for the I/Q modulator of R&S SMIQ in realtime. The internal or external digital input signals are made up of serial or parallel data bit streams, clock signals, signals for burst control and triggering. A PRBS generator of different sequence lengths is contained in the modulation coder as an internal signal source.

The modulation coder allows free selection of the format, baseband filtering and symbol rate of digital modulation. The selected parameters can be varied within a wide range.

Four ways of generating digital signals

Vector Signal Generators R&S SMIQ can generate digitally modulated signals in four different ways.

1. Vector modulation
In this mode, externally generated I/Q signals are applied to the I/Q modulator of R&S SMIQ. I/Q Modulation Generator R&S AMIQ together with Simulation Software R&S WinIQSIM™ are perfect tools for the generation of external I/Q signals.

2. Digital modulation
The universal modulation coder (R&S SMIQB20) and data generator (R&S SMIQB11) options provide a platform for generating digitally modulated signals that are variable in a wide range. Modulation mode, filtering, data source and symbol rate can be selected by the user.

3. Digital standards
The digital standards provide at a keystroke base-station and mobile-station signals to telecommunication standards – based on the capabilities of the optional digital modulation coder (R&S SMIQB20). The TDMA (time division multiple access) standards GSM, GSM-EDGE, DECT, NADC (IS-54C, IS-136), PDC and PHS come with the optional data generator (R&S SMIQB11). The CDMA standard IS-95 (R&S SMIQB42) or WCDMA according to NTT DoCoMo systems (R&S SMIQB43) as well as WCDMA according to 3GPP/FDD (R&S SMIQB45) are also available as an option. The number of future digital standards that can be simultaneously accommodated in R&S SMIQ is unlimited.

4. Arbitrary waveform generator
The arbitrary waveform generator (option R&S SMIQB60) is an integrated I/Q modulation source adding extra functionality to R&S SMIQ. It allows the generation of **arbitrary** modulation signals such as COFDM, multicarrier, or noise. The most convenient way of generating a wide variety of signals is by computing them with the supplied R&S WinIQSIM™ PC software and loading them into the unit (see data sheet PD 0757.3970). Signals computed with the aid of commercial mathematical programs can be transferred, too, using free-of-charge auxiliary software (R&S IQWizard).

Comprehensive synchronization capabilities

The optional Data Generator R&S SMIQB11 with a memory of 15 Mbit, which can be extended up to 79 Mbit, is the internal data source for the modulation coder. These data are also available at the outputs, eg as reference for BER (bit error rate) measurements.

Up to six different control signals can be generated synchronously with the data bits to provide symbol-accurate trigger signals, control frequency hopping and mark level bursts. With the aid of these control signals external measurements can be synchronized.

The internally generated data streams of the data generator can conveniently be synchronized to external trigger events. A comprehensive range of functions such as trigger delay is available for this purpose. The switching threshold of the trigger input can be adjusted to the level of external signals.

Convenient burst signal generation

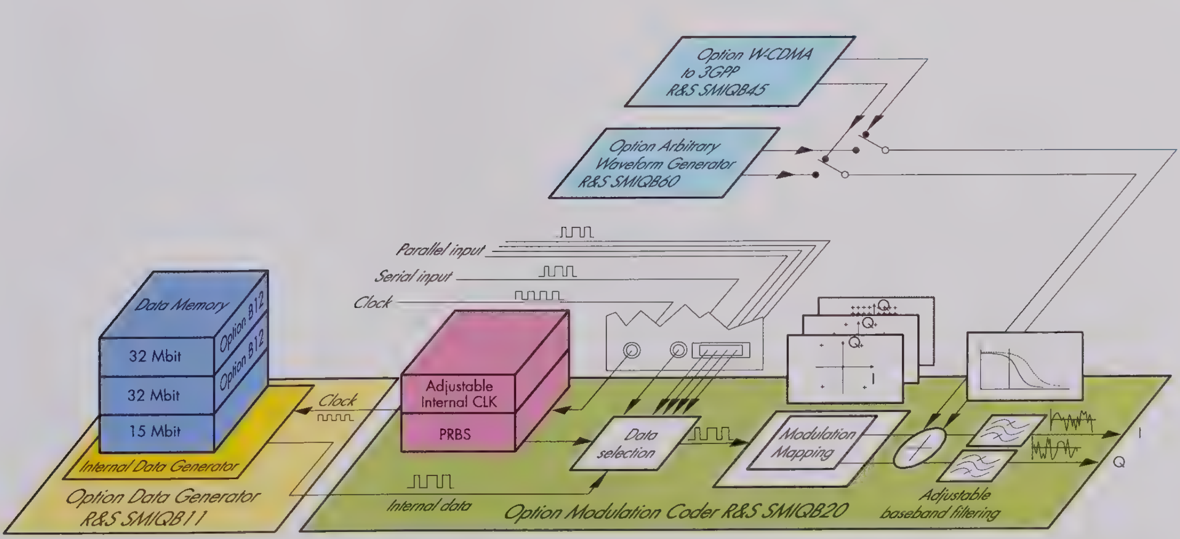
Symbol-synchronous amplitude control of the RF signal is required especially for generating test signals for mobile radio systems using a TDMA method.

In addition to ensuring a large dynamic range, the switching of timeslots should be such that the spectrum due to switching is suppressed to a very high degree.

R&S SMIQ is ideal to meet these requirements. Convenient menus allow timeslots to be defined independently of one another, reduced in level or completely switched off. Moreover, the slew rate and the shape of the switching signal edge can be varied.

READY FOR
WCDMA

... fit already today for tomorrow's communication


Option W-CDMA
to 3GPP
R&S SMIQB45
Option Arbitrary
Waveform Generator
R&S SMIQB60
Parallel input
Serial input
Clock
Data Memory
Option B12
Option B12
32 Mbit
32 Mbit
15 Mbit
Internal Data Generator
Option Data Generator
R&S SMIQB11
Clock
Adjustable
Internal CLK
PRBS
Data
selection
Internal data
Modulation
Mapping
Q
I
Adjustable
baseband filtering
I
Q
Option Modulation Coder R&S SMIQB20

Principle of digital modulation signal generation

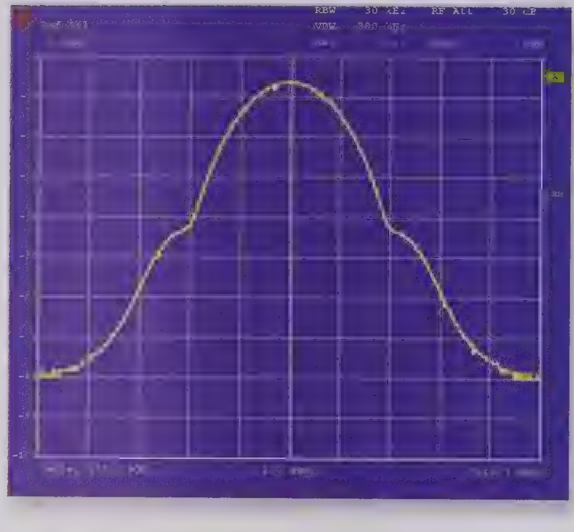
Digital modulation

Standards

R&S SMIQ fitted with the two options modulation coder (R&S SMIQB20) and data generator (R&S SMIQB11) provides standard-conformal signals for testing mobile and base stations of the main mobile radio networks. These test signals contain the necessary protocol information and frame structures for testing receivers.

The timeslots (bursts) and their data contents can be specified for the TDMA standards GSM, GSM-EDGE, DECT, NADC, PDC and PHS via user-friendly menus. The main burst types are predefined and available at a keystroke. They can easily be modified, stored and reused for tests.

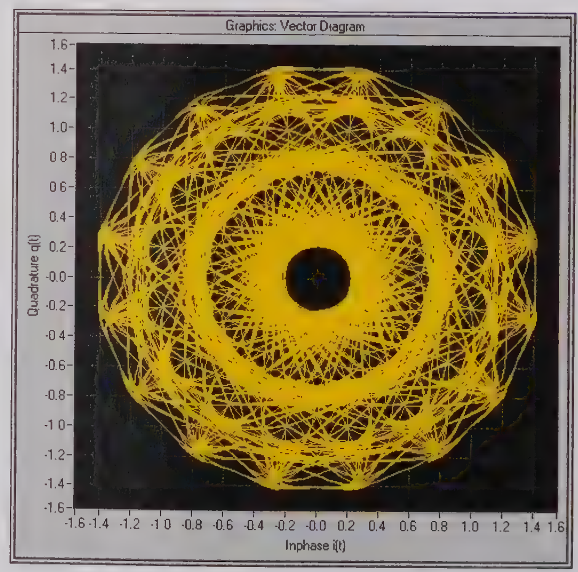
Continuous PRBS data streams and internally generated data lists as well as externally provided serial data streams can be inserted in realtime into the data fields of the frame structures.



GSM spectrum

GSM Global System for Mobile Communication

Frames and timeslot configuration conform to the GSM standard. Each timeslot (burst) has a certain data structure depending on its use, eg as traffic channel or channel for frequency synchronization.



EDGE vector diagram

GSM-EDGE System for Mobile Communication

In comparison with GSM and GMSK modulation GSM-EDGE is based on 8PSK with 3π/8 rotated modulation. Modulation change between GMSK and 8PSK is possible slot by slot.

DECT Digital Enhanced Cordless Telecommunication Standard

This operating mode allows signals to be generated to ETSI DECT standard.

NADC North American Digital Cellular (IS-54C, IS-136)

The data protocol structure conforms to NADC specifications IS-54C and IS-136. The following predefined burst types are available: uplink burst, downlink burst, all data.

PDC Personal Digital Cellular (RCR STD-27C)

The data protocol structure conforms to PDC specification RCR-27C. This standard is largely identical with the NADC standard.

PHS Personal Handy Phone System (RCR STD-28)

The data protocol structure conforms to PHS specification RCR-28. The following predefined burst types are provided: control physical slot, communication physical slot, sync, TCH, VOX, all data.

CDMA IS-95 Code Division Multiple Access (with option R&S SMIQB42)

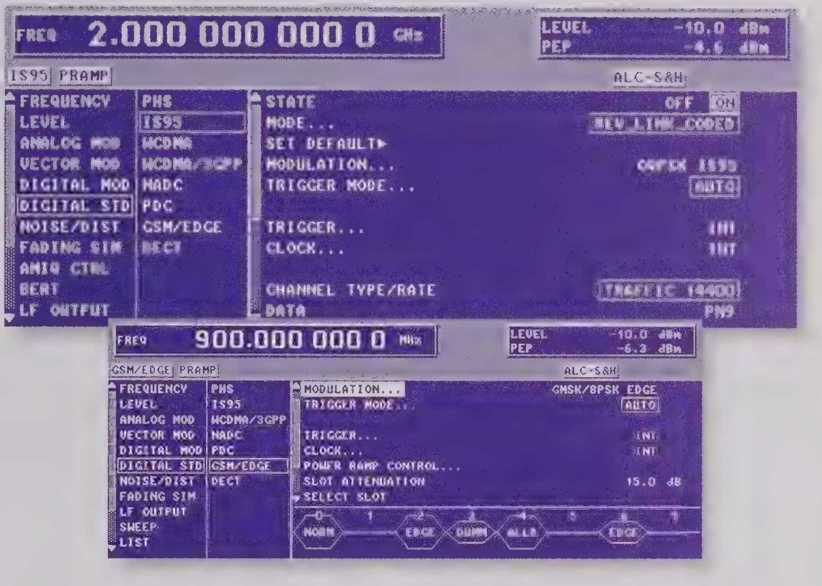
For the CDMA base-station signal (forward link) up to 64 code channels can be generated with user-selected Walsh codes. The power of the code channels can be selected independently for up to four channels. The channel data consist of various internal PRBS or fixed data patterns.

The mobile-station signal (reverse link) can be configured for full-rate as well as for half-rate operation. Moreover it is possible to generate a channel-coded reverse-link signal.

WCDMA to NTT DoCoMo and ARIB (with option R&S SMIQB43)

WCDMA is one of the favoured technologies for 3rd generation mobile radio.

Fitted with option WCDMA (R&S SMIQB43), R&S SMIQ is able to generate WCDMA signals to the Japanese specifications of NTT DoCoMo and ARIB[1].


FREQ 2.000 000 000 0 GHz
LEVEL -10.0 dBm
PEP -4.6 dBm
IS95
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
NOISE/DIST
FADING SIM
AMIQ CTRL
BERT
LF OUTPUT
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
STATE OFF ON
MODE...
SET DEFAULT
MODULATION...
TRIGGER MODE...
TRIGGER...
CLOCK...
CHANNEL TYPE/RATE
DATA PN9
FREQ 900.000 000 0 MHz
LEVEL -10.0 dBm
PEP -6.3 dBm
GSM/EDGE
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
NOISE/DIST
FADING SIM
LF OUTPUT
SWEEP
LIST
PHS
IS95
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
MODULATION... GMSK/8PSK EDGE
TRIGGER MODE... AUTO
TRIGGER... INT
CLOCK... INT
POWER RAMP CONTROL...
SLOT ATTENUATION 15.0 dB
SELECT SLOT
0
1
2
3
4
5
6
7
NORM
EDGE
ALL0
EDGE

Like with IS-95 CDMA, uplink (mobile station to base station) and downlink (base station to mobile station) can be simulated with up to 15 code channels. A chip rate of 4.096 Mcps is preset, but can be varied any time.

There is a choice of different types of physical channels, such as perch, common control or dedicated physical channels. The frame structure consisting of various data fields (traffic power control or long code mask symbol) is automatically generated for each type of channel.

WCDMA to 3GPP/FDD (with option R&S SMIQB45)

Software option R&S SMIQB45 supports the generation of downlink and uplink signals in line with the 3GPP standard (FDD mode). As the standardization process is not yet completed, the functionality of this option will continuously be adapted to the relevant standard modifications and expansions (for functionality see specifications).

The physical channels including their slot structure are simulated as a whole. Therefore the signals exactly conform to the 3GPP standard regarding timing, spectral distribution and amplitude probability distribution and thus allow correct measurements on the components to be tested.

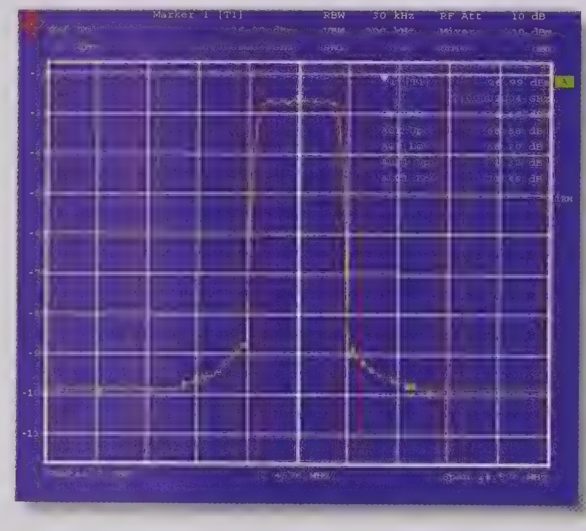
---

[1] Association of Radio Industries and Businesses (ARIB), Specifications of Air Interface for a 3 G Mobile System

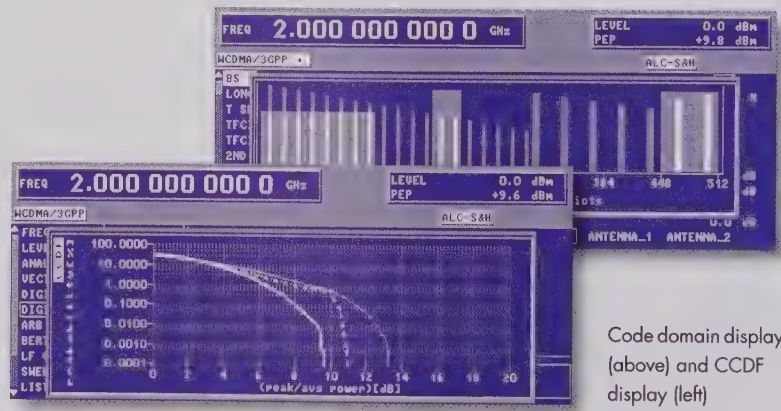
Digital modulation

Signals can be configured in many different ways:

- The current 3GPP standard is supported, stipulating a chip rate of 3.84 Mcps and 15 slots/frame.
- Up to four base or mobile stations with separately selected scrambling code can be simulated. One BS may have up to 128 data channels in addition to special channels. An MS can be operated in the three modes PRACH only, PCPCH only and DPCCH + DPDCH (max. 6 DPDCHs).
- Symbol rate, channelization code, power (can even be varied in time) as well as data contents and timing offset can be selected for each code channel (timing offset can be used to influence signal statistics and thus crest factor).
- P-/S-CPICH, P-/S-SCH, P-/S-CCPCH, AP-/CD-AICH, PDSCH, DL-DPCCH and DPCHs with their corresponding slot structure can be generated in the downlink.
- Transmit diversity is also already supported.
- The clipping function allows simple simulation of the clipping measures implemented in every base station.
- With the R&S SMIQ firmware version 5.70 or higher, WCDMA 3GPP (FDD) signals are generated to 3GPP version 4.1.0. For the downlink, test models 1 (with 16/32/64 channels), 2, 3 (16/32 channels) and 4 have been implemented (to TS25.141). This enables the user to activate downlink test signals to 3GPP specification with a keystroke. There are two non-standardized test scenarios available for the uplink (DPCCH + 1 DPDCH at 60 ksps and DPCCH + 1 DPDCH at 960 ksps).

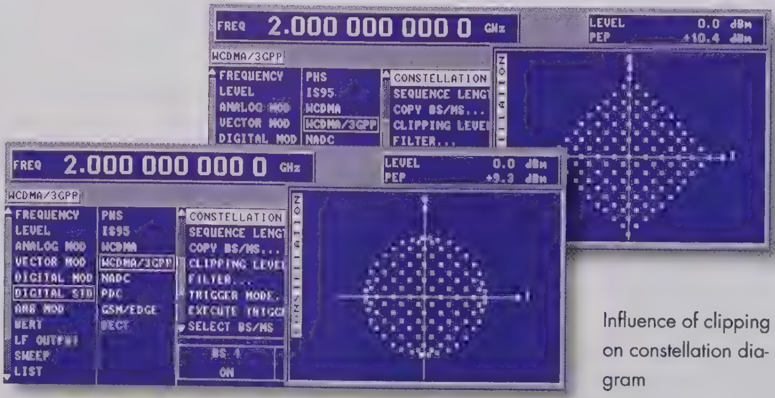

Marker 1 [T1]
RBW 30 kHz
RF Att 10 dB

WCDMA spectrum


FREQ 2.000 000 000 0 GHz
LEVEL 0.0 dBm PEP +9.8 dBm
WCDMA/3GPP

FREQ 2.000 000 000 0 GHz
LEVEL 0.0 dBm PEP +9.6 dBm
WCDMA/3GPP
ALC-S&H
BS
384
448
512
ANTENNA_1
ANTENNA_2
100.0000
10.0000
1.0000
0.1000
0.0100
0.0010
0.0001
0
2
4
6
8
10
12
14
16
18
20
(peak/avg power)[dB]

Code domain display (above) and CCDF display (left)


FREQ 2.000 000 000 0 GHz
LEVEL 0.0 dBm PEP +10.4 dBm
WCDMA/3GPP

FREQ 2.000 000 000 0 GHz
LEVEL 0.0 dBm PEP +9.3 dBm
WCDMA/3GPP
BS 4 ON
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
BERT
LF OUTPUT
SWEEP
LIST
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
CONSTELLATION
SEQUENCE LENGTH
COPY BS/MS...
CLIPPING LEVEL
FILTER...
TRIGGER MODE
EXECUTE TRIGGER
SELECT BS/MS

Influence of clipping on constellation diagram

- The long signal length of up to 13 frames (with 3.84 Mcps) allows realistic signals to be generated.

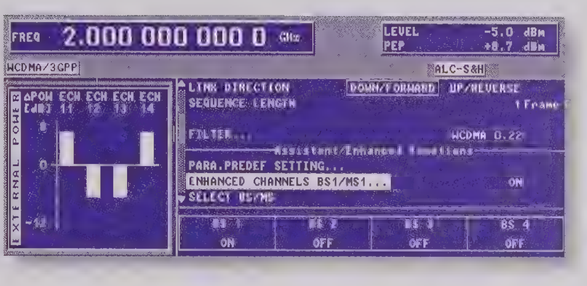
Despite the large variety of functions provided by this option, WCDMA signals can quickly be generated with the aid of assistant functions. With the aid of predefined settings which may additionally be varied through the selection of the crest factor and the number of data channels a WCDMA signal can be generated with a few keystrokes. Further editing tools allow simultaneous configuration of numerous data channels and copying of a complete BS or MS configuration.

But there is much more. R&S SMIQ also provides numerous tools for checking the selected settings: overlapping of individual code channels in the code domain (domain conflicts) is displayed and can automatically be resolved by a keystroke. The graphic display of constellation diagram, CCDF, occupied code domain and active channels allows the generated signals to be checked for conformance to expectations even without the use of an analyzer.

Enhanced functions for WCDMA 3GPP (FDD) digital standard with option R&S SMIQB48

This option expands the functionality of option R&S SMIQB45 WCDMA 3GPP. It allows the generation of up to four enhanced channels that can be combined with the standard channels.

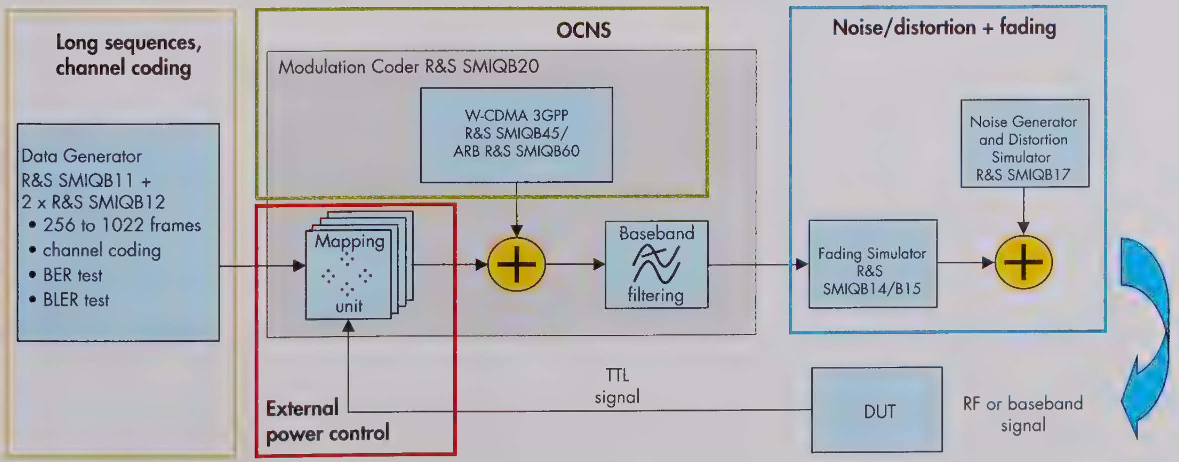
Display of power control graph with external power control


FREQ
2.000 000 000 0 GHz
LEVEL -5.0 dBm
PEP +8.7 dBm
WCDMA/3GPP
ALC-S&H
EXTERNAL POWER
ΔPOW [dB]
ECH 11
ECH 12
ECH 13
ECH 14
0
-12
BS 1
BS 2
BS 3
BS 4

LINK DIRECTION DOWN/FORWARD UP/REVERSE
SEQUENCE LENGTH 1 Frame
FILTER... WCDMA 0.22
Assistant/Enhanced Functions
PARA.PREDEF SETTING...
ENHANCED CHANNELS BS1/MS1... ON
SELECT BS/MS
BS 1 ON
BS 2 OFF
BS 3 OFF
BS 4 OFF

This opens up a variety of other applications:

- The maximum sequence length of four enhanced channels is 256 frames. If only one channel is required, the maximum is 1022 frames (compare sequence length with R&S SMIQB45: 13 frames). Very long signal sequences and continuous PRBS sequences (eg PN9) like those often required for BER measurements can thus be implemented for the channel under test.
- For the enhanced channels, data fields and the transmit power control (TPC) field of the slots can be filled from data lists. This allows the use of externally precoded data or the generation of long power control profiles for the DUT.
- The code channel power of enhanced channels can be varied **in realtime** by an external control signal. This enables testing the closed-loop power control function of a mobile station for example.
- From R&S SMIQ firmware version 5.40, the four enhanced channels feature channel coding both in uplink and downlink in accordance with the reference measurement channels definition. This enables receiver and performance tests to TS 25.101, TS 25.104 and TS25.14. In addition, 12.2 kbps AMR speech to TS 25.944 is supported. From R&S SMIQ firmware 5.85 also the channel coded RACH and CPCH to TS 25.141 are supported.


Long sequences, channel coding
Data Generator
R&S SMIQB11 +
2 x R&S SMIQB12
• 256 to 1022 frames
• channel coding
• BER test
• BLER test
OCNS
Modulation Coder R&S SMIQB20
W-CDMA 3GPP
R&S SMIQB45/
ARB R&S SMIQB60
Mapping unit
Baseband filtering
Noise/distortion + fading
Noise Generator and Distortion Simulator R&S SMIQB17
Fading Simulator R&S SMIQB14/B15
External power control
TTL signal
DUT
RF or baseband signal

Option R&S SMIQB60: Complete test scenario with enhanced functions to digital standard WCDMA 3GPP (FDD)

- With the R&S SMIQ firmware version 5.65 or higher, the option R&S SMIQB48 makes it also possible to generate a realtime downlink broadcast channel (BCH) with an incrementing system frame number (SFN; 0 to 4094). The BCH can be combined with all reference measurement channels (RMC 12.2 kbps, 64 kbps, 144 kbps, 384 kbps) or an AMR (12.2 kbps). The sequence length of these channels (RMC and AMR) can be up to 2044 frames.
  Application examples:
  - Receiver and performance tests to TS25.101
  - Test of synchronization of mobile to a base-station signal combined with continuous measurement of DTCH and DCCH bit error rate and block error rate using a PN9 data sequence.
- For a realistic simulation of WCDMA scenarios, up to 64 background mobile stations can be generated in uplink in addition to the four standard mobile stations. In downlink, as many as 508 background channels (DPCHs) can be generated in addition to the four enhanced channels. 16 OCNS (orthogonal channel noise simulation) channels according to the 3GPP specification TS 25.241 can be generated upon a keystroke.
- Bit errors can be created and inserted into the data of the enhanced channels. In this way the internal BER testers of base or mobile stations can be checked for example.
- Block errors (BLERs) can be inserted into the channel-coded data.
- It is possible to generate WCDMA signals of up to 2 minutes repetition rate by combining standard channels (R&S SMIQB45) and enhanced channels (R&S SMIQB48) of different subsequence length.

Arbitrary Waveform Generator R&S SMIQB60

To further enhance the versatility of the modulation coder, a dual-channel arbitrary waveform generator (ARB) with a maximum clock rate of 40 MHz is available as an option. It can store up to 512 ksamples of externally computed I/Q values.

The supplied R&S WinIQSIM™ software allows the calculation of arbitrary modulation signals, for example COFDM, multicarrier and noise, and downloading them into R&S SMIQ. Together with a convenient data editor, R&S WinIQSIM™ can calculate any kind of TDMA frame configuration, simulate impairments by superimposed interference signals, etc.

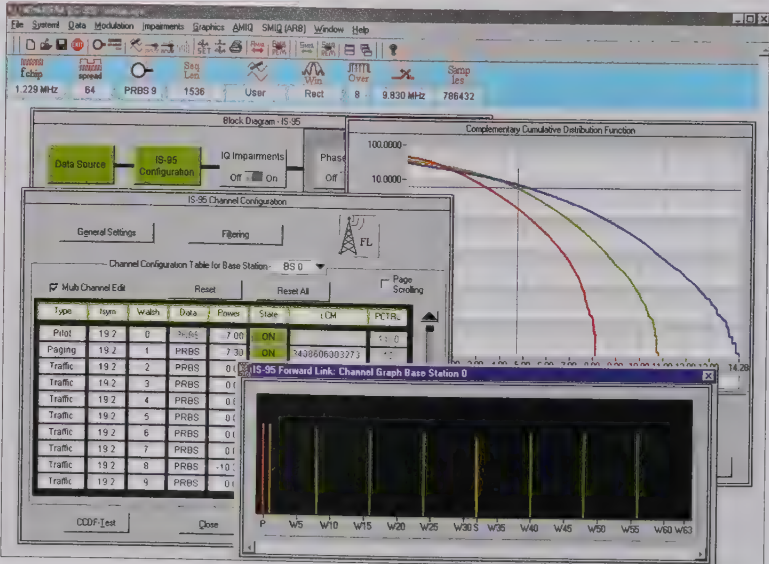
Note: The 512 ksample waveform memory cannot actually be compared with the relevant data of conventional ARB generators. In R&S SMIQB60, the oversampling needed for suppressing repetitive spectra by means of the analog filter is effected automatically and in realtime by way of hardware interpolation. In this way, no waveforms created by oversampling have to be stored. With WCDMA, for example, this allows the storage of waveforms that would require 1.25 Msample output memory in the case of conventional ARB generation.

Digital standard IS-95 with R&S SMIQ and options R&S SMIQK11 and R&S SMIQB60 (ARB)

In addition to generating IS-95 signals with option R&S SMIQB42, R&S SMIQ in conjunction with Arbitrary Waveform Generator R&S SMIQB60 now simulates CDMA signals to the North American standard IS-95A. Option R&S SMIQK11 enables IS-95 functionality under R&S WinIQSIM™ .

Up to eight complete base stations comprising 64 code channels each are available in forward link and up to 16 mobile stations in reverse link. The channel power can be set independently for all code channels.

Moreover, adjacent-channel power can be calculated for the first and the


File
System
Data
Modulation
Impairments
Graphics
AMIQ
SMIQ (ARB)
Window
Help
Block Diagram - IS-95
Data Source
IS-95 Configuration
IQ Impairments
Off On
Phase
Off
Complementary Cumulative Distribution Function
100.0000
10.0000
14.28
IS-95 Channel Configuration
General Settings
Filtering
FL
Reset
Reset All
Page Scrolling
CCDF-Test
Close
IS-95 Forward Link: Channel Graph Base Station 0
P
W5
W10
W15
W20
W25
W30 S
W35
W40
W45
W50
W55
W60 W63

fchip	spread		Seq Len		Win	Over		Samples
1.229 MHz	64	PRBS 9	1536	User	Rect	8	9.830 MHz	786432

Channel Configuration Table for Base Station - BS 0

☑ Multi Channel Edit

Type	fsym	Walsh	Data	Power	State	LCM	PCTRL
Pilot	19.2	0	[illegible]	7.00	ON		
Paging	19.2	1	PRBS	7.30	ON	3408606003273	
Traffic	19.2	2	PRBS	0			
Traffic	19.2	3	PRBS	0			
Traffic	19.2	4	PRBS	0			
Traffic	19.2	5	PRBS	0			
Traffic	19.2	6	PRBS	0			
Traffic	19.2	7	PRBS	0			
Traffic	19.2	8	PRBS	-10			
Traffic	19.2	9	PRBS	0			

second adjacent channel and output as a spectral display. The CCDF trace too can be displayed.

Digital standard cdma2000 with R&S SMIQ and options R&S SMIQK12 and R&S SMIQB60 (ARB)
CDMA signals to the North American standard IS-2000 can be simulated by means of software option R&S SMIQK12 in conjunction with Arbitrary Waveform Generator R&S SMIQB60. Option R&S SMIQK12 enables cdma2000 functionality under R&S WinIQSIM™.

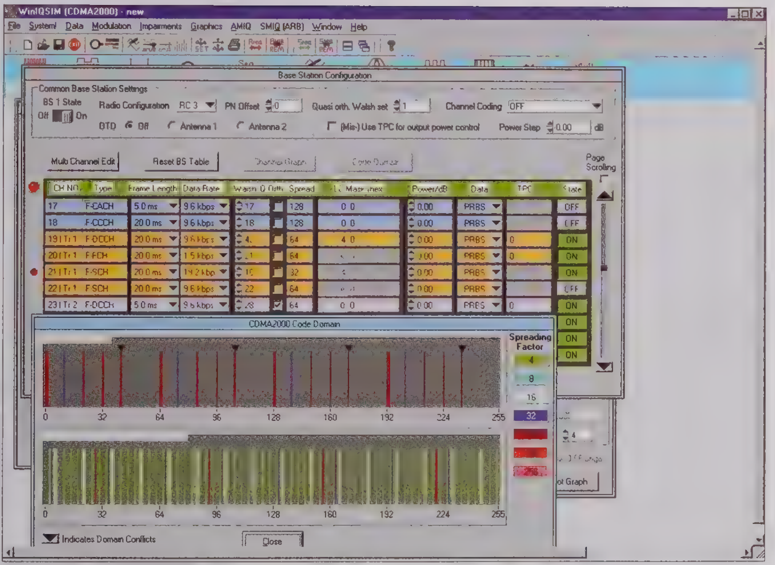
The modes 1X direct spread, 3X direct spread and 3X multicarrier (forward link only) are available. In forward link four base stations of max. 91 code channels can be set, in reverse link four mobile stations of max. 13 code channels each (irrespective of the radio configuration in each case).

In mode 1X, radio configurations RC 1 to RC 5 are available, in mode 3X RC 6 to RC 9.

Channel coding can be set for each base station and mobile station (selectable modes: coding off, coding complete, without interleaving, interleaving only).

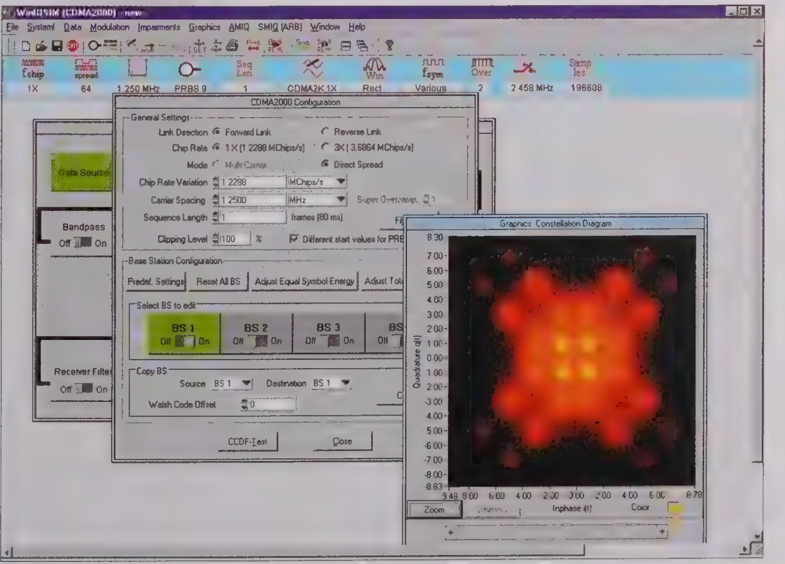
R&S WinIQSIM™ enables graphic display of CCDF traces, channel graph, domain conflicts and code domain (the latter two only in forward link).

Convenient operation with R&S WinIQSIM™ ...


WinIQSIM (CDMA2000) - new
File
System
Data
Modulation
Impairments
Graphics
AMIQ
SMIQ (ARB)
Window
Help
Base Station Configuration
Common Base Station Settings
Multi Channel Edit
Reset BS Table
Channel Graph
Code Domain
Page Scrolling
CDMA2000 Code Domain
Spreading Factor
4
8
16
32
0
32
64
96
128
160
192
224
255
Indicates Domain Conflicts
Close

BS 1 State: Off ☐ On
Radio Configuration: RC 3
PN Offset: 0
Quasi orth. Walsh set: 1
Channel Coding: OFF
OTD: ● Off ○ Antenna 1 ○ Antenna 2
☐ (Mis-) Use TPC for output power control
Power Step: 0.00 dB

CH NO.	Type	Frame Length	Data Rate	Walsh	Q Orth	Spread	Mask hex	Power/dB	Data	TPC	State
17	F-CACH	5.0 ms	9.6 kbps	17	☐	128	0 0	0.00	PRBS		OFF
18	F-CCCH	20.0 ms	9.6 kbps	18	☐	128	0 0	0.00	PRBS		OFF
19 | Tr 1	F-DCCH	20.0 ms	9.6 kbps	4	☐	64	4 0	0.00	PRBS	0	ON
20 | Tr 1	F-FCH	20.0 ms	1.5 kbps	[illegible]	☐	64	[illegible]	[illegible]	PRBS	0	ON
21 | Tr 1	F-SCH	20.0 ms	19.2 kbps	[illegible]	☐	32	[illegible]	0.00	PRBS		ON
22 | Tr 1	F-SCH	20.0 ms	9.6 kbps	22	☐	64	[illegible]	0.00	PRBS		OFF
23 | Tr 2	F-DCCH	5.0 ms	9.6 kbps	23	☑	64	0 0	0.00	PRBS	0	ON


WinIQSIM (CDMA2000) - new
File
System
Data
Modulation
Impairments
Graphics
AMIQ
SMIQ (ARB)
Window
Help
Data Source
Bandpass
Off On
Receiver Filter
Off On
CDMA2000 Configuration
Predef. Settings
Reset All BS
Adjust Equal Symbol Energy
Select BS to edit
BS 1
BS 2
BS 3
Off On
CCDF-Test
Close
Graphics Constellation Diagram
8.30
7.00
6.00
5.00
4.00
3.00
2.00
1.00
0.00
-8.83
9.48
8.78
Quadrature q(t)
Inphase i(t)
Zoom
Color

fchip 1X; spread 64; 1.250 MHz; PRBS 9; Seq Len 1; CDMA2K 1X; Win Rect; fsym Various; Over 2; 2.458 MHz; Samples 196608

General Settings
Link Direction: ● Forward Link ○ Reverse Link
Chip Rate: ● 1 X (1.2288 MChips/s) ○ 3X (3.6864 MChips/s)
Mode: ○ Multi Carrier ● Direct Spread
Chip Rate Variation: 1.2288 MChips/s
Carrier Spacing: 1.2500 MHz
Super Oversamp.: 1
Sequence Length: 1 frames (80 ms)
Clipping Level: 100 %
☑ Different start values for PRB
Copy BS
Source: BS 1
Destination: BS 1
Walsh Code Offset: 0

Special options

Fading simulation – options R&S SMIQB14/B15

With the optional fading simulator, the R&S SMIQ models are the first signal generators allowing tests that correspond to the capabilities of conventional simulators. Fading is thus no longer a matter of highly specialized measurement technology.

Fading simulation in R&S SMIQ is based on the WSSUS (wide sense stationary uncorrelated scattering) model and meets the test specifications of all main mobile radio standards, such as GSM Rec. 05.05. Both internal and external I/Q baseband signals are provided with defined multipath propagation factors through digital signal processing. Conversion to the RF with calibrated level setting is made with the available hardware of R&S SMIQ.

6-path fading with R&S SMIQB14

Option R&S SMIQB14 allows realistic simulation of a received signal that is composed of up to 6 propagation paths, irrespective of the selected modulation mode, with RF bandwidths of up to 14 MHz (3 dB).

Each of the 6 propagation paths can be individually parameterized in a wide range.

Rayleigh, Rice and lognormal fading profiles can be selected independently for each path. Likewise, attenuation, delay and speed can be set separately for each propagation path.

In addition to user configurations, preprogrammed settings in line with test specifications of mobile radio standards (GSM, NADC, IS-95 CDMA and TETRA) can be called at a keystroke, which greatly facilitates operation.

Why fading tests?

Short-time signal fading, as caused by multipath propagation, strongly affects the error rate of the received signal due to the short symbol periods in digital mobile radio

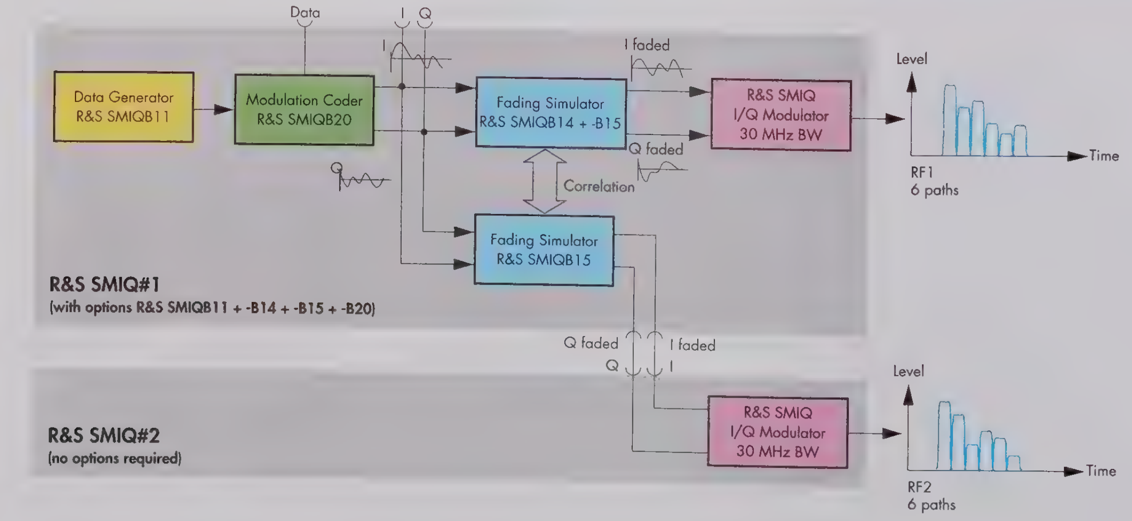
Modern digital systems overcome these problems with the aid of appropriate error control coding methods as well as algorithms for delay equalizing.

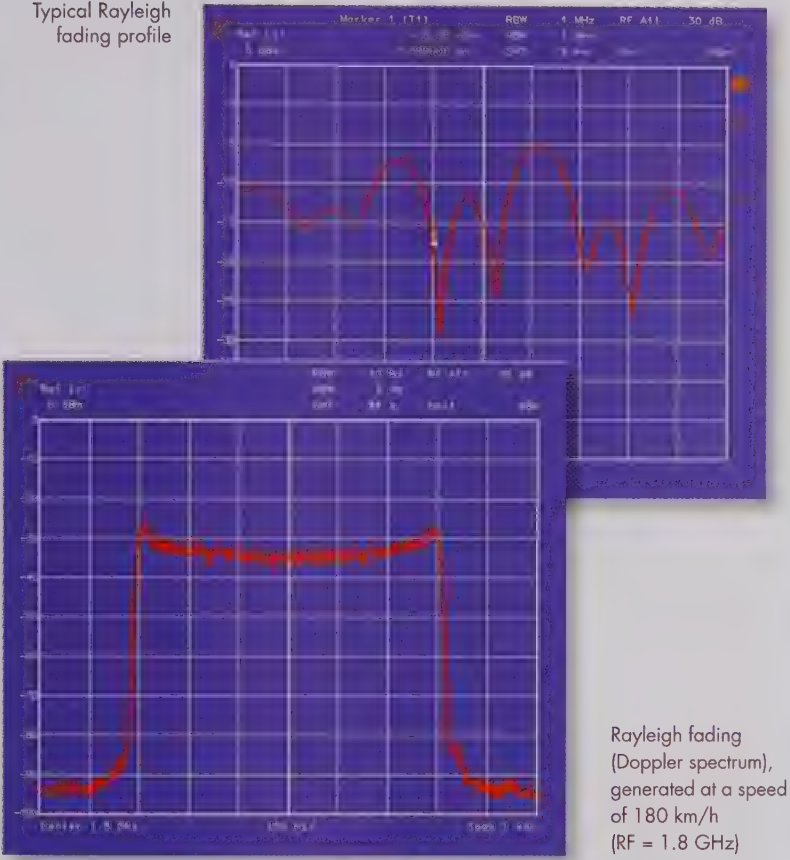
Interleaving is employed to overcome the problem of losing large parts of the messages.

Resistance to fading is an essential quality criterion of digital mobile radio systems and means a considerable competitive advantage for the manufacturer.

Tests with real-world signals using fading simulators are a must to spot the weak points in new concepts at an early stage so that appropriate modifications can be made.

Generating two correlated faded RF channels


Data
I
Q
Data Generator R&S SMIQB11
Modulation Coder R&S SMIQB20
I
Q
I faded
Fading Simulator R&S SMIQB14 + -B15
Q faded
Correlation
R&S SMIQ I/Q Modulator 30 MHz BW
Level
Time
RF1
6 paths
Fading Simulator R&S SMIQB15
R&S SMIQ#1
(with options R&S SMIQB11 + -B14 + -B15 + -B20)
Q faded
I faded
Q
I
R&S SMIQ#2
(no options required)
R&S SMIQ I/Q Modulator 30 MHz BW
Level
Time
RF2
6 paths



Typical Rayleigh fading profile

Rayleigh fading (Doppler spectrum), generated at a speed of 180 km/h (RF = 1.8 GHz)

Advantages of the R&S SMIQ fading concept

- **Unrivalled price/performance ratio**
  For the first time, fading in a quality corresponding to that of a high-grade simulator is available at a fraction of the costs previously involved.
- **Compact**
  With this concept, neither external RF signals nor a LO signal are required to simulate fading, so that simulation is simply a compact one-box solution.
- **Versatile with calibrated output levels**
  R&S SMIQ fading capability can be used without any restriction on the frequency and level range of the signal generator (–140 to –5 dBm). The user can define and store his own fading scenarios.
- **Easy to operate**
  Preprogrammed settings in line with the test specifications of mobile radio standards can be recalled at a keystroke. Tests can be carried out easily and rapidly.

12-path fading with R&S SMIQB15
Option R&S SMIQB15 provides another 6 paths for fading, which can be parameterized exactly as the first 6 paths, to give a total of 12 paths.

2-channel fading (6 paths per channel)
For testing base-station receivers with two separate antenna inputs (diversity), the I/Q output signals of options R&S SMIQB14 and -B15 can be separated in the (6+6)-path fading mode.

While the I/Q signals of the first fading simulator (option R&S SMIQB14) are used for driving the internal I/Q modulator, the second fading option (R&S SMIQB15) is used to feed a second vector signal generator. In this mode, the individual propagation paths of the two options can be correlated with each other one by one in pairs.

Enhanced fading functions for WCDMA 3GPP with option R&S SMIQB49
R&S SMIQB49 extends the functionality of fading options R&S SMIQB14/B15 to include WCDMA 3GPP channel simulation. It adds three new modes to the fading simulator so that all scenarios defined in 3GPP Release 99 can be simulated:

- In fine delay mode, fading simulator resolution is increased to 1 ns with up to four paths being available.
- In moving delay mode, two paths are simulated: for one path the delay remains constant, whereas for the other path the delay varies continuously.
- In birth-death mode, there are two paths changing delay in steps in accordance with the 3GPP channel model.

Special options (continued)

Noise generation and distortion simulation – R&S SMIQB17

Real signals

A signal generator is normally used to generate as near as possible ideal signals. For testing receivers, it is however also necessary to simulate real transmitting and receiving conditions. This is exactly what option R&S SMIQB17 has been designed for.

Noise generator

With the aid of the noise generator, an additive white Gaussian noise (AWGN) signal can be superimposed on the output signal of R&S SMIQ. The ratio of carrier power to noise power (C/N) can be varied with high resolution over a wide range. This allows for instance precise sensitivity measurement of receiver circuits with defined C/N.

Distortion simulator

The distortion simulator allows simulation of amplitude and phase distortion (eg of a travelling wave tube in a satellite output stage). All that has to be done is to enter via the IEC/IEEE bus the input values of the AM/AM and AM/φM characteristics to be simulated. The complete characteristics forming the basis for nonlinear distortion of the I/Q baseband signals are calculated by means of spline interpolation. It is possible to distort I/Q signals irrespective of whether they are generated by the internal modulation coder or applied from an external source.

Digital signal processing in the baseband (I and Q signals) is used both for the generation of the AWGN signal and distortion of the output signal. This ensures high accuracy and excellent reproducibility of measurements.

Versatile applications

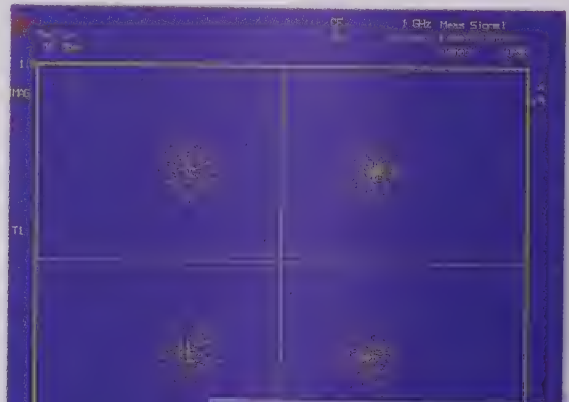
Distortion simulation and additive noise can be combined with the following R&S SMIQ functions: vector modulation, digital modulation, digital standards and fading simulator.

The method in detail

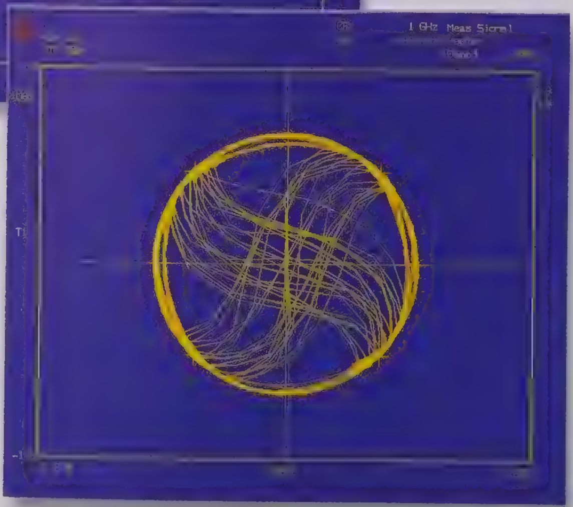
The distortion characteristics are internally represented by 2000 interpolation points which are determined by cubic spline interpolation from up to 30 input values. Several distortion characteristics can be stored under user-defined names and recalled. AM/AM and AM/φM distortion may also be defined by entering polynomial coefficients up to the fifth order.

Inverse polynomials can be selected for compensating the distortion of an external amplifier. The noise bandwidth can be varied in a wide range.

Constellation diagram of noisy QPSK signal



Vector diagram of distorted modulation signal

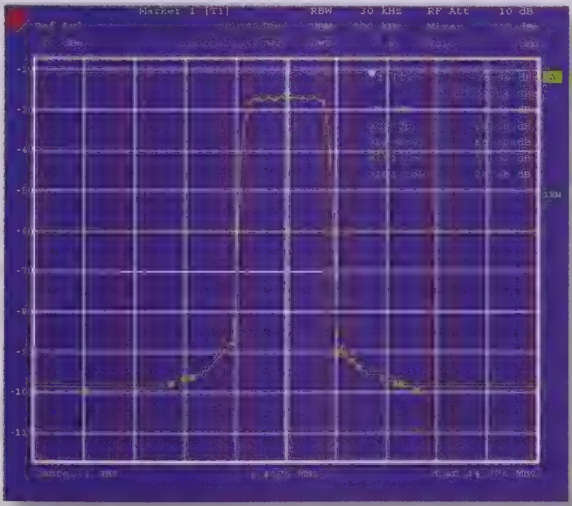


Low ACP for IS-95 CDMA and WCDMA – R&S SMIQB47

Challenging test requirements

Modern communications systems that are based on access methods like CDMA or WCDMA place demanding requirements on terminals and components.

For testing components (eg base-station power amplifier) that are used in such systems special test sources with outstanding characteristics regarding adjacent-channel power ratio (ACPR) are required. The measurement equipment used should cause as little interference as possible.



ACP measurement for a WCDMA signal with option R&S SMIQB47

ACPR – unrivalled low

The extraordinarily low ACPR of the basic R&S SMIQ unit is made possible by the built-in high-grade I/Q modulator. In conjunction with option R&S SMIQB47 providing additional filtering in the I/Q signal path, R&S SMIQ becomes an ideal signal source suited to meet the most stringent requirements. This option has especially been designed for IS-95 CDMA (1.2288 Mcps) and WCDMA (3.840 Mcps and 4.096 Mcps/8.192 Mcps respectively) and, with adjacent-channel power reduction, provides sufficient measurement margin for characterizing and testing components (eg amplifiers).

The adjacent-channel power ratio for a WCDMA signal with one DTCH (dedicated traffic channel) is typically –67 dBc[1]. For IS-95 CDMA the typical adjacent-channel power ratio is –78 dBc[2].

Option R&S SMIQB47 can be used both with internal signals of the modulation coder (R&S SMIQB20) or with an external I/Q source (eg R&S AMIQ).

Fast CPU

Fit for production

Time is money! R&S SMIQ is equipped with a fast CPU for applications in production where complete test runs should take no more than a few seconds.

This CPU reduces the standard setting times for frequency and level to 3 ms (without switching the mechanical attenuators) to provide maximum throughput and so ensures competitiveness and economy of the production line.

BER measurement R&S SMIQB21

Measuring the bit error rate has become a frequently used method for the verification of digital communication systems (eg measuring the sensitivity or selectivity of receivers, subsystems and components). The retrofittable option (R&S SMIQB21) allows R&S SMIQ to be used for these BER (bit-error-rate) measurements. The device under test (DUT) has to deliver the data to be tested and the associated clock. If the DUT does not have its own clock, it can be generated by R&S SMIQ and output via bit and/or symbol clock. The built-in BER tester compares these data with the nominal data and calculates the error rate. Various standard PRBS sequences (PN9, PN15, etc) are used as nominal data. The result of the BER measurement is also available via the remote-control interface.

[1] 3.84 MHz bandwidth, 5 MHz offset, 2.5 MHz I/Q filter

[2] 9 channels, 885 kHz offset, 850 kHz I/Q filter

Applications

Type-approval testing of digital base and mobile stations
Mobile and base stations are complex systems, where signal generators are used for testing out system parts such as receivers, modulators and amplifiers. Due to its versatility and signal quality, R&S SMIQ is ideally suited to handle these tasks.

Sensitivity measurements on digital receivers
Sensitivity measurements on digital receivers require high modulation quality as well as precise level setting over a wide range.

Due to the losses in automatic test systems – caused by cables and the use of power splitters, relays, attenuators, etc – the absolute level accuracy is less important than the reproducibility of the settings. R&S SMIQ yields excellent values in this respect.

Frame structures with defined data contents for BER measurements are made available by programmed standard settings.

Selectivity measurements on digital receivers
Low phase and broadband noise, high spurious suppression of >70 dB even with activated digital modulation as well as excellent adjacent-channel power ratios make R&S SMIQ an ideal source for selectivity measurements on digital receivers.

Base-station transmitter test
For testing base-station transmitters, R&S SMIQ is able to supply the data signals of the internal data generator (DATA, DATA CLK, SYMB CLK) as well as the analog I/Q baseband signals.

Testing of equalizers
The optional fading simulator for R&S SMIQ is ideal for testing the equalizers of digital receivers. While conventional fading simulators need external RF input signals, R&S SMIQ generates its own modulated RF signal. New equalizer concepts can thus be tested at a very early stage in development.

Tolerance tests on digital systems
In addition to ideal signals, R&S SMIQ also allows the generation of signals

R&S SMIQ in TETRA simulator

Module test with R&S SMIQ, R&S AMIQ and Signal Analyzer R&S FSIQ

with defined degradations (I/Q impairments or noise and distortion with option R&S SMIQB17) as well as the variation of bit rates and filtering to determine tolerance limits and to detect potential critical spots in new systems.

Components tests

The high setting speed and modulation quality make R&S SMIQ supreme for use in the development and production of digital components and modules.

The high intermodulation suppression of the I/Q modulator of R&S SMIQ ensures excellent adjacent-channel power ratios of the modulated output signal for conclusive linearity measurements on amplifier components.

The high spurious suppression of >70 dB of the R&S SMIQ output signal allows accurate measurements on mixer components.

Development of new digital communication systems

R&S SMIQ offers all capabilities that are required for developing new digital communication systems.

Featuring a modulation bandwidth of 30 MHz (–3 dB), the I/Q modulator of R&S SMIQ is ideal for future broadband systems.

The optional modulation coder allows any digital modulation modes – from BPSK through to 256QAM – to be generated with variable data rate and baseband filtering.

With the maximum symbol rate of 18 Msymbol/s broadband digital modulation modes can be generated. Any TDMA structures can be produced with the aid of a programmable burst generator.

The modulation coder of R&S SMIQ is already equipped for future applications, eg in the field of broadband spread-spectrum systems (wireless local loop, wireless LAN).

R&S SMIQ meets already today the challenges of tomorrow's market.

Ordering information

Vector Signal Generator

R&S SMIQ02B (300 kHz to 2.2 GHz)	1125.5555.02
R&S SMIQ03B (300 kHz to 3.3 GHz)	1125.5555.03
R&S SMIQ04B (300 kHz to 4.4 GHz)	1125.5555.04
R&S SMIQ06B (300 kHz to 6.4 GHz)	1125.5555.06

Accessories supplied	power cable, operating manual

Options

Reference Oscillator OCXO	R&S SM-B1	1036.7599.02
FM/φM Modulator	R&S SM-B5	1036.8489.02
Data Generator	R&S SMIQB11	1085.4502.04
Memory Extension, 32 Mbit	R&S SMIQB12	1085.2800.04
Fading Simulator, 6 paths	R&S SMIQB14	1085.4002.02
Second Fading Simulator for 12 paths or 2 channels	R&S SMIQB15	1085.4402.02
Noise Generator and Distortion Simulator	R&S SMIQB17	1104.9000.02
RF and AF Rear Connectors	R&S SMIQB19	1085.2997.02
Modulation Coder	R&S SMIQB20	1125.5190.02
BER Measurement	R&S SMIQB21	1125.5490.02
Digital Standard IS-95 CDMA	R&S SMIQB42	1104.7936.02
Digital Standard WCDMA acc. to NTT DoCoMo 1.0, ARIB 0.0 standard	R&S SMIQB43	1104.8032.02
Digital Standard WCDMA acc. to 3GPP (FDD)	R&S SMIQB45	1104.8232.02
Low ACP for IS-95 CDMA and WCDMA	R&S SMIQB47	1125.5090.02
Modification Kit for Low ACP (factory installation only)	R&S SMIQU47	1125.5149.02
Extended Functions for WCDMA (3GPP)	R&S SMIQB48	1105.0587.02
Extended Fading Functions for WCDMA (3GPP)	R&S SMIQB49	1105.1083.02
Arbitrary Waveform Generator incl.R&S WinIQSIM™	R&S SMIQB60	1136.4390.02
TETRA T1 Simulator	R&S SMIQ-K8	1136.4290.02
Digital Standard IS-95 CDMA (for option R&S SMIQB60)	R&S SMIQK11	1105.0287.02
Digital Standard cdma2000 (for option R&S SMIQB60)	R&S SMIQK12	1105.0435.02
Digital Standard WCDMA TDD Mode (3GPP) (for option R&S SMIQB60)	R&S SMIQK13	1105.1231.02
Digital Standard TD-SCDMA (for option R&S SMIQB60)	R&S SMIQK14	1105.1383.02
OFDM Signal Generation, HIPERLAN/2 (for option R&S SMIQB60)	R&S SMIQK15	1105.1531.02
Digital Standard IEEE 802.11b (for option R&S SMIQB60)	R&S SMIQK16	1154.7700.02
Digital Standard 1xEV-DO (for option R&S SMIQB60)	R&S SMIQK17	1154.7800.02
Digital Standard IEEE 802.11a (for option R&S SMIQB60)	R&S SMIQK18	1154.7952.02

Additional hint: R&S SMIQ02B/03B (R&S SMIQ04B/06B) can be equipped with up to three (two) of the following options: R&S SMIQ-B5, R&S SMIQB14, R&S SMIQB15, R&S SMIQB17

Application software

PC Software: Generation of data and control lists	R&S SMIQ-K1	*)
PC Software: Bluetooth signals for R&S SMIQ	R&S SMIQ-K5	*)
PC Software: User mappings and user filters for R&S SMIQ	R&S User Mod	*)

*) available at www.rohde-schwarz.com

Recommended extras

19" Adapter	R&S ZZA-94	0396.4905.00
Service Kit	R&S SM-Z3	1085.2500.02
BNC Adapter for rear panel, D type connector PAR DATA	R&S SMIQ-Z5	1104.8555.02
90° Power Splitter	R&S SMIQ-Z9	1104.9580.02
Trolley for Transit Case	R&S ZZK-1	1014.0510.00
Transit Case	R&S ZZK-944	1013.9366.00
Service Manual R&S SMIQ		1085.2445.24

Instrument upgrades

R&S SMIQ02B to R&S SMIQ03B	R&S SMIQU03	1125.5855.03
R&S SMIQ03B to R&S SMIQ04B	R&S SMIQU04	1125.5855.04
R&S SMIQ04B to R&S SMIQ06B	R&S SMIQU06	1125.5855.06

For specifications see separate data sheet (enclosed)

Certified Quality System
ISO 9001
DQS REG. NO 1954

Certified Environmental System
ISO 14001
REG. NO 1954

ROHDE&SCHWARZ

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READY FOR
WCDMA

ROHDE&SCHWARZ
SIGNAL GENERATOR
SMIQ 03B
2.000 000 000 0 GHz
LEVEL -10.0 dBm
PEP +0.7 dBm
DATA INPUT
MENU VARIATION

Specifications for
Vector Signal Generator R&S SMIQ

Valid from 7/2002

ROHDE & SCHWARZ

Specifications are guaranteed under the following conditions: 30 minutes warmup time at ambient temperature, specified environmental conditions met, calibration cycle adhered to, and total calibration performed. Data designed „overrange" are not guaranteed. Data without tolerances: typical values. – In compliance with the 3GPP standard, chip rates are specified in Mcps (million chips per second), whereas bit rates, symbol rates and sample rates are specified in kbps (thousand bits per second) or ksps (thousand symbols/samples per second). Mcps, kbps and ksps are not SI units.

RF features

Frequency

Range	
R&S SMIQ02B	300 kHz to 2.2 GHz
R&S SMIQ03B	300 kHz to 3.3 GHz
R&S SMIQ04B	300 kHz to 4.4 GHz
R&S SMIQ06B	300 kHz to 6.4 GHz
Resolution (CW, analog modulation, attenuator mode AUTO)	0.1 Hz
Setting time to within $<1\times10^{-7}$ for f>450 MHz and <240 Hz for f<450 MHz after IEC/IEEE-bus delimiter with ALC MODE set to ON or ALC OFF MODE set to TABLE	
Normal operation	
$f \leq 3.3$ GHz	<3 ms
$f > 3.3$ GHz	<3.3 ms
After trigger pulse in list mode	
$f \leq 3.3$ GHz	<500 µs
$f > 3.3$ GHz	<700 µs
Fast restore mode	
$f \leq 3.3$ GHz	<800 µs
$f > 3.3$ GHz	<1 ms
Phase offset	adjustable in steps of 0.1°

Reference frequency

	Standard	**Option R&S SM-B1**
Aging (after 30 days of operation)	1×10^{-6}/year	$<1\times10^{-9}$/day
Temperature effect (0°C to 50°C)	2×10^{-6}	$<5\times10^{-8}$
Warm-up time		≤15 min
Output for internal reference		
Frequency	10 MHz	
Level	8 dBm	
Source impedance	50 Ω	
Input for external reference		
Frequency	1 MHz to 16 MHz in 1 MHz steps	
Permissible frequency drift	3×10^{-6}	
Input level	0.1 V to 2 V rms	
Input impedance	200 Ω	
Electronic tuning (EXT. TUNE)	1×10^{-7}/V	values to standard, but with Adjustment State On
Input voltage range	0 V to ±10 V	
Input impedance	10 kΩ	

Level

Range	
R&S SMIQ02B/03B	–144 dBm to +13 dBm (PEP) 1)
R&S SMIQ04B/06B	–144 dBm to +10 dBm (PEP) 1)
Overranging without guarantee of specs	up to 16 dBm
Resolution (CW, FM, φM, attenuator mode AUTO)	0.1 dB or 0.01 dB

Total level uncertainty >–127 dBm 2) 12), CW		
f ≤2.5 GHz	<0.5 dB	
f >2.5 GHz to 4 GHz	<0.9 dB	
f >4 GHz to 6.4 GHz	<1.2 dB	
Output impedance	50 Ω	
VSWR	f ≤2.2 GHz	2.2 GHz < f ≤ 6.4 GHz
max. level	<1.8	<2.0
≤–3 dBm	<1.5	<1.8
Setting time to within 0.1 dB from settled level after IEC/IEEE bus delimiter in CW, FM, φM	<25 ms with mechanical attenuator <2.5 ms without mechanical attenuator	
Non-interrupting level setting		
FIXED mode	setting range >20 dB	
ELECTRONIC mode	setting range >80 dB	
Overload protection	protects the unit from externally applied RF power (from 50 Ω source) and DC voltage	
Max. permissible RF power	50 W (R&S SMIQ02B/R&S SMIQ03B) 1 W (R&S SMIQ04B/R&S SMIQ06B)	
Max. permissible DC voltage	35 V (R&S SMIQ02B/R&S SMIQ03B) 0 V (R&S SMIQ04B/R&S SMIQ06B)	

Spectral purity 2)

Spurious		
Harmonics at levels ≤10 dBm (R&S SMIQ02B/03B) at levels ≤7 dBm (R&S SMIQ04B/06B)	<–30 dBc	
Nonharmonics		
CW, carrier offset >10 kHz		
0.3 MHz to 450 MHz	<–74 dBc	
>450 MHz to 1500 MHz	<–80 dBc	
>1500 MHz to 3000 MHz	<–74 dBc	
>3000 MHz to 3300 MHz	<–60 dBc	
>3300 MHz	as with vector modulation	
Vector modulation,		
carrier offset 10 kHz to < 300 MHz		
0.3 MHz to 3300 MHz	<–70 dBc	
carrier offset ≥300 MHz		
0.3 MHz to 3300 MHz	<–60 dBc	
carrier offset 10 kHz to < 900 MHz		
>3300 MHz to 6000 MHz	<–64 dBc	
>6000 MHz	<–58 dBc	
carrier offset ≥900 MHz		
>3300 MHz, ≥5 dBm	<–50 dBc	
Broadband noise, CW, carrier offset >5 MHz, measurement bandwidth 1 Hz		
f >20 MHz to 450 MHz	<–136 dBc (–142 dBc typ.)	
f >450 MHz to 3040 MHz	<–138 dBc (–144 dBc typ.)	
f >3040 MHz to 3300 MHz	<–136 dBc (–142 dBc typ.)	
f >3300 MHz to 6400 MHz	<–132 dBc (–138 dBc typ.)	
Broadband noise, vector modulation,		
f >20 MHz, carrier offset >5 MHz to 3300 MHz	<–136 dBc (–140 dBc typ.)	
f >20 MHz, carrier offset >3300 MHz to 6400 MHz	<–133 dBc (–137 dBc typ.)	
SSB phase noise, carrier offset 20 kHz, measurement bandwidth 1 Hz	CW	Vector/dig. mod.
f = 20 MHz to 450 MHz	<–116 dBc	<–119 dBc
f = 1 GHz	<–126 dBc	<–123 dBc
f = 2 GHz	<–120 dBc	<–120 dBc
f = 3 GHz	<–116 dBc	<–116 dBc
f = 6 GHz	<–110 dBc	<–110 dBc

Residual FM, rms (f = 1 GHz)	
0.3 kHz to 3 kHz (ITU-T)	<1 Hz
0.02 kHz to 23 kHz	<4 Hz
Residual AM, rms (0.02 kHz to 23 kHz)	<0.02%

Sweep

RF sweep, AF sweep	digital sweep in discrete steps
Modes	automatic, single shot, manual or external trigger, linear or logarithmic
Sweep range	user-selectable
Step width (lin)	user-selectable
Step width (log)	0.01% to 100%
Level sweep	not available with vector or digital modulation
Modes	automatic, single shot, manual or external trigger, logarithmic
Sweep range	0.1 dB to 20 dB
Step width (log)	0.1 dB to 20 dB
Step time	3 ms to 5 s
Resolution	0.1 ms
Markers	3, user-selectable
MARKER output signal	TTL level (HCT), selectable polarity
X output	0 V to 10 V
BLANK output signal	TTL level (HCT), selectable polarity

Internal modulation generator

Frequency range	0.1 Hz to 1 MHz
Resolution	0.1 Hz
Frequency error	<(1×10^{-4} of setting + 0.012 Hz)
Frequency response up to 100 kHz	<0.4 dB
Frequency response up to 1 MHz	<2 dB
Distortion up to 100 kHz (R_L >200 Ω, peak level 1 V)	<0.2%
Open-circuit voltage at LF socket	1 mV to 4 V peak
Resolution	1 mV
Setting error at 1 kHz	1% + 1 mV
Output impedance	approx. 10 Ω
Frequency setting time (after receiving last IEC/IEEE-bus character)	<3 ms

Vector modulation

Level accuracy with vector modulation, additional error with ALC OFF, relative to CW	<0.3 dB
Mode	external DC
I and Q modulation inputs	
Input impedance	50 Ω
VSWR (DC to 30 MHz)	<1.2
If R&S SMIQ is fitted with **two** fading simulators (R&S SMIQB14 and R&S SMIQB15) **and** a noise generator and distortion simulator (R&S SMIQB17), the VSWR of the I and Q inputs for vector modulation (DC to 30 MHz) degrades to	<1.25
Input voltage for full-scale input	$\sqrt{I^2 + Q^2} = 0.5$ V (1 V EMF with 50 Ω source)
Static error vector 3)	
level ≤8 dBm PEP (R&S SMIQ02B/03B)	
level ≤5 dBm PEP (R&S SMIQ04B/06B)	
up to 3.3 GHz	
rms value	<0.5%
peak value	<1%
>3.3 GHz	
rms value	<1%
peak value	<2%
Modulation frequency response	
up to 5 MHz	<0.4 dB
up to 30 MHz	<3 dB
Carrier leakage at 0 V input voltage, referred to full-scale input 3)	<–45 dBc
I/Q impairments	
Carrier leakage	
Setting range	0% to 50%
Resolution	0.5%
I ≠ Q	
Setting range	–12% to +12%
Resolution	0.1%
Quadrature offset	
Setting range	–10° to +10°
Resolution	0.1°
Envelope control	
RF level can be controlled with an analog voltage of 0 V to 1 V via the POWER RAMP input	1 V: set level 0 V: maximum level attenuation
Input impedance	10 kΩ
Linear range	0 dB to –30 dB (–35 dB typ.)
Attenuation error at –20 dB	<0.5 dB
On/off ratio	>80 dB
Delay time	1 µs typ.
Rise/fall time (10% to 90%)	<1 µs

Amplitude modulation

Modes	internal, external AC/DC
Modulation depth	0% to 100%
Resolution	0.1%
Setting error at 1 kHz (m <80%)	<4% of reading +1%
AM distortion at 1 kHz	
m = 30%	<1%
m = 80%	<2%
Modulation frequency range, RF ≥5 MHz	DC to 50 kHz
for RF <5 MHz	DC to 3 kHz
Modulation frequency response	
20 Hz to 20 kHz	<3 dB
for RF <5 MHz, 20 Hz to 3 kHz	<3 dB
Incidental φM at 30% AM,	
AF = 1 kHz, peak value	<0.1 rad
Modulation input EXT1	
Input impedance	>100 kΩ
Input voltage for selected modulation depth	1 V peak
High/low indication (10 Hz to 50 kHz)	for inaccuracy >3%

Broadband amplitude modulation

Mode	external DC
Modulation frequency response	
up to 10 MHz	<1 dB
up to 30 MHz	<3 dB
Modulation input (broadband AM)	
Input impedance	50 Ω
Input voltage for 100% AM	0.25 V peak

Pulse modulation

Modes	external
On/off ratio	>80 dB
Rise/fall time (10%/90%)	30 ns typ.
Pulse repetition frequency	0 kHz to 1 MHz
Pulse delay	200 ns typ.
Modulation input PULSE	
Input signal	TTL level (HCT)
Input impedance	>10 kΩ

Frequency modulation with option R&S SM-B5

Modes	internal, external AC/DC, two-tone with two modulation channels FM1 and FM2
Max. deviation without I/Q modulation	
0.3 MHz to 450 MHz	2 MHz
>450 MHz to 750 MHz	0.5 MHz
>750 MHz to 1500 MHz	1 MHz
>1500 MHz to 3300 MHz	2 MHz
>3300 MHz to 6400 MHz	4 MHz

Max. deviation with I/Q modulation	
0.3 MHz to 750 MHz	2 MHz
>750 MHz to 1200 MHz	1 MHz
>1200 MHz to 3300 MHz	2 MHz
>3300 MHz to 6400 MHz	4 MHz
Resolution	<1%, min. 10 Hz
Setting error at AF = 1 kHz	<(3% of setting + 20 Hz)
FM distortion at AF = 1 kHz and half maximum deviation	<0.5%
Modulation frequency range	
with maximum deviation	DC to 500 kHz
at <25% of maximum deviation	DC to 2 MHz
Modulation frequency response	
10 Hz to 100 kHz	<0.5 dB
10 Hz to 2 MHz	<3 dB
Incidental AM at 40 kHz deviation, AF = 1 kHz, carrier frequency >5 MHz	<0.1%
Carrier frequency offset with FM	<0.01% of maximum deviation +1% of selected deviation
EXT1, EXT2 modulation inputs	
Input impedance	>100 kΩ
Input voltage for selected modulation depth	1 V peak
High/low indication (10 Hz to 100 kHz)	for inaccuracy >3%

Phase modulation with option R&S SM-B5[13]

Modes	internal, external AC/DC, two-tone with two modulation channels PM1 and PM2
Max. deviation without I/Q modulation	
0.3 MHz to 450 MHz	20 rad
>450 MHz to 750 MHz	5 rad
>750 MHz to 1500 MHz	10 rad
>1500 MHz to 3300 MHz	20 rad
>3300 MHz to 6400 MHz	40 rad
Max. deviation with I/Q modulation	
0.3 MHz to 750 MHz	20 rad
>750 MHz to 1200 MHz	10 rad
>1200 MHz to 3300 MHz	20 rad
>3300 MHz to 6400 MHz	40 rad
Resolution	<1%, min. 0.001 rad
Setting error at AF = 1 kHz	<3% of reading + 0.01 rad
Distortion at AF = 1 kHz and half maximum deviation	<1%
Modulation frequency range	DC to 100 kHz
Modulation frequency response	
10 Hz to 100 kHz	<0.8 dB
EXT1, EXT2 modulation inputs	
Input impedance	>100 kΩ
Input voltage for selected modulation depth	1 V peak
High/low indication (10 Hz to 100 kHz)	for inaccuracy >3%

Digital modulation

Digital modulation with optional Modulation Coder R&S SMIQB20

Modes	internal, external serial, external parallel
Predefined modulation settings	APCO C4FM, APCO CQPSK, CDPD, CT2, DECT, GSM, IRIDIUM, NADC, PDC, PHS, TETRA, TFTS, PWT, ICO BPSK, ICO GMSK, ICO QPSK, GSM EDGE, CDMA IS-95, WCDMA, QPSK
Internal PRBS	selectable lengths: 2^9-1, $2^{15}-1$, $2^{16}-1$, $2^{20}-1$, $2^{21}-1$ and $2^{23}-1$
I/Q bandwidth	12 MHz
Modulation specifications apply at levels ≤8 dBm (PEP) with R&S SMIQ02B/03B and at levels ≤5 dBm (PEP) with R&S SMIQ04B/06B	
Total level uncertainty at levels >–127 dBm with digital modulation, crest factor <20 dB [2] [3]	
f ≤2,5 GHz	<0.7 dB
f >2,5 GHz to 4 GHz	<1.2 dB
f >4 GHz	<1.5 dB
For best short time repeatability use ALL OFF mode table	
Clock generation	
Clock mode	internal or external
Resolution	0.001 Hz
Error	$<2^{-42}$, related to reference frequency
Inputs	DATA, BIT CLOCK, SYMBOL CLOCK, PAR DATA
Serial data are taken from BNC connectors, parallel data (symbols) from rear PAR DATA connector. Parallel symbols may contain 1 to 8 bits and read in using an internal or external clock signal	
Trigger threshold	–2.5 V to +2.5 V, selectable, resolution 0.01 V
Input impedance	1 kΩ to ground, 50 Ω to ground
Max. data rate, serial	30 MHz, 50 MHz typ.
Max. symbol rate, parallel	18 MHz
Outputs	DATA, BIT CLOCK, SYMBOL CLOCK, PAR DATA, (all TTL levels)
I and Q baseband signals, output voltage, EMF, peak value	$\sqrt{I^2+Q^2} = 1$ V
Power ramp	
Output voltage	0 V to 1 V
Output impedance	10 Ω
Level attenuation	
via LEV ATT input	
Range	0 dB to 70 dB
Additional level error caused by attenuation [3]	<1 dB (up to 35 dB), <1.5 dB (up to 70 dB)
Envelope control	
Modes	
Analog	External via POWER RAMP input (for data see vector modulation above). With an internal power ramp, the connector serves as an output.
Digital	Internal or external via BURST GATE input/output (PAR DATA connector). The BURST GATE input triggers a power ramp (TTL levels). The low/high transition starts the ramp function from blanking level to maximum level, the high/low transition from maximum level to blanking level. With an internal power ramp, the connector serves as an output.
Operating range	1 kHz to 2.5 MHz
Rise/fall time	
Setting range	0.25 symbols to 32 symbols
Resolution	1/4 symbol
Minimum time	1 µs
Modulation modes	ASK, FSK, GMSK, PSK, QAM

Baseband filter Each filter can be used with any type of modulation. Exception: GMSK/GFSK only with Gaussian baseband filter	√cos (α = 0.1 to 0.99), cos (α = 0.1 to 0.99), resolution 0.01 Gauss, B x T = 0.15 to 2.5, resolution 0.01 GaussLin, B x T = 0.3 Bessel, B x T = 1.25 and 2.5/ IS-95 with or without equalizer / special filter for WCDMA, APCO C4FM / special filter for TETRA / split phase, B x T = 0.15 to 2.5 / rectangular
Filter modes	low EVM: for minimum error vector low ACP: for minimum adjacent-channel power
ASK	
Symbol rate	100 Hz to 18 MHz [11], max. 5 MHz
Modulation depth	0% to 100%
FSK	
Modulation modes	2FSK, 4FSK, 4FSK APCO, GFSK
Symbol rate	100 Hz to 7.5 MHz [11]
Shift	(0.1 to 100) x f_{symb} , max. 5 MHz
Resolution	<0.5%
Deviation error, rms value for shift 200 Hz to 650 kHz, symbol rate <1.3 Msymbol/s, filter √cos or cos with α = 0.25 to 0.7 or Gauss with B x T = 0.2 to 0.7	<1.3%
GMSK	
Bit rate	100 Hz to 7.5 MHz [11]
Modulation phase error with PRBS data,	
up to 1 Mbit/s, rms value	<1°
Peak value	<3°
PSK	
Modulation modes	BPSK, QPSK, OQPSK, QPSK (IS-95), OQPSK (IS-95), QPSK (ICO), QPSK (INMARSAT), π/4DQPSK, π/4QPSK, 8PSK, 8PSK EDGE
Symbol rate	100 Hz to 18 MHz [11]
Error vector magnitude, rms for filter √cos, cos, IS-95, WCDMA	
up to 200 ksymbol/s, α ≥0.25	<1.2%
up to 1.5 Msymbol/s, α ≥0.25	<2%
up to 3 Msymbol/s, α ≥0.25	<3%
>3 Msymbol/s, α ≥0.25	typ. <3%
TETRA ACP	
f = (380 to 470) MHz, (915 to 988) MHz, level ≤8 dBm PEP, low distortion output mode	
Offset 25 kHz	≤–71 dB, –74 dB typ.
Offset 50 kHz	≤–76 dB, –80 dB typ.
QAM	
Modulation modes	16QAM, 32QAM, 64QAM, 256QAM
Symbol rate	100 Hz to 18 MHz [11]
Error vector magnitude, rms with 16QAM for filter √cos, cos, IS-95, WCDMA	<2%
up to 1 Msymbol/s, α ≥0.25	<3%
up to 3 Msymbol/s, α ≥0.25	<3% typ.
>3 Msymbol/s, α ≥0.25	

User mapping	via IEEE bus with software User Mod
Modulation modes	PSK, QAM, FSK
Modulation symbols	1 to 8 bit per symbol
Coding	differential, phase offset (PSK)
Range of symbol rate	like PSK, QAM and FSK
User baseband filter	via IEEE bus with software User Mod
Impulse length	8 or 16 symbols long
Oversampling	3 to 32
Modulation coding	differential coding, differential and Gray, GSM, NADC, TETRA, TFTS, PDC, PHS, differential phase coding, APCO25, PWT, INMARSAT, VDL

Modulation with coding

The table below shows the possible combinations of modulation with coding (X = combination possible).

	Coding off	Differential	Differential phase	Differential + Gray	GSM	NADC, PDC, PHS, TETRA, APCO25, PWT	TFTS/ TETRA	INMARSAT	VDL
ASK	X	X		X					
BPSK	X	X		X					
QPSK	X	X		X				X	
QPSK (IS-95)	X	X		X				X	
INMARSAT	X	X		X				X	
QPSK ICO	X	X		X				X	
OQPSK	X	X		X				X	
OQPSK (IS-95)	X	X		X				X	
π/4 QPSK	X								
π/4 DQPSK	X					X	X		
8PSK	X	X		X					X
8PSK_EDGE	X	X		X					X
2FSK	X	X		X	X				
4FSK, 4FSK APCO	X	X		X					
GFSK	X	X		X	X				
GMSK	X	X		X	X				
16QAM	X	X	X	X					
32QAM	X	X	X	X					
64QAM	X	X	X	X					
256QAM	X		X						

Data generator (option R&S SMIQB11)

Programmable data memory for modulation data, envelope-control and trigger signals. The data generator can be operated only in conjunction with the optional modulation coder (R&S SMIQB20).

Memory capacity	15 Mbit
Max. symbol rate	8.5 MHz
Modes	automatically repeating, single shot, manually or externally triggered

Inputs	
Trigger input TRIGIN	for starting the data sequences in the data memory
Trigger frequency	<10 kHz
Selectable trigger delay	0 to $2^{16}-1$ symbols
Selectable trigger suppression	0 to $2^{26}-1$ symbols after trigger
Switching threshold	–2.5 V to 2.5 V, selectable, resolution 0.1 V
Input impedance	1 kΩ to ground, 50 Ω to ground
Required pulse width	>50 ns
Outputs	
DATA modulation data	see data under "Digital Modulation"
BURST GATE, LEV ATT	control signals for envelope control and level attenuation
CW	control signal for switching off digital modulation
TRIGOUT 1, TRIGOUT 2	user-programmable trigger signals
TRIGOUT 3	trigger signal on event
HOP	control signal for frequency hopping in LIST MODE

Memory extension (option R&S SMIQB12)

The data generator memory can be extended to max. 79 Mbit by fitting up to two options R&S SMIQB12.

Memory capacity	32 Mbit

Digital standards with options R&S SMIQB20 and R&S SMIQB11

Modulation and ACP specifications apply at the respective standard frequency ranges and at levels
≤8 dBm PEP with R&S SMIQ02B/03B
≤5 dBm PEP with R&S SMIQ04B/06B

Data sources	
with TDMA	internal, external via RS232 interface (SERDATA)
with CDMA	internal
Digital standard GSM / EDGE	**to GSM standard**
Frequency	
Standard	880 MHz to 960 MHz/1710 MHz to 2000 MHz
Range	same as R&S SMIQ
Modulation	GMSK or 8PSK EDGE (8PSK with 3π/8 rotation)
Symbol rate	
Standard	270 833 Hz
Range	100 Hz to 300 kHz
Baseband filter (GMSK)	
Standard	Gauss, BxT = 0.3
Range	Gauss, BxT = 0.2 to 0.7
Baseband filter (8PSK EDGE)	Gauss Linear
Frame structure	1 to 8 slots user-definable any burst types can be combined, incl. GSM and EDGE mixed
Burst types	NORMAL, DUMMY, ALL DATA, NORMAL EDGE
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 3 symbols, <18 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9-1, $2^{11}-1$, $2^{15}-1$, $2^{16}-1$, $2^{20}-1$, $2^{21}-1$, $2^{23}-1$, programmable data memory
External serial modulation data	asynchronous via RS232 interface (SERDATA)

GMSK	
Phase error, rms value	<1°, 0.3° typ.
Phase error, peak value	<3°, 1° typ.
8PSK EDGE	
Error vector magnitude, rms	<1.2%, 0.6% typ.
Power density spectrum, typ. values (standard, resolution bandwidth 30 kHz, without power ramping)	
Offset 200 kHz	–34 dB
Offset 400 kHz	–70 dB
Offset 600 kHz	–78 dB
Digital standard DECT	**to ETS 300 175-2 and ETS 300 176-1**
Frequency	
Standard	1880 MHz to 1900 MHz
Range	same as R&S SMIQ
Modulation	GFSK (standard), π/4 DQPSK
Deviation with GFSK	
Standard	288 kHz
Range	(0.1 to 1) x f_{symb}
Symbol rate	
Standard	1 152000 Hz
Range with GFSK	100 Hz to 1200 kHz
Range with π/4 DQPSK	100 Hz to 1200 kHz
Baseband filter	
Standard	Gauss, B x T = 0.5
Range	Gauss, B x T = 0.2 to 0.7 √cos (α = 0.2 to 0.7), cos (α = 0.2 to 0.7)
Frame structure	1 to 12 slots of 24 slots user-definable
Slot types	FULL (basic), DOUBLE (high capacity), ALL DATA
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 2 symbols, <10 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9–1, 2^{15}–1, 2^{16}–1, 2^{20}–1, 2^{21}–1, 2^{23}–1, programmable data memory
Deviation error, rms	<1.3%
Special functions	
Timing adjustment simulation	lengthening (+) or shortening (–) of every 35th frame
Range	–4 bit to +4 bit (resolution 1 bit)
Jitter simulation	time lead for even-numbered frames
Range	0 bit to +4 bit (resolution 1 bit)
Slot timing shift	time shifting of a single slot
Range	–9 bit to +9 bit (resolution 1 bit)
Selectable preamble	normal or prolonged
Response during ramp-up	modulated or CW
Digital standard NADC	**to IS-54 and IS-136**
Frequency	
Standard	824 MHz to 894 MHz/1850 MHz to 2000 MHz
Range	same as R&S SMIQ
Modulation	π/4 DQPSK
Symbol rate	
Standard	24.300 kHz
Range	100 Hz to 200 kHz

Baseband filter	
Standard	$\sqrt{\cos}\,(\alpha=0.35)$, $\cos\,(\alpha=0.35)$
Range	$\sqrt{\cos}\,(\alpha=0.2$ to $0.7)$, $\cos\,(\alpha=0.2$ to $0.7)$
Frame structure	1 to 6 slots user-definable for uplink and downlink
Burst types	Up/Down TCH, ALL DATA, Up SHORT
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 3 symbols, <123.4 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9-1, $2^{11}-1$, $2^{15}-1$, $2^{16}-1$, $2^{20}-1$, $2^{21}-1$, $2^{23}-1$, programmable data memory
External serial modulation data	asynchronous via RS232 interface (SERDATA)
Error vector magnitude, rms	<1.2%, 0.4% typ.
Adjacent-channel power, typ. values, without power ramping	
Offset 30 kHz	–35 dBc
Offset 60 kHz	–75 dBc
Offset 90 kHz	–78 dBc
Digital standard PDC	**to RCR STD-27**
Frequency	
Standard	810 MHz to 826 MHz/940 MHz to 956MHz /1429 MHz to 1453 MHz/1477 MHz to 1501 MHz
Range	same as R&S SMIQ
Modulation	π/4 DQPSK
Symbol rate	
Standard	21 kHz
Range	100 Hz to 200 kHz
Baseband filter	
Standard	$\sqrt{\cos}\,(\alpha=0.5)$, $\cos\,(\alpha=0.5)$
Range	$\sqrt{\cos}\,(\alpha=0.2$ to $0.7)$, $\cos\,(\alpha=0.2$ to $0.7)$
Frame structure	1 to 6 slots user-definable for uplink and downlink
Burst types	TCH, SYNC, VOX, ALL DATA
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 2 symbols, <95.2 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9-1, $2^{11}-1$, $2^{15}-1$, $2^{16}-1$, $2^{20}-1$, $2^{21}-1$, $2^{23}-1$, programmable data memory
External serial modulation data	asynchronous via RS232 interface (SERDATA)
Error vector magnitude, rms	<1.2%, 0.4% typ.
Adjacent-channel power, typ. values, without power ramping	
Offset 50 kHz	–74 dBc
Offset 100 kHz	–78 dBc
Digital standard PHS	to RCR STD-28
Frequency	
Standard	1895.0 MHz to 1918.1 MHz
Range	same as R&S SMIQ
Modulation	π/4 DQPSK
Symbol rate	
Standard	192 kHz
Range	100 Hz to 200 kHz
Baseband filter	
Standard	$\sqrt{\cos}\,(\alpha=0.5)$, $\cos\,(\alpha=0.5)$
Range	$\sqrt{\cos}\,(\alpha = 0.2$ to $0.7)$, $\cos\,(\alpha = 0.2$ to $0.7)$

Frame structure	1 to 8 slots user-definable
Burst types	TCH (32 kbit and 16 kbit channel), SYNC, VOX, ALL DATA
Burst on/off ratio	>80 dB
Burst rise/fall time	corresponding to 2 symbols, <13 µs
Slot attenuation, selectable	0 dB to 70 dB
Internal modulation data	PRBS, 2^9-1, $2^{11}-1$, $2^{15}-1$, $2^{16}-1$, $2^{20}-1$, $2^{21}-1$, $2^{23}-1$, programmable data memory
External serial modulation data	asynchronous via RS232 interface (SERDATA)
Error vector magnitude, rms	<1.2%, 0.4% typ.
Adjacent-channel power, typ. values, without power ramping)	
Offset 600 kHz	–74 dBc
Offset 900 kHz	–76 dBc

Digital standard IS-95 CDMA with option R&S SMIQ B42

Modulation and ACP specifications apply at the respective standard frequency ranges and at levels
≤8 dBm PEP with R&S SMIQ02B/03B
≤5 dBm PEP with R&S SMIQ04B/06B

To TIA standard IS-95A and J-STD-008	
Frequency	
Standard	824 MHz to 894 MHz, 1850 MHz to 2000 MHz
Range	same as R&S SMIQ
Modulation	QPSK, OQPSK
Chip rate	
Standard	1.2288 MHz
Range	0.1 Mcps to 7 Mcps
Reverse link coded	0.1 Mcps to 1.3 Mcps
Sequence length	
Forward link	1 superframe (80 ms)
Reverse link	1 superframe (80 ms)
	if user-definable data lists are used: calculation in real time, ie unlimited sequence length
Reverse link coded	calculation in real time, ie unlimited sequence length
Baseband filter	
Standard	IS-95 with or without equalizer
Other filters	√cos (α=0.2 to 0.7), cos (α=0.2 to 0.7)
Forward link, mode 18	
Number of code channels	1 to 18
Walsh code selectable	0 to 63
Code channel power	0.0 dB to –30 dB, 4 user-definable levels
Forward link, mode 64	
Number of code channels	1 to 64
Code channel power	0.0 dB to –30 dB, 2 user-definable levels
Reverse link	full-rate mode, half-rate mode with random power gating
Reverse link coded	incl. frame quality indicator, convolutional encoder, block interleaver traffic channel, 9600/4800/2400/1200 bps traffic channel, 14400/7200/3600/1800 bps access channel, 4800 bps

Internal modulation data	
Forward link, 19200 bit/s	PRBS, 0 sequence, 1 sequence, 01 alternating, different for each code channel
Reverse link, 28800 bit/s	PRBS, 0 sequence, 1 sequence, 01 alternating, programmable data memory
Reverse link coded	PRBS, 2^9-1, $2^{15}-1$, $2^{16}-1$, $2^{20}-1$, $2^{21}-1$, $2^{23}-1$, programmable data memory
Synchronization signals (chip rate 1.2288 Mcps)	chip clock, input and output, 2 outputs for 80 ms, 80/3 ms, 20 ms, 2 s clock, trigger input
Modulation accuracy ρ	>0.9996
Adjacent-channel power ratio at 30 kHz bandwidth	
Reverse link	
Offset 885 kHz	–77 dBc typ.
Offset 1.25 MHz	–83 dBc typ.
Offset 1.98 MHz	–84 dBc typ.
With option R&S SMIQB47, IQ filter 850 kHz	
Offset 885 kHz	<–78 dBc, –82 dBc typ.
Offset 1.25 MHz	<–83 dBc, –87 dBc typ.
Offset 1.98 MHz	<–85 dBc, –89 dBc typ.
9 channels forward link	
Offset 885 kHz	–77 dBc typ.
Offset 1.25 MHz	–79 dBc typ.
Offset 1.98 MHz	–80 dBc typ.
With option R&S SMIQB47, IQ filter 850 kHz	
Offset 885 kHz	<–74 dBc, –78 dBc typ.
Offset 1.25 MHz	<–80 dBc, –84 dBc typ.
Offset 1.98 MHz	<–83 dBc, –86 dBc typ.

Digital standard WCDMA with option R&S SMIQB43

To NTT DoCoMo 1.0 and ARIB standard 0.0

Modulation and ACP specifications apply at the respective standard frequency ranges and at levels
≤8 dBm PEP with R&S SMIQ02B/03B
≤5 dBm PEP with R&S SMIQ04B/06B

Frequency	
Standard	1800 MHz to 2200 MHz
Range	same as R&S SMIQ
Modulation	QPSK, OQPSK
General settings	
Chip rate	
Standard	4.096 Mcps
Range	0.1 Mcps to 7 Mcps
Link direction	uplink and downlink
Sequence length	45 frames without option R&S SMIQB12 150 frames with 1 option R&S SMIQB12 240 frames with 2 options R&S SMIQB12
Baseband filter	
Standard	WCDMA 0.22
Other filters	√cos (α = 0.1 to 0.7), cos (α = 0.1 to 0.7)

Code channels and spreading	
Number	mode 4: 4 channels with different power mode 8: 8 channels, 1 channel with different power and 7 channels with equal power mode 15: 15 channels with equal power
Multicode operation	yes
Code channel power	0.0 dB to –30 dB
Short code	selectable for each code channel
Range	0 to 127
LMS	1 to FF hex
Long code	selectable for each code channel
Initial value uplink	0 to 1FFFFFFFFFF hex
Initial value downlink	0 to 3FFFF hex
Time offset	0 to 40959 chips (1 radio frame)
Physical channel with frame structure	
Link direction	downlink, uplink, uplink IQ-multiplexed to ARIB 0.0
Downlink channels	perch 1, common control 64 ksps sample rate, dedicated channel with 32, 64, 128, 256, 512, 1024 ksps sample rate
Uplink channels	common control 64 ksps sample rate, dedicated channel with 32, 64, 128, 256, 512, 1024 ksps sample rate
Uplink channels (ARIB)	dedicated control channel with 16 ksps sample rate, dedicated data channel with 16, 32, 64, 128, 256, 512, 1024 ksps sample rate
Data offset	time offset, separately adjustable for each code channel
Range offset	0 to 1 radio frame
Resolution offset	1 symbol
Internal modulation data	
DATA field	PRBS, 2^9–1, 2^{11}–1, 2^{15}–1, 2^{16}–1 programmable data memory
TPC field	00, 11, alternating, programmable data memory
Synchronization signals	chip clock, input and output outputs for slot, frame clock or marker for repetition of chip sequence trigger input
Error vector magnitude, rms	<2%, 1.5% typ.
with option R&S SMIQB47, IQ filter 2.5 MHz	<3%, 1.8% typ.
Adjacent-channel power, 1 DTCH	
Offset 5 MHz, low distortion output mode	–67 dBc typ. 8)
Offset 10 MHz, low noise output mode	–70 dBc typ. 8)
With option R&S SMIQB47, IQ filter 2.5 MHz	
Offset 5 MHz, low distortion output mode	<–65 dBc, –70 dBc typ. 8)
Offset 10 MHz, low noise output mode	<–71 dBc, –74 dBc typ. 8)

Digital standard WCDMA 3GPP (FDD) with option R&S SMIQB45

To 3GPP standard 4.1.0 (FDD)

3GPP (FDD) version	4.1.0 to 3GPP technical specifications TS25.211, TS25.213, TS25.141, TS25.101 and TS25.104
Frequency	
Standard	1800 MHz to 2200 MHz
Range	same as R&S SMIQ
General settings	
Chip rate	
Standard	3.840 Mcps,
Range	1 Mcps to 5 Mcps

Link direction	uplink (reverse link) and downlink (forward link)
Sequence length	1 to 13 frames
Baseband filter	
Standard	√cos, α = 0.22
Other filters	√cos, (α = 0.1 to 0.99), cos (α = 0.1 to 0.99), user filter
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering and reduces the crest factor. The range is 1 to 100 %.
Code channels	
Downlink	up to 512 data channels (plus special channels) divided among up to four base stations (BS) with 128 code channels each
Uplink	up to four mobile stations (MS) each operating in one of modes PRACH only, PCPCH only, DPCCH + DPDCHs
Physical channels in downlink	
P-CPICH	Primary Common Pilot Channel
Symbol rate	15 ksps, fixed
Channelization code	0, fixed
Slot structure	predefined symbols
S-CPICH	Secondary Common Pilot Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Slot structure	predefined symbols
P-SCH	Primary Sync Channel
Symbol rate	15 ksps, fixed
Slot structure	synchronization code (SC)
S-SCH	Secondary Sync Channel
Symbol rate	15 ksps, fixed
Slot structure	synchronization code (SC)
P-CCPCH	Primary Common Control Physical Channel
Symbol rate	15 ksps, fixed
Channelization code	1, fixed
Slot structure	data
S-CCPCH	Secondary Common Control Physical Channel
Symbol rate	15, 30, 60, 120, 240, 480, 960 ksps
Channelization code	depending on symbol rate, 0 to max. 255
Slot structure	data, TFCI, pilot
PICH	Page Indication Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Number of PIs per frame	18, 36, 72, 144
Slot structure	page indicator bits, not used bits
AP-AICH	Access Preamble Acquisition Indication Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Slot structure	acquisition indicators, empty symbols

AICH	Acquisition Indication Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Slot structure	acquisition indicators, empty symbols
PDSCH	Physical Downlink Shared Channel
Symbol rate	15, 30, 60, 120, 240, 480, 960 ksps
Channelization code	depending on symbol rate, 0 to max. 255
Slot structure	data
DL-DPCCH	Dedicated Physical Control Channel
Symbol rate	7.5 ksps, fixed
Channelization code	0 to 511
Slot structure	TPC, pilot
DPCH	Dedicated Physical Channel
Symbol rate	7.5, 15, 30, 60, 120, 240, 480, 960 ksps
Channelization code	depending on symbol rate, 0 to max. 511
Slot structure	data 1, TPC, TFCI, data 2, pilot
Physical channels in uplink	
PRACH	Physical Random Access Channel
Symbol rate	15, 30, 60, 120 ksps
Frame structure	preamble(s), message part consisting of data and control section
Preamble part power	–60 dB to 0 dB
Data part power	–60 dB to 0 dB
Control part power	–60 dB to 0 dB
Preamble repetition	1 to 10
Signature	0 to 15
Access slot	0 to 14
Message part length	1 or 2 frames
TFCI	0 to 1023
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. 24 bit long)
PCPCH	Physical Common Packet Channel
Symbol rate	15, 30, 60, 120, 240, 480, 960 ksps
Frame structure	access preamble(s), collision detection preamble, power control preamble, message part consisting of data and control section
Preamble part power	–60 dB to 0 dB
Data part power	–60 dB to 0 dB
Control part power	–60 dB to 0 dB
Preamble power step	0 dB to 10 dB
Preamble repetition	1 to 10
Signature	0 to 15
Access slot	0 to 14
Message part length	1 to 10 frames
Power control preamble length	0 or 8 slots
FBI state	OFF/1 bit/2 bit
FBI pattern	all 0, all 1 and bit pattern (max. 24 bit long)
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. 24 bit long)
DPCCH	Dedicated Physical Control Channel
Symbol rate	15 ksps, fixed
Channelization code	0, fixed
DL–UL timing offset	1024 chips, fixed
Slot format	0 to 5
FBI state	OFF/1 bit/2 bit
FBI pattern	all 0, all 1 and bit pattern (max. 24 bit long)
TFCI state	OFF/ON
TFCI	0 to 1023
Use TPC for dynamic output	OFF/ON
Power control	if this function is active, the TPC pattern is used to vary the transmit power of the MS code channels versus time
Output power control step	–10 dB to +10 dB

DPDCH	Dedicated Physical Data Channel
Overall symbol rate	overall data rate of all uplink DPDCHs 15, 30, 60, 120, 240, 480, 960, 2 x 960, 3 x 960, 4 x 960, 5 x 960, 6 x 960 ksps
Active DPDCHs	1 to 6, depending on overall symbol rate
Symbol rate	fixed for active DPDCHs, depending on overall symbol rate
Channelization code	fixed for active DPDCHs, depending on overall symbol rate
Channel power	–60 dB to 0 dB for all DPDCHs
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. 24 bit long)

Parameters for each base station (BS)

State	OFF/ON
2nd search code group	0 to 63 (depending on scrambling code)
Scrambling code	0 to 5FFFF hex or off
TFCI state	OFF/ON
TFCI	0 to 1023
TPC pattern readout mode	use of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10
Use TPC for dynamic output power control	OFF/ON if this function is active, the TPC pattern is used to vary the transmit power of the code channels versus time
Output power control step	–10 dB to +10 dB
Transmit diversity	OFF/antenna 1/antenna 2 if this function is active, the output signal for antenna 1 or antenna 2 can be generated as defined in the standard

Parameter for each mobile station (MS)

State	OFF/ON
Mode	PRACH only, PCPCH only, DPCCH + DPDCHs
Scrambling code	0 to FF FFFF hex
Scrambling code mode	long, short, off
TPC pattern	all 0, all 1 and bit pattern (max. 24 bit long)
TPC pattern readout mode	use of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10

Parameters for each downlink code channel, independently selectable

State	OFF/ON
Symbol rate	between 7.5 ksps and 960 ksps, depending on type of physical channel
Channelization code	range 0 to max. 511, depending on symbol rate and type of physical channel
Power	–60 dB to 0 dB
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. 24 bit long)
Timing offset	separately adjustable for each code channel 0 to 150 (in units of 256 chips)
Pilot length	2, 4, 8, 16 bit depending on symbol rate
TPC pattern	all 0, all 1 and bit pattern (max. 24 bit long)
Multicode state	OFF/ON

Assistant functions to facilitate operation	
Test models	
Downlink	test model 1 with 16/32/64 channels test model 2 test model 3 with 16/32 channels test model 4
Uplink (not standardized)	DPCCH + 1 DPDCH at 60 ksps sample rate DPCCH + 1 DPDCH at 960 ksps sample rate
Parameterizable predefined settings	generation of complex signal scenarios in downlink with parameterizable default settings selectable parameters: use and symbol rate of special channels (for synchronization of mobile), number and symbol rate of data channels, crest factor: minimal/average/worst
Multichannel edit	common configuration of data channels of BS channel table; selectable parameters, partly with start value and step size: range of data channels to be set, symbol rate, channelization code with step size, channel power with step size, data, TPC, timing offset with step size, multicode state, state
Copy BS/MS	adopting the configuration of a BS for another BS/MS for the definition of multi-BS/MS scenarios or BS signals with more than 128 channels parameters: source and destination of copying, channelization code offset for simple definition of BS signals with more than 128 channels and continuous channelization codes
Resolve domain conflicts	elimination of code channel overlapping in code domain (domain conflicts) occurring in a BS/MS
Graphic displays	
Domain conflicts	Display of domain conflicts (overlapping of code channels in code domain) in the lines concerned of the channel tables. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code domain	Display of code domain occupied by current BS. Domain areas in which conflicts occur are highlighted. The distribution of code channels in the code domain as well as channel powers are shown qualitatively.
Channel graph	Display of all active channels of a BS versus the channel table index. The powers of the code channels are shown qualitatively.
CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of complex IQ samples exceeding a predefined threshold. Together with the current CCDF, the CCDFs of the two 3GPP signals last generated can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus IQ samples of current 3GPP signal. This diagram allows qualitative assessment of channel configuration, channel power ratios, and the effect of parameters such as data and data offset.

Adjacent-channel power, 1 DPCH (crest factor=5,4 dB)	
Chip rate 3.84 MHz	
Without option R&S SMIQB47	
Offset 5 MHz, low distortion output mode	–67 dBc typ.[8]
Offset 10 MHz, low noise output mode	–70 dBc typ. [8]
With option R&S SMIQB47, IQ filter 2.5 MHz	
Offset 5 MHz, low distortion output mode	<–65 dBc, –70 dBc typ.[8]
Offset 10 MHz, low noise output mode	<–71 dBc, –74 dBc typ.[8]
Adjacent-channel power, test model 1, 64 DPCH (crest factor=10,6 dB)	
Without option R&S SMIQB47	
Offset 5 MHz, low distortion output mode	–64 dBc typ.[8]
Offset 10 MHz, low noise output mode	–67 dBc typ.[8]
With option R&S SMIQB47, IQ filter 2.5 MHz	
Offset 5 MHz, low distortion output mode	<–64 dBc, –68 dBc typ.[8]
Offset 10 MHz, low noise output mode	<–67 dBc, –70 dBc typ.[8]

Enhanced functions for digital standard WCDMA 3GPP (FDD) with option R&S SMIQB48

3GPP (FDD) version	4.1.0 to 3GPP technical specifications TS25.101, TS25.104, TS25.141, TS25.211 and TS25.213

Option R&S SMIQB45 WCDMA 3GPP is extended by the following functions:

Enhanced Channels

Channels of WCDMA system in R&S SMIQ that offer enhanced functionality compared with standard channels of option R&S SMIQB45.
Can be used in downlink for max. four DPCHs and in uplink for one DPCCH and max. six DPDCHs.
All DPCHs or DPDCHs have the same symbol rate.
Enhanced functions at a glance:

- Sequences of up to 1022 frames
- Realtime BCH with incrementing SFN
- Data lists for data fields and TPC field
- External power control
- Channel coding
- Bit error insertion
- Block error insertion
- Orthogonal channel noise simulation (OCNS)
- Additional mobile stations

Sequences of up to 1022 frames	generation of WCDMA signals with length of max. 256 frames with four active enhanced channels and max. 1022 frames with one active enhanced channel
Applications	– continuous measurement of physical bit error rate (without channel coding) on code channel with PN9 data without wrap-around problems – use of user data (data lists) with externally processed long data sequences for enhanced channels
Realtime BCH with incrementing SFN	Generation of a realtime downlink BCH (coded P-CCPCH) with incrementing system frame number (0 to 4094). BCH can be combined with all reference measurement channels (bit rate 12.2 kbps, 64 kbps, 144 kbps, 384 kbps) or AMR of 12.2 kbps bit rate. Max. sequence length:

RMC	12.2 kbps	2044 frames
RMC	64 kbps	512 frames
RMC	144 kbps	512 frames
RMC	384 kbps	512 frames
AMR	12.2 kbps	2044 frames

Application	– receiver and performance tests to TS25.101 – test of mobile synchronization to BS signal combined with: continuous measurement of DTCH and DCCH bit and block error rate using PN9 data

Data lists for data fields and TPC field	For the enhanced channels, the data fields and the transmit power control (TPC) field of the slots can be filled from data lists. This allows the use of externally precoded data or the generation of long power control profiles for the DUT.
Applications	– measurement of UE power control steps – measurement of UE max. output power
External power control	Variation of output power of max. 4 enhanced channels in realtime via external control line. The power of all active enhanced channels can be increased or decreased jointly by means of a TTL signal.
Common parameters	
Power step	0.25 dB to 30 dB
Power up range	0 dB to 30 dB
Power down range	0 dB to 30 dB
Parameters for each enhanced channel	
Start power	–60 dB to 0 dB
Power control	OFF; UP; DOWN
Graphic display	current output power (differential power relative to start power) of channels with external power control shown by bargraph
Application	test of SIR based closed loop power control
Channel coding	Coding of up to four enhanced channels in accordance with definition of reference measurement channels given in TS25.101, TS25.104 and TS25.141. In addition, AMR speech 12.2 kbps to TS25.944 and RACH/CPCH (TB size 168 bit or 360 bit, data PN9 fixed) to TS25.141 are supported. Common coding scheme and symbol rate for all enhanced channels.
Implemented reference measurement channels	– uplink reference measurement channel for 12.2 kbps, 64 kbps, 144 kbps, 384 kbps – downlink reference measurement channel for 12.2 kbps, 64 kbps, 144 kbps, 384 kbps
Channel coding structure	– CRC attachment – tail bit attachment – convolutional coding or turbo coding, depending on symbol rate – 1st interleaving – radio frame segmentation – rate matching – 2nd interleaving
Sequence length of coded signal	
4 enhanced channels	up to 256 frames (10 ms each)
1 enhanced channel	up to 1022 frames (10 ms each)
Applications	bit error rate (BER) measurements to TS25.101/104 (radio transmission and reception), eg: – blocking characteristics – spurious response – intermodulation characteristics block error rate (BLER) measurements to TS25.101/104 (radio transmission and reception), eg: – demodulation of dedicated channel under static propagation conditions (in conjunction with R&S SMIQB17) – demodulation of dedicated channel under multipath fading propagation conditions (in conjunction with R&S SMIQB14 and -B17) – test of receiver decoder

Bit error insertion	generation of bit errors by impairment of data stream, either before coding in case of active channel coding, or otherwise at the physical layer
Parameter	
Bit error rate (nominal BER)	10^{-1} to 10^{-7} with display of resulting BER
Application	verification of internal BER calculation to TS25.141 (BS conformance testing)
Block error insertion	generation of block errors by impairment of CRC during coding of enhanced channels
Parameter	
Block error rate (nominal BLER)	10^{-1} to 10^{-4} with display of resulting BLER
Application	verification of internal BLER calculation to TS25.141 (BS conformance testing)
Orthogonal channel noise simulation (OCNS)	Simulation of orthogonal background or interference channels for enhanced channels of a base station. If this feature is activated, 16 DPCHs according to TS25.241, 4.1.0, table C.6 are added. The total power of the OCNS part is adjusted automatically in order to achieve a total power of 0 dB.
Applications	– test of mobile receiver under realistic conditions – measurement of maximum input level to TS25.101
Additional mobile stations	Simulation of up to 64 mobile stations in addition to the four user-configurable mobile stations of option R&S SMIQB45. The scrambling codes of the additional mobiles differ from one another.
Parameters	
Number of additional MS	1 to 64
Scrambling code step	1 to 1000 hex
Power offset	–20 to 20 dB
Applications	base station test under realistic receiving conditions
Requirements for installation of option R&S SMIQB48	R&S SMIQxxB with options R&S SMIQB20, R&S SMIQB45, R&S SMIQB11. Maximum sequence length of enhanced channels requires maximum memory extension of data generator, ie two options R&S SMIQB12.

Arbitrary waveform generator with option R&S SMIQB60

Waveform memory, interpolation	
Output memory	
Length of waveform	1 to 524216 in steps of one sample

Note: The specified waveform length cannot be directly compared with the relevant data of conventional ARB generators. In R&S SMIQB60, the oversampling needed for suppressing repetitive spectra by means of the analog filter is effected automatically and in realtime by way of hardware interpolation, ie the stored waveform is not extended by the oversampling factor. For W-CDMA signals, for example, oversampling of only 1.62 is needed. This compares with a conventional ARB with oversampling of 4, meaning that R&S SMIQB60 output memory capacity corresponds to 1.25 Msamples.

Resolution	12 bit
Loading time for 512k I/Q samples	4 s
Nonvolatile memory	
Number of blocks	22 (one waveform occupies at least one block)
Block size	65527
Interpolation	
Interpolation bandwidth (–0.1dB)	0.375 x clock rate
Repetitive spectra suppression through analog filter	>70 dB

Clock generation	
Clock rate	1 kHz to 40 MHz
Resolution	0.001 Hz
Clock mode	internal or external
Error	$<2^{-42}$ related to reference frequency
Signal output	
Channels	2 (I and Q)
Output impedance	50 Ω
Output level (EMF, peak)	
Normal mode	$\sqrt{I^2 + Q^2} = 1\,V$
Manual mode	–6 dB to 0 dB referred to 1 V, setting range up to +3 dB
Level difference between channels	<0.2% at 1 kHz 3)
DC offset	<–54 dB in normal mode 3)
Frequency response	
Magnitude	
up to 12 MHz	<1 dB
up to 10 MHz	0.1 dB typ.
Group delay	
up to 10 MHz	1 ns typ.
I/Q imbalance	
Magnitude	
up to 10 MHz	0.05 dB typ.
Group delay	
up to 10 MHz	0.5 ns typ.
SFDR (sinewave 1 MHz, clock 4 MHz, measurement range up to 12 MHz)	>60 dB
Trigger	
Trigger modes	auto, retrig, armed auto, armed retrig
Trigger source	internal or external
External trigger input	threshold –2.5 V to 2.5 V, impedance 1 kΩ / 50 Ω
Externe trigger frequency	<10 MHz
Externer trigger delay range	0 to 2^{16} samples
Externer trigger inhibit range	0 to 2^{26} samples
Pulse width	>50 ns
Trigger outputs	
Number	2
Delay	0 to 524216 samples
On time	1 to 524215 samples
Off time	1 to 524215 samples
Level	TTL
Graphic displays	
CCDF	determination and graphic display of CCDF of waveform loaded into output memory; CCDF also serves for crest factor determination. The CCDF traces of the three waveforms last loaded can be displayed simultaneously.

Operation with WinIQSIM™

WinIQSIM™ is a Windows software that allows a wide variety of I and Q baseband signals to be calculated on a PC (see WinIQSIM™ data sheet PD 0757.6940). From version 3.30, the software supports downloading of waveforms into R&S SMIQ and operation of option R&S SMIQB60 from a PC.

Software options R&S SMIQK11 to -K18

For specifications of digital standards with R&S WinIQSIM™ and R&S SMIQB60, R&S SMIQK11 to -K18 please refer to the WinIQSIM™ data sheet PD 0757.6940

Simultaneous modulation

Any combination is possible with the following exceptions:
Simultaneous FM and φM
Simultaneous digital modulation and vector modulation

Overview of digital TDMA standards

The table below summarizes the key data for the digital TDMA standards implemented in R&S SMIQ. Options R&S SMIQB20 and R&S SMIQB11 are required for all standards.

	GSM (GMSK)	GSM-EDGE (8PSK)	DECT	NADC	PDC	PHS
Error vector magnitude, rms	N/A	<1.2 %, 0.6 % typ.	N/A	<1.2 %, 0.4 % typ.	<1.2 %, 0.4 % typ.	<1.2 %, 0.4 % typ.
Phase error (standard), rms	<1°, 0.3° typ.	N/A	N/A	N/A	N/A	N/A
Peak value	<3°, 1° typ.					
Deviation error, rms	N/A	N/A	<1.3 %	N/A	N/A	N/A
Channel spacing/kHz	200	200	1728	30	25	300
Power density spectrum, typ. resolution BW 30 kHz						
Offset 200 kHz	–34 dB	–34 dB	N/A	N/A	N/A	N/A
400 kHz	–70 dB	–70 dB				
600 kHz	–78 dB	–78 dB				
Adjacent-channel power ratio (ACPR), typ.						
at adjacent channel	N/A	N/A	N/A	–35 dBc	–	–
at 1st alternate channel				–75 dBc	–74 dBc	–74 dBc
at 2nd alternate channel				–78 dBc	–	–76 dBc
at 3rd alternate channel				–	–78 dBc	–
Burst types	NORMAL, DUMMY, ALL DATA	NORMAL EDGE	FULL (basic), DOUBLE (high capacity), ALL DATA	Up/Down TCH, ALL DATA, Up SHORT	TCH, SYNC, VOX, ALL DATA	TCH (32 kbit and 16 kbit), SYNC, VOX, ALL DATA

Options for special applications

Fading simulation with options R&S SMIQB14, R&S SMIQB15[13]

RF bandwidth (–3 dB)	>14 MHz
Additional frequency response	
up to 5 MHz offset from carrier	<0.6 dB, <0.4 dB typ.
Carrier leakage	–45 dBc typ.
Setting time after RF frequency change	<3 ms
Modes	external via I and Q modulation inputs, internal with option R&S SMIQB20
Number of paths and channels	
with option R&S SMIQB14	6 paths, 1 channel
with options R&S SMIQB14 and -B15	12 paths, 1 channel, or 6 + 6 paths, 2 channels with second R&S SMIQ through simple retrofit (for instructions see manual)
Path attenuation	
Range	0 dB to 50 dB
Resolution	0.1 dB
Error (in range 0 dB to 20 dB)	<0.3 dB
Path delay	
Range	0 µs to 1600 µs
Resolution	50 ns
Error	<5 ns
Doppler shift	
Frequency range	0.1 Hz to 1600 Hz
Speed range	$v_{min} = \frac{0,03 \times 10^9 \frac{m}{s^2}}{f_{RF}}$ $v_{max} = \frac{479 \times 10^9 \frac{m}{s^2}}{f_{RF}}$
Example with f_{RF} =1 GHz: v_{min}= 0.1 km/h, v_{max}=1724 km/h	
Resolution	0.1 km/h, m/s, mph
Error	<0.13%
Rayleigh fading	
Pseudo noise interval	>372 h
Deviation from theoretical CPDF[4] for P_{avg} = 0 dB	
Path attenuation from –20 dB to + 10 dB	<1 dB, <0.3 dB typ.
Path attenuation from –30 dB to –20 dB	<2 dB, <0.3 dB typ.
Rice fading	
Power ratio[5]	
Range	–30 dB to +30 dB
Resolution	0.1 dB
Frequency ratio	
Range	–1 to +1
Resolution	0.05
Lognormal fading, Suzuki fading	
Standard deviation	
Range	0 dB to 12 dB
Resolution	1 dB
Local constant	l_{min} to 200 m $l_{min} = \frac{12 \times 10^9 \frac{m}{s}}{f_{RF}}$
Correlation	paths 1 to 6 with paths 7 to 12
Magnitude range	0% to 100%
Resolution	5%
Phase range	0° to 360°
Resolution	1°
General data; thermal loading	specs valid in range 0 °C to 45°C

Enhanced fading functions for WCDMA 3GPP with option R&S SMIQB49

The following data deviate from the specifications for R&S SMIQB14/R&S SMIQB15:

Modes	standard fading, fine delay, moving delay, birth-death
Setting time after RF frequency change	6 ms
Fine delay mode	
RF bandwidth	4.8 MHz
Number of paths	2 (with R&S SMIQB14), 4 (with R&S SMIQB14 + R&S SMIQB15)
Profiles	Rayleigh, pure Doppler
Delay	25 ns to 1637 μs
Delay resolution	1 ns
Moving delay mode	
RF bandwidth	4.8 MHz
Number of paths	2
Delay, path 1	0 to 1000 μs (in 50 ns steps)
Delay, path 2	$\text{delay path 1} + \frac{\text{delay variation}_{\text{(pk-pk)}}}{2} \times \sin \frac{2\pi t}{\text{variation period}}$
Delay variation (peak-peak)	150 ns to 50 μs
Variation period	10 s to 500 s
Delay step size	<1 ns
Profiles	none
Birth-death mode	
Number of paths	2
Profiles	pure Doppler
Delay	5 μs to 1000 μs
Delay range (birth-death process)	5 μs to +5 μs (not variable)
Delay grid	1 μs (not variable)
Hopping dwell	100 ms to 5 s

Noise and distortion simulation with option R&S SMIQB17 13)

RF bandwidth (–3 dB)	>14 MHz
Additional frequency response up to 5 MHz offset from carrier	<0.6 dB, 0.4 dB typ.
Carrier leakage 9)	–40 dBc typ.
Distortion simulator	
Type of distortion	AM/AM and AM/φM distortion of modulation signal
Distortion characteristic	each characteristic programmable by entering up to 30 input values via IEC/IEEE bus or by entering up to five polynomial coefficients
Resolution	12 bit
Noise generator (AWGN)	
Distribution density	Gaussian, statistically independent for I and Q
Crest factor	14 dB
C/N	
Range	–30 dB to 30 dB
Resolution	0.1 dB
Error for system bandwidth = symbol rate and C/N <20 dB 3)	
Vector, PSK, QAM modulation	<0.4 dB
FSK, GMSK modulation 10)	<0.4 dB
System bandwidth	relevant bandwidth for determining noise power N
Range	10 kHz to 10 MHz
Resolution	1×10^{-2}
Output spectrum	white noise
Frequency response up to 0.7 x system bandwidth and 5 MHz offset from carrier at RF output 3)	<0.8 dB

Bit error rate measurement with option R&S SMIQB21

The data supplied by the DUT are compared with a reference pseudo-random bit sequence.

Pseudo-random bit sequences (PRBS)	$2^{9}-1$, $2^{11}-1$, $2^{15}-1$, $2^{16}-1$, $2^{20}-1$, $2^{21}-1$, $2^{23}-1$
Clock source	supplied by DUT; a clock pulse is required for each valid bit
Clock rate	100 Hz to 30 MHz
Synchronization time	24 clock cycles
Interface	9-pin sub-D connector, sub-D/BNC cable supplied with option
Data	TTL
Data enable	TTL
Clock	TTL
Restart	TTL
Setup time	10 ns
Hold time	2 ns
Polarity	normal and inverted (data, clock, data enable)
Measurement time	selectable through maximum number of data bits or bit errors (max. 2^{31} bit each), continuous measurement
Measurement result	BER in ppm, % or decade values (if selected number of data bits or bit errors is attained) status displays: not synchronized, no clock, no data

Option R&S SMIQB47 for improved adjacent-channel power ratio for WCDMA and CDMA IS-95

Modulation and ACP specifications apply at the respective standard frequency ranges and at levels
≤8 dBm PEP with R&S SMIQ02B/03B
≤5 dBm PEP with R&S SMIQ04B/06B

Selectable baseband filters to improve ACP values (values see at Digital Standards CDMA/WCDMA)

Bandwidth	OFF, 850 kHz, 2.5 MHz, 5 MHz
WCDMA chiprate 3.84 Mcps /4.096 Mcps, 1DPCH/1DTCH, f = 1800 MHz to 2200 MHz, IQ filter 2.5 MHz	
Offset 5 MHz, low distortion output mode	<–65 dBc, –70 dBc typ. 8)
Offset 10 MHz, low noise output mode	<–71 dBc, –74 dBc typ. 8)
IS-95 CDMA, f = 824 MHz to 894 MHz and 1850 MHz to 2000 MHz, IQ filter 850 kHz	
Reverse link	
Offset 885 kHz	<–78 dBc, –82 dBc typ.
Offset 1.25 MHz	<–83 dBc, –87 dBc typ.
Offset 1.98 MHz	<–85 dBc, –89 dBc typ.
9 channels forward link	
Offset 885 kHz	<–74 dBc, –78 dBc typ.
Offset 1.25 MHz	<–80 dBc, –84 dBc typ.
Offset 1.98 MHz	<–83 dBc, –86 dBc typ.
Error vector magnitude, rms	
WCDMA chiprate 3.84 Mcps /4.096 Mcps, IQ filter 2.5 MHz	<3%, 1.8% typ.
IS-95 CDMA IQ filter 850 kHz	<2%, 1.3% typ.

Other data

Memory for instrument settings

50 storable settings

Memory sequence modes	automatic, single shot, manual or external trigger
Step time	50 ms to 60 s
Resolution	1 ms

List mode

Frequency and level values can be stored in a list and set in an extremely short time; permissible level variation: 90 dB

Modes	automatic, single shot, manual or external trigger
Max. number of channels	2000
Dwell time	0.5 ms to 1 s
Resolution	0.1 ms

Remote control

System	IEC 60625 (IEEE 488)
Command set	SCPI 1993.0
Connector	24-contact Amphenol
IEC/IEEE-bus address	0 to 30
Interface functions	SH1, AH1, T6, L4, SR1, RL1, PP1, DC1, DT1, C0
System	RS 232
Command set	SCPI 1993.0
Connector	9-contact D-SUB
Baud rate	1.2 kbit/s to 115.2 kbit/s

General data

Power supply	100 V to 240 V (AC) ± 10%, 50 Hz to 400 Hz, autosetting to AC supply, max. 300 VA
Electromagnetic compatibility	to EN 55011 and EN 61326-2 (EMC directive of EU)
RF leakage at carrier frequency <1 GHz	<0.1 µV (induced in a two-turn coil 25 mm dia. at a distance of 25 mm from any surface of the enclosure)
Immunity to RFI	10 V/m
Ambient conditions	
Operating temperature range	0°C to 50°C [6] to IEC60068
Storage temperature range	–40°C to +70°C
Climatic conditions	95% relative humidity at +25°C/+40°C cyclically, to IEC 60068
Mechanical resistance	
Sinewave vibrations	5 Hz to 150 Hz, max. 2 g at 55 Hz, max. 0.5 g in range 55 Hz to 150 Hz, to IEC 60068, IEC 61010 and MIL-T-28800D, class 5
Random	10 Hz to 300 Hz, acceleration 1.2 g (rms)
Shock	40 g shock spectrum, to MIL-STD-810D, MIL-T-28800D, class 3 and 5
Safety	to EN 61010-1
Dimensions (W x H x D)	435 mm x 192 mm x 460 mm
Weight	25 kg when fully equipped

Ordering information

Vector Signal Generator R&S SMIQ02B 300 kHz to 2.2 GHz		1125.5555.02
R&S SMIQ03B 300 kHz to 3.3 GHz		1125.5555.03
R&S SMIQ04B 300 kHz to 4.4 GHz		1125.5555.04
R&S SMIQ06B 300 kHz to 6.4 GHz		1125.5555.06
Accessories supplied	power cable, operating manual	
Options		
Reference Oscillator OCXO	R&S SM-B1	1036.7599.02
FM/φM Modulator	R&S SM-B5[13]	1036.8489.02
Data Generator	R&S SMIQB11	1085.4502.04
Memory Extension, 32 Mbit	R&S SMIQB12	1085.2800.04
Fading Simulator, 6 paths	R&S SMIQB14[13]	1085.4002.02
Second Fading Simulator for 12 paths or 2 channels	R&S SMIQB15[13]	1085.4402.02
Noise Generator and Distortion Simulator	R&S SMIQB17[13]	1104.9000.02
RF and AF Rear Connectors	R&S SMIQB19	1085.2997.02
Modulation Coder	R&S SMIQB20	1125.5190.02
BER Measurement	R&S SMIQB21	1125.5490.02
Digital Standard IS-95 CDMA	R&S SMIQB42	1104.7936.02
Digital Standard WCDMA to NTT DoCoMo 1.0, ARIB 0.0 standard	R&S SMIQB43	1104.8032.02
Digital Standard WCDMA to 3GPP (FDD)	R&S SMIQB45	1104.8232.02
Low ACP for IS-95 CDMA and W-CDMA	R&S SMIQB47	1125.5090.02
Extended Functions for WCDMA (3GPP)	R&S SMIQB48	1105.0587.02
Extended Fading Functions for WCDMA (3GPP)	R&S SMIQB49	1105.1083.02
Arbitrary Waveform Generator incl. R&S WinIQSIM™	R&S SMIQB60	1136.4390.02
TETRA T1 Simulator	R&S SMIQ-K8	1136.4290.02
Digital Standard IS-95 CDMA (for option R&S SMIQB60)	R&S SMIQK11	1105.0287.02
Digital Standard cdma2000 (for option R&S SMIQB60)	R&S SMIQK12	1105.0435.02
Digital Standard WCDMA TDD Mode (3GPP) (for option R&S SMIQB60)	R&S SMIQK13	1105.1231.02
Digital Standard TD-SCDMA (for option R&S SMIQB60)	R&S SMIQK14	1105.1383.02
OFDM Signal Generation, HIPERLAN/2 (for option R&S SMIQB60)	R&S SMIQK15	1105.1531.02
Digital Standard IEEE 802.11b (for option R&S SMIQB60)	R&S SMIQK16	1154.7700.02
Digital Standard 1xEV-DO (for option R&S SMIQB60)	R&S SMIQK17	1154.7800.02
Digital Standard IEEE 802.11a (for option R&S SMIQB60)	R&S SMIQK18	1154.7952.02
Hint: R&S SMIQ02B/03B (R&S SMIQ04B/06B) can be equipped with max. three (two) of the following options: R&S SM-B5, R&S SMIQB14, R&S SMIQB15, R&S SMIQB17		
Application software		
PC Software: Generation of data and control lists	R&S SMIQ-K1	*)
PC Software: Bluetooth signals for R&S SMIQ	R&S SMIQ-K5	*)
PC Software: User mappings and user filters for R&S SMIQ	R&S User Mod	*)
	*) available at www.rohde-schwarz.com	
Recommended extras		
19" Adapter	R&S ZZA-94	0396.4905.00
Service Kit	R&S SM-Z3	1085.2500.02
BNC Adapter for rear panel, D type connector PAR DATA	R&S SMIQ-Z5	1104.8555.02
90° Power Splitter	R&S SMIQ-Z9	1104.9580.02
Trolley for Transit Case	R&S ZZK-1	1014.0510.00
Transit Case	R&S ZZK-944	1013.9366.00
Service Manual R&S SMIQ		1085.2445.24

Instrument upgrades		
R&S SMIQ02B to R&S SMIQ03B	R&S SMIQU03	1125.5855.03
R&S SMIQ03B to R&S SMIQ04B	R&S SMIQU04 [7]	1125.5855.04
R&S SMIQ04B to R&S SMIQ06B	R&S SMIQU06 [7]	1125.5855.06
Modification Kit for Low ACP	R&S SMIQU47 [7]	1125.5149.02

[1] PEP = peak envelope power.

[2] Data apply to RF≥5 MHz unless specified otherwise and for ATTENUATOR MODE AUTO function.

[3] After 1 hour warmup time and recalibration during 4 hours of operation with temperature variations <5 °C.

[4] CPDF = cumulative probability distribution function; levels referred to average value of output level.

[5] Ratio of discrete and distributed component.

[6] Contrast of LCD lower at higher temperature.

[7] Factory installation only.

[8] Spectrum analyzer settings RBW 30 kHz, VBW 300 kHz, detector RMS.

[9] Typical value for QPSK modulation (crest factor approx. 4 dB), referred to average power from sum of carrier and noise power for C/N >5 dB. Carrier leakage deteriorates with increasing crest factor of modulation signal.

[10] For symbol rate <300 ksym/s.

[11] Spectral components exceeding max. IQ bandwidth will be suppressed.

[12] Additional error with ALC OFF <0.3 dB.

[13] R&S SMIQ02B/03B (R&S SMIQ04B/06B) can be equipped with up to three (two) of the following options: R&S SM-B5, R&S SMIQB14, R&S SMIQB15, R&S SMIQB17

Certified Environmental System
ISO 14001
REG. NO 1954

Certified Quality System
ISO 9001
DQS REG. NO 1954

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ROHDE&SCHWARZ
SIGNAL GENERATOR
300kHz ... 3.3GHz
SMIQ 03B
1125.5555.03
FREQ 2.000 000 000 0 GHz
LEVEL -10.0 dBm
PEP +0.7 dBm
ALC-S&H
WCDMA/3GPP
(peak/avg Power)[dB]
DATA INPUT
RETURN
SELECT

Digital Standards GPS, TETRA, 3GPP (TDD), TD-SCDMA

Supplement to Vector Signal Generator SMIQ

ROHDE & SCHWARZ

Digital standard GPS with option SMIQB51

This new option allows the static signal of a GPS satellite to be simulated. This enables the user to carry out basic GPS receiver testing. The most important settings according to GPS standard can be activated on the SMIQ by a keystroke.

The data sources available are bit patterns of up to 16 bits in length and data lists. It is also possible to use navigation data.

Several navigation data lists (i.e. 25 frames of GPS navigation data) can be generated by means of the free external "Almanac Uploader" PC software. This software is part of the Application Note 1GP46 that can be downloaded from the Rohde&Schwarz homepage.

Certified Quality System
ISO 9001
DQS REG. NO 1954

Certified Environmental System
ISO 14001
REG. NO 1954

TETRA T1 simulator with option SMIQ-K8

SMIQ-K8 is a PC-based software package for generating a TETRA T1 test signal to ETS 300-392/ETS 300-394. It is designed for putting RF components into operation and supporting ETS 300 394-1 tests. SMIQ-K8 generates all data sequences including all control sequences required to operate the signal generator.

Characteristics

- The TETRA frame (bit stream) is generated according to the selected burst type, i.e. control burst (CB), normal burst (NB) or synchronization burst (SB)
- The frames are generated for the uplink (mobile station [MS] transmitting) and the downlink (base station [BS] transmitting)
- The channel types AACH, BSCH, BNCH, TCH, STCH and SCH are generated
- Channel coding is performed for all channels; scrambling with system code, base colour code, mobile country code and mobile network code can be set separately for each channel
- Frame repetition of 1 to 3 multiframes can be selected via sequence length
- The T1 test signal is generated for the V+D (voice and data) test on MS and BS DUTs
- Channel types 1 to 4, 15 and 17 can be set for the downlink and channel types 7 to 11, 16 and 18 for the uplink
- The bit stream can be generated either from pseudo-random sequences (CCITT O.153) or from user-selectable sequences
- The SMIQ-K8 software package calculates the appropriate TETRA T1 signal and transfers it to the SMIQ via the GPIB or RS232 interface; all the SMIQ settings that are needed are included

3GPP (TDD) with option SMIQK13 TD-SCDMA with option SMIQK14

Signals to the time division duplex wideband CDMA standard according to version 3.4.0 of the 3GPP technical specifications (TS 25.221, TS 25.223) can be simulated using the internal arbitrary waveform generator (option SMIQB60) of the SMIQ and the option SMIQK13.

The SMIQ's arbitrary waveform generator and the option SMIQK14 make it possible to simulate signals to the time division synchronous CDMA standard of the China Wireless Telecommunication Standard Group (CWTS).

In both cases, the signal parameters are operated and set as usual via the proven Simulation Software WinIQSIM™ (free download from the Rohde&Schwarz homepage). The user can choose from a wide variety of graphical output modes (e.g. display of CCDF, constellation diagram, channel power and code domain).

Specifications

Digital standard GPS with SMIQ and option SMIQB51

Function	simulation of static signal of GPS satellite using option SMIQB51; prerequisites: SMIQB11 (data generator) and SMIQB20 (modulation coder), SMIQ firmware version 5.65 or higher
General settings	
Frequency	default L1 = 1575.42 MHz, user-selectable in entire frequency range of SMIQ
Doppler shift	–10 kHz to +10 kHz, selectable in steps of 0.1 Hz
Modulation	BPSK
Baseband filter	rectangular filter
Trigger output	C/A code sequence (1023 chips), navigation data bit (20460 chips), navigation data word (30 data bits), navigation subframe (10 data words), navigation frame (5 data subframes)
Output level	default –125 dBm, user-selectable in entire output level range of SMIQ
GPS settings	
Channels	1, simulation of one GPS satellite
C/A codes	1 to 37, Gold codes consisting of 1023 chips each, user-selectable
Chip rate	1.023 MHz selectable from 500 kHz to 1.5 MHz automatic tracking depending on Doppler shift and frequency
Navigation data	user-selectable bit patterns of up to 16 bit, user-definable data lists of any length, length only limited by memory capacity (SMIQB11, SMIQB12), simple generation of several navigation data lists of 37500 bits each, i.e. 25 frames of GPS navigation data, for example by means of free external PC software "AlmanacUploader", Application Note 1GP46
Navigation data rate	50 Hz, depending on selected chip rate (1 navigation data bit equals 20460 chips)

Digital standard TETRA with SMIQ and option SMIQ-K8

Function	SMIQ-K8 is a PC-based software package for generating a TETRA T1 test signal according to ETS300-392/ETS300-394; the T1 test signal is generated for the V+D (voice and data) test on MS and BS DUTs; it is designed for putting RF components into operation and supporting ETS300 394-1 tests; SMIQ-K8 generates all data sequences including all control sequences required to operate the signal generator. Prerequisites: – Modulation Coder SMIQB20 (or previous model SMIQB10) – Data Generator SMIQB11 – The BNC adapter (SMIQ-Z5) for the rear-panel SUB-D connector "PAR DATA" is recommended if the SMIQ is to be triggered externally – Firmware version of SMIQ higher than 3.8

General settings	
Frequency	user-selectable in entire frequency range of SMIQ; the TETRA frequency can be set by "frequency band", "main carrier number", "offset", "duplex spacing" and "reverse operation"
Channel coding	channel coding is performed for all channels; scrambling with base colour code, mobile country code and mobile network code can be set separately for each channel
Modulation	$\pi/4$ DQPSK (2 bits per symbol)
Baseband filter	TETRA filter
Symbol rate	18000 symbols/s
Output level	user-selectable in entire output level range of SMIQ
TETRA settings	
Channels	channel types 1 to 4, 15 and 17 for downlink and channel types 7 to 11, 16 and 18 for uplink; up to 3 hyperframes (duration ≈ 61.2 s each)
Burst type	TETRA frame (bit stream) generated according to burst type selected, i.e. control burst (CB), normal burst (NB) or synchronization burst (SB)
Channel types	AACH, BSCH, BNCH, TCH, STCH and SCH generated
Data	bit stream generated either from pseudo-random sequences (CCITT 0.153) or from user data
Interfaces	The SMIQ-K8 software calculates the appropriate TETRA T1 signal and transfers it to the SMIQ via the GPIB or RS232 interface; all the SMIQ settings that are needed are included

Digital standard 3GPP WCDMA TDD mode with SMIQ and options SMIQK13 and SMIQB60 (ARB)

Function	simulation of signals to time division duplex wideband CDMA standard according to version 3.4.0 of the 3GPP technical specifications TS 25.221, TS 25.223, available as SMIQK13 software option and option SMIQB60 (arbitrary waveform generator)
General settings	
Chip rate	
Standard	3.84 Mchip/s
Range	1 kchip/s to max. 40 Mchip/s
Mode	– downlink only: the base-station components of a cell are active – uplink only: the mobile-station components of a cell are active – downlink and uplink: both base-station and mobile-station components of a cell are active
Sequence length	entry in slots (0.667 ms each) or frames (10 ms each), max. length depending on oversampling 1 to 6 frames (for oversampling 2 in WinIQSIM™ (oversampling > 4 as a result of hardware oversampling in SMIQ))
Baseband filter	
Standard	root raised cosine filter, roll off=0.22
Other filters	same as in single-carrier system
Cells	simulation of up to 4 cells, each comprising 15 slots
Clipping level	Setting of clipping value relative to highest peak in percent; clipping takes place prior to baseband filtering; either scalar mode or vector mode can be selected; clipping reduces the crest factor; The range is 1% to 100%

Parameters for each cell	
State	OFF/ON
Frame configuration	The link direction (uplink or downlink) can be set independently for each of the 15 slots of the frame; all single- and multi-switching point configurations are possible
Scrambling code	0 to 127 scrambling code can be disabled for testing
Code group	automatic selection depending on scrambling code 0 to 31
Midamble allocation method	default common equal to scrambling code
Guard field power state	OFF (according to standard) ON
Parameters for each downlink slot	
State	OFF/ON
Slot mode	downlink dedicated: simulation of up to 16 DPCHs and max. 6 special channels
Burst type	1 and 2
SCH assoc. t_offset	automatic selection depending on scrambling code 0 to 31
SCH code allocation	cases 1 and 2 to TS 25.223
Page indicator length	2, 4, 8
Parameters for each uplink slot	
Slot mode	uplink dedicated: simulation of up to 16 DPCHs and 1 special channel PRACH: simulation of one physical random access channel
TPC pattern readout mode	application mode for TPC pattern continuous, single + all 0, single + all 1, single + alt. 01, single + alt. 10
Burst type	1, 2 and 3
Parameters in uplink PRACH mode	
Burst type	3
Start frame	selection of first frame in which PRACH is sent 0 to 10
PRACH length	length of PRACH message part 1 to 10 frames
User	index of user to which PRACH is assigned
Midamble and midamble shift	display of midamble used and of midamble time shift, depending on midamble allocation method and user
Spreading factor	8 and 16
Spreading code	spreading code of channel, range depending on spreading factor 1 to max. 16
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as in single-carrier system, another PRBS source with differing start values for different code channels

Physical channels	
Downlink	Primary Common Control Physical Channel (P-CCPCH) Secondary Common Control Physical Channel (S-CCPCH) Primary Sync Channel (P-SCH) Secondary Sync Channel (S-SCH) Physical Downlink Shared Channel (PDSCH) Page Indicator Channel (PICH) Dedicated Physical Channel (DPCH)
Uplink	Physical Random Access Channel (PRACH) Physical Uplink Shared Channel (PUSCH) Dedicated Physical Channel (DPCH)
Parameters for each code channel, independently selectable	
State	OFF/ON
User	1 to 16 with burst types 1 and 3 1 to 6 with burst type 2
Spreading factor	depending on channel type and link direction 1, 2, 4, 8, 16
Spreading code	depending on channel type and spreading factor 1 to max. 16
Midamble and midamble shift	display of midamble used and of midamble time shift, depending on midamble allocation method and user
TFCI/TPC combination	combination of TFCI and TPC fields, TPC in uplink only, uplink: TFCI 0 | TPC 0, TFCI 0 | TPC 2; TFCI 4 | TPC 2, TFCI 8 | TPC 2, TFCI 16 | TPC 2, TFCI 32 | TPC 2 downlink: TFCI 0, TFCI 4, TFCI 8, TFCI 16, TFCI 32
TFCI	transport format combination indicator 0 to 1023
TPC pattern	bit pattern (max. length 16 bits) as a data source for the TPC field of the channel, in uplink only
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as in single-carrier system, another PRBS source with differing start values for different code channels
Assistant functions to facilitate operation	
Copy cell	adopting the configuration of a cell for another cell to define multicell scenarios parameters: source and destination of copying
Resolve domain conflicts	elimination of code channel overlapping in code domain occurring in a slot (domain conflicts)
Graphic displays	
Domain conflicts	display of domain conflicts (overlapping of code channels in code domain) in channel table lines concerned; occupied code domain of code channels involved in conflict can be displayed upon request
Code domain	graphic display of code domain occupied by active slot; domain sectors in which conflicts occur are highlighted; code channel distribution in code domain and channel powers are displayed
Channel graph	display of all active channels of a slot versus the channel table index, the powers of the individual code channels being indicated
CCDF	The complementary cumulative distribution function of the current signal is displayed; this function gives the probability of the magnitudes of the complex I/Q samples exceeding a predefined threshold; together with the current CCDF, the CCDFs of any number of 3GPP TDD signals generated last can be displayed to observe the effect of parameter changes; the crest factor of the signal can be seen in the CCDF
Constellation diagram	display of constellation diagram versus IQ samples of current 3GPP TDD signal; this diagram allows qualitative assessment of channel configuration, channel power ratios, and effect of TDD system parameters

Digital standard TD-SCDMA with SMIQ and options SMIQK14 and SMIQB60 (ARB)

Function	simulation of signals to time division synchronous CDMA standard of China Wireless Telecommunication Standard Group (CWTS), available as SMIQK14 software option and option SMIQB60 (arbitrary waveform generator)
General settings	
Chip rate Standard Range	 1.28 Mchip/s 1 kchip/s to max. 40 Mchip/s
Mode	– downlink only: the base-station components of a cell are active – uplink only: the mobile-station components of a cell are active – downlink and uplink: both base-station and mobile-station components of a cell are active
Sequence length	entry in slots (5 ms each), max. length depending on oversampling 1 to 40 frames (for oversampling 2 in WinIQSIM™ (oversampling > 4 as a result of hardware oversampling in SMIQ))
Baseband filter Standard Other filters	 root raised cosine filter, roll off = 0.22 same as in single-carrier system
Cells	simulation of up to 4 cells, each comprising 7 traffic slots and 3 special slots
Clipping level	Setting of clipping value relative to highest peak in percent; clipping takes place prior to baseband filtering; either scalar mode or vector modes can be selected; clipping reduces the crest factor; the range is 1% to 100%
Parameters for each cell	
State	OFF/ON
Frame structure	total of 7 traffic slots, slot 0 always reserved for downlink, slot 1 to switching point reserved for uplink, other slots reserved for downlink special slots between slots 0 and 1: downlink pilot slot (DwPTS), guard period (GP) and uplink pilot slot (UpPTS)
Scrambling code	0 to 127 scrambling code can be disabled for testing
SYNC code	automatic selection depending on scrambling code 0 to 31
Switching point	time of switching between uplink and downlink slots 1 to 6
Layer 1 control fields	can be enabled and disabled to simulate burst types 1 and 2
DwPTS power	–60 dB to 0 dB
Parameters for each downlink slot	
State	OFF/ON
Slot mode	downlink dedicated: simulation of up to 16 DPCHs and max. 5 special channels
TPC pattern readout mode	application mode for TPC pattern: continuous, single + hold 01, single + hold 10, single + all up, single + all down
Sync shift repetition M	number of frames to which the sync shift bits are distributed 1 to 500
Stealing flag	value of the two stealing bits 0 to 3

Parameters for each uplink slot	
Slot mode	uplink dedicated: simulation of up to 16 DPCHs PRACH: simulation of one physical random access channel
TPC pattern readout mode	application mode for TPC pattern: continuous, single + hold 01, single + hold 10, single + all up, single + all down
Sync shift repetition M	number of frames to which the sync shift bits are distributed 1 to 500
Stealing flag	value of the two stealing bits 0 to 3
Parameters in uplink PRACH mode	
SYNC 1	SYNC 1 code 0 to 7
UpPTS start frame	selection of first frame in which UpPTS is sent 1 to 6
UpPTS repetition	number of UpPTS repetitions 1 to 10
PRACH length	length of PRACH message part 1 to 10 frames
Gross data rate	17.6 kbps, 35.2 kbps
Spreading code	depending on gross data rate 0 to max. 15
Sync shift pattern	bit pattern (max. length 16 bits) as a data source for the sync shift field of the channel
TPC pattern	bit pattern (max. length 16 bits) as a data source for the TPC field of the channel
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as in single-carrier system, another PRBS source with differing start values for different code channels
Physical channels	
Downlink	Primary Common Control Physical Channel (P-CCPCH) Secondary Common Control Physical Channel (S-CCPCH) Physical Forward Access Channel (F-FACH) Downlink Pilot Time Slot (DwPTS) Dedicated Physical Channel (DPCH)
Uplink	Physical Random Access Channel (PRACH) Uplink Pilot Time Slot (UpPTS) Dedicated Physical Channel (DPCH)
Parameters for each code channel, independently selectable	
State	OFF/ON
Gross data rate	depending on channel type 17.6 kbps, 35.2 kbps, 70.4 kbps, 140.8 kbps, 281.6 kbps (spreading factors 1, 2, 4, 8, 16)
Spreading code	depending on channel type and gross data rate 0 to max. 15
Midamble shift	time shift of midamble in chips: 0 to 120, step width 8 chips
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as in single-carrier system, another PRBS source with differing start values for different code channels
Sync shift pattern	bit pattern (max. length 16 bits) as a data source for the sync shift field of the channel
TPC pattern	bit pattern (max. length 16 bits) as a data source for the TPC field of the channel

Assistant functions to facilitate operation	
Predefined settings	generation of complex signal scenarios with parameterizable default settings selectable parameters: – use of special channels (P-CCPCH) – number and gross data rate of data channels – crest factor: minimal/average/worst
Copy cell	adopting the configuration of a cell for another cell to define multicell scenarios parameters: source and destination of copying
Resolve domain conflicts	elimination of code channel overlapping in code domain occurring in a slot (domain conflicts)
Graphic displays	
Domain conflicts	display of domain conflicts (overlapping of code channels in code domain) in channel table lines concerned; occupied code domain of code channels involved in conflict can be displayed upon request
Code domain	graphic display of code domain occupied by active slot; domain sectors in which conflicts occur are highlighted; code channel distribution in code domain and channel powers are displayed
Channel graph	display of all active channels of a slot versus the channel table index, the powers of the individual code channels being indicated
CCDF	The complementary cumulative distribution function of the current signal is displayed; this function gives the probability of the magnitudes of the complex I/Q samples exceeding a predefined threshold; together with the current CCDF, the CCDFs of any number of TD-SCDMA signals generated last can be displayed to observe the effect of parameter changes; the crest factor of the signal can be seen in the CCDF
Constellation diagram	display of constellation diagram versus IQ samples of current TD-SCDMA signal; this diagram allows qualitative assessment of channel configuration, channel power ratios, and effect of TD-SCDMA system parameters

Ordering information

Digital Standard GPS	SMIQB51	1105.1683.02
TETRA T1 Simulator	SMIQ-K8	1136.4290.02
Digital Standard 3GPP (TDD) (for option SMIQB60)	SMIQK13	1105.1231.02
Digital Standard TD-SCDMA (for option SMIQB60)	SMIQK14	1105.1383.02

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www.rohde-schwarz.com · Customer Support: Tel. +49 1805124242, Fax +49 89 4129-13777, E-mail: CustomerSupport@rohde-schwarz.com

PD 1 1 Digital Standards GPS, TETRA, 3GPP TDD, TD-SCDMA · Trade names are trademarks of the owners · Subject to chang without tolerances: typical values
Printed in Germany 08/0

Simulation Software R&S WinIQSIM™

... ideal for the generation of digitally modulated signals

- Calculation of digitally modulated I/Q and IF signals
- For driving the I/Q Modulation Generator R&S AMIQ and the internal arbitrary waveform generator of the R&S SMIQ (R&S SMIQB60)
- Single-carrier, multicarrier, multicarrier mixed signals and CDMA signals
- WCDMA 3GPP FDD mode including data sets for the test models to 3GPP
- WCDMA 3GPP TDD mode optional (R&S AMIQK13/R&S SMIQK13)
- TD-SCDMA optional (R&S AMIQK14/R&S SMIQK14)
- IS-95 CDMA optional (R&S AMIQK11/R&S SMIQK11)
- CDMA2000 optional (R&S AMIQK12/R&S SMIQK12)
- Versatile data editor
- Superposition/simulation of impairments
- Graphical display
- Can be enhanced by import interface for additional software
- 1xEV-DO optional (R&S AMIQK17/R&S SMIQK17)
- IEEE 802.11b optional (R&S AMIQK16/R&S SMIQK16)
- IEEE 802.11a optional (R&S AMIQK18/R&S SMIQK18)

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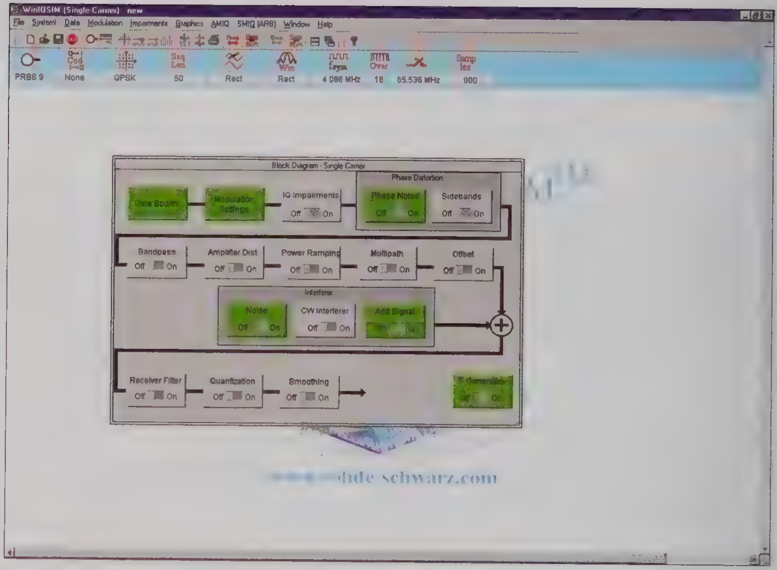
It has never been so easy

R&S WinIQSIM™ was especially developed for the generation of digitally modulated signals. Complex signals can thus easily be generated. The graphical user interface allows intuitive operation, supported by context-sensitive help. The convenient way of creating any TDMA frame configurations with the aid of a data editor, and the generation of multicarrier signals as well as of complex WCDMA signals make R&S WinIQSIM™ suitable for a wide range of applications. Moreover, additive impairments can be superimposed on a signal.

The signals generated with the aid of the R&S WinIQSIM™ software can be output by the Arbitrary Waveform Generator R&S AMIQ and the integrated solution in the R&S SMIQ (option R&S SMIQB60). R&S WinIQSIM™ is provided with these two arbitrary waveform generators free of charge.


WinIQSIM (Single Carrier) - new
File System Data Modulation Impairments Graphics AMIQ SMIQ (ARB) Window Help
PRBS 9
None
QPSK
50
Rect
Rect
4.096 MHz
16
65.536 MHz
800
Block Diagram - Single Carrier
Data Source
Modulation Settings
IQ Impairments
Phase Distortion
Phase Noise
Sidebands
Bandpass
Amplifier Dist
Power Ramping
Multipath
Offset
Interferer
Noise
CW Interferer
Add Signal
Receiver Filter
Quantization
Smoothing
Generation
Off
On

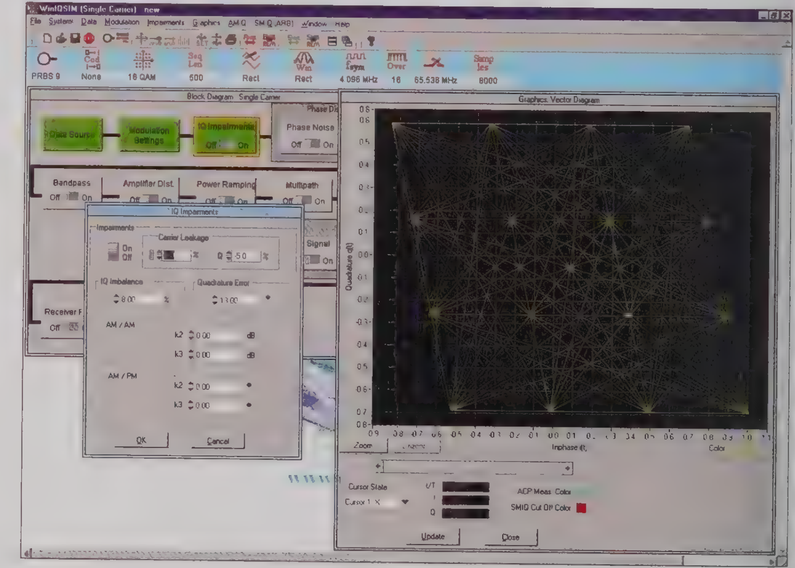
1

Clearly structured menus in the form of a signal flow chart

Install it and go ahead (1)

In developing R&S WinIQSIM™, great importance was attached to user-friendly operation. The main parameters of a signal, for example, are indicated in a status line. The context-sensitive online help enables handling of even complex functions without consulting the manual.

The program always starts with the settings of the previous session, thus ensuring easy continuation of work.


WinIQSIM (Single Carrier) - new
File System Data Modulation Impairments Graphics AMIQ SMIQ (ARB) Window Help
PRBS 9
None
16 QAM
500
Rect
Rect
4.096 MHz
16
65.536 MHz
8000
Block Diagram - Single Carrier
Data Source
Modulation Settings
IQ Impairments
Phase Noise
Bandpass
Amplifier Dist
Power Ramping
Multipath
Receiver F
IQ Impairments
Impairments
On
Off
Carrier Leakage
IQ Imbalance
Quadrature Error
AM / AM
AM / PM
k2
k3
dB
OK
Cancel
Graphics: Vector Diagram
Quadrature (I/Q)
Inphase (I)
Zoom
Cursor State
Cursor 1
ACP Meas Color
SMIQ Cut Off Color
Update
Close

2

Simulation of I/Q impairments, here for 16QAM

Single carrier (2, 3)

Modulation parameters such as type of modulation, coding, symbol rate, filter and window functions as well as oversampling can be set for a single-carrier signal.

Impairments which may be caused by a real I/Q modulator are also taken into consideration. It is, for example, easy to simulate I/Q imbalance, carrier leakage or quadrature error. The simulation of VCO noise or phase and frequency offsets of an oscillator are some of the very special features of R&S WinIQSIM™. These and many other settings enable the user to take real impairments into account early in the development phase of components and modules.

WCDMA, CDMA (4 to 11b)

The comprehensive functionality of R&S WinIQSIM™ allows various WCDMA systems to be realized: for example, both modes of the 3GPP standard, FDD (frequency division duplex) and TDD (time division duplex), are implemented. Signals can likewise be generated in accordance with the TD-SCDMA standard. And the North-American standards CDMA2000 and IS-95 are also included in R&S WinIQSIM™.

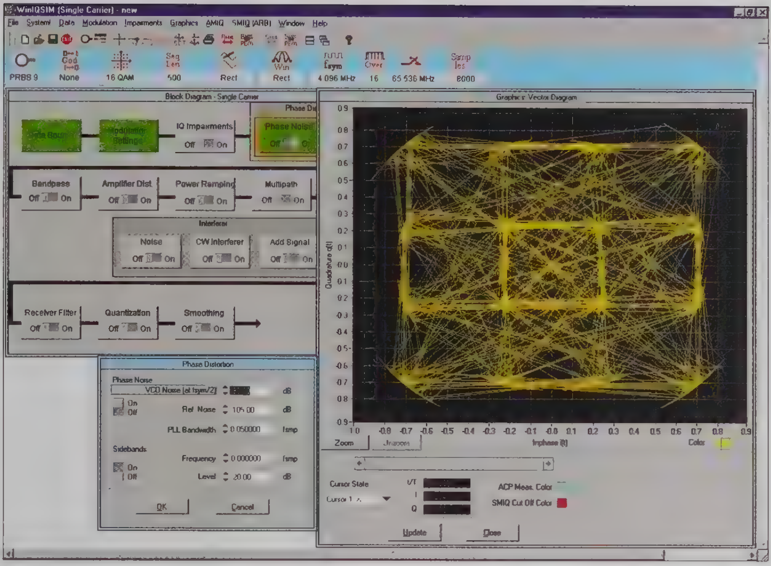
All data and control channels defined by the relevant standard are supported. These include the synchronization channels such as primary and secondary common control channel (P-CCPCH and S-CCPCH) or synchronization channels (P-SCH and S-PCH).

For the WCDMA standards as well as for the IS-95 and CDMA2000 standards, the orthogonal codes, data sources (PRBS, pattern or user-programmable sequences), and the power of the individual code channels can be varied, so that a large variety of signals can be generated.

R&S WinIQSIM™ provides various display modes for visualizing the settings. The code domain display shows the distribution and occupancy of the individual channels in the code domain. Any code domain conflicts can be automatically resolved by a click. The channel graph includes all active channels. Synchronization and special channels are shown in red; data channels in green.

For statistical evaluation of the CDMA signal characteristics, R&S WinIQSIM™ allows the complementary cumulative distribution function (CCDF) to be calculated (including the crest factor) and graphically displayed. In addition, the resulting adjacent-channel power can be calculated.

3


WinIQSIM (Single Carrier) - new
File System Data Modulation Impairments Graphics AMIQ SMIQ (ARB) Window Help
Block Diagram - Single Carrier
Data Source
Modulation Settings
IQ Impairments
Phase Noise
Bandpass
Amplifier Dist
Power Ramping
Multipath
Interferer
Noise
CW Interferer
Add Signal
Receiver Filter
Quantization
Smoothing
Off On
Graphics: Vector Diagram
Quadrature (Q)
Inphase (I)
Zoom
Color
Cursor State
Cursor 1
ACP Meas. Color
SMIQ Cut Off Color
Update
Close

PRBS 9 · None · 16 QAM · Seq Len 500 · Rect · Win Rect · fsym 4 096 MHz · Over 16 · 65 536 MHz · Samples 8000

Phase Distortion

Phase Noise

VCO Noise (at fsym/2) dB

Ref. Noise 105.00 dB

PLL Bandwidth 0.050000 fsmp

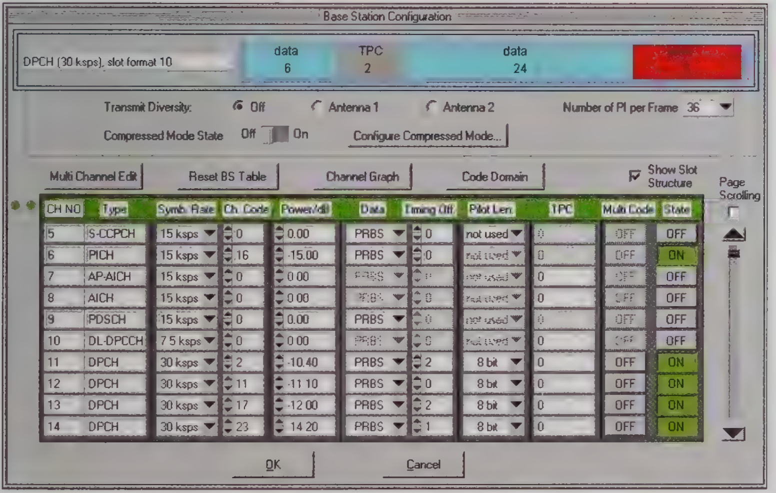
Sidebands

Frequency 0.000000 fsmp

Level 20.00 dB

Simulation of defined phase noise on a 16QAM-modulated signal

4


Base Station Configuration
data
TPC
Multi Channel Edit
Reset BS Table
Channel Graph
Code Domain
Page Scrolling
OK
Cancel

DPCH (30 ksps), slot format 10 — data 6 · TPC 2 · data 24

Transmit Diversity: ● Off ○ Antenna 1 ○ Antenna 2

Number of PI per Frame 36

Compressed Mode State Off On

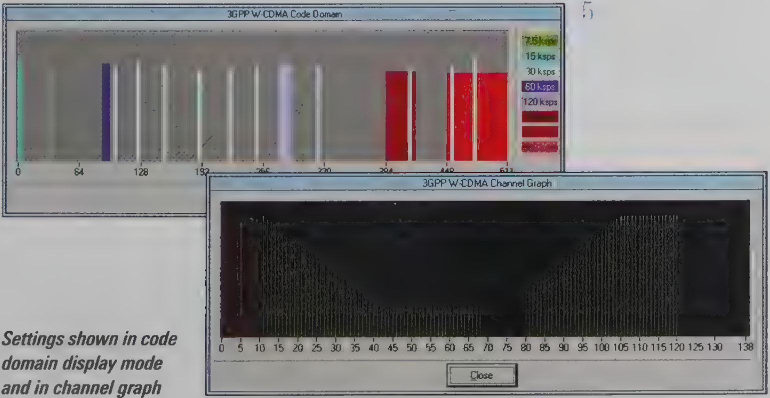
Configure Compressed Mode...

☑ Show Slot Structure

CH NO	Type	Symb. Rate	Ch. Code	Power/dB	Data	Timing Off.	Pilot Len.	TPC	Multi Code	State
5	S-CCPCH	15 ksps	0	0.00	PRBS	0	not used	0	OFF	OFF
6	PICH	15 ksps	16	-15.00	PRBS	0	not used	0	OFF	ON
7	AP-AICH	15 ksps	0	0.00	PRBS	0	not used	0	OFF	OFF
8	AICH	15 ksps	0	0.00	PRBS	0	not used	0	OFF	OFF
9	PDSCH	15 ksps	0	0.00	PRBS	0	not used	0	OFF	OFF
10	DL-DPCCH	7.5 ksps	0	0.00	PRBS	0	not used	0	OFF	OFF
11	DPCH	30 ksps	2	-10.40	PRBS	2	8 bit	0	OFF	ON
12	DPCH	30 ksps	11	-11.10	PRBS	0	8 bit	0	OFF	ON
13	DPCH	30 ksps	17	-12.00	PRBS	2	8 bit	0	OFF	ON
14	DPCH	30 ksps	23	-14.20	PRBS	1	8 bit	0	OFF	ON

Definition of a code channel scenario for WCDMA 3GPP FDD mode

5


3GPP W-CDMA Code Domain
7.5 ksps
15 ksps
30 ksps
60 ksps
120 ksps
0
64
128
192
256
320
384
448
512
3GPP W-CDMA Channel Graph
0
5
10
15
20
25
30
35
40
45
50
55
60
65
70
75
80
85
90
95
100
105
110
115
120
125
130
138
Close

Settings shown in code domain display mode and in channel graph

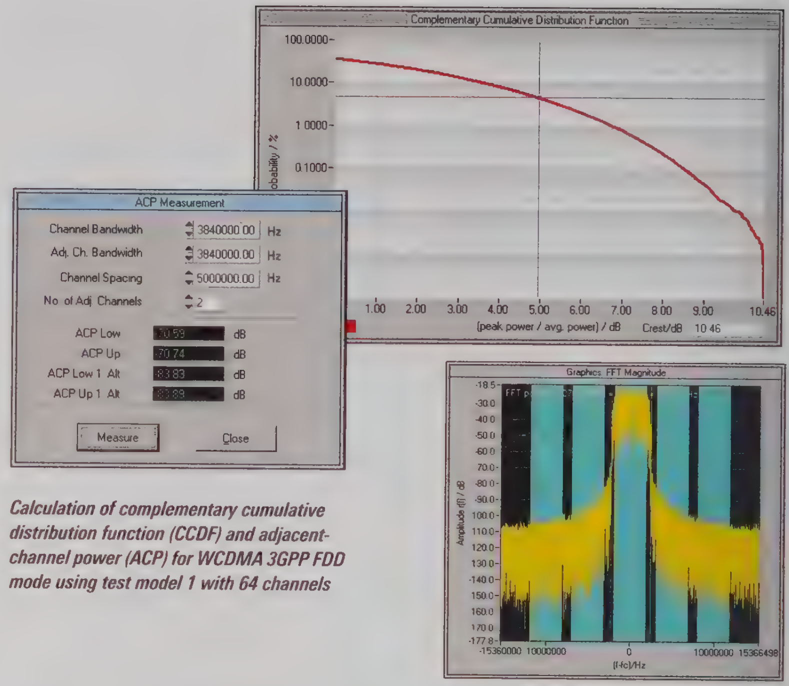
Depending on the selected symbol rate, up to 512 code channels with a chip rate of 3.84 Mcps are generated in the FDD mode for testing base stations under realistic as well as under worst-case conditions.

For this purpose, signals are generated which contain up to four mobile or base stations with different scrambling codes. R&S WinIQSIM™ also allows the power of the individual data channels to be varied via TPC (transmit power control), which is used to control the power of the different channels in line with the 3GPP standard.

R&S WinIQSIM™ supports the antenna diversity schemes specified by the 3GPP standard. Either the specification for antenna 1 or that for antenna 2 can be used so that the signal will be generated in line with the 3GPP specification.

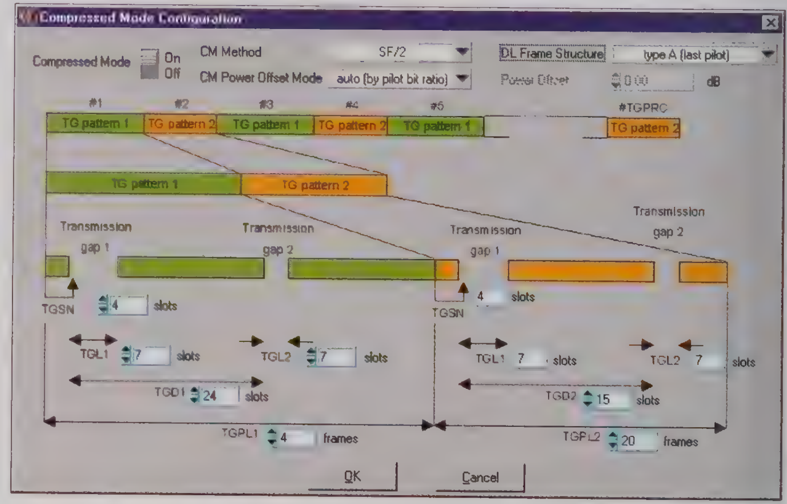
In the uplink, the mobile station can operate in one of the three permitted modes: PRACH only (physical random access channel), PCPCH only (physical common packet channel) and DPCCH + DPDCH (dedicated physical control channel and dedicated physical data channel).

The versatile settings enable even very specific tests to be carried out. For 3GPP FDD, for example, the compressed mode is supported, which allows handover of a mobile station from a 3GPP FDD base station to a base station (3GPP FDD, 3GPP TDD or GSM) with a different frequency. For this purpose, transmission and reception of the 3GPP FDD signal has to be interrupted for a certain time. In this transmission gap, the mobile station can change to the frequency of the potential new base station in order to read, for example, the system information or the receive level of this base station. To allow the same data quantity to be transmitted in the remaining shorter time, data is compressed. R&S WinIQSIM™ allows extensive user-defined settings for all physical layer compressed mode parameters.


Complementary Cumulative Distribution Function
100.0000
10.0000
1.0000
0.1000
obability / %
1.00
2.00
3.00
4.00
5.00
6.00
7.00
8.00
9.00
10.46
(peak power / avg. power) / dB
Crest/dB 10.46
ACP Measurement
Measure
Close
Graphics FFT Magnitude
FFT
Amplitude (f) / dB
-18.5
-30.0
-40.0
-50.0
-60.0
-70.0
-80.0
-90.0
-100.0
-110.0
-120.0
-130.0
-140.0
-150.0
-160.0
-170.0
-177.8
-15360000
10000000
0
10000000
15366498
(f-fc)/Hz

Channel Bandwidth 3840000.00 Hz
Adj. Ch. Bandwidth 3840000.00 Hz
Channel Spacing 5000000.00 Hz
No. of Adj. Channels 2
ACP Low -70.59 dB
ACP Up -70.74 dB
ACP Low 1. Alt -83.83 dB
ACP Up 1. Alt -83.89 dB

Calculation of complementary cumulative distribution function (CCDF) and adjacent-channel power (ACP) for WCDMA 3GPP FDD mode using test model 1 with 64 channels


Compressed Mode Configuration
On
Off
#1
#2
#3
#4
#5
#TGPRC
TG pattern 1
TG pattern 2
TG pattern 1
TG pattern 2
TG pattern 1
TG pattern 2
TG pattern 1
TG pattern 2
Transmission gap 1
Transmission gap 2
Transmission gap 1
Transmission gap 2
OK
Cancel

Compressed Mode: On
CM Method: SF/2
CM Power Offset Mode: auto (by pilot bit ratio)
DL Frame Structure: type A (last pilot)
Power Offset: 0.00 dB
TGSN 4 slots
TGL1 7 slots
TGL2 7 slots
TGD1 24 slots
TGPL1 4 frames
TGSN 4 slots
TGL1 7 slots
TGL2 7 slots
TGD2 15 slots
TGPL2 20 frames

Editing of compressed mode

In the TDD mode of the 3GPP standard, the link directions of the individual slots can be conveniently selected. The user can define whether each timeslot is to act as an uplink or a downlink.

Up to four cells with 15 timeslots each can be generated; different spreading factors are permitted for each channel. For the data channels (DPCH), all spreading factors permitted by the standard are available.

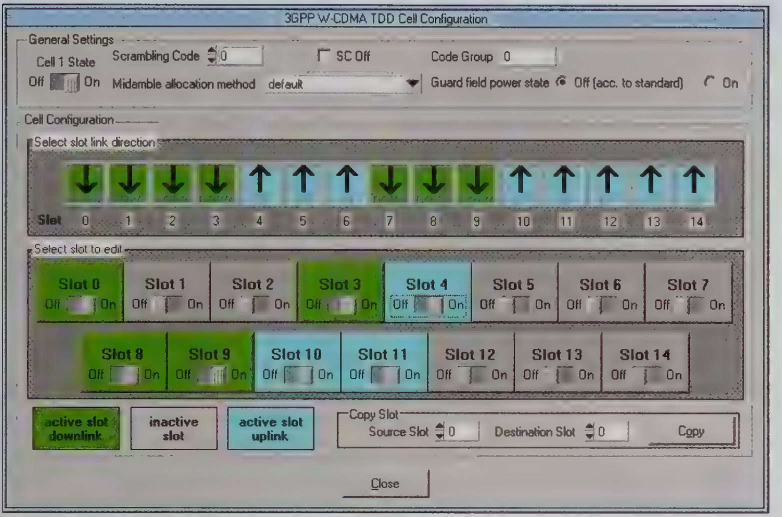
In the TDD mode, it is very important to calculate the CCDF not only for the total signal, but also for a specific timeslot. Since the system is made up of timeslots that can be switched on or off independently of one another, only the CCDF of an active slot is often of interest. This can then be used, for example, to optimally design the output amplifier of a mobile phone, since the latter is active in one slot only.

TD-SCDMA is basically similar to the 3GPP TDD mode. The two modes differ in the chip rate, which is 1.28 Mcps for TD-SCDMA instead of 3.84 Mcps in the TDD mode. According to the TD-SCDMA standard, the link direction of the individual slots cannot be selected by the user as conveniently as in the 3GPP TDD mode, a fact that has been taken into account by R&S WinIQSIM™.

With TD-SCDMA, special timeslots are provided for the uplink and the downlink. To carry out certain tests on mobile stations, only the downlink pilot may be active, however. R&S WinIQSIM™ considers this fact by generating the downlink signals only.

With IS-95, the previous US standard for CDMA technology is included in R&S WinIQSIM™. With CDMA2000, the following generation of the US standard has also been implemented. R&S WinIQSIM™ supports the modes 1X with 1.2288 Mcps and 3X with 3.6864 Mcps; the 3X mode can optionally be generated according to the direct-spread or multicarrier method. Up to four mobile or base stations can be simulated simultaneously. The same applies to the 1xEV-DO standard (see Fig. 11a, page 6), which represents a further development of the CDMA2000 1x mode and is also supported by R&S WinIQSIM™. 1xEV-DO stands for CDMA2000 1x Evolution Data Only. This standard enables packet-oriented data transfer at a rate of up to 2.4 Mbps in a 1.25 MHz CDMA2000 1x channel.

The open software concept of R&S WinIQSIM™ allows continuous adaptation to the rapid development of third-generation mobile radio standards. The user is thus always up to the state of the art.

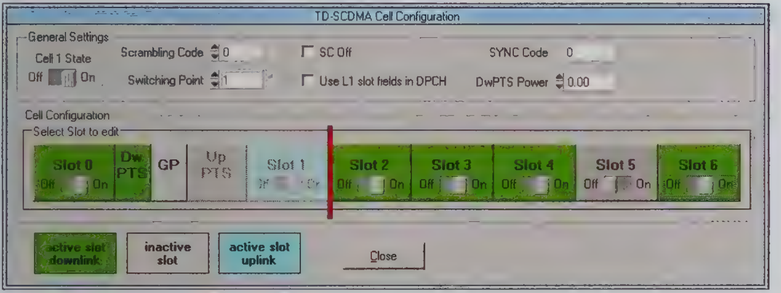

3GPP W-CDMA TDD Cell Configuration

General Settings
Cell 1 State: Off / On
Scrambling Code: 0
☐ SC Off
Code Group: 0
Midamble allocation method: default
Guard field power state: ● Off (acc. to standard) ○ On

Cell Configuration
Select slot link direction
Slot
0
1
2
3
4
5
6
7
8
9
10
11
12
13
14
Select slot to edit
Slot 0
Slot 1
Slot 2
Slot 3
Slot 4
Slot 5
Slot 6
Slot 7
Slot 8
Slot 9
Slot 10
Slot 11
Slot 12
Slot 13
Slot 14
Off
On
active slot downlink
inactive slot
active slot uplink
Copy
Copy Slot
Source Slot: 0
Destination Slot: 0
Close

8

User-defined settings of the timeslots for 3GPP TDD mode


TD-SCDMA Cell Configuration

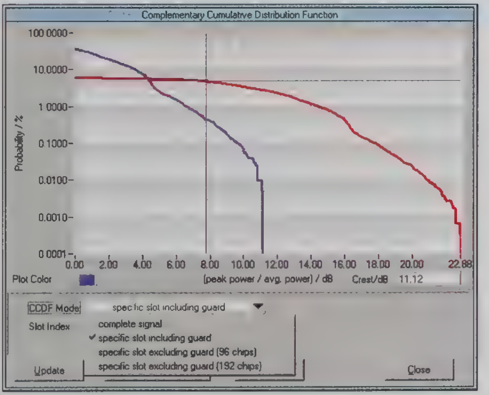
General Settings
Cell 1 State: Off / On
Scrambling Code: 0
☐ SC Off
SYNC Code: 0
Switching Point: 1
☐ Use L1 slot fields in DPCH
DwPTS Power: 0.00

Cell Configuration
Select Slot to edit
Slot 0
Dw PTS
GP
Up PTS
Slot 1
Slot 2
Slot 3
Slot 4
Slot 5
Slot 6
Off
On
active slot downlink
inactive slot
active slot uplink
Close

9

Configuration of TD-SCDMA signal

CCDF of a TDD signal calculated for the complete signal (red) and for an active timeslot (blue)



Crest/dB: 11.12
CCDF Mode: specific slot including guard

10

Due to the superposition of many code channels, high power peaks occur in all CDMA and WCDMA signals, which is reflected in a high crest factor. This means that a wide dynamic range is required for the transmission system with all its components such as power amplifiers. Since extreme signal peaks are relatively rare, as can be seen from the CCDF, clipping of the signal peaks can be performed without essentially degrading the bit error rate. Clipping prior to baseband filtering does not cause a change in the frequency spectrum of the signal, either.

The clipping level can be set between 1% and 100% relative to the maximum level peak. In the TDD mode of the 3GPP standard, and also with TD-SCDMA, scalar clipping is available in addition to conventional vector clipping.

W-LAN (12a, 12b)

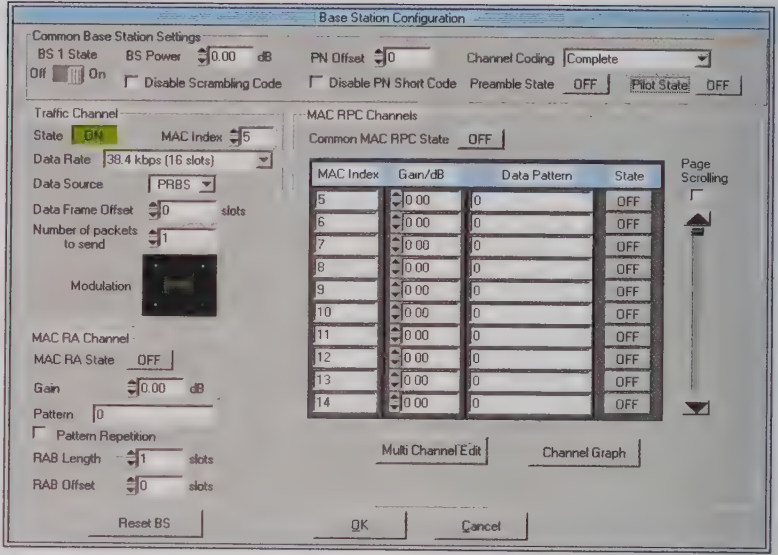
In addition to the comprehensive functionality for the mobile radio standards, R&S WinIQSIM™ also covers the Wireless LAN standards IEEE 802.11b and IEEE 802.11a.

The four data rates used by 802.11b (1 Mbps, 2 Mbps, 5.5 Mbps and 11 Mbps) as well as all the possible modulation modes (DBPSK, DQPSK, CCK and PBCC) are fully supported by R&S WinIQSIM™. A direct sequence spread spectrum method is used for radio transmission. Irrespective of the data rate, a chip rate of 11 Mcps is used with this method.

R&S WinIQSIM™ is also capable of generating signals to IEEE 802.11a. It supports all bit rates (6 Mbps to 54 Mbps) defined by this standard as well as the specified modulation mode (OFDM) including coding.

Since data is transferred in packets with IEEE 802.11b and IEEE 802.11a, R&S WinIQSIM™ enables the number of packets, the packet length and the idle time between the packets to be entered. For test purposes, R&S WinIQSIM™ can additionally simulate a continuous data stream without packet structure (unframed mode).

Other OFDM standards (e.g. HIPERLAN/2) are covered by the additional software program R&S WinIQOFDM*).


Base Station Configuration
Common Base Station Settings
Off On
Traffic Channel
MAC RPC Channels
MAC RA Channel
Modulation
Page Scrolling
Multi Channel Edit
Channel Graph
Reset BS
OK
Cancel

BS 1 State: On
BS Power 0.00 dB
PN Offset 0
Channel Coding: Complete
☐ Disable Scrambling Code
☐ Disable PN Short Code
Preamble State OFF
Pilot State OFF

State ON
MAC Index 5
Data Rate 38.4 kbps (16 slots)
Data Source PRBS
Data Frame Offset 0 slots
Number of packets to send 1

Common MAC RPC State OFF

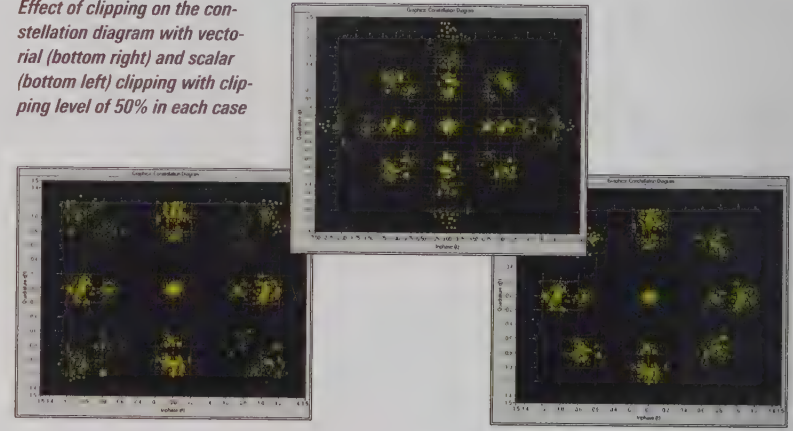
MAC Index	Gain/dB	Data Pattern	State
5	0.00	0	OFF
6	0.00	0	OFF
7	0.00	0	OFF
8	0.00	0	OFF
9	0.00	0	OFF
10	0.00	0	OFF
11	0.00	0	OFF
12	0.00	0	OFF
13	0.00	0	OFF
14	0.00	0	OFF

MAC RA State OFF
Gain 0.00 dB
Pattern 0
☐ Pattern Repetition
RAB Length 1 slots
RAB Offset 0 slots

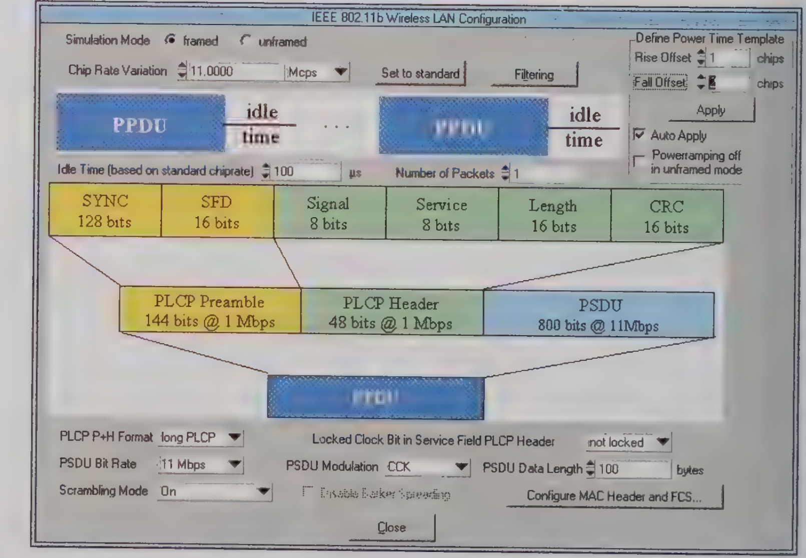
Configuration of a 1xEV-DO base station

11a

Effect of clipping on the constellation diagram with vectorial (bottom right) and scalar (bottom left) clipping with clipping level of 50% in each case



11b


IEEE 802.11b Wireless LAN Configuration
Define Power Time Template
Set to standard
Filtering
Apply
PPDU
idle time
. . .
SYNC
128 bits
SFD
16 bits
Signal
8 bits
Service
8 bits
Length
16 bits
CRC
16 bits
PLCP Preamble
144 bits @ 1 Mbps
PLCP Header
48 bits @ 1 Mbps
PSDU
800 bits @ 11Mbps
Configure MAC Header and FCS...
Close

Simulation Mode ● framed ○ unframed
Chip Rate Variation 11.0000 Mcps
Rise Offset 1 chips
Fall Offset chips
☑ Auto Apply
☐ Powerramping off in unframed mode
Idle Time (based on standard chiprate) 100 µs
Number of Packets 1
PLCP P+H Format long PLCP
Locked Clock Bit in Service Field PLCP Header not locked
PSDU Bit Rate 11 Mbps
PSDU Modulation CCK
PSDU Data Length 100 bytes
Scrambling Mode On
☐ Disable Barker Spreading

Operating menu for Wireless LAN standard IEEE 802.11b

12a

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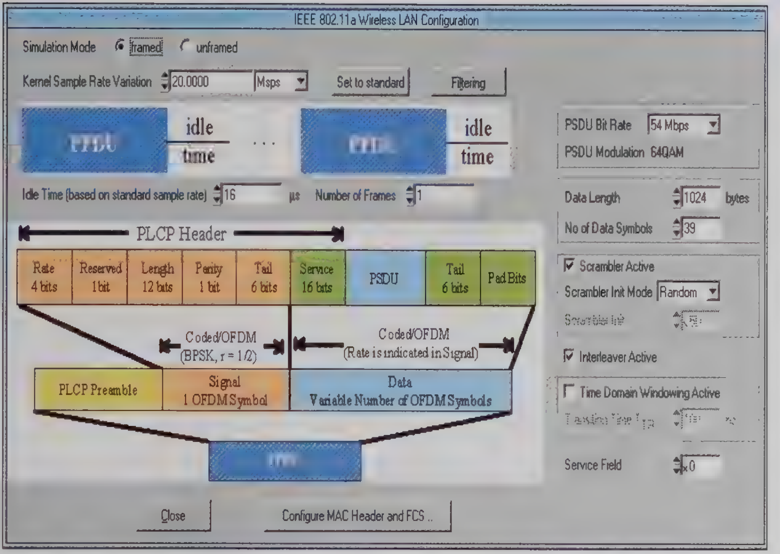
*) Available at www.rohde-schwarz.com

Data editor (13, 14)

Another special feature of R&S WinIQSIM™ is the data editor for convenient generation of TDMA frame structures, which is especially designed for the single-carrier mode. R&S WinIQSIM™ already provides preconfigured files for the main TDMA standards such as GSM, GSM/EDGE, DECT, PDC and NADC. A choice of different burst types with the associated data structure is available for the individual systems. Frame and timeslot configuration conform to the relevant standard. Basic configurations can easily be modified, stored and used again in subsequent tests.

12b


IEEE 802.11a Wireless LAN Configuration
Set to standard
Filtering
PPDU
idle time
PLCP Header
Rate 4 bits
Reserved 1 bit
Length 12 bits
Parity 1 bit
Tail 6 bits
Service 16 bits
PSDU
Tail 6 bits
Pad Bits
Coded/OFDM (BPSK, r = 1/2)
Coded/OFDM (Rate is indicated in Signal)
PLCP Preamble
Signal 1 OFDM Symbol
Data Variable Number of OFDM Symbols
PPDU
Close
Configure MAC Header and FCS ..

Simulation Mode ● framed ○ unframed

Kernel Sample Rate Variation 20.0000 Msps

Idle Time (based on standard sample rate) 16 µs

Number of Frames 1

PSDU Bit Rate 54 Mbps

PSDU Modulation 64QAM

Data Length 1024 bytes

No of Data Symbols 39

☑ Scrambler Active

Scrambler Init Mode Random

☑ Interleaver Active

☐ Time Domain Windowing Active

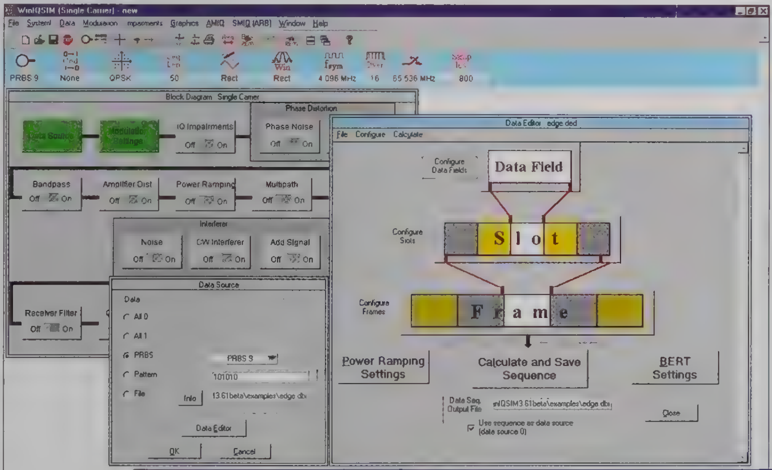
Service Field x0

Operating menu for Wireless LAN standard IEEE 802.11a

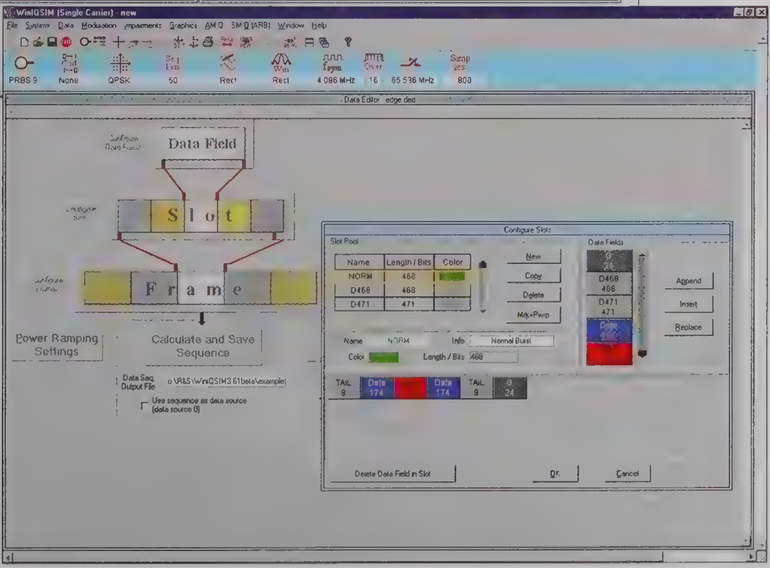
The data editor provides users involved in defining or developing new TDMA standards with an almost infinite number of possibilities. The structure of a TDMA signal with its basic elements (data fields of a burst) can be completely defined and successively configured into bursts and frames. In this way, it is possible to design an individual standard. In addition to the graphical representation of the data structures, power ramping can also be defined at the data level.

Main menu of data editor

13


WinIQSIM (Single Carrier) - new
File System Data Modulation Impairments Graphics AMIQ SMIQ (ARB) Window Help
PRBS 9
None
QPSK
50
Rect
Rect
4.096 MHz
16
65.536 MHz
800
Block Diagram Single Carrier
Data Source
Modulation Settings
IQ Impairments
Phase Distortion
Phase Noise
Bandpass
Amplifier Dist
Power Ramping
Multipath
Interferer
Noise
CW Interferer
Add Signal
Receiver Filter
Off On
Data Source
Data
○ All 0
○ All 1
● PRBS
○ Pattern
○ File
PRBS 9
101010
Info
Data Editor
OK
Cancel
Data Editor edge.ded
File Configure Calculate
Configure Data Fields
Data Field
Configure Slots
Slot
Configure Frames
Frame
Power Ramping Settings
Calculate and Save Sequence
BERT Settings
Data Seq. Output File
Use sequence as data source (data source 0)
Close

14


WinIQSIM (Single Carrier) - new
File System Data Modulation Impairments Graphics AMIQ SMIQ (ARB) Window Help
PRBS 9
None
QPSK
50
Rect
Rect
4.096 MHz
16
65.536 MHz
800
Data Editor edge.ded
Configure Data Fields
Data Field
Configure Slots
Slot
Configure Frames
Frame
Power Ramping Settings
Calculate and Save Sequence
Data Seq. Output File
Use sequence as data source (data source 0)
Configure Slots
Slot Pool
Data Fields
New
Copy
Delete
Mrk+Pwr
Append
Insert
Replace
Delete Data Field in Slot
OK
Cancel

Name	Length / Bits	Color
NORM	468	🟩
D468	468	
D471	471	

Name NORM

Info Normal Burst

Length / Bits 468

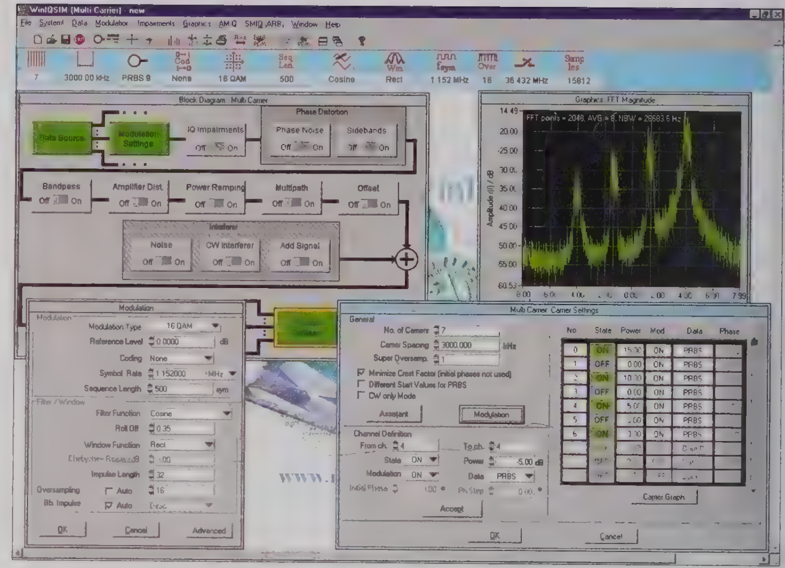
Definition of slots in the data editor

Multicarrier signals (15, 16)

In addition to single-carrier signals, multicarrier signals with all their characteristic parameters such as number of carriers (up to 512), carrier spacing, modulation (same for all carriers) and carrier power can be simulated. In this way, composite signals consisting of modulated and unmodulated carriers or signals with several superimposed impairments can be generated. What makes this application so attractive is that only one generator is needed to produce these signals, which means an enormous cost benefit.

Another operating mode (multicarrier mixed signal mode) allows up to 32 differently modulated carriers to be combined with any signal from various systems (single-carrier, multicarrier, WCDMA 3GPP FDD and TDD, TDSCDMA, CDMA2000, IS-95) at variable power levels. Signal scenarios such as several different WCDMA carriers can thus be simultaneously simulated and generated by the R&S AMIQ or R&S SMIQB60.

15


WinIQSIM [Multi Carrier] - new
File System Data Modulation Impairments Graphics AMIQ SMIQ ARB Window Help
Block Diagram - Multi Carrier
Data Source
Modulation Settings
IQ Impairments
Phase Distortion
Phase Noise
Sidebands
Bandpass
Amplifier Dist.
Power Ramping
Multipath
Offset
Interferer
Noise
CW Interferer
Add Signal
Off On
Graphics: FFT Magnitude
FFT points = 2048, AVG = 8, NBW = 26683.6 Hz
Amplitude (f) / dB
Multi Carrier: Carrier Settings

Modulation

Modulation Type: 16 QAM
Reference Level: 0.0000 dB
Coding: None
Symbol Rate: 1.152000 MHz
Sequence Length: 500 sym
Filter Function: Cosine
Roll Off: 0.35
Window Function: Rect
Impulse Length: 32
Oversampling: ☐ Auto 16
Bb. Impulse: ☑ Auto

Multi Carrier: Carrier Settings

No. of Carriers: 7
Carrier Spacing: 3000.000 kHz
Super Oversamp.: 1
☑ Minimize Crest Factor (initial phases not used)
☐ Different Start Values for PRBS
☐ CW only Mode

Channel Definition

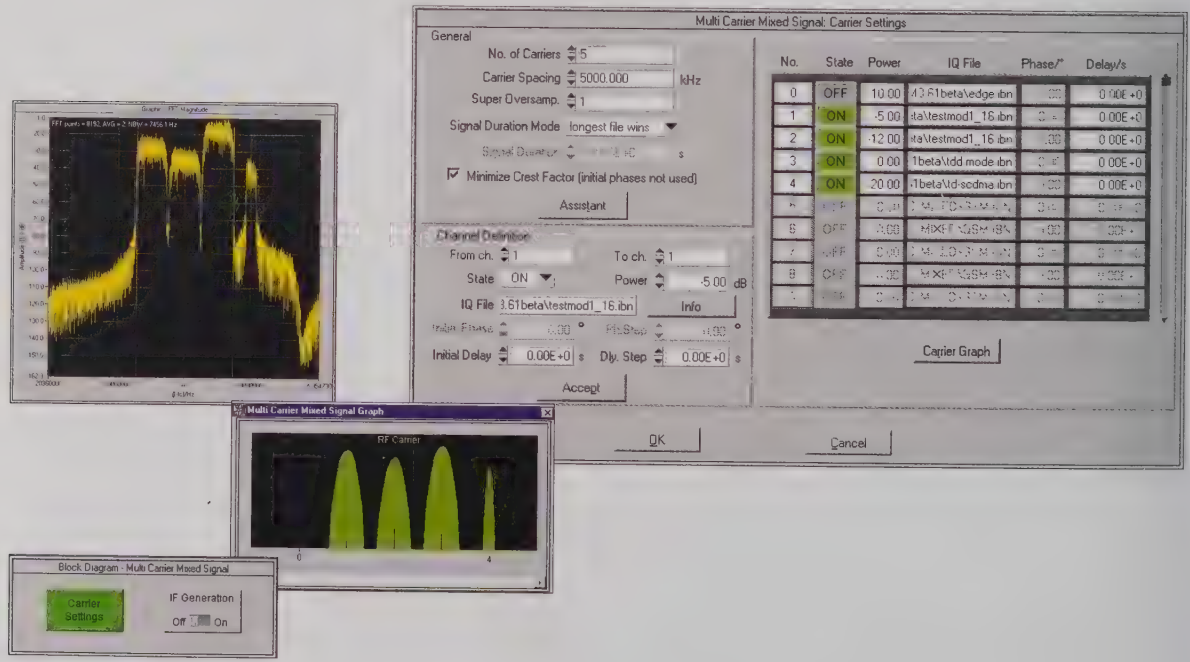
From ch.: 4
To ch.: 4
State: ON
Power: -5.00 dB
Modulation: ON
Data: PRBS

No	State	Power	Mod	Data	Phase
0	ON	15.00	ON	PRBS	
1	OFF	0.00	ON	PRBS	
2	ON	10.00	ON	PRBS	
3	OFF	0.00	ON	PRBS	
4	ON	5.00	ON	PRBS	
5	OFF	[illegible]	ON	PRBS	
6	ON	[illegible]	ON	PRBS	

Generation of a multicarrier signal

Generation of a multicarrier signal comprising two WCDMA 3GPP FDD signals, one TDD signal and one TD-SCDMA signal

16


Graphics: FFT Magnitude
FFT points = 8192, AVG = 2, NBW = 7456.1 Hz
Amplitude (f) / dB
f / kHz
Multi Carrier Mixed Signal Graph
RF Carrier
0
4
Block Diagram - Multi Carrier Mixed Signal
Carrier Settings
IF Generation
Off On
Assistant
Accept
Info
Carrier Graph
OK
Cancel

Multi Carrier Mixed Signal: Carrier Settings

General

No. of Carriers: 5
Carrier Spacing: 5000.000 kHz
Super Oversamp.: 1
Signal Duration Mode: longest file wins
☑ Minimize Crest Factor (initial phases not used)

Channel Definition

From ch.: 1
To ch.: 1
State: ON
Power: -5.00 dB
IQ File: 3.61beta\testmod1_16.ibn
Initial Delay: 0.00E+0 s
Dly. Step: 0.00E+0 s

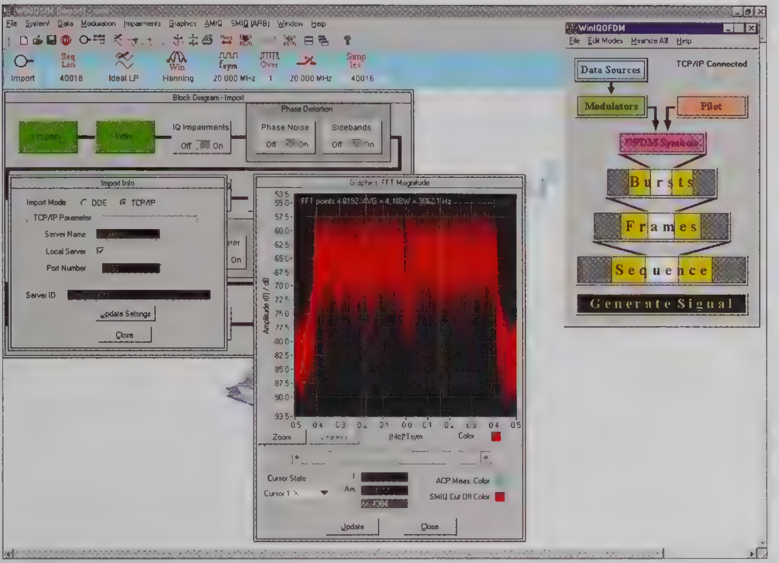
No.	State	Power	IQ File	Phase/°	Delay/s
0	OFF	10.00	43.61beta\edge.ibn	[illegible]	0.00E+0
1	ON	-5.00	ta\testmod1_16.ibn	[illegible]	0.00E+0
2	ON	-12.00	ta\testmod1_16.ibn	[illegible]	0.00E+0
3	ON	0.00	1beta\tdd mode.ibn	[illegible]	0.00E+0
4	ON	20.00	1beta\td-scdma.ibn	[illegible]	0.00E+0
[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
6	OFF	[illegible]	[illegible]	[illegible]	[illegible]
[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]
8	OFF	[illegible]	[illegible]	[illegible]	[illegible]
[illegible]	[illegible]	[illegible]	[illegible]	[illegible]	[illegible]

Import system (17)

Data from other PC programs can be read in via the import system. The TCP/IP or the dynamic data exchange (DDE) interface serves as the software interface.

Data can, for example, be imported from the R&S WinIQOFDM software, which is used for generating OFDM-modulated signals. Through subsequent processing in R&S WinIQSIM™, signal modifications such as baseband filtering and superimposed impairments can be applied to the signal to be generated.

The import interface also forms the basis for further applications (e.g. R&S IQWizard, for more information see www.rohde-schwarz.com) or customer-specific enhancements.


WinIQSIM
Import
Block Diagram - Import
IQ Impairments
Phase Distortion
Phase Noise
Sidebands
Import Info
Import Mode
DDE
TCP/IP
TCP/IP Parameter
Server Name
Local Server
Port Number
Server ID
Update Settings
Close
Graphics: FFT Magnitude
Amplitude (f) / dB
Zoom
Color
Cursor State
ACP Meas. Color
SMIQ Out Off Color
Update
Close
WinIQOFDM
TCP/IP Connected
Data Sources
Modulators
Pilot
OFDM Symbols
Bursts
Frames
Sequence
Generate Signal

17

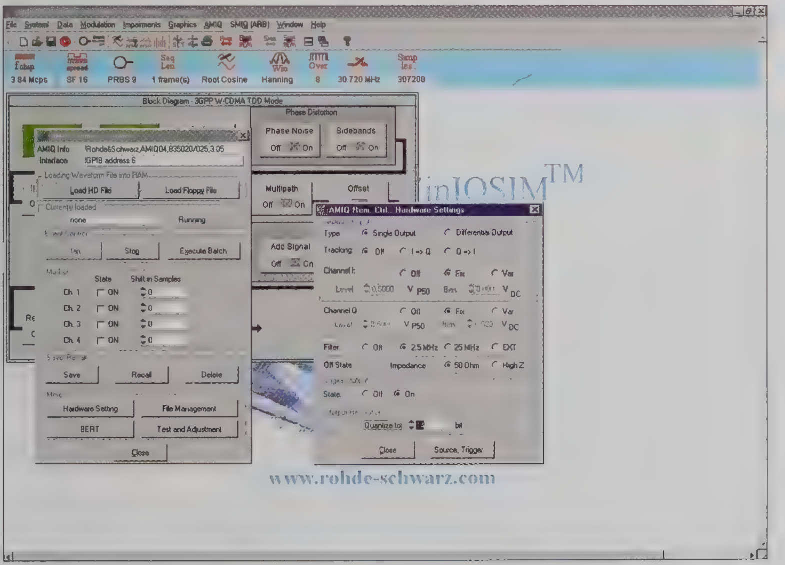
Functioning of the import system with R&S WinIQOFDM software

Remote-control functions (18)

The R&S WinIQSIM™ PC program is used to control and operate the I/Q Modulation Generators R&S AMIQ and the SMIQ-integrated solution R&S SMIQB60.

For the R&S AMIQ , it provides file management on the internal hard disk, and controls the hardware settings and all other functions.

The functionality of R&S WinIQSIM™ regarding device control is especially important in bit error rate measurements with the R&S AMIQ (option R&S AMIQ-B1). In addition to performing the control functions, the software outputs the measurement results in an R&S WinIQSIM™ window.


Block Diagram - 3GPP W-CDMA TDD Mode
3.84 Mcps
SF 16
PRBS 9
1 frame(s)
Root Cosine
Hanning
Phase Distortion
Phase Noise
Sidebands
Multipath
Offset
Add Signal
AMIQ Info
Interface
Load HD File
Load Floppy File
Execute Batch
Save
Recall
Delete
Hardware Setting
File Management
BERT
Test and Adjustment
Close
AMIQ Rem. Ctrl. Hardware Settings
Single Output
Differential Output
Close
Source, Trigger
WinIQSIM™
www.rohde-schwarz.com

18

User interface for controlling the R&S AMIQ with R&S WinIQSIM™

Specifications

User interface	Windows interface with context-sensitive help
Systems	single-carrier, multicarrier, multicarrier mixed signal, WCDMA 3GPP FDD, WCDMA 3GPP TDD, TD-SCDMA, IS-95, CDMA2000

Single carrier

Simulation of digitally modulated single-carrier signals incl. TDMA

Modulation modes	
PSK	BPSK, QPSK, offset QPSK, π/4DQPSK, 8PSK, 8PSK EDGE;
Parameter	reference level = –10 dB to 3 dB PSK rotation = 0 to 15 x π/8
QAM	16/32/64/256QAM
Parameter	reference level = –10 dB to 3 dB
FSK	MSK, 2FSK, 4FSK, GTFM
Parameter	modulation index = 0.1 to 12 GTFM b = 0 to 1
User-specific modulation	definition of customized modulation modes (PSK, QAM, FSK) via data interface with up to 4.096 mapping states
Baseband filters	Fourier approximation design method with windowing
Digital filters	rectangular √cosine, α = 0.01 to 0.99 cosine, α = 0.01 to 0.99 Gaussian, BxT = 0.1 to 3.0 Gaussian EDGE partial response no filter
User-specific filter	customized filter defined via file interface, specification of impulse response in time domain with up to 1024 coefficients, different filter coefficients for I and Q channel possible
Window	rectangular Hanning Kaiser, β = 0.01 to 10.0 Hamming Chebyshev, ripple = 10 dB to 80 dB
Window length	1 to 32 (integer)
Oversampling	1 to 32 (integer)
Symbol rate	10 symb/s to max. 100 Msymb/s
Coding	Gray, Diff, Gray Diff, GSM Diff, NADC, TFTS, MSAT Diff, Phase Diff, none
Data sources	all 0, all 1, PRBS (7, 9, 11, 15, 16, 20, 21, 23), pattern (max. 79 bit), user-defined data sequence via file interface
Data editor	definition of TDMA data structures with modularity at three levels: data field, slot and frame, definition of power-time templates
Data fields	up to 50 different fields, length up to 1000 bit, data content: all 0, all 1, pattern (max. 79 bit) or PRBS
Slots	up to 24 different slots, any combination of up to 36 data fields
Frame	any combination of up to 36 slots
Sequence length	1 to max. 4 (16) Msample (R&S AMIQ03/R&S AMIQ04)
Simulation of impairments and transfer characteristics	
I/Q impairment	carrier leakage I and Q (–50% to +50%) I/Q imbalance (–30% to +30%) quadrature offset (–30° to +30°) AM/AM conversion (k2; k3 –3 to +3 dB) AM/φM conversion (k2; k3 –30° to +30°)
Phase noise	simulation of impairments of phase-locked loop (VCO) and discrete spurious lines
Bandpass	simulation of bandpass at the RF with amplitude and group delay distortion
Amplifier models	amplifiers with soft and hard limiting, nonlinearities: AM/AM k3, k5 –3 dB to +3 dB; AM/φM k3, k5 –30° to +30°
Power ramping	ramp function: linear, $\cos^2$ rise/fall time: 0 to 16 Tsymb level: –80 dB to 0 dB
Multipath propagation	up to 6 paths with different delays, start phases and levels
Offset	phase offset: –180° to +180° frequency offset: –0.35 f_{sample} to +0.35 f_{sample}
Additive impairments	
Noise	E_b/N_0 = –3 dB to +80 dB, bandwidth 0.5/1/2/4/8/16 f_{symbol}
Sinewave interferer	C/I = –3 dB to +80 dB, frequency –0.35 f_{sample} to +0.35 f_{sample}
Superimposed signal	addition of a previously calculated signal, level –80 dB to +3 dB
Receiver filters	rectangular √cosine, α = 0.01 to 0.99 Gaussian, BxT = 0.1 to 3.0 user-specific (see above)
Quantization	I/Q resolution:1 x 10^{-6} to 0.5; filter coefficient resolution: 10^{-6} to 0.5
Smoothing	smoothing the wraparound of the I/Q signal between signal end and signal start: in range 2 sample to 32 sample
Graphical output	user-selectable scaling, zoom function, delta marker; display modes: i(t), q(t), r(t), phi(t), r(t), f(t), eye I, eye Q, eye F, vector diagram, constellation diagram, magnitude/phase/group-delay spectrum, additionally CCDF and ACP (see below)
CCDF function	determination and graphical display of complementary cumulative distribution function with calculation of crest factor
ACP calculation	calculation of adjacent-channel power in the spectrum display (ACP up, low and ACP up 1st alt, low 1st alt)
IF signal generation	modulation of calculated I/Q signal to IF in range 0.01 MHz to 25 MHz (output to I channel of R&S AMIQ)

Multicarrier

Simulation of multicarrier signals with same or without modulation

Number of carriers	max. 512 carriers
Parameters of each carrier	state on/off, power, modulation on/off, data source, start phase
Modulation modes	same as with single-carrier system, each carrier can be modulated or not, modulated carriers use the same modulation mode
Baseband filtering	same as with single-carrier system, identical for all modulated carriers
Coding	same as with single-carrier system, identical for all modulated carriers
Data sources	4 different sources, 3 same as in single-carrier system, another PRBS source with different start values for different carriers
Data editor	same as with single-carrier system
Carrier power	–80 dB to 0 dB
Start phase of carrier CW signal	0° to 360° selectable for each carrier or automatic setting for minimizing the crest factor

Sequence length	1 to max. 4 (16) Msample (R&S AMIQ03/R&S AMIQ04)
Simulation of impairments and transfer characteristics	same as with single-carrier system, identical for all modulated carriers
Smoothing	same as with single-carrier system
Graphical output	same as with single-carrier system
CDF function	same as with single-carrier system
ACP calculation	same as with single-carrier system
IF signal generation	same as with single-carrier system

Multicarrier mixed signal

Simulation of differently modulated multicarrier signals and signals of different systems on the carriers

Number of carriers	max. 32
Parameters of each carrier	state on/off, power, I/Q modulation file, start phase
I/Q modulation signal	an I/Q signal file onto which the carrier is to be modulated can be defined for each carrier; these signal files can be generated in all systems
Carrier power	–80 dB to 0 dB
Start phase of carrier CW signal	0° to 360° selectable for each carrier or automatic setting for minimizing the crest factor
Signal period	automatically adapted to longest or shortest carrier signal period or user-selectable (max. duration 4 Msample (16 Msample)/sample rate, R&S AMIQ03/R&S AMIQ04)
Graphical output	same as with single-carrier system
CCDF function	same as with single-carrier system
ACP calculation	same as with single-carrier system
IF signal generation	same as with single-carrier system

WCDMA 3GPP FDD

Version 4.1.0 to 3GPP Technical Specifications TS25.211, TS25.213, TS25.141, TS25.101 and TS25.104

General settings	
Chip rate	
Standard	3.840 Mcps (15 slots/frame)
Range	10 cps to 100 Mcps
Link direction	uplink (reverse link) and downlink (forward link)
Sequence length	1 to 26 frames R&S AMIQ03 (oversampling 4) 1 to 104 frames R&S AMIQ04 (oversampling 4) 1 to 6 frames R&S SMIQB60 (oversampling 2)
Baseband filter	
Standard	$\sqrt{\cos}$, $\alpha = 0.22$
Other filters	same as with single-carrier system
Clipping level	setting of clipping value relative to highest peak in percent; clipping takes place prior to baseband filtering and reduces the crest factor; range 1% to 100 %
Code channels	
Downlink	up to 512 data channels (plus special channels) divided among up to four base stations (BS) with 128 code channels each
Uplink	up to four mobile stations (MS) each operating in one of modes PRACH only, PCPCH only, DPCCH + DPDCHs

Physical channels in downlink	
P-CPICH	Primary Common Pilot Channel
Symbol rate	15 ksps, fixed
Channelization code	0, fixed
Slot structure	predefined symbols
S-CPICH	Secondary Common Pilot Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Slot structure	predefined symbols
P-SCH	Primary Sync Channel
Symbol rate	15 ksps, fixed
Slot structure	synchronization code (SC)
S-SCH	Secondary Sync Channel
Symbol rate	15 ksps, fixed
Slot structure	synchronization code (SC)
P-CCPCH	Primary Common Control Physical Channel
Symbol rate	15 ksps, fixed
Channelization code	1, fixed
Slot structure	data
S-CCPCH	Secondary Common Control Physical Channel
Symbol rate	15, 30, 60, 120, 240, 480, 960 ksps
Channelization code	depending on symbol rate, 0 to max. 255
Slot structure	data, TFCI, pilot
PICH	Page Indication Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Number of PIs per frame	18, 36, 72, 144
Slot structure	page indicator bits, not used bits
AP-AICH	Access Preamble Acquisition Indication Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Slot structure	acquisition indicators, empty symbols
AICH	Acquisition Indication Channel
Symbol rate	15 ksps, fixed
Channelization code	0 to 255
Slot structure	acquisition indicators, empty symbols
PDSCH	Physical Downlink Shared Channel
Symbol rate	15, 30, 60, 120, 240, 480, 960 ksps
Channelization code	depending on symbol rate, 0 to max. 255
Slot structure	data
DL-DPCCH	Dedicated Physical Control Channel
Symbol rate	7.5 ksps, fixed
Channelization code	0 to 511
Slot structure	TPC, pilot
DPCH	Dedicated Physical Channel
Symbol rate	7.5, 15, 30, 60, 120, 240, 480, 960 ksps depending on symbol rate
Channelization code	0 to max. 511
Slot structure	data 1, TPC, TFCI, data 2, pilot
Physical channels in uplink	
PRACH	Physical Random Access Channel
Symbol rate	15, 30, 60, 120 ksps
Frame structure	preamble(s), message part consisting of data and control section
Preamble part power	–60 dB to 0 dB
Data part power	–60 dB to 0 dB
Control part power	–60 dB to 0 dB
Preamble repetition	1 to 10
Signature	0 to 15
Access slot	0 to 14
Message part length	1 or 2 frames
TFCI	0 to 1023
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)

PCPCH	Physical Common Packet Channel
Symbol rate	15, 30, 60, 120 ksps
Frame structure	access preamble(s), collision detection preamble, power control preamble, message part consisting of data and control section
Preamble part power	–60 dB to 0 dB
Data part power	–60 dB to 0 dB
Control part power	–60 dB to 0 dB
Preamble power step	0 dB to 10 dB
Shared resource mode	ON/OFF
Preamble repetition	1 to 10
Signature	0 to 15
Access slot	0 to 14
Message part length	1 to 10 frames
Power control preamble length	0 or 8 slots
FBI state	OFF/1 bit/2 bit
FBI pattern	all 0, all 1 and bit pattern (max. length 16 bit)
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)
DPCCH	Dedicated Physical Control Channel
Symbol rate	15 ksps, fixed
Channelization code	0, fixed
DL–UL timing offset	1024 chips, fixed
FBI state	OFF/1 bit/2 bit
FBI pattern	all 0, all 1 and bit pattern (max. length 16 bit)
TFCI state	ON/OFF
TFCI	0 to 1023
Use TPC for dynamic output	ON/OFF
Power control	If this function is active, the TPC pattern is used to vary the transmit power of the MS code channels versus time.
Output power control step	–10 dB to +10 dB
DPDCH	Dedicated Physical Data Channel
Overall symbol rate	overall data rate of all uplink DPDCHs 15, 30, 60, 120, 240, 480, 960, 2 x 960, 3 x 960, 4 x 960, 5 x 960, 6 x 960 ksps
Active DPDCHs	1 to 6, depending on overall symbol rate
Symbol rate	fixed for active DPDCHs, depending on overall symbol rate
Channelization code	fixed for active DPDCHs, depending on overall symbol rate
Channel power	–60 dB to 0 dB for all DPDCHs
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)

Parameters for each base station (BS)

State	ON/OFF
2nd search code group	0 to 63 (depending on scrambling code)
Scrambling code	0 to 5FFFF hex or off
TFCI state	ON/OFF
TFCI	0 to 1023
TPC pattern readout mode	use of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10
Use TPC for dynamic output power control	ON/OFF If this function is active, the TPC pattern is used to vary the transmit power of the code channels versus time.
Output power control step	–10 dB to +10 dB
Transmit diversity	OFF/antenna 1/antenna 2 If this function is active, the output signal for antenna 1 or antenna 2 can be generated as defined in the standard.

Parameters for each mobile station (MS)

State	ON/OFF
Mode	PRACH only, PCPCH only, DPCCH + DPDCHs
Scrambling code	0 to FF FFFF hex
Scrambling code mode	long, short, off
TPC pattern	all 0, all 1 and bit pattern (max. length 16 bit)
TPC pattern readout mode	use of TPC pattern: continuous, single + all 0, single + all 1, single + alternating 01, single + alternating 10

Parameters independently selectable for each downlink code channel

State	ON/OFF
Symbol rate	7.5 ksps to 960 ksps, depending on type of physical channel
Channelization code	0 to max. 511, depending on symbol rate and type of physical channel
Power	–60 dB to 0 dB
User data	PRBS: PN9, PN11, PN15, PN16 all 0, all 1 and bit pattern (max. length 16 bit)
Timing offset	separately adjustable for each code channel 0 to 149 (in units of 256 chips)
Pilot length	2, 4, 8, 16 bit, depending on symbol rate
TPC pattern	all 0, all 1 and bit pattern (max. length 16 bit)
Multicode state	ON/OFF

Compressed mode

State	ON/OFF
Compressed mode method	higher layer scheduling, puncturing (downlink only) or SF/2
Downlink frame structure	type A (last pilot) or type B (first TPC, last pilot)
Power offset for compressed slots	automatic or manual in range 0 dB to 10 dB
Number of transmission patterns	1 or 2
Number of transmission gaps per pattern	2
TGSN (transmission gap slot number) TGL1, TGL2 (transmission gap length 1, 2) TGD1, TGD2 (transmission gap distance 1, 2) TGPL1, TGPL2 (transmission gap pattern length 1, 2)	user-selectable within the range permitted by the standard; conflicting parameters are displayed and solutions proposed

Assistant functions to facilitate operation

Test models (supplied as example files)	test model 1 with 16/32/64 channels test model 2 test model 3 with 16/32 channels test model 4
Parameterizable predefined settings	Generation of complex signal scenarios in downlink with parameterizable default settings. Selectable parameters: use and symbol rate of special channels (for synchronization of mobile station), number and symbol rate of data channels, crest factor: minimal/average/worst
Multichannel edit	Common configuration of data channels of BS channel table. Selectable parameters, partly with start value and step size: range of data channels to be set, symbol rate, channelization code with step size, channel power with step size, data, TPC, timing offset with step size, multicode state, state

Copy BS/MS	Adopting the configuration of a BS for another BS/MS to define multi-BS/MS scenarios or BS signals with more than 128 channels. Parameters: source and destination of copying, channelization code offset for simple definition of BS signals with more than 128 channels and continuous channelization codes
Resolve domain conflicts	Elimination of code channel overlapping in code domain (domain conflicts) occurring in a BS/MS
Graphical displays	
Domain conflicts	Display of domain conflicts (overlapping of code channels in code domain) in the channel table lines concerned. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code domain	Display of code domain occupied by current BS. Domain areas in which conflicts occur are highlighted. The distribution of code channels in the code domain as well as the channel powers are shown qualitatively.
Channel graph	Display of all active channels of a BS versus the channel table index. The powers of the code channels are shown qualitatively.
CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of complex I/Q samples exceeding a predefined threshold. Together with the current CCDF, the CCDFs of the two 3GPP signals last generated can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus I/Q samples of current 3GPP signal. This diagram allows qualitative assessment of channel configuration, channel power ratios, and effect of parameters such as data and data offset.

WCDMA 3GPP TDD (with option R&S AMIQK13/R&S SMIQK13)

Simulation of signals to time division duplex wideband CDMA standard according to version 4.1.0 of the 3GPP Technical Specification TS 25.221, TS 25.223, available as Software Option R&S AMIQK13 of the R&S AMIQ or Software Option R&S SMIQK13 and Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ

General settings	
Chip rate	
Standard	3.84 Mcps
Range	R&S AMIQ: 10 cps to max. 100 Mcps R&S SMIQ (B60): 1 kcps to max. 40 Mcps
Mode	downlink only: the base station components of a cell are active uplink only: the mobile station components of a cell are active downlink and uplink: both the base station and the mobile station components of a cell are active
Sequence length	entry in slots (0.667 ms each) or frames (10 ms each), max. length depending on oversampling R&S AMIQ03 (for oversampling 4): 1 to 26 frames R&S AMIQ04 (for oversampling 4): 1 to 104 frames R&S SMIQB60 (for oversampling 2 in R&S WinIQSIM™ (oversampling >4 as a result of hardware oversampling in the R&S SMIQ)): 1 to 6 frames
Baseband filter	
Standard	root raised cosine filter, roll off = 0.22
Other filters	same as with single-carrier system
Cells	simulation of up to 4 cells, each comprising 15 slots
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering. Either scalar mode or vector mode can be selected. Clipping reduces the crest factor. The range is 1% to 100%.
Parameters for each cell	
State	ON/OFF
Frame structure	The link direction (uplink or downlink) can be set independently for each of the 15 slots of the frame. All single- and multi-switching point configurations can be simulated.
Scrambling code	0 to 127 scrambling code can be disabled for testing
Code group	automatic selection depending on scrambling code 0 to 31
Midamble allocation method	default common equal to scrambling code
Guard field power state	OFF (according to standard)/ON
Parameters for each downlink slot	
State	ON/OFF
Slot mode	downlink dedicated: simulation of up to 16 DPCHs and max. 6 special channels
Burst type	1 and 2
SCH assoc. t_offset	automatic selection depending on scrambling code 0 to 31
SCH code allocation	cases 1 and 2 to TS 25.223
Page indicator length	2, 4, 8
Parameters for each uplink slot	
Slot mode	uplink dedicated: simulation of up to 16 DPCHs and 1 special channel PRACH: simulation of one Physical Random Access Channel
TPC pattern readout mode	application mode for TPC pattern continuous, single + all 0, single + all 1, single + alt. 01, single + alt. 10
Burst type	1, 2 and 3
Parameters in uplink PRACH mode	
Burst type	3
Start frame	selection of first frame in which PRACH is sent 0 to 10
PRACH length	length of PRACH message part 1 to 10 frames
User	index of user to which PRACH is assigned
Midamble and midamble shift	display of midamble used and of midamble time shift, depending on midamble allocation method and user
Spreading factor	8 and 16

Spreading code	spreading code of channel, range depending on spreading factor 1 to max. 16
Power	–60 dB to 0 dB
Physical channels	
Data	4 different data sources, 3 same as with single-carrier system, another PBRS source with differing start values for different code channels
Downlink	Primary Common Control Physical Channel (P-CCPCH) Secondary Common Control Physical Channel (S-CCPCH) Primary Sync Channel (P-SCH) Secondary Sync Channel (S-SCH) Physical Downlink Shared Channel (PDSCH) Page Indicator Channel (PICH) Dedicated Physical Channel (DPCH)
Uplink	Physical Random Access Channel (PRACH) Physical Uplink Shared Channel (PUSCH) Dedicated Physical Channel (DPCH)
Parameters independently selectable for each code channel	
State	ON/OFF
User	1 to 16 with burst types 1 and 3 1 to 6 with burst type 2
Spreading factor	depending on channel type and link direction 1, 2, 4, 8, 16
Spreading code	depending on channel type and spreading factor 1 to max. 16
Midamble and midamble shift	display of midamble used and of midamble time shift, depending on midamble allocation method and user
TFCI/TPC combination	combination of TFCI and TPC fields, TPC in uplink only, uplink: TFCI 0 | TPC 0, TFCI 0 | TPC 2, TFCI 4 | TPC 2, TFCI 8 | TPC 2, TFCI 16 | TPC 2, TFCI 32 | TPC 2 downlink: TFCI 0, TFCI 4, TFCI 8, TFCI 16, TFCI 32
TFCI	transport format combination indicator 0 to 1023
TPC pattern	bit pattern (max. length 16 bit) as a data source for the TPC field of the channel, in uplink only
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as single-carrier system, another PBRS source with differing start values for different code channels
Assistant functions to facilitate operation	
Copy cell	Adopting the configuration of a cell for another cell to define multicell scenarios. Parameters: source and destination of copying
Resolve domain conflicts	Elimination of code channel overlapping in code domain occurring in a slot (domain conflicts).
Graphical displays	
Domain conflicts	Display of domain conflicts (overlapping of code channels in code domain) in the channel table lines concerned. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code domain	Display of code domain occupied by active slot. Domain areas in which conflicts occur are highlighted. Code channel distribution in the code domain and channel powers are displayed.
Channel graph	Display of all active channels of a slot versus the channel table index. The powers of the individual code channels are indicated.
CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of the complex I/Q samples exceeding a predefined threshold. Together with the current CCDF, the CCDFs of any number of 3GPP TDD signals generated last can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus I/Q samples of current 3GPP TDD signal. This diagram allows qualitative assessment of channel configuration, channel power ratios, and effect of TDD system parameters.

Digital standard TD-SCDMA (with option R&S AMIQK14 R&S SMIQK14)

Simulation of signals according to time division synchronous CDMA standard of China Wireless Telecommunication Standard Group (CWTS), available as Software Option R&S AMIQK14 of the R&S AMIQ or Software Option R&S SMIQK14 and Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ

General settings	
Chip rate	
Standard	1.28 Mcps
Range	R&S AMIQ: 10 cps to max. 100 Mcps R&S SMIQ (B60): 1 kcps to max. 40 Mcps
Mode	downlink only: the base station components of a cell are active uplink only: the mobile station components of a cell are active downlink and uplink: both the base station and the mobile station components of a cell are active
Sequence length	entry in frames (5 ms each), max. length depending on oversampling and R&S AMIQ03 (for oversampling 4): 1 to 156 frames R&S AMIQ04 (for oversampling 4): 1 to 625 frames R&S SMIQ (B60): (for oversampling 2 in R&S WinIQSIM™ (oversampling >4 as a result of hardware oversampling in the R&S SMIQ)): 1 to 40 frames
Baseband filter	
Standard	root raised cosine filter, roll off = 0.22
Other filters	same as with single-carrier system
Cells	simulation of up to 4 cells, each comprising 7 traffic slots and 3 special slots
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering. Either scalar mode or vector mode can be selected. Clipping reduces the crest factor. The range is 1% to 100%.

Parameters for each cell

State	ON/OFF
Frame structure	total of 7 traffic slots, slot 0 always reserved for downlink, slot 1 to switching point reserved for uplink, other slots reserved for downlink; special slots between slots 0 and 1: Downlink Pilot Slot (DwPTS), Guard Period (GP) and Uplink Pilot Slot (UpPTS)
Scrambling code	0 to 127 scrambling code can be disabled for testing
SYNC code	automatic selection depending on scrambling code 0 to 31
Switching point	switchover between uplink and downlink slots 1 to 6
Layer 1 control fields	can be enabled and disabled to simulate burst types 1 and 2
DwPTS power	–60 dB to 0 dB

Parameters for each downlink slot

State	ON/OFF
Slot mode	downlink dedicated: simulation of up to 16 DPCHs and max. 5 special channels
TPC pattern readout mode	application mode for TPC pattern: continuous, single + hold 01, single + hold 10, single + all up, single + all down
Sync shift repetition mode	number of frames to which sync shift bits are distributed 1 to 500
Stealing flag	value of the two stealing bits 0 to 3

Parameters for each uplink slot

Slot mode	uplink dedicated: simulation of up to 16 DPCHs PRACH: simulation of one Physical Random Access Channel
TPC pattern readout mode	application mode for TPC pattern: continuous, single + hold 01, single + hold 10, single + all up, single + all down
Sync shift repetition mode	number of frames to which sync shift bits are distributed 1 to 500
Stealing flag	value of the two stealing bits 0 to 3

Parameters in uplink PRACH mode

SYNC 1	SYNC 1 code 0 to 7
UpPTS start frame	selection of first frame in which UpPTS is sent 1 to 6
UpPTS repetition	number of UpPTS repetitions 1 to 10
PRACH length	length of PRACH message part 1 to 10 frames
Gross data rate	17.6 kbps, 35.2 kbps
Spreading code	depending on gross data rate 0 to max. 15
Sync shift pattern	bit pattern (max. length 16 bit) as a data source for the sync shift field of the channel
TPC pattern	bit pattern (max. length 16 bit) as a data source for the TPC field of the channel
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels

Physical channels

Downlink	Primary Common Control Physical Channel (P-CCPCH) Secondary Common Control Physical Channel (S-CCPCH) Physical Forward Access Channel (F-FACH) Downlink Pilot Time Slot (DwPTS) Dedicated Physical Channel (DPCH)
Uplink	Physical Random Access Channel (PRACH) Uplink Pilot Time Slot (UpPTS) Dedicated Physical Channel (DPCH)

Parameters independently selectable for each code channel

State	ON/OFF
Gross data rate	depending on channel type 17.6 kbps, 35.2 kbps, 70.4 kbps, 140.8 kbps, 281.6 kbps (spreading factors 1, 2, 4, 8, 16)
Spreading code	depending on channel type and gross data rate 0 to max. 15
Midamble shift	time shift of midamble in chips: 0 to 120, step width 8 chips
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels
Sync shift pattern	bit pattern (max. length 16 bit) as a data source for the sync shift field of the channel
TPC pattern	bit pattern (max. length 16 bit) as a data source for the TPC field of the channel

Assistant functions to facilitate operation

Predefined settings	Generation of complex signal scenarios with parameterizable default settings. Selectable parameters: use of special channels (P-CCPCH), number and gross data rate of data channels, crest factor: minimal/average/worst
Copy cell	Adopting the configuration of a cell for another cell to define multicell scenarios. Parameters: source and destination of copying
Resolve domain conflicts	Elimination of code channel overlapping in code domain occurring in a slot (domain conflicts).

Graphical displays

Domain conflicts	Display of domain conflicts (overlapping of code channels in code domain) in the channel table lines concerned. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code domain	Graphical Display of code domain occupied by active slot. Domain areas in which conflicts occur are highlighted. Code channel distribution in the code domain and channel powers are displayed.
Channel graph	Display of all active channels of a slot versus the channel table index. The powers of the individual code channels are indicated.

CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of the complex I/Q samples exceeding a predefined threshold. Together with the current CCDF, the CCDFs of any number of TD-SCDMA signals generated last can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus I/Q samples of current TD-SCDMA signal. This diagram allows qualitative assessment of channel configuration, channel power ratios, and effect of TD-SCDMA system parameters.

Digital standard IS-95 (with option R&S AMIQK11/R&S SMIQK11)

Simulation of CDMA signals to North American standard IS-95 A, available as Software Option R&S AMIQK11 of the R&S AMIQ or Software Option R&S SMIQK11 in conjunction with the Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ

General settings

Chip rate	
Standard	1.2288 Mcps
Range	R&S AMIQ: 10 cps to 100 Mcps R&S SMIQB60: 1 kcps to 40 Mcps
Link direction	forward link and reverse link
Sequence length	entry in symbols (1536 symbols correspond to 80 ms frame), max. length depending on oversampling R&S AMIQ03 (for oversampling 4): 1 to 10 frames R&S AMIQ04 (for oversampling 4): 1 to 40 frames R&S SMIQB60 (for oversampling 2 in R&S WinIQSIM™, and oversampling >4 as a result of hardware oversampling in the R&S SMIQ): 1 to 2 frames
Baseband filter	
Standard	CDMA2000 1X (corresponds to IS-95 filter)
Other filters	same as with single-carrier system
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering and reduces the crest factor. The range is 1% to 100 %.
Parameters for each base station	PN offset 0 to 511
Parameters for each code channel	state on/off, power, data, long code mask
Physical channels in forward link	
Pilot	
Paging	parameters: data, long code mask with PCN and pilot PN field
Sync	parameter: data
Traffic	parameters: data, long code mask with permuted ESN field
Physical channels in reverse link	
Access	parameters: data, long code mask with ACN, PCN, base ID and pilot PN field
Traffic	parameters: data, long code mask with permuted ESN field
Channel power	–40 dB to 0 dB
Modulation data	4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels
Baseband filtering	same as with single-carrier system
Simulation of impairments and transmission characteristics	same as with single-carrier system
Smoothing	same as with single-carrier system
Graphical output	same as with single-carrier system
CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of complex I/Q samples exceeding a predefined threshold. Together with the current CCDF, the CCDFs of any number of IS-95 signals last generated can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
ACP calculation	Calculation of adjacent-channel power in spectrum display (ACP up, low and ACP up 1st alt, low 1st alt)
IF signal generation	Modulation of calculated I/Q signal to intermediate frequency in range 0.01 MHz to 25 MHz (output to I channel of R&S AMIQ)

Digital standard CDMA2000 (with option R&S AMIQK12/R&S SMIQK12)

Simulation of CDMA signals to North American standard IS-2000, available as Software Option R&S AMIQK12 of the R&S AMIQ or Software Option R&S SMIQK12 in conjunction with the Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ

General settings

Chip rate	
Standard	1.2288 Mcps (1X), 3.6864 Mcps (3X)
Range	R&S AMIQ: 10 cps to 100 Mcps R&S SMIQB60: 1 kcps to 40 Mcps
Carrier spacing	
Standard	1.25 MHz
Variable	R&S AMIQ: 0 to 10 MHz R&S SMIQB60: 0 to 2 MHz
Modes	1X Direct Spread 3X Direct Spread 3X Multi Carrier (forward link only)
Link direction	forward link and reverse link
Sequence length	entry in frames of 80 ms, max. length depending on chip rate, mode and oversampling R&S AMIQ03 (for oversampling 4): 1 to 10 frames at 1.2288 Mcps (1X) 1 to 6 frames at 3.6864 Mcps (3X) Multi Carrier 1 to 3 frames at 3.6864 Mcps (3X) Direct Spread R&S AMIQ04 (for oversampling 4): 1 to 40 frames at 1.2288 Mcps (1X) 1 to 26 frames at 3.6864 Mcps (3X) Multi Carrier 1 to 13 frames at 3.6864 Mcps (3X) Direct Spread R&S SMIQB60 (for oversampling 2 in R&S WinIQSIM™, and oversampling >4 as a result of hardware oversampling in the R&S SMIQ): 1 to 2 frames at 1.2288 Mcps (1X) 1 frame at 3.6864 Mcps (3X) Multi Carrier 1 frame at 3.6864 Mcps (3X) Direct Spread
Baseband filter	
Standard	CDMA2000 1X CDMA2000 3X Direct Spread
Other filters	same as with single-carrier system
Code channels	
Forward link	4 base stations with max. 91 code channels each (depending on radio configuration)
Reverse link	4 mobile stations with max. 13 code channels each (depending on radio configuration)

Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering and reduces the crest factor. The range is 1% to 100 %.
Parameters for each base station (BS)	
State	ON/OFF
Radio configuration	
Chip rate 1.2288 Mcps (1X)	RC 1 to RC 5
Chip rate 3.6864 Mcps (3X)	RC 6 to RC 9
PN offset	0 to 511
Quasi-orthogonal Walsh sets	set 1 to set 3
Channel coding	All levels of channel coding provided by IS-2000 (e.g. frame quality indicator, convolutional encoder, symbol puncture and interleaver) are available. All combinations of frame lengths and data rates are supported. Four modes are available: – off: channel coding off – complete: complete channel coding on – without interleaving: channel coding on, but without interleaver – interleaving only: channel coding off, interleaver active only
Transmit diversity (OTD)	off / antenna 1 / antenna 2 If this function is active, the output signal for antenna 1 or antenna 2 can be generated as defined in the standard.
Use TPC for dynamic output power control	ON/OFF If this function is active, the TPC pattern is used to vary the transmit power of the code channels versus time.
Output power control step	–10 dB to +10 dB
Parameters for each mobile station (MS)	
State	ON/OFF
Radio configuration	
Chip rate 1.2288 Mcps (1X)	RC 1 to RC 4
Chip rate 3.6864 Mcps (3X)	RC 5 to RC 6
Channel coding	All levels of channel coding provided by IS-2000 (e.g. frame quality indicator, convolutional encoder, symbol puncture and interleaver) are available. All combinations of frame lengths and data rates are supported. Four modes are available: – off: channel coding off – complete: complete channel coding on – without interleaving: channel coding on, but without interleaver – interleaving only: channel coding off, interleaver active only
Use TPC for dynamic output power control	ON/OFF If this function is active, the TPC pattern (selectable bit pattern, max. length 16 bit) is used to vary the transmit power of the code channels versus time.
Output power control step	–10 dB to +10 dB

Channel types	
Forward link	Special channels: – Forward Pilot (F-PICH) – Sync (F-SYNC) – Paging (F-PCH) – Transmit Diversity Pilot (F-TDPICH) – Auxiliary Pilot (F-APICH) – Auxiliary Transmit Diversity Pilot (F-ATDPCH) – Broadcast (F-BCH) – Quick Paging (F-QPCH) – Common Power Control (F-CPCCH) – Common Assignment (F-CACH) – Forward Common Control (F-CCCH) Traffic channels: – Forward Dedicated Control (F-DCCH) – Forward Fundamental (F-FCH) – Forward Supplemental (F-SCH)
Reverse link	Special channels: – Reverse Pilot (R-PICH) – Access (R-ACH) – Enhanced Access (R-EACH) – Reverse Common Control (R-CCCH) – Reverse Dedicated Control (R-DCCH) Traffic channels: – Reverse Fundamental (R-FCH) – Reverse Supplemental Code (R-SCCH) – Reverse Supplemental 1 (R-S1CH) – Reverse Supplemental 2 (R-S2CH)
Parameters independently selectable for each forward link code channel	
State	ON/OFF
Frame length	depending on channel type and radio configuration: 5 ms, 10 ms, 20 ms, 40 ms, 80 ms
Data rate	depending on channel type and radio configuration: 1.2 kbps to max. 1036.8 kbps
Walsh code	depending on channel type and radio configuration: 0 to max. 255
Quasi-orthogonal code	ON/OFF
Long code mask	0 to 3FF FFFF FFFF hex
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels
TPC pattern	bit pattern (max. length 16 bit)
Parameters independently selectable for each reverse link code channel	
State	ON/OFF
Frame length	depending on channel type and radio configuration: 5 ms, 10 ms, 20 ms, 40 ms, 80 ms
Data rate	depending on channel type and radio configuration: 1.2 kbps to max. 1036.8 kbps
Long code mask	0 to 3FF FFFF FFFF hex
Power	–60 dB to 0 dB
Data	4 different data sources, 3 same as with single-carrier system, another PRBS source with differing start values for different code channels
Assistant functions to facilitate operation	
Parameterizable predefined settings (forward link only)	Generation of complex signal scenarios with parameterizable default settings. Selectable parameters: – use of special channels (F-PICH, F-SYNC, number of F-QPCHs) – number, frame length and data rate of data channels – crest factor: minimal/average/worst

Multichannel edit (forward link only)	Common configuration of data channels of BS channel table. Selectable parameters, partly with start value and step size: – range of data channels to be set – frame length – data rate – Walsh code with step width – state of quasi-orthogonal Walsh set – channel power with step size – data – TPC – state
Copy BS/MS	Adopting the configuration of a BS/MS for another BS/MS to define multi-BS/MS scenarios. Parameters: – source and destination of copying – Walsh code offset (forward link)
Graphical displays	
Domain conflicts (forward link only)	Display of domain conflicts (overlapping of code channels in code domain) in the channel table lines concerned. The code domain occupied by the code channels involved in the conflict can also be displayed.
Code Domain (forward link only)	Display of code domain occupied by current BS. Domain areas in which conflicts occur are highlighted. The distribution of code channels in the code domain as well as the channel powers are displayed.
Channel graph	Display of all active channels of a BS/MS versus the channel table index. The powers of the code channels are shown.
CCDF	Display of complementary cumulative distribution function of current signal. This function gives the probability of the magnitudes of complex I/Q samples exceeding a predefined threshold. Together with the current CCDF, the CCDFs of any number of CDMA2000 signals last generated can be displayed to observe the effect of parameter changes. The crest factor of the signal can be seen in the CCDF.
Constellation diagram	Display of constellation diagram versus I/Q samples of current CDMA signal. This diagram allows qualitative assessment of channel configuration, channel power ratios, and effect of selected Walsh codes.

Digital standard IEEE 802.11b Wireless LAN (with option R&S AMIQK16/SMIQK16)

Simulation of signals to Wireless LAN standard IEEE 802.11b, available as Software Option R&S AMIQK16 of the R&S AMIQ or
Software Option R&S SMIQK16 in conjunction with the Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ

General settings	
Chip rate	
Standard	11 Mcps
Range	R&S AMIQ: 10 cps to max. 100 Mcps R&S SMIQ (B60): 1 kcps to max. 40 Mcps
Simulation mode	framed mode: generation of a sequence of data packets with the frame structure defined by the standard, interrupted by an idle time unframed mode: generation of a non-packet-oriented signal without frame structure, with the modulation modes and data rates defined by the 802.11b standard
User data	same as with single-carrier system
Baseband filter	
Standard	Gaussian , BxT = 0.3
Other filters	same as with single-carrier system
Parameters in framed mode	
Idle time	time between two successive packets (PPDUs) in µs; range 0 µs to 10000 µs
Number of packets	number of data packets to be generated; the minimum number of packets is 1; the maximum number depends on the packet length, idle time and oversampling
PLCP preamble and header format	long PLCP and short PLCP
PSDU bit rate	1 Mbps, 2 Mbps, 5.5 Mbps and 11 Mbps
PSDU modulation	DBPSK, DQPSK, CCK and PBCC (depending on PSDU bit rate)
PSDU data length	length of user data field in bytes of the packet to be transferred, range 0 byte to 2312 byte
Scrambling	activating or deactivating data packet scrambling
MAC header	activating and configuring the MAC header with the parameters Frame Control, Duration/ID, Address 1-4 and Sequence Control
Frame check sequence	activating or deactivating a 32 bit (4 byte) check sum for protecting the MAC header and the user data (frame body)
Power time template	automatic configuration of power ramping at the beginning and end of the data packets with shiftable start points of the ramps relative to the data packet
Parameters in unframed mode	
PSDU bit rate	1 Mbps, 2 Mbps, 5.5 Mbps and 11 Mbps
PSDU modulation	DBPSK, DQPSK, CCK and PBCC (depending on PSDU bit rate)
Sequence length	length of signal to be generated in bytes, maximum length depending on oversampling
Scrambling	activating or deactivating scrambling

Digital standard IEEE 802.11a Wireless LAN

Simulation of signals to Wireless LAN standard IEEE 802.11a, available as Software Option R&S AMIQK18 of the R&S AMIQ or Software Option R&S SMIQK18 in conjunction with the Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ.

General settings	
Kernel sample rate	
Standard	20 Msps
Range	R&S AMIQ: 10 sps to max. 100 Msps R&S SMIQ (B60): 1 ksps to max. 40 Msps

Simulation mode	framed mode: generation of a sequence of data packets with the frame structure defined by the standard, interrupted by an idle time unframed mode: generation of a non-packet-oriented signal without frame structure, with the modulation modes and data rates defined by the 802.11a standard
User data	same as with single-carrier system
Baseband filter	
Standard	ideal lowpass with Hanning window
Other filters	same as with single-carrier system
Parameters in framed mode	
Idle time	time between two successive packets (PPDUs) in µs; range 0 µs to 10000 µs
Number of packets	number of data packets to be generated; the minimum number of packets is 1; the maximum number depends on the packet length, idle time and oversampling
PLCP preamble	predefined according to 802.11a
PLCP SIGNAL field	automatically calculated
PSDU bit rate	6, 9, 12, 18, 24, 36, 48 and 54 Mbps
PSDU modulation	BPSK, QPSK, 16QAM or 64QAM, automatically set depending on specified PSDU bit rate
PSDU data length	length of user data field in bytes of the packet to be transferred, range 0 byte to 2312 byte, directly proportional to number of data symbols
Number of data symbols	number of OFDM symbols in data portion of packet, directly proportional to PSDU data length
Scrambling	data packet scrambling can be activated or deactivated; initial scrambler state can be set randomly or to a user-defined value
Interleaver	interleaver can be activated or deactivated
Time domain windowing	time domain windowing supported with transition times between 0 ns and 1000 ns
Service field	user-defined service field value supported
MAC header	activating and configuring the MAC header with the parameters Frame Control, Duration/ID, Address 1-4 and Sequence Control
Frame check sequence	activating or deactivating a 32 bit (4 byte) check sum for protecting the MAC header and the user data
Parameters in unframed mode	
PSDU bit rate	6, 9, 12, 18, 24, 36, 48 and 54 Mbps
PSDU modulation	BPSK, QPSK, 16QAM or 64QAM, automatically set depending on specified PSDU bit rate
PSDU data length	data length in bytes to be transferred, range 0 byte to 2312 byte, directly proportional to number of data symbols
Number of data symbols	number of OFDM symbols to be generated, directly proportional to PSDU data length
Scrambling	data scrambling can be activated or deactivated; initial scrambler state can be set randomly or to a user-defined value
Interleaver	interleaver can be activated or deactivated
Time domain windowing	time domain windowing supported with transition times between 0 ns and 1000 ns
Service field	user-defined service field value supported

Digital standard 1xEV-DO with R&S AMIQ and option R&S AMIQK17 or R&S SMIQ and options R&S SMIQK17 and R&S SMIQB60 (ARB)

Simulation of 1xEV-DO signals to North American Standard "CDMA2000 High Rate Packet Data Air Interface Specification", available as Software Option R&S AMIQK17 of the R&S AMIQ or Software Option R&S SMIQK17 in conjunction with the Arbitrary Waveform Generator Option R&S SMIQB60 of the R&S SMIQ

General settings	
Chip rate	
Standard	1.2288 Mcps
Range	R&S AMIQ: 10 cps to max. 100 Mcps R&S SMIQ (B60): 1 kcps to max. 40 Mcps
Link direction	forward link (simulation of up to 4 base stations) and reverse link (simulation of up to 4 mobile stations)
Sequence length	entry in frames of 26.67 ms, max. length depending on oversampling; R&S AMIQ02 and R&S AMIQ03 (for oversampling 4): 1 to 30 frames R&S AMIQ04 (for oversampling 4): 1 to 122 frames R&S SMIQB60 (for oversampling 2 in R&S WinIQSIM™ (oversampling >4 as a result of hardware oversampling in the R&S SMIQ)): 1 to 7 frames
Baseband filter	
Standard	CDMA2000 1X CDMA2000 1x + equalizer
Other filters	same as with single-carrier system
Clipping level	Setting of clipping value relative to highest peak in percent. Clipping takes place prior to baseband filtering and reduces the crest factor. The range is 1 % to 100 %.
Data sources for traffic channels	4 different data sources, 3 same as with single-carrier system, plus 1 PRBS source with differing start values for different code channels
Parameters for each base station (BS)	
State	ON/OFF
BS power	–80 dB to 0 dB
PN offset	0 to 511
Channel coding	All levels of channel coding provided by 1xEV-DO (e.g. convolutional encoder, symbol puncture and interleaver) are available. Four modes are available: – off: channel coding off – complete: complete channel coding on – without interleaving: channel coding on, but without interleaver – interleaving only: channel coding off, interleaver active only
Parameters for each mobile station (MS)	
State	ON/OFF
Mode	Access Mode Traffic Mode
Channel coding	All levels of channel coding provided by 1xEV-DO (e.g. convolutional encoder, symbol puncture and interleaver) are available. Four modes are available: – off: channel coding off – complete: complete channel coding on – without interleaving: channel coding on, but without interleaver – interleaving only: channel coding off, interleaver active only
Long code mask I/Q	0 to 3FF FFFF FFFF hex
Channel types	
Forward link	Pilot Channel, Preamble Traffic Channel MAC Reverse Activity Channel (MAC RA) up to 59 MAC Reverse Power Control Channels (MAC RPCs)

Reverse Link	Access Mode: Pilot Channel Data Channel Traffic Mode: Data Rate Control Channel (DRC) Acknowledge Channel (ACK) Pilot Channel Reverse Rate Indicator Channel (RRI) Traffic Channel
Data rates and modulation of forward traffic channel	38.4 kbps, 16 slots, QPSK 76.8 kbps, 8 slots, QPSK 153.6 kbps, 4 slots, QPSK 307.2 kbps, 2 slots, QPSK 307.2 kbps, 4 slots, QPSK 614.4 kbps, 1 slot, QPSK 614.4 kbps, 2 slots, QPSK 921.6 kbps, 2 slots, 8PSK 1228.8 kbps, 1 slot, QPSK 1228.8 kbps, 2 slots, 16QAM 1843.2 kbps, 1 slot, 8PSK 2457.6 kbps, 1 slot, 16QAM
Data rates of reverse data/ traffic channel	9.6 kbps 19.2 kbps 38.4 kbps 76.8 kbps 153.6 kbps
Assistant functions to facilitate operation	
Multichannel edit (forward link MAC RPC)	Common configuration of MAC RPC channels of a BS. Selectable parameters: – range of MAC RPC channels to be set – gain and gain step – data pattern – state
Copy BS/MS	Adopting the configuration of a BS/MS for another BS/MS to define multi-BS/ MS scenarios.

Import system

Import of I/Q data from other applications via a dynamic data exchange (DDE) interface or via TCP/IP ; further processing in R&S WinIQSIM™, e.g. baseband filtering or superposition of impairments

Baseband filtering	same as with single-carrier system
Sequence length	R&S AMIQ03: 1 sample to max. 4 Msample R&S AMIQ04: 1 sample to max. 16 Msample R&S SMIQB60: 1 sample to max. 524.216 sample
Simulation of impairments and transfer characteristics	same as with single-carrier system
Smoothing	same as with single-carrier system
Graphical output	same as with single-carrier system
ACP calculation	calculation of adjacent-channel power in spectrum display (ACP up, low and ACP up 1st alt, low 1st alt)
IF signal generation	modulation of calculated I/Q signal to IF in range 0.01 MHz to 25 MHz (output to I channel of R&S AMIQ)

Miscellaneous

Waveform transmission to R&S AMIQ	interfaces: IEC/IEEE bus (GPIB), RS-232-C, floppy; conversion of I/Q signal to 14 bit R&S AMIQ format: user-selectable clipping level (over- and underranging possible)
Remote control of R&S AMIQ	download and starting of waveforms, hardware configuration, alignment and fine adjustment, file management, BER test
For data transfer	IEC/IEEE bus (GPIB): card (from National Instruments) with drivers, IEC/IEEE bus cable; RS-232-C null-modem cable
System requirements	PC compatible to industry standard, CPU clock min. 100 MHz, Windows 95/ 98 with 32 Mbyte RAM or Windows NT with 48 Mbyte RAM recommended, at least 50 Mbyte hard disk memory, mouse, monitor: 1024 x 768 pixels with 256 colours recommended

Ordering information

I/Q Modulation Generator	R&S AMIQ	
4 Msample		1110.2003.03
16 Msample		1110.2003.04
Internal Arbitrary Waveform Generator in the R&S SMIQ	R&S SMIQB60	1136.4390.02
Digital Standard		
IS-95	R&S AMIQK11 R&S SMIQK11	1122.2003.02 1105.0287.02
CDMA2000	R&S AMIQK12 R&S SMIQK12	1122.2503.02 1105.0435.02
1xEV-DO	R&S AMIQK17 R&S SMIQK17	1122.3000.02 1154.7800.02
WCDMA TDD Mode (3GPP)	R&S AMIQK13 R&S SMIQK13	1122.2603.02 1105.1231.02
TD-SCDMA	R&S AMIQK14 R&S SMIQK14	1122.2703.02 1105.1383.02
IEEE 802.11b	R&S AMIQK16 R&S SMIQK16	1122.2903.02 1154.7700.02
IEEE 802.11a	R&S AMIQK18 R&S SMIQK18	1122.3100.02 1154.7952.02
OFDM Signal Generation	R&S AMIQK15 R&S SMIQK15	1122.2803.02 1105.1531.02

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SIGNAL GENERATOR
FREQ 2.140 000 000 0 GHz
LEVEL +19.1 dBm
PEP +30.0 dBm

Vector Signal Generator R&S SMIQ03HD

Dedicated to 3GPP

Supplement to
Vector Signal Generator R&S SMIQ
(see data sheet PD 0757.2438)

- Wide dynamic range: ACLR 70 dB typ. for 3GPP test model 1, 64 DPCH
- Single-carrier scenarios, enhanced features with option R&S SMIQB57 (3GPP downlink)
  - –Further improvement of ACLR (77 dB typ.)
  - –High output power (up to +30 dBm PEP)
- Multicarrier scenarios: integrated baseband filters to improve ACLR of 1 to 4 WCDMA carriers
- Short frequency and level setting time
- Optional fading simulator (R&S SMIQB14/B15) and noise generator/distortion simulator (R&S SMIQB17)

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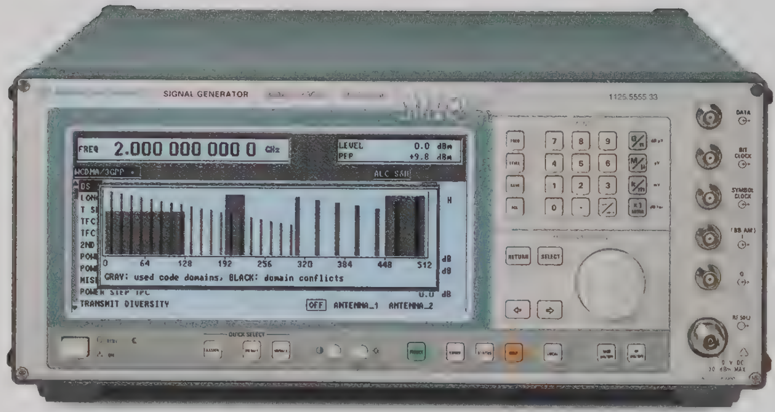


The third-generation mobile radio standards use broadband transmission methods to allow the configuration of communication networks with high data rates. WCDMA with its bandwidth of 3.84 MHz and the underlying CDMA method places particularly stringent requirements on the total transmission chain.

The signal statistics of a WCDMA signal reveal high crest factors (peak-to-average power ratios). Therefore, amplifiers with a wide linear range are required to ensure distortion-free transmission not only of average transmit power but also of high power peaks.

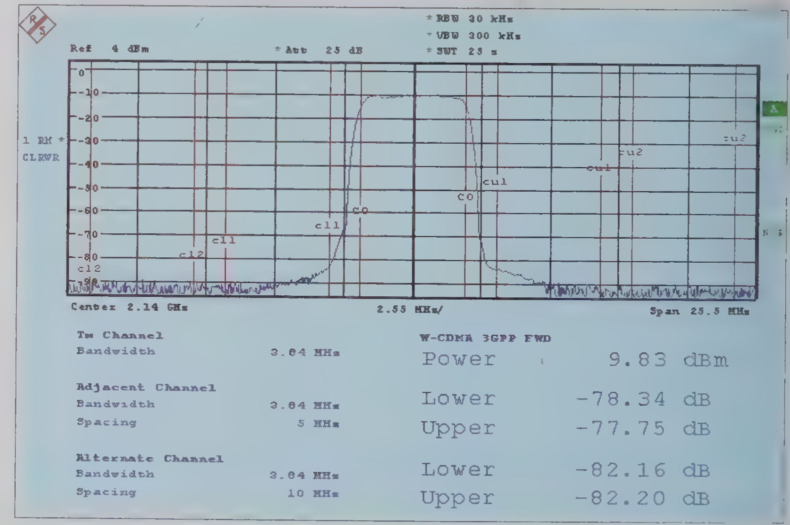
The requirements on base station power amplifiers become even more stringent since the amplifiers also transmit multicarrier signals within the 60 MHz downlink band. In addition to single-carrier power amplifiers (SCPA), multicarrier power amplifiers (MCPA) are increasingly used. Signal sources featuring wide dynamic range and high accuracy, such as the R&S SMIQ03HD, are required for the development and production testing of the amplifiers. WCDMA specifications allow base stations only a very low adjacent-channel power (ACP). The R&S SMIQ03HD supplies a test signal whose adjacent-channel leakage ratio (ACLR) is much better than the one required for base stations so that measurements can be carried out on amplifiers with sufficient dynamic range.

Extremely wide dynamic range for WCDMA 3GPP single-carrier signals in the downlink in conjunction with high signal output power

The use of a special filter option (R&S SMIQB57) improves the signal quality of a WCDMA single-carrier signal in the downlink (2110 MHz to 2170 MHz) regarding adjacent-channel power to a level previously unattained by any signal generator (ACLR 77 dB in adjacent channel and 82 dB in alternate channel). The high output power of the option (up to +30 dBm PEP) is an additional benefit. Additional driver amplifiers for driving the components are not required. This is a great benefit especially for the manufacturers of base station components. Costs are reduced and signal quality is not impaired by the noise of an external amplifier.

If more than one WCDMA carriers are to be generated, several R&S SMIQ03HD signal generators fitted with the R&S SMIQB57 option can be combined

FIG 1: ACLR measurement on single-carrier WCDMA signal (test model 1, 64 DPCH) using R&S SMIQB57



Tx Channel
Bandwidth 3.84 MHz

Adjacent Channel
Bandwidth 3.84 MHz
Spacing 5 MHz

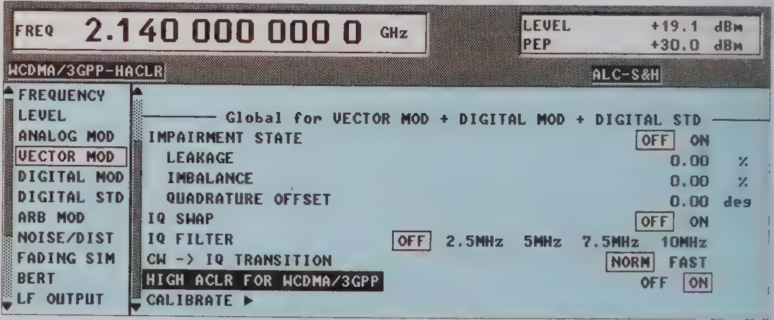
Alternate Channel
Bandwidth 3.84 MHz
Spacing 10 MHz

W-CDMA 3GPP FWD
Power 9.83 dBm

Lower -78.34 dB
Upper -77.75 dB

Lower -82.16 dB
Upper -82.20 dB

FIG 2: Special mode for high ACLR



FREQ 2.140 000 000 0 GHz
LEVEL +19.1 dBm
PEP +30.0 dBm

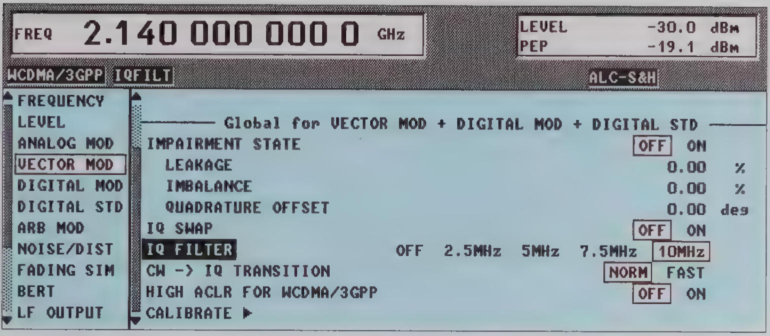
Global for VECTOR MOD + DIGITAL MOD + DIGITAL STD
IMPAIRMENT STATE OFF ON
LEAKAGE 0.00 %
IMBALANCE 0.00 %
QUADRATURE OFFSET 0.00 deg
IQ SWAP OFF ON
IQ FILTER OFF 2.5MHz 5MHz 7.5MHz 10MHz
CW -> IQ TRANSITION NORM FAST
HIGH ACLR FOR WCDMA/3GPP OFF ON
CALIBRATE ▶

to provide a multicarrier scenario of highest spectral purity.

Multicarrier scenarios can also be generated less elaborately using one signal generator only.

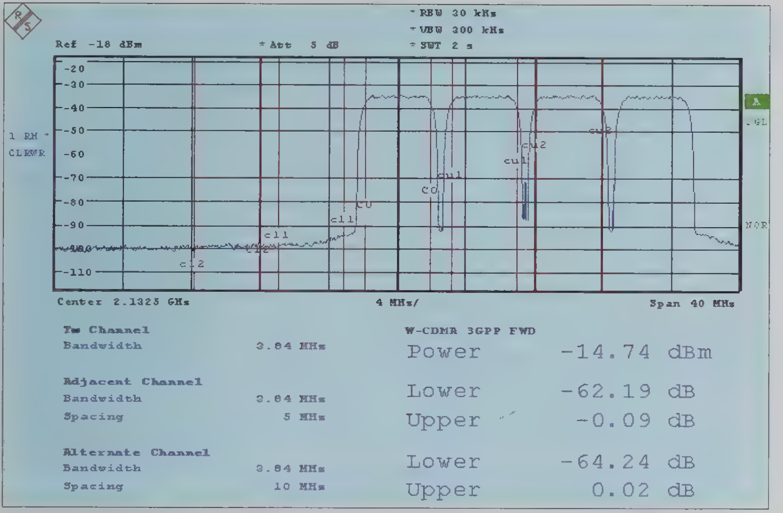
Wide dynamic range for single-carrier and multicarrier signals through integrated baseband filters for highest ALCR at 1 to 4 WCDMA carriers

Multicarrier signals can be generated by the R&S SMIQ03HD with optionally integrated Arbitrary Waveform Generator R&SSMIQB60, or by the R&S SMIQ03HD in combination with the I/Q Modulation Generator R&S AMIQ. The WCDMA multicarrier signals are calculated with the aid of the user-configurable R&S WinIQSIM™ Software that is available free of charge, and transferred to the arbitrary waveform generators. For each of the four scenarios (1 to 4 carriers), an I/Q filter (integrated as standard) with a cutoff frequency of 2.5 MHz, 5 MHz, 7.5 MHz or 10 MHz is switched on. Optimum ACLR values are thus obtained for each of the four possible carrier scenarios.



```
FREQ  2.140 000 000 0 GHz                       LEVEL        -30.0  dBm
                                                PEP          -19.1  dBm
WCDMA/3GPP  IQFILT                                           ALC-S&H
FREQUENCY
LEVEL       ——————— Global for VECTOR MOD + DIGITAL MOD + DIGITAL STD ———
ANALOG MOD  IMPAIRMENT STATE                                   OFF  ON
VECTOR MOD    LEAKAGE                                         0.00   %
DIGITAL MOD   IMBALANCE                                       0.00   %
DIGITAL STD   QUADRATURE OFFSET                               0.00  deg
ARB MOD     IQ SWAP                                            OFF  ON
NOISE/DIST  IQ FILTER           OFF  2.5MHz  5MHz  7.5MHz  10MHz
FADING SIM  CW -> IQ TRANSITION                               NORM  FAST
BERT        HIGH ACLR FOR WCDMA/3GPP                           OFF  ON
LF OUTPUT   CALIBRATE ▶
```

FIG 3: Four different I/Q filters allow ACLR performance of single-carrier to four-carrier WCDMA signals to be optimized

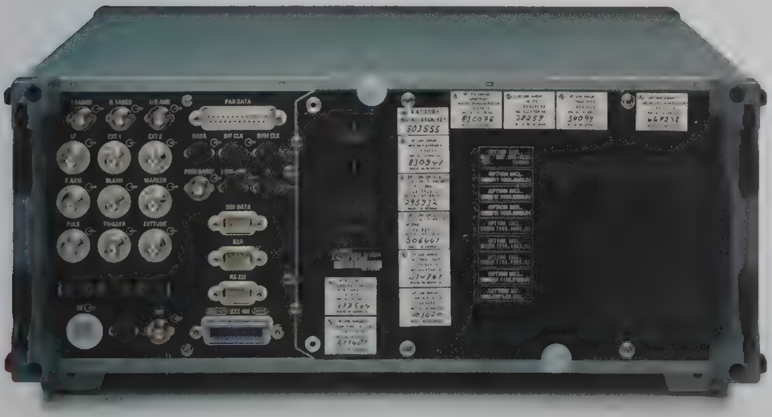


```
Tx Channel                            W-CDMA 3GPP FWD
Bandwidth            3.84 MHz         Power        -14.74 dBm

Adjacent Channel
Bandwidth            3.84 MHz         Lower        -62.19 dB
Spacing                 5 MHz         Upper         -0.09 dB

Alternate Channel
Bandwidth            3.84 MHz         Lower        -64.24 dB
Spacing                10 MHz         Upper          0.02 dB
```

FIG 4: ACLR measurement on four-carrier signal (signal generated by a single R&S SMIQ03HD)

Short setting time for level and frequency

For cost-effective production, a high throughput of modules or devices is required. With its extremely short setting times (frequency/level setting time: <3 ms/<2.5 ms) the R&S SMIQ03HD easily meets this requirement. Setting times can be further reduced in special modes (List mode: <500 µs, Fast Restore mode: <800 µs).

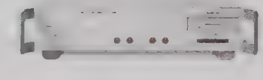


Optional fading simulator or noise generator/distortion simulator

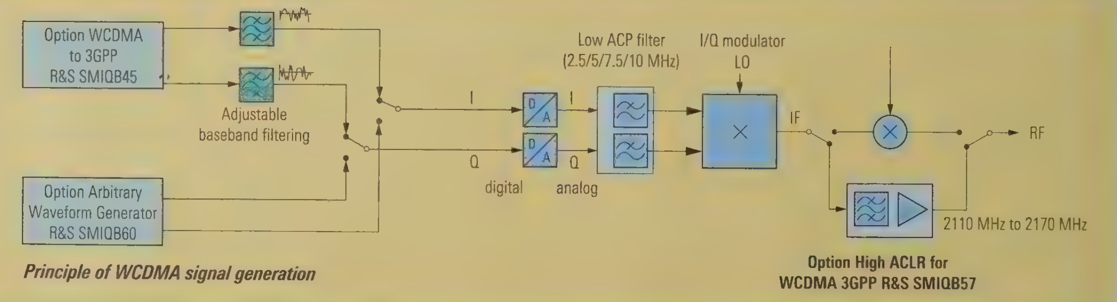
For universal applications of the R&S SMIQ03HD, fading simulators (R&S SMIQB14/B15) and a noise generator/distortion simulator (R&S SMIQB17) can be used to generate realistic signals. The Vector Signal Generators R&S SMIQ are the only generators on the market that can be equipped with internal fading simulators (one-box solution). Moreover, it is possible to use an additional option (R&S SMIQB49) for generating 3G fading scenarios (dynamic fading, 3GPP TS 25.141) for performance tests.

Further information about these options can be found in the R&S SMIQ data sheet.

Recommended equipment configurations for SCPA/MCPA applications

SCPA	Description	Type	Order No.
Internal baseband generation 	Vector Signal Generator	R&S SMIQ03HD	1125.5555.33
	Modulation Coder	R&S SMIQB20	1125.5190.02
	Data Generator	R&S SMIQB11	1805.4502.04
	Digital Standard WCDMA 3GPP FDD	R&S SMIQB45 [1]	1104.8232.02
	High ACLR for WCDMA 3GPP (2110 MHz to 2170 MHz)	R&S SMIQB57	1105.1831.02
Ext. baseband generation (e.g. using R&S AMIQ) 	Vector Signal Generator	R&S SMIQ03HD	1125.5555.33
	High ACLR for WCDMA 3GPP (2110 MHz to 2170 MHz)	R&S SMIQB57	1105.1831.02
	I/Q Modulation Generator, incl. R&S WinIQSIM™	R&S AMIQ	1110.2003.03 1110.2003.04

MCPA	Description	Type (multicarrier signals with one R&S SMIQ03HD)	Type (multicarrier signals with two or more R&S SMIQ03HD externally combined)	Order No.
Internal baseband generation 	Vector Signal Generator	R&S SMIQ03HD	R&S SMIQ03HD	1125.5555.33
	Modulation Coder	R&S SMIQB20	R&S SMIQB20	1125.5190.02
	Data Generator	R&S SMIQB11	R&S SMIQB11	1805.4502.04
	Digital Standard WCDMA 3GPP FDD	–	R&S SMIQB45 [1]	1104.8232.02
	High ACLR for WCDMA 3GPP (2110 MHz to 2170 MHz)	–	R&S SMIQB57	1105.1831.02
	Arbitrary Waveform Generator incl. R&S WinIQSIM™	R&S SMIQB60	–	1136.4390.02
Ext. baseband generation (e.g. using R&S AMIQ) 	Vector Signal Generator	R&S SMIQ03HD	R&S SMIQ03HD	1125.5555.33
	I/Q Modulation Generator, incl. R&S WinIQSIM™	R&S AMIQ	R&S AMIQ	1110.2003.03 1110.2003.04


Option WCDMA to 3GPP R&S SMIQB45
Adjustable baseband filtering
Option Arbitrary Waveform Generator R&S SMIQB60
I
Q
D/A
digital
analog
Low ACP filter (2.5/5/7.5/10 MHz)
I/Q modulator
LO
IF
RF
2110 MHz to 2170 MHz
Option High ACLR for WCDMA 3GPP R&S SMIQB57

Principle of WCDMA signal generation

Specifications

Specifications apply under the following conditions: 30 minutes warmup time at ambient temperature, specified environmental conditions met, calibration cycle adhered to, and total calibration performed. Data designated "overrange" not warranted.
For general data please refer to the standard data sheet of the R&S SMIQ (PD 0757.2438). The following data differs from the standard data or refers to additional features.

Digital Standard WCDMA 3GPP FDD (option R&S SMIQB45)

Single-carrier measurements	
Adjacent-channel leakage ratio, frequency 1850 MHz to 2200 MHz, level ≤8 dBm (PEP)	
1 DPCH (crest factor 5.4 dB, I/Q filter 2.5 MHz) Offset 5 MHz, low-distortion output mode Offset 10 MHz, low-noise output mode	 >67 dB, 71 dB typ. >73 dB, 76 dB typ.
Test model 1, 64 DPCH (crest factor 10.6 dB, I/Q filter 2.5 MHz) Offset 5 MHz, low-distortion output mode Offset 10 MHz, low-noise output mode	 >66 dB, 70 dB typ. >70 dB, 73 dB typ.

Multicarrier measurements

Adjacent-channel leakage ratio, frequency 2110 MHz to 2170 MHz, level ≤8 dBm PEP; multicarrier signals generated with the internal Arbitrary Waveform Generator R&S SMIQB60	
2 carriers, test model 1, 64 DPCH (crest factor 11 dB, I/Q filter 5 MHz) Offset 5 MHz, low-distortion output mode Offset 10 MHz, low-distortion output mode	 >60 dB, 64 dB typ. >65 dB, 68 dB typ.
3 carriers, test model 1, 64 DPCH (crest factor 11.3 dB, I/Q filter 7.5 MHz) Offset 5 MHz, low-distortion output mode Offset 10 MHz, low-distortion output mode	 >59 dB, 63 dB typ. >62 dB, 65 dB typ.
4 carriers, test model 1, 64 DPCH (crest factor 11.8 dB, I/Q filter 10 MHz) Offset 5 MHz, low-distortion output mode Offset 10 MHz, low-distortion output mode	 >58 dB, 62 dB typ. >61 dB, 64 dB typ.

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ISO 9001
DQS REG. NO 1954

High ACLR[1] for WCDMA 2110 MHz to 2170 MHz with option R&S SMIQB57

Frequency	
Frequency range	2110 MHz to 2170 MHz
3GPP channel bandwidth	3.84 MHz
Level	
Output level (PEP), normal output mode Overrange	−130 dBm to 27 dBm 30 dBm
Uninterrupted level setting Attenuator mode fixed	 >30 dB
Repeatability ALC STATE ON (CW mode) ALC STATE OFF (time interval 5 minutes, temperature interval 5 °C)	 0.05 dB typ. <0.15 dB
Linearity error (in displayed level range, attenuator mode fixed)	<0.2 dB over temperature[2], 0.1 dB typ.
Total level uncertainty[2] Attenuator mode auto (−120 dBm to 25 dBm (PEP)) CW Digital modulation	 <0.5 dB <0.7 dB
VSWR, output impedance Level >15 dBm (PEP) Level ≤15 dBm (PEP)	 <1.8 <1.5
Maximal permissible reverse power	1 W
Spectral purity	
Harmonics Level <25 dBm (PEP) Level <15 dBm (PEP)	 <−30 dBc, −40 dBc typ. <−40 dBc, −50 dBc typ.
Nonharmonics Carrier offset >10 kHz	 <−86 dBc
Subharmonics	none
Error vector magnitude (WCDMA, 3.84 Mcps), rms	4% typ.

[1] ACLR definition according to 3GPP TS 25.141:
Adjacent-channel leakage power ratio is the ratio of the average power centered on the assigned channel frequency to the average power centered on an adjacent channel frequency. In both cases the average power is measured with a filter that has root raised cosine (RRC) filter response with roll-off $\alpha = 0.22$ and a bandwidth equal to the chip rate.

[2] The specifications only apply to temperatures from +10 °C to +40 °C.

Specifications R&S SMIQB57 (continued)

Adjacent-channel leakage ratio for a single-carrier signal generated with Digital Standard WCDMA 3GPP FDD (option R&S SMIQB45)[2]

Adjacent-channel leakage ratio 1 DPCH (crest factor 5.4 dB, average power ≤10 dBm)	
Offset 5 MHz, low-distortion output mode	>75 dB, 78 dB typ.
Offset 10 MHz, low-noise output mode	>81 dB, 84 dB typ.
Adjacent-channel leakage ratio Test model 1, 64 DPCH (crest factor 10.6 dB, average power ≤10 dBm)	
Offset 5 MHz, low-distortion output mode	>74 dB, 77 dB typ.
Offset 10 MHz, low-noise output mode	>79 dB, 82 dB typ.

Restrictions on other data when using option R&S SMIQB57

General: Modulation bandwidth is reduced to 3GPP channel bandwidth. Due to steep bandpass filtering, additional amplitude and group delay distortions occur.

Topic concerned	Remark
– Broadband amplitude modulation	–
– Digital modulation	Increased EVM at higher symbol rates
– Digital Standard IS-95 CDMA R&S SMIQB42	IQ filter 850 kHz is missing
– Digital Standard WCDMA R&S SMIQB43, Digital Standard WCDMA 3GPP (FDD) R&S SMIQB45, Enhanced Functions for Digital Standard WCDMA 3GPP (FDD) R&S SMIQB48	Increased EVM
– Arbitrary Waveform Generator R&S SMIQB60	–
– Fading Simulators R&S SMIQB14/15	–
– Noise Generator/Distortion Simulator R&S SMIQB17	–
– Amplitude modulation	Not possible with option R&S SMIQB57
– Level setting, attenuator mode electronic	Not possible with option R&S SMIQB57
– Level setting, ALC OFF, MODE TABLE	Not possible with option R&S SMIQB57

Ordering information

Vector Signal Generator	R&S SMIQ03HD	1125.5555.33
Accessories supplied		
Power cable, operating manual		

Options

High ACLR for WCDMA 3GPP (2110 MHz to 2170 MHz)	R&S SMIQB57[1]	1105.1831.02

[1] Factory installation only

For all other options/recommended extras/application software please refer to the data sheet of the R&S SMIQ (PD 0757.2438).

Additional hints

R&S SMIQ03HD can be equipped with up to three of the following options: R&S SM-B5, R&S SMIQB14, R&S SMIQB15, R&S SMIQB17 and R&S SMIQB57.

R&S SMIQB47 cannot be fitted into the R&S SMIQ03HD (the I/Q filters 2.5 MHz, 5 MHz, 7.5 MHz and 10 MHz for High ACLR are fitted as standard in the R&S SMIQ03HD, the 850 kHz I/Q filter is omitted.)

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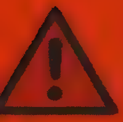
PD 075. .22 · Vector Signal Generator R&S SMIQ03HD · Trade names are trademarks of the owners · Subject to change · Data witho nces: typical values · Printed in Germany 0702

Safety Instructions

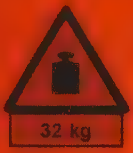
This unit has been designed and tested in accordance with the EC Certificate of Conformity and has left the manufacturer's plant in a condition fully complying with safety standards.

To maintain this condition and to ensure safe operation, the user must observe all instructions and warnings given in this operating manual.

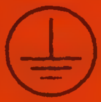
Safety-related symbols used on equipment and documentation from R&S:



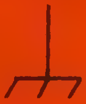
Observe operating nstructions



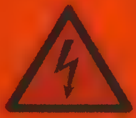
Weight indication for units >18 kg



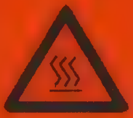
PE terminal



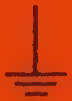
Ground terminal



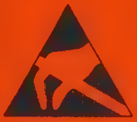
Danger! Shock hazard



Warning! Hot surfaces



Ground



Attention! Electrostatic sensitive devices require special care

1. The unit may be used only in the operating conditions and positions specified by the manufacturer. Unless otherwise agreed, the following applies to R&S products:

   IP degree of protection 2X, Pollution severity 2, overvoltage category 2, altitude max. 2000 m.

   The unit may be operated only from supply networks fused with max. 16 A.

2. For measurements in circuits with voltages V_{rms} > 30 V, suitable measures should be taken to avoid any hazards.

   (using, for example, appropriate measuring equipment, fusing, current limiting, electrical separation, insulation).

3. If the unit is to be permanently wired, the PE terminal of the unit must first be connected to the PE conductor on site before any other connections are made. Installation and cabling of the unit to be performed only by qualified technical personnel.

4. For permanently installed units without built-in fuses, circuit breakers or similar protective devices, the supply circuit must be fused such as to provide suitable protection for the users and equipment.

5. Prior to switching on the unit, it must be ensured that the nominal voltage set on the unit matches the nominal voltage of the AC supply network.

   If a different voltage is to be set, the power fuse of the unit may have to be changed accordingly.

6. Units of protection class I with disconnectible AC supply cable and appliance connector may be operated only from a power socket with earthing contact and with the PE conductor connected.

7. It is not permissible to interrupt the PE conductor intentionally, neither in the incoming cable nor on the unit itself as this may cause the unit to become electrically hazardous.

   Any extension lines or multiple socket outlets used must be checked for compliance with relevant safety standards at regular intervals.

8. If the unit has no power switch for disconnection from the AC supply, the plug of the connecting cable is regarded as the disconnecting device. In such cases it must be ensured that the power plug is easily reachable and accessible at all times (length of connecting cable approx. 2 m). Functional or electronic switches are not suitable for providing disconnection from the AC supply.

   If units without power switches are integrated in racks or systems, a disconnecting device must be provided at system level.

9. Applicable local or national safety regulations and rules for the prevention of accidents must be observed in all work performed.

   Prior to performing any work on the unit or opening the unit, the latter must be disconnected from the supply network.

   Any adjustments, replacements of parts, maintenance or repair may be carried out only by authorized R&S technical personnel.

   Only original parts may be used for replacing parts relevant to safety (eg power switches, power transformers, fuses). A safety test must be performed after each replacement of parts relevant to safety.

   (visual inspection, PE conductor test, insulation-resistance, leakage-current measurement, functional test).

continued overleaf

10. Ensure that the connections with information technology equipment comply with IEC950 / EN60950.
11. Lithium batteries must not be exposed to high temperatures or fire.

    Keep batteries away from children.

    If the battery is replaced improperly, there is danger of explosion. Only replace the battery by R&S type (see spare part list).

    Lithium batteries are suitable for environmentally-friendly disposal or specialized recycling. Dispose them into appropriate containers, only.

    Do not short-circuit the battery.
12. Equipment returned or sent in for repair must be packed in the original packing or in packing with electrostatic and mechanical protection.
13. Electrostatics via the connectors may damage the equipment. For the safe handling and operation of the equipment, appropriate measures against electrostatics should be implemented.
14. Any additional safety instructions given in this manual are also to be observed.

Certified Quality System

ISO 9001

DQS REG. NO 1954-04

Sehr geehrter Kunde,

Sie haben sich für den Kauf eines Rohde & Schwarz-Produktes entschieden. Hiermit erhalten Sie ein nach modernsten Fertigungsmethoden hergestelltes Produkt. Es wurde nach den Regeln unseres Qualitätsmanagementsystems entwickelt, gefertigt und geprüft. Das Rohde & Schwarz-Qualitätsmanagementsystem ist nach ISO 9001 zertifiziert.

Dear Customer,

You have decided to buy a Rohde & Schwarz product. You are thus assured of receiving a product that is manufactured using the most modern methods available. This product was developed, manufactured and tested in compliance with our quality management system standards.
The Rohde & Schwarz quality management system is certified according to ISO 9001.

Cher client,

Vous avez choisi d'acheter un produit Rohde & Schwarz. Vous disposez donc d'un produit fabriqué d'après les méthodes les plus avancées. Le développement, la fabrication et les tests respectent nos normes de gestion qualité.
Le système de gestion qualité de Rohde & Schwarz a été homologué conformément à la norme ISO 9001.

ROHDE&SCHWARZ

ROHDE&SCHWARZ
EC Certificate of Conformity

CE

Certificate No.: 2002-23

This is to certify that:

Equipment type	Stock No.	Designation
SMIQ06ATE	1125.5555.26	Vector Signal Generator

complies with the provisions of the Directive of the Council of the European Union on the approximation of the laws of the Member States

- relating to electrical equipment for use within defined voltage limits (73/23/EEC revised by 93/68/EEC)
- relating to electromagnetic compatibility (89/336/EEC revised by 91/263/EEC, 92/31/EEC, 93/68/EEC)

Conformity is proven by compliance with the following standards:

EN61010-1 : 1993 + A2 : 1995
EN50081-1 : 1992
EN61000-6-2 : 1999

Affixing the EC conformity mark as from 2002

ROHDE & SCHWARZ GmbH & Co. KG
Mühldorfstr. 15, D-81671 München

Munich, 2002-05-07

Central Quality Management FS-QZ / Becker

ROHDE&SCHWARZ
EC Certificate of Conformity

CE

Certificate No.: 2002-09

This is to certify that:

Equipment type	Stock No.	Designation
SMIQ03HD	1125.5555.33	Vector Signal Generator

complies with the provisions of the Directive of the Council of the European Union on the approximation of the laws of the Member States

- relating to electrical equipment for use within defined voltage limits (73/23/EEC revised by 93/68/EEC)
- relating to electromagnetic compatibility (89/336/EEC revised by 91/263/EEC, 92/31/EEC, 93/68/EEC)

Conformity is proven by compliance with the following standards:

EN61010-1 : 1993 + A2 : 1995
EN55011 : 1998 + A1 : 1999, Klasse B
EN61326-1 : 1997 + A1 : 1998

For the assessment of electromagnetic compatibility, the limits of radio interference for Class B equipment as well as the immunity to interference for operation in industry have been used as a basis.

Affixing the EC conformity mark as from 2002

ROHDE & SCHWARZ GmbH & Co. KG
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Munich, 2002-02-28

Central Quality Management FS-QZ / Becker

ROHDE&SCHWARZ
EC Certificate of Conformity

CE

Certificate No.: 99015

This is to certify that:

Equipment type	Order No.	Designation
SMIQ02B	1125.5555.02	Vector Signal Generator
SMIQ03B	1125.5555.03	
SMIQ04B	1125.5555.04	
SMIQ06B	1125.5555.06	
SMIQB10	1085.5009.02	Modulation Coder
SMIQB11	1085.4502.02/.04	Data Generator
SMIQB12	1085.2800.02/.04	Memory Extension
SMIQB14	1085.4002.02	Fading Simulator
SMIQB15	1085.4402.02	Second Fading Simulator
SMIQB17	1104.9000.02	Noise Generator
SMIQB20	1125.5190.02	Modulation Coder
SMIQB21	1125.5490.02	BER Measurement
SMIQB46	1105.0006.02	Low ACP
SMIQB47	1125.5090.02	Low ACP
SMIQB60	1136.4390.02	Arbitrary Waveform Generator

complies with the provisions of the Directive of the Council of the European Union on the approximation of the laws of the Member States

- relating to electrical equipment for use within defined voltage limits (73/23/EEC revised by 93/68/EEC)
- relating to electromagnetic compatibility (89/336/EEC revised by 91/263/EEC, 92/31/EEC, 93/68/EEC)

Conformity is proven by compliance with the following standards:

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EN50081-1 : 1992
EN50082-2 : 1995

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	4425 Nicole Drive	(301) 459 28 10
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1 Preparation for Use

1.1 Putting into Operation

Before putting the SMIQ into operation, please make sure that

- the covers of the casing are put on and screwed,
- the ventilation openings are free,
- no signal voltage levels exceeding the permissible limits are applied at the inputs,
- the outputs of the instrument are not overloaded or connected incorrectly.

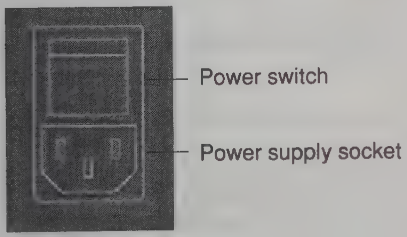
If these points are not observed, the instrument might be damaged.

1.1.1 Supply Voltage

The SMIQ can be operated at a.c. systems from 90 V to 132 V and 180 V to 265 V at system frequencies from 47 Hz to 440 Hz. The power supply socket is situated at the rear of the instrument. The instrument automatically sets itself to the voltage applied within the permissible voltage ranges. It is not necessary to set the instrument to a certain supply voltage.

1.1.2 Switching On/Off the Instrument

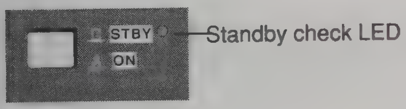
Power switch at the rear of the instrument



Switch on/off: ➢ Press power switch at the top (I) / bottom (0)

The power switch can remain switched on permanently. Switching off is only necessary when the instrument is to be completely disconnected from the mains.

On/off switch at the front of the instrument



Switch on: ➢ Press switch.
The instrument is ready for operation.

Switch off: ➢ Release switch.
The instrument assumes the STANDBY mode.

1.1.3 Initial Status

Upon switching on, the instrument either automatically assumes the status which was set when it was switched off (parameter POWER-ON STATE PREVIOUS SETTING in LEVEL-LEVEL menu) or the RF output is disconnected (POWER-ON STATE RF OFF).
If the instrument need not to be operated from the initial status any further, a defined default status should be established by pressing the [PRESET] key prior to further settings.

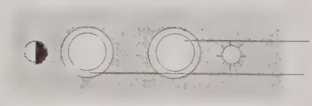
STANDBY Mode

In the STANDBY mode the optional reference oscillator (option SM-B1) remains switched on, which increases frequency accuracy.

Frequency accuracy after switching on when the oven-controlled reference oscillator is fitted (option SM-B1)

When switching on from the STANDBY mode, the specified frequency accuracy is reached immediately. If the power switch was switched off, the reference oscillator needs some minutes of warm-up time to reach its nominal frequency. During this period of time, the output frequency does not yet reach its final value either. In the status line in the header field of the display the message "OVEN COLD" is displayed for this time.

1.1.4 Setting Contrast and Brightness of the Display



Contrast and brightness of the display can be set by means of the contrast and brightness controls situated below the display.

1.1.5 RAM with Battery Back-Up

The SMIQ has a static read-write memory (CMOS-RAM) with battery back-up, in which 50 different complete settings of the instrument can be stored (cf. Chapter 2, section "Storing and Calling of Instrument Settings"). In addition, all data and/or lists the user enters himself, such as for list mode, memory sequence, and user correction of the level, are stored in the RAM. Further, all data of the calibrations running within the instrument in the SMIQ are stored in the RAM (cf. Chapter 2, section "Calibration").

A lithium battery with a service life of approx. 5 years serves to supply the RAM with power. When the battery is discharged, the data stored will be lost. For exchange of the battery see Service Manual.

1.1.6 Preset Setting

A defined setting status is achieved by pressing the [PRESET] key.

Preset Status:

RF frequency	100 MHz
RF level	-30 dBm
Reference frequency	internal, adjustment off
Offsets	0
Modulations	switched off
Transient-free level setting	switched off, level attenuator mode: AUTO
Internal level control	level ALC: AUTO
User correction	level UCOR: OFF
LF output	switched off
Sweep	switched off
List mode	switched off
Memory sequence	switched off
Suppression of indications	system security: unaltered
Protection of calibration data	protection lock: unaltered
Settings stored	unaltered
Data, lists etc. stored	unaltered
IEC-bus address	unaltered
Beeper	unaltered

All parameters and circuit states, even those of operating modes which are not activated, are preset by means of Preset.

The presettings going beyond the above list can be seen from the menu representations as of Section 2.4 which each indicate the Preset setting status.

1.2 Functional Test

On switching on the instrument and permanently during operation, the SMIQ carries out a self test. The ROM contents as well as the battery of the non-volatile RAM are checked on switching on the instrument and the RAM contents with every calling the memory. The most important instrument functions are automatically monitored during operation.

If an error is detected, the message "ERROR" is displayed in the status line. For further identification of the error, press the [ERROR] key. Thereupon a description of the error/s is displayed (cf. Chapter 2, section "Error Messages"). Return to the menu exited by pressing the [RETURN] key.

If required, the self tests can be induced purposefully. See Chapter 4, section "Functional Test".
Further, internal test points can be polled by the user and the results be read out and displayed. See Chapter 2, section "Voltage Indication of Test Points".

1.3 Fitting the Options

Due to its variety of options, the SMIQ offers the possibility of providing the instrument with the equipment exactly corresponding to the application. Newly fitted options are automatically recognized and the relevant parameters added in the menu.

After every change of the instrument configuration, the CMOS RAM has to be cleared as the storage data shift:

- ➢ Switch off the instrument
- ➢ Switch the instrument on again with the [RESET] key pressed

The internal calibration routines VECTOR MOD, VCO SUM, and LEV PRESET now have to be called up again to restore the cleared calibration values.
These routines are accessible via menu UTILITIES-CALIB (see also Chapter 2, section "calibration"). The calibration routines have to be carried out in the following order:

1. VCO SUM (Summing loop)
2. VECTOR MOD
3. LEV PRESET

1.3.1 Opening the Casing

Caution:
Prior to opening the SMIQ, unplug the power connector.

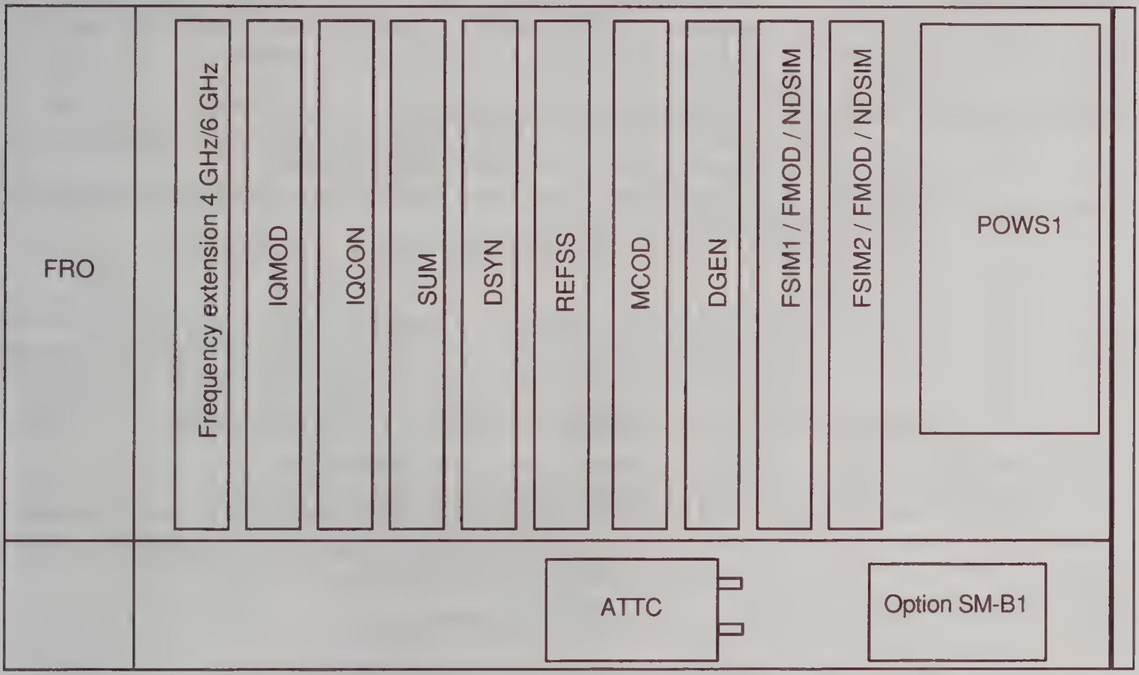
Remove paneling

- ➢ Remove four screws in the two tilt feet at the rear of the instrument.
- ➢ Remove the upper paneling towards the top and rear.
- ➢ Turn the instrument.
- ➢ Remove the lower paneling towards the top and rear.

Open ventilation ducts

When an option is fitted at a slot which has not been used up to now, the appropriate ventilation duct of the plexiglas plate at the left in the casing frame must be opened. The openings are pre-punched so that the respective part is easy to break out.

1.3.2 Overview of the Slots


FRO
Frequency extension 4 GHz/6 GHz
IQMOD
IQCON
SUM
DSYN
REFSS
MCOD
DGEN
FSIM1 / FMOD / NDSIM
FSIM2 / FMOD / NDSIM
POWS1
ATTC
Option SM-B1

FRO = front unit
FMOD = FM/PM modulator
IQMOD = I/Q modulator
IQCON = I/Q converter
SUM = summing loop
DSYN = digital synthesis
REFSS = reference/step synthesis

MCOD = modulation coder
DGEN = data generator
FSIM = fading simulator
POWS1 = power supply
ATTC = attenuator
NDSIM = Noise generator and distortion simulator

Fig. 1-1 SMIQ, view from the top

1.3.3 Option SM-B1 - Reference Oscillator OCXO

Fitting the option

- Fasten the option at the back end of the lateral opening by means of the screw threads provided there.
- If the two last slots are both occupied, one of these modules must be removed temporarily.
- Feed ribbon cable W710 through the rear square cut-out to the motherboard, insert into connector X22 and snap in the locking.
- Feed coaxial cable W170 from socket X711 of the option through the second cut-out along the rear transverse panel to connector X74 at the A7 module, reference/step synthesis, via the motherboard and insert there.

Set tuning voltage and calibrate OCXO

The crystal oscillator was factory-tuned to nominal frequency and the appropriate tuning voltage indicated on the cover of the module. The calibration value now has to be calculated from this value and transferred to the memory of the signal generator.

Calculate calibration value

The tuning voltage is generated by a 12b-bit-D/A converter which is scaled such that a tuning voltage of 12 volts is generated with calibration value (CALIBRATION DATA) 4000.
The calibration value is thus calculated from the tuning voltage (V_{tun}) as follows

$$\text{CALIBRATION DATA} = V_{tun} \times 4000 \ / \ 12$$

For checking purposes, the voltage at pin 16 of plug X22 on the motherboard can be remeasured and corrected if necessary. A check by means of frequency measurement may only be made after a warm-up of 2 hours and against a calibrated reference.

Store calibration value

➢ Unlock protection level 2 with code 250751.

v Call menu UTILITIES-CALIB-REF OSC.

➢ Enter the calculated calibration voltage with CALIBRATION DATA by means of the rotary knob or keypad.

➢ Select STORE CALIBRATION DATA

➢ Terminate entry using the [SELECT] key
The new calibration value is stored in the EPROM.

Note: *The flash EPROM does not permit the deletion of individual data. Thus new memory space is occupied for each calibration. If there is no memory space available any more, the EPROM must be cleared by an authorized service shop and be written into anew. Thus a calibration should only be made if necessary*

1.3.4 Option SM-B5 - FM/PM Modulator

The FM/PM modulator is fitted at the slot with label 'FMOD'.

Fitting the option

➢ Withdraw cable W89 from X99 of the summing loop and use again.

➢ Establish the following connections:

Cable	From	To	Signal
W89	A8-X89	A6-X67	FDSYN
W65	A6-X65	A7-X71	REF100
W67	A6-X69	A9-X99	FDFM

1.3.5 Option SMIQB11 - Data Generator

The Data Generator is fitted at the slot with label "DGEN".

- ➢ Plug the module into the slot.
- ➢ Lock it and fasten all screws.
- ➢ Plug W341 onto X341.
- ➢ Open the air inlets at the housing frame by breaking out the safety glass plate which belongs to the option.

1.3.6 Option SMIQB12 - Memory Extension to Data Generator

The Data Generator can be upgraded with up to two memory extension units (SMIQB12). The first Memory Extension is fitted at the slot with label "X350", the second Memory Extension at slot with label "X351".

- ➢ Remove option Data Generator from SMIQ.
- ➢ Withdraw the screws of the labelled screening cover and remove the cover.
- ➢ The first memory extension unit has to be pluged on connector X350 near the battery.
- ➢ The second memory extension unit has to be pluged on connector X351.
- ➢ Refit the screening cover of the Data Generator module.
- ➢ Plug the Data Generator back into the appropriate place, lock it and fasten all screws.

1.3.7 Option SMIQB14 - Fading Simulator FSIM1

The Fading Simulator is fitted at the slot with label 'FSIM1'.

Before fitting the option SMIQB14 (FSIM1) first check the correct settings of the jumpers on the module. For FSIM modules of series 1085.XXXX (see screening cover) the jumper setting also depends on the number of fading simulators installed, either one (FSIM1) or two (FSIM1 and FSIM2).

After an instrument warm-up period of 2 hours, the IQ Modulator should be calibrated.

Two coaxial connecting cables (Stock No. 1085.4448.00) are part of the equipment supplied with the option. The cables can be connected to the outputs I FADED and Q FADED at the rear of SMIQ.

Checking the jumper settings

Jumper Setting for the Fading Simulator FSIM1:
(Cf. labeling 'Jumper Setting' and labeling on the screening cover of the module)

FSIM1	**One Fading Simulator installed FADING SIMULATOR:** `1085.XXXX`			**Two Fading Simulators installed FADING SIMULATOR:** `1085.XXXX`			**FADING SIMULATOR:** `1114.XXXX`		
Jumper connects	X1.1	with	X1.2	X1.1	with	X1.2	X1.2	with	X1.3
	X8.1	with	X8.2	X8.2	with	X8.3	X8.1	with	X8.2
	X9.1	with	X9.2	X9.2	with	X9.3	X9.2	with	X9.3
	X12.2	with	X12.3	X12.2	with	X12.3	X12.2	with	X12.3
	X13.1	with	X13.2	X13.2	with	X13.3	X13.1	with	X13.2
	X15.1	with	X15.2	X15.1	with	X15.2	X15.1	with	X15.2
	X16.1	with	X16.2	X16.2	with	X16.3	none		

Fading modules of series 1114.XXXX also require the setting of a DIP switch. The setting is indicated on the screening cover.

	FADING SIMULATOR `1114.XXXX`	
Switch	1	2
FSIM1	OFF	ON

Fitting the option

- ➢ Plug the module into the appropriate slot, lock it and fasten all screws.
- ➢ Open the air inlets at the housing frame by breaking out the safety glass plate which belongs to the option.
- ➢ Depending on the fact whether the option SMIQB20 (MCOD) has been installed in the SMIQ, the following coaxial connections have to be made:

Note: Please store the remaining cables. They will be required if further options will be installed at a later stage or options will be removed.

		SMIQ with option MCOD	**SMIQ without option MCOD**	**Remark about cable**
Cable	**from FSIM1**	**to**	**to**	
W361	X361	MCOD X325	-	W361 was provided with FSIM1
W244	X361	-	FRO I socket	Withdraw W244 from X244 of module IQMOD
W363	X363	MCOD X328	-	W363 was provided with FSIM1
W245	X363	-	FRO Q socket	Withdraw W245 from X245 of module IQMOD
W367	X367	IQMOD X244	IQMOD X244	W367 was provided with FSIM1
W388	X368	REAR I FADED	REAR I FADED	W388 was provided with FSIM1
W370	X370	IQMOD X245	IQMOD X245	W370 was provided with FSIM1
W391	X371	REAR Q FADED	REAR Q FADED	W391 was provided with FSIM1

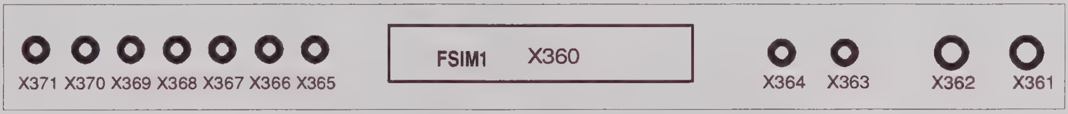


Fig. 1-2 Module FSIM

- ➢ The included adhesive label 'Option included' is to be fixed at the rear panel of the SMIQ.

Calibrating the Fading Simulator and the I/Q Modulator

- ➢ Warm-up the instrument for 2 hours.
- ➢ Call up menu UTILITIES - CALIB - VECTOR MOD.

1.3.8 Option SMIQB15 - Second Fading Simulator (FSIM2)

The second Fading Simulator is fitted at the slot with label 'FSIM2'.

Before fitting the option SMIQB15 (FSIM2) please check the correct settings of the jumpers on both fading modules FSIM1 (see Section 1.3.7) and FSIM2. The jumper settings depend on the series of the FSIM module, they are also indicated on the screening cover.

After an instrument warm-up period of 2 hours, the IQ Modulator should be calibrated.

Checking the jumper settings

Jumper Setting for the Fading Simulator FSIM2:
(cf. module number and labeling 'Jumper Setting' on the screening cover of the module)

FSIM2	FADING SIMULATOR 1085.XXXX			FADING SIMULATOR 1114.XXXX		
Jumper connects	X1.2	with	X1.3	X1.1	with	X1.2
	X8.2	with	X8.3	X8.2	with	X8.3
	X9.2	with	X9.3	X9.1	with	X9.2
	X12.1	with	X12.2	X12.1	with	X12.2
	X13.2	with	X13.3	X13.2	with	X13.3
	X15.2	with	X15.3	X15.2	with	X15.3
	X16.2	with	X16.3	none		

Fading modules of series 1114.XXXX also require the setting of a DIP switch. The setting is indicated on the screening cover.

	FADING SIMULATOR 1114.XXXX	
Switch	1	2
FSIM1	OFF	ON
FSIM2	ON	OFF

Fitting the option

- Plug the module into the appropriate slot for the FSIM2, lock it and fasten all screws.
- Open the air inlets at the housing frame by breaking out the safety glass plate which belongs to the option.
- The following coaxial connections have to be made:

	FSIM2 Module number 1085.XXXX	FSIM2 Module number 1114.XXXX	FSIM1
Cable	**from**	**from**	**to**
W362	X361	X362	X362
W364	X363	X364	X364
W365	X365	X365	X365
W366	X367	X368	X366
W369	X370	X371	X369

Note: *Please store the remaining cables. They will be required if further options will be installed at a later stage or options will be removed.*

- The included adhesive label 'Option included' is to be fixed at the rear panel of the SMIQ.

Calibrating the Fading Simulator and the I/Q Modulators

- Warm-up the instrument for 2 hours.
- Call up menu UTILITIES - CALIB - VECTOR MOD.

Retrofit for 2-channel fading

Cables W388 and W391 have to be repositioned for 2-channel fading.

- Unplug W388 from X368 of FSIM1 and plug to X368 (1085.XXXX) or X367 (1114.XXXX) of FSIM2.
- Unplug W391 from X371 of FSIM1 and plug to X371 (1085.XXXX) or X370 (1114.XXXX) of FSIM2.

1.3.9 Option SMIQB17 - Noise Generator and Distortion Simulator

Depending on which options are fitted, the NDSIM module is mounted in the slot labelled FSIM1/FMOD/NDSIM or FSIM2/FMOD/NDSIM or E6GHZ/FMOD/NDSIM (in older units slot FSIM1/FMOD or FSIM2/FMOD).

After an instrument warm-up period of 1 hour, the NDSIM as well as the IQ Modulator should be calibrated. Then the instrument is ready for use.

Six coaxial connecting cables (W601, W602, W603, W388, W605, W391 and the adhesive option label are part of the equipment supplied with the option.

Fitting the option

- ➢ Plug the module into the appropriate slot.
- ➢ Lock it and fasten all screws.
- ➢ Open the air inlets at the housing frame by breaking out the safety glass plate which belongs to the option.
- ➢ If options FSIM1 and FSIM2 are fitted, cables W388 and W391 have to be connected as shown in Fig. 1-3.
- ➢ Depending on which modules are installed in the SMIQ, the following coaxial connections are to be made:

		SMIQ **without option MCOD** **without option FSIM1** **without option FSIM2**		**SMIQ** **with option MCOD** **without option FSIM1** **without option FSIM2**		**Remark about cable**
Cable	**from NDSIM**	**to**		**to**		
W244	X601	FRO	I-connector	-		Cable was provided with unit
W245	X602	FRO	Q-connector	-		Cable was provided with unit
W601	X601	-		MCOD	X325	W601 was provided with NDSIM
W602	X602	-		MCOD	X328	W602 was provided with NDSIM
W603	X603	IQMOD	X244	IQMOD	X244	W603 was provided with NDSIM
W388	X604	REAR	I FADED	REAR	I FADED	W388 was provided with NDSIM
W605	X605	IQMOD	X245	IQMOD	X245	W605 was provided with NDSIM
W391	X606	REAR	Q FADED	REAR	Q FADED	W391 was provided with NDSIM

		SMIQ **with option MCOD** **with option FSIM1** **without option FSIM2**		**SMIQ** **with option MCOD** **with option FSIM1** **with option FSIM2**		**Remark about cable**
Cabel	**from NDSIM**	**to**		**to**		
W601	X601	FSIM1	X367	FSIM1	X367	W601 was provided with NDSIM
W602	X602	FSIM1	X370	FSIM1	X370	W602 was provided with NDSIM
W603	X603	IQMOD	X244	IQMOD	X244	W603 was provided with NDSIM
W388	X604	REAR	I FADED	REAR	I FADED	W388 was provided with NDSIM
W605	X605	IQMOD	X245	IQMOD	X245	W605 was provided with NDSIM
W391	X606	REAR	Q FADED	REAR	Q FADED	W391 was provided with NDSIM

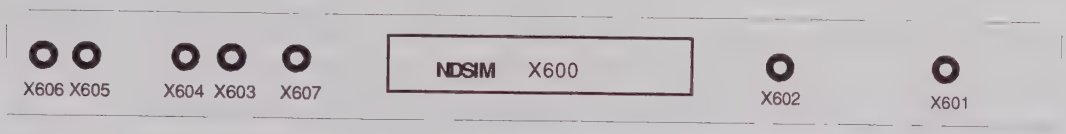


Fig. 1-3 Module NDSIM

Connector X607 is unused and provided only in some modules.

- ➢ The included adhesive label 'Option included' is to be fixed at the rear panel of the SMIQ.

Calibrating the NDSIM and the I/Q Modulator

- ➢ Warm-up the instrument for 1 hours.
- ➢ Call up menu UTILITIES - CALIB - VECTOR MOD.

1.3.10 Option SMIQB20 - Modulation Coder

The Modulation Coder is fitted at the slot with label "MCOD".

- ➢ Plug the module into the appropriate place, lock it and fasten all screws.
- ➢ Open the air inlets at the housing frame by breaking out the safety glass plate which belongs to the option
- ➢ Depending on the fact whether the option SMIQB14 (FSIM1) has been installed in the SMIQ, the following coaxial connections have to be made (cf. Fig. 1-4):

Note: *Please store the remaining cables. They will be required if further options will be installed at a later stage or options will be removed.*

		SMIQ without option FSIM 1		SMIQ with option FSIM 1		Remark about cable
Cable	from MCOD	to		to		
W322	X321	REFSS	X72	REFSS	X72	W322 was provided with MCOD.
W72	X322	DSYN	X81	DSYN	X81	Withdraw W72 from X72 of module REFSS.
W243	X323	BACK	POW RAMP socket	BACK	POW RAMP socket	Withdraw W243 from X243 of module IQMOD.
W324	X324	IQMOD	X243	IQMOD	X243	W324 was provided with MCOD.
W325	X325	IQMOD	X244		-	W325 was provided with MCOD.
W361	X325		-	FSIM 1	X361	W361 was provided with FSIM1.
	X326		-	-	-	-
W244	X327	FRO	I socket	FRO	I socket	Withdraw W244 from X244 of module IQMOD. If FSIM1 is installed withdraw W244 from X361 of module FSIM1.
W328	X328	IQMOD	X245		-	W328 was provided with MCOD.
W363	X328		-	FSIM 1	X363	W363 was provided with FSIM1.
	X329		-	-	-	-
W245	X330	FRO	Q socket	FRO	Q socket	Withdraw W245 from X245 of module IQMOD. If FSIM1 is installed withdraw W245 from X363 of module FSIM1.

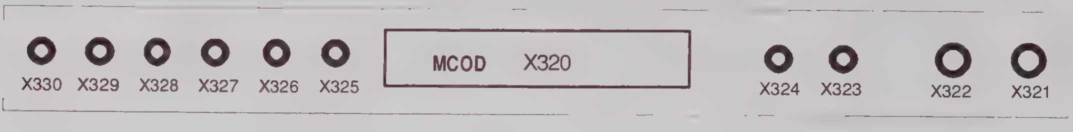


Fig. 1-4 Module MCOD

- The included adhesive label "Option included" is to be fixed at the rear panel of the SMIQ.

1.3.11 Option SMIQB21 - Bit Error Rate Test

Software option SMIQB21 has to be enabled by entering a key upon installation. An option label containing the installation key is supplied and has to be affixed to the rear of SMIQ for service and repair purposes.
As a prerequisite, SMIQ must be fitted with hardware option SMIQB20 (modulation coder).

Enabling the option

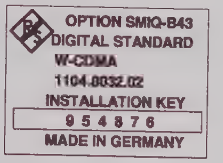
- Switch on SMIQ.
- Call UTILITIES menu (select with rotary knob, confirm with [SELECT] key).
- Call INSTALL menu ==> [SELECT].
- Call OPTION TO INSTALL menu ==> [SELECT].
- Select option SMIQB21 BERT ==> [SELECT].
- Enter the 6-digit installation key shown on the option label into the INSTALLATION KEY line. Then press the [ENTER] key.
- You will receive a message confirming that the installation key has been verified and the option is installed.
- To make the option available, the unit has to be switched off and on again.
- Once the installation has been successfully completed, BER measurement can be selected from the main menu.

Once the installation has been completed, the availability of the new option can be checked in the module list in the UTILITIES DIAG CONFIG menu.

1.3.12 Other Software Options SMIQB42 / SMIQB43 / SMIQB45 / SMIQB47 / SMIQB48 / SMIQB49 / SMIQB51 / SMIQB60 / SMIQK11 / SMIQK12 / SMIQK13 / SMIQK14 / SMIQK15 / SMIQK16 / SMIQK17 / SMIQK18

You have acquired a software option to go with your Signal Generator SMIQ from Rohde & Schwarz. This option must be enabled by an installation keyword. Please affix the enclosed option label including the installation keyword to the rear panel of the SMIQ since it is required for service and repair. For installation of software option SMIQB21 cf. section 1.3.11.

Enclosed option label with a typical keyword:



OPTION SMIQ-B43
DIGITAL STANDARD
W-CDMA
1104.8032.02
INSTALLATION KEY
9 5 4 8 7 6
MADE IN GERMANY

<== Example

For a sucessful installation the necessary hardware options as well as the firmware version are listed in the following table. The firmware version is indicated in the right part of the display when only the main menu appears. If your instrument is equipped with an older version, update the firmware first.

The current SMIQ firmware version can be downloaded from the Rohde & Schwarz website:

www.rohde-schwarz.com
(TEST & MEASUREMENT -> SIGNAL GENERATION -> PRODUCTS -> SMIQ -> Download Area)

This website also contains installation information in the form of firmware release notes.

Should you have any downloading or installation problems, please contact your nearest Rohde & Schwarz representative.

Option	Necessary hardware/software options	Firmware version (at least)	Remark
SMIQB42	SMIQB10 (MCOD) and SMIQB11 (DGEN)	3.20	
	SMIQB20 (MCOD2) and SMIQB11 (DGEN)	5.10 HX	
SMIQB43	SMIQB10 (MCOD) and SMIQB11 (DGEN)	3.80	
	SMIQB20 (MCOD2) and SMIQB11 (DGEN)	5.10 HX	
SMIQB45	SMIQB20 (MCOD2) and SMIQB11 (DGEN)	5.10 HX	
SMIQB47	IQ-Modulator (IQMOD) variant 8	3.91	
SMIQB48	SMIQB20 (MCOD2), SMIQB11 (DGEN) and SMIQB45	5.30 HX	
SMIQB49	SMIQB14 and/or SMIQB15 (FSIM)	5.40 HX	
SMIQB51	SMIQB20 (MCOD2) and SMIQB11 (DGEN)	5.65 HX	
SMIQB60	SMIQB20 (MCOD2) and SMIQB11 (DGEN)	5.20 HX	PC software WinIQSIM recommended
SMIQK11	SMIQB60	5.20 HX	PC software WinIQSIM recommended
SMIQK12		5.20 HX	
SMIQK13		5.20 HX	
SMIQK14		5.20 HX	
SMIQK15		5.20 HX	
SMIQK16		5.70 HX	
SMIQK17		5.70 HX	
SMIQK18		5.80 HX	

Enabling option:

- ➢ Switch on SMIQ.
- ➢ Call up menu UTILITIES. (Select it by means of the rollkey, confirm with [SELECT] key).
- ➢ Call up menu INSTALL ==> [SELECT]
- ➢ Call up menu OPTION TO INSTALL ==> [SELECT]
- ➢ Select the option you want to install ==> [SELECT]
- ➢ Read the digit keyword on the option label and enter it into the corresponding field in the INSTALLATION KEY line. Then press the [ENTER] key.
- ➢ A message will then be issued to the effect that the keyword has been checked and that the option is being installed.
- ➢ For the option to be available the unit has to be switched off and then on again.

After installation, the new option is listed in the module list of the UTILITIES-DIAG-CONFIG menu. In case of any problems contact your Rohde&Schwarz service center.

1.3.13 Option SMIQB19 - Rear Panel Connections for RF and LF

The SMIQ can be retrofitted to include rear panel connections for RF and LF for mounting it into a 19" rack using option SMIQB19. The mounting instructions are attached to the option.

1.4 Mounting into a 19" Rack

Caution: *Ensure free air inlet at the perforation of the side walls and air outlet at the rear of the instrument in rack mounting.*

The SMIQ can be mounted into a 19" rack by means of rack adapter ZZA-94 (stock no. 396.4905.00). The mounting instructions are attached to the adapter.

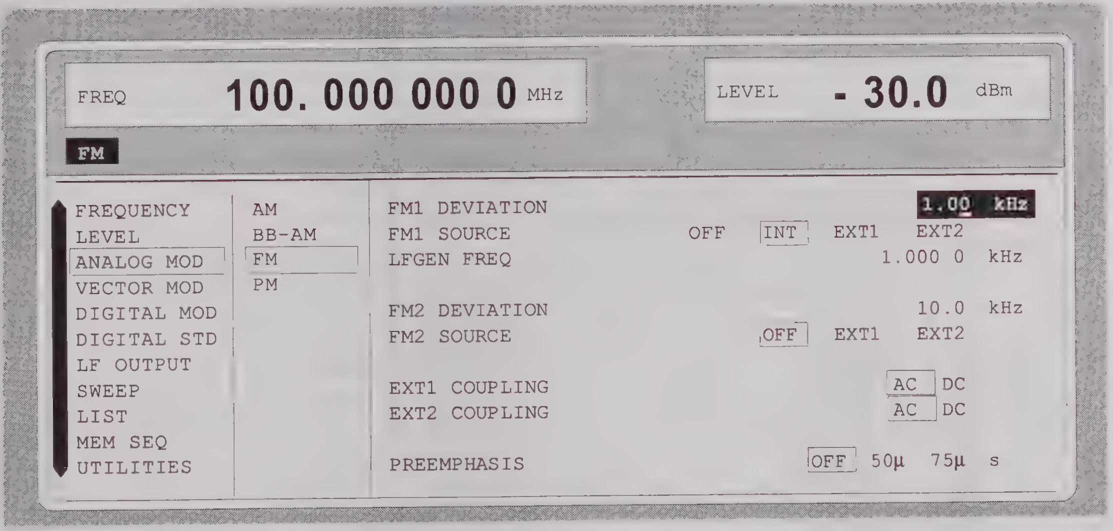
2 Operation

2.1 Front and Rear Panel

2.1.1 Display

(cf. Fig. 2-1, Front panel view)

1



```
FREQ     100. 000 000 0 MHz          LEVEL    - 30.0   dBm
FM

FM1  DEVIATION                                  1.00  kHz
FM1  SOURCE               OFF   INT   EXT1   EXT2
LFGEN  FREQ                              1.000 0  kHz

FM2  DEVIATION                                  10.0  kHz
FM2  SOURCE                     OFF   EXT1   EXT2

EXT1  COUPLING                            AC  DC
EXT2  COUPLING                            AC  DC

PREEMPHASIS                     OFF   50µ   75µ   s
```

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
AM
BB-AM
FM
PM

The display shows in the

header field:
- the current frequency and level settings (considering an offset).
- status messages.
- error messages.

menu field:
- the main menu and the submenus selected with the current settings.

Parameters can be selected and changed in the menus indicated.

see as well
Chapter 2,
Section "Design of the Display"

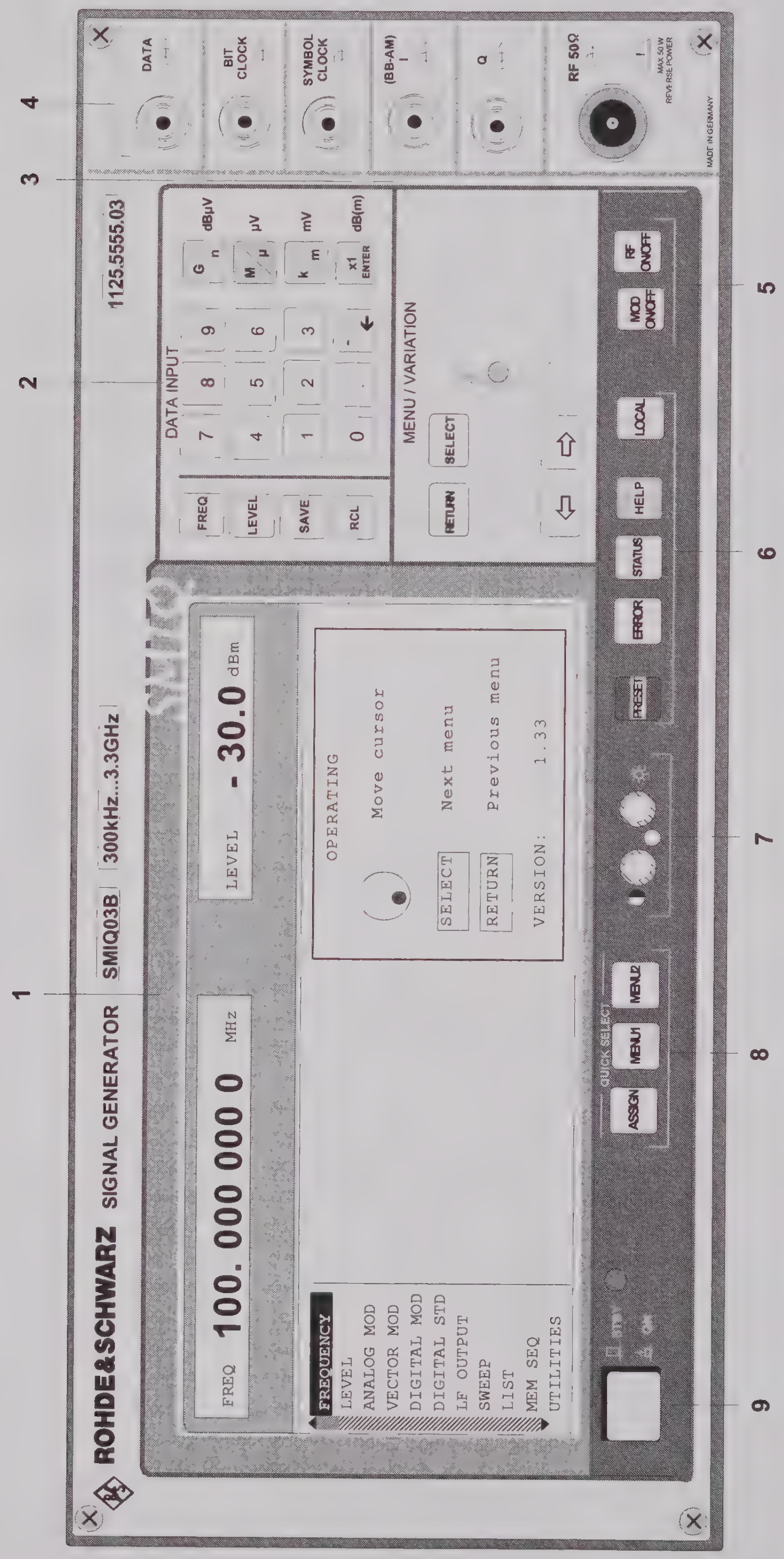

1
2
3
4
5
6
7
8
9
ROHDE&SCHWARZ SIGNAL GENERATOR
SMIQ03B
300kHz...3.3GHz
1125.5555.03
FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
OPERATING
Move cursor
SELECT Next menu
RETURN Previous menu
VERSION: 1.33
DATA INPUT
FREQ
LEVEL
SAVE
RCL
7 8 9
4 5 6
1 2 3
0 . -
G n dBµV
M µ µV
k m mV
x1 ENTER dB(m)
MENU / VARIATION
RETURN
SELECT
DATA
BIT CLOCK
SYMBOL CLOCK
(BB-AM) I
Q
RF 50Ω
MAX 50 W REVERSE POWER
MADE IN GERMANY
RF ON/OFF
MOD ON/OFF
LOCAL
HELP
STATUS
ERROR
PRESET
QUICK SELECT
ASSIGN
MENU1
MENU2
STBY
ON

Fig. 2-1 Front panel view

2.1.2 Controls and Inputs/Outputs of the Front Panel

(cf. Fig. 2-1, front panel view)

2 *DATA INPUT*

Parameter field

FREQ
LEVEL
SAVE
RCL

Parameters RF frequency and RF level can be entered directly by means of the parameter keys, alternatively to menu operation. The input value (indicated in the header field) considers the offset, see Sections 2.4 and 2.5. Further, complete instrument settings can be stored and called.

FREQ	Opens the setting of the RF frequency via value input or variation by means of a rotary knob. The current menu is maintained. Return to the menu by means of the [RETURN] key. (Setting of the RF frequency also in the FREQUENCY menu).
LEVEL	Opens the setting of the RF level via value input or variation by means of a rotary knob. The current menu is maintained. Return to the menu by means of the [RETURN] key. (Setting of the RF level also in the LEVEL menu).
SAVE	Opens the storing of the current instrument setting. Memory selection is effected by entering a number (1 to 50) and is finished by means of the [ENTER] key.
RCL	Opens the calling of an instrument setting stored. Memory selection is effected by entering a number (1 to 50) and is finished by means of the [ENTER] key.

see as well Chapter 2 Section "Use of [FREQ] and [LEVEL] Keys"

Section "RF Frequency"

Section "RF Level"

Section "Storing and Calling of Instrument Settings"

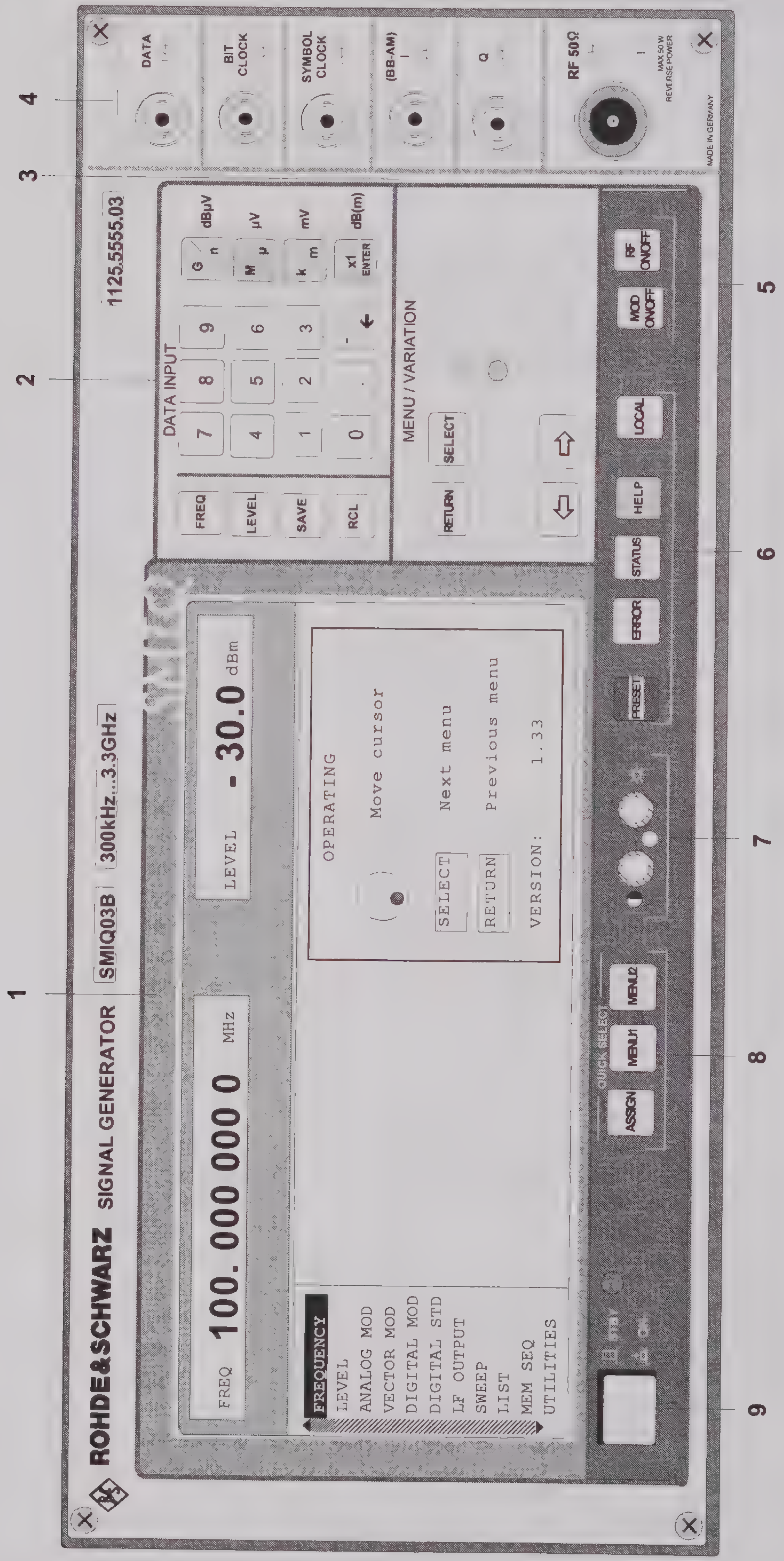
Numeric input field

7
8
9
4
5
6
1
2
3
0
.
-
←

Numeric values, decimal point and minus sign can be entered by means of the digital keys.,

0...9	Enters the digit.
•	Enters the decimal point
-/←	Enters the minus sign. Deletes the last input (digit, sign or decimal point) - key [BACKSPACE].

see as well Chapter 2 Section "Basic Operating Steps"


1
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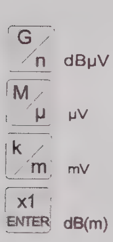
ROHDE&SCHWARZ SIGNAL GENERATOR
SMIQ03B
300kHz...3.3GHz
1125.5555.03
FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
OPERATING
Move cursor
SELECT
Next menu
RETURN
Previous menu
VERSION: 1.33
DATA INPUT
FREQ
LEVEL
SAVE
RCL
7
8
9
4
5
6
1
2
3
0
.
-
G n dBµV
M µ µV
k m mV
x1 ENTER dB(m)
MENU / VARIATION
RETURN
SELECT
QUICK SELECT
ASSIGN
MENU1
MENU2
PRESET
ERROR
STATUS
HELP
LOCAL
MOD ON/OFF
RF ON/OFF
STBY
ON
DATA
BIT CLOCK
SYMBOL CLOCK
(BB-AM) I
Q
RF 50Ω
MAX. 50 W REVERSE POWER
MADE IN GERMANY

Fig. 2-1 Front panel view

2 DATA INPUT

Unit keys with enter function



The unit keys terminate the input of values and specify the multiplication factor for the respective basic unit. The basic units are displayed next to the input field while numbers are entered. In the case of level settings, the unit keys specify the unit.

G/n	dBµV	Selects giga/nano, with RF level dBµV, with LF level dBu.
M/µ	µV	Selects mega/micro, with level µV.
k/m	MV	Selects kilo/milli, with level mV.
1x Enter	dB(m)	Terminates entries in the basic unit and value inputs without unit. Selects with level dBm Selects with level offset and level step width dB.

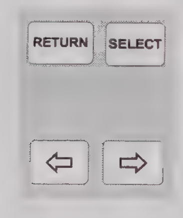
In order to change to another level unit, simply press the unit key desired. Parameter LEVEL must be activated, e.g. by pressing the [LEVEL] key.

see as well
Chapter 2
Section
"Basic Operating
Steps"

Section
"Change Unit of Level"

3 MENU/VARIATION

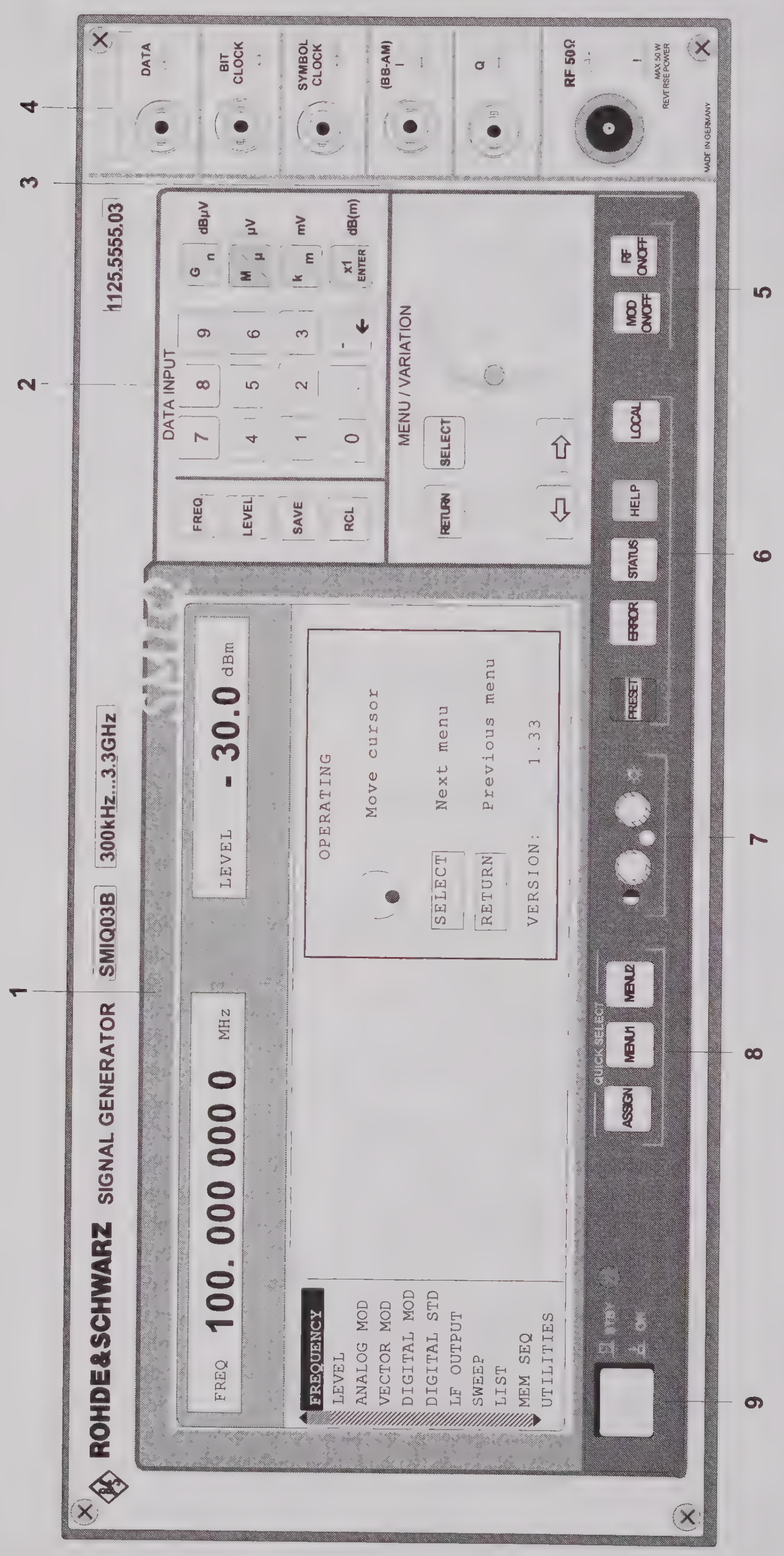
Menu keys



The menu keys access the menus and settings within the menus.

RETURN	Returns the menu cursor to the next higher menu level.
SELECT	Acknowledges the choice marked by the menu cursor
⇐	Moves the digit cursor to the left by one position in the marked value indication. Moves the menu cursor to the left by one position in a 1-out-of-n selection.
⇒	Moves the digit cursor to the right by one position in the marked value indication. Moves the menu cursor to the right by one position in a 1-out-of-n selection.

see as well
Chapter 2
Section
"Basic Operating
Steps"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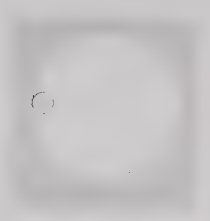
ROHDE&SCHWARZ SIGNAL GENERATOR
SMIQ03B
300kHz...3.3GHz
1125.5555.03
FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
OPERATING
Move cursor
SELECT
Next menu
RETURN
Previous menu
VERSION: 1.33
DATA INPUT
FREQ
LEVEL
SAVE
RCL
7
8
9
4
5
6
1
2
3
0
.
-
G n
dBµV
M µ
µV
k m
mV
x1 ENTER
dB(m)
MENU / VARIATION
RETURN
SELECT
QUICK SELECT
ASSIGN
MENU1
MENU2
PRESET
ERROR
STATUS
HELP
LOCAL
MOD ON/OFF
RF ON/OFF
STBY
ON
DATA
BIT CLOCK
SYMBOL CLOCK
(BB-AM) I
Q
RF 50Ω
MAX 50 W REVERSE POWER
MADE IN GERMANY

Fig. 2-1 Front panel view

3 MENU/VARIATION

Rotary knob

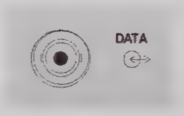
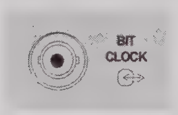
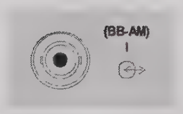


The rotary knob moves the menu cursor over the positions of a menu level to choose from or varies the value of a parameter. The variation is either effected in steps of one or in a step width that can be specified at will.

See as well Chapter 2 Section "Basic Operating Steps"

Section "Sample Setting for First Users"

4

	DATA	Input external data signal for digital modulation. Input resistance 1 kΩ or 50 Ω, Trigger threshold can be set from -2.5 to + 2.5V, max. ± 15 V, max. 40 mA. Output* data signal with operating mode internal. Level: TTL	See as well Chapter 2, Section "Digital Modulation"
	BIT CLOCK	Input* external clock-pulse signal for synchronization of external data signal. Input resistance 1 kΩ or 50 Ω, Trigger threshold can be set from -2.5 to + 2.5 V, max. ± 15 V, max. 40 mA. Output* clock-pulse signal with operating mode internal. Level: TTL	
SYMBOL CLOCK	SYMBOL CLOCK	Input* external clock signal for synchronization of the external data signal with polyvalent modulation types with several bits per symbol. Input resistance 1 kΩ or 50 Ω, Trigger threshold can be set from -2.5 to + 2.5V, max. ± 15 V, max. 40 mA. Output* symbol clock signal with operating mode internal. Level TTL	
	(BB-AM) I	Input external modulation signal for I/Q modulation and broadband-AM. Output* I-signal with operating mode internal. Input/output resistance 50 Ω. Nominal voltage (I/Q): U_S = 0.5 V Nominal voltage (BB-AM): U_S=0.25V max. permissible overvoltage: ± 5V	Section "Vector Modulation" and Section "Broadband AM"

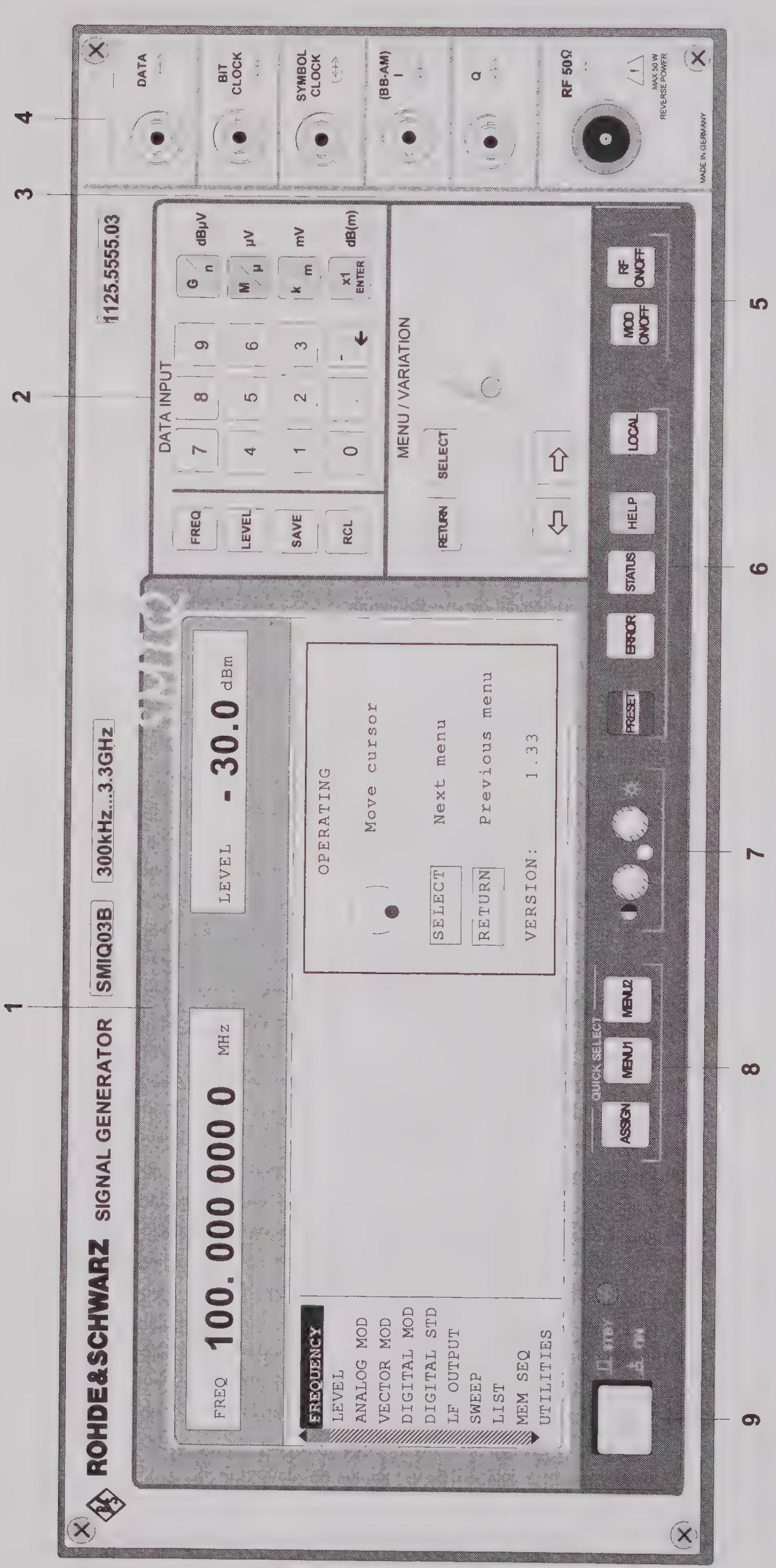
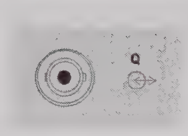
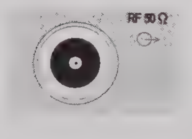

1
2
3
4
5
6
7
8
9
ROHDE&SCHWARZ
SIGNAL GENERATOR
SMIQ03B
300kHz...3.3GHz
1125.5555.03
DATA
BIT CLOCK
SYMBOL CLOCK
(BB-AM) I
Q
RF 50Ω
MAX 50 W REVERSE POWER
MADE IN GERMANY
DATA INPUT
FREQ
LEVEL
SAVE
RCL
7 8 9
4 5 6
1 2 3
0 . -
G n dBµV
M µ µV
k m mV
x1 ENTER dB(m)
MENU / VARIATION
RETURN
SELECT
SMIQ
FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
OPERATING
Move cursor
SELECT Next menu
RETURN Previous menu
VERSION: 1.33
STBY
ON
QUICK SELECT
ASSIGN
MENU1
MENU2
PRESET
ERROR
STATUS
HELP
LOCAL
MOD ON/OFF
RF ON/OFF

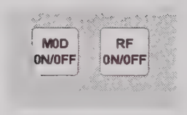
Fig. 2-1 Front panel view

4

	Q	Input external modulation signal for I/Q modulation. Output* Q-signal with operating mode internal. Input/output resistance 50 Ω. Nominal voltage: $U_S = 0.5$ V max. permissible overvoltage: ± 5V	Section "Vector Modulation"
	RF	Output RF signal. Source resistance 50 Ω max. permissible HF-power: 50 W max. permissible overvoltage: 35 V	Section "Use of [ON/OFF] and [MOD ON/OFF] Key"

* When fitted with option Modulation coder, SMIQB20

5

	MOD ON/OFF	Switches on/off the modulation selected in the UTILITIES MOD KEY menu.	See as well Chapter 2 Section "Use of [RF ON/OFF] and [MOD ON/OFF] Keys"
	RF ON/OFF	Switches on/off the RF signal.	

6

PRESET
ERROR
STATUS
HELP
LOCAL

PRESET	Establishes a defined instrument status.
ERROR*	Indicates error and caution messages.
STATUS*	Indicates the instrument status.
HELP*	Indicates context-sensitive auxiliary text.
LOCAL	Switches the instrument from the REMOTE mode (remote control) to the LOCAL mode (manual control).

See as well Chapter 1 Section "Preset Setting"

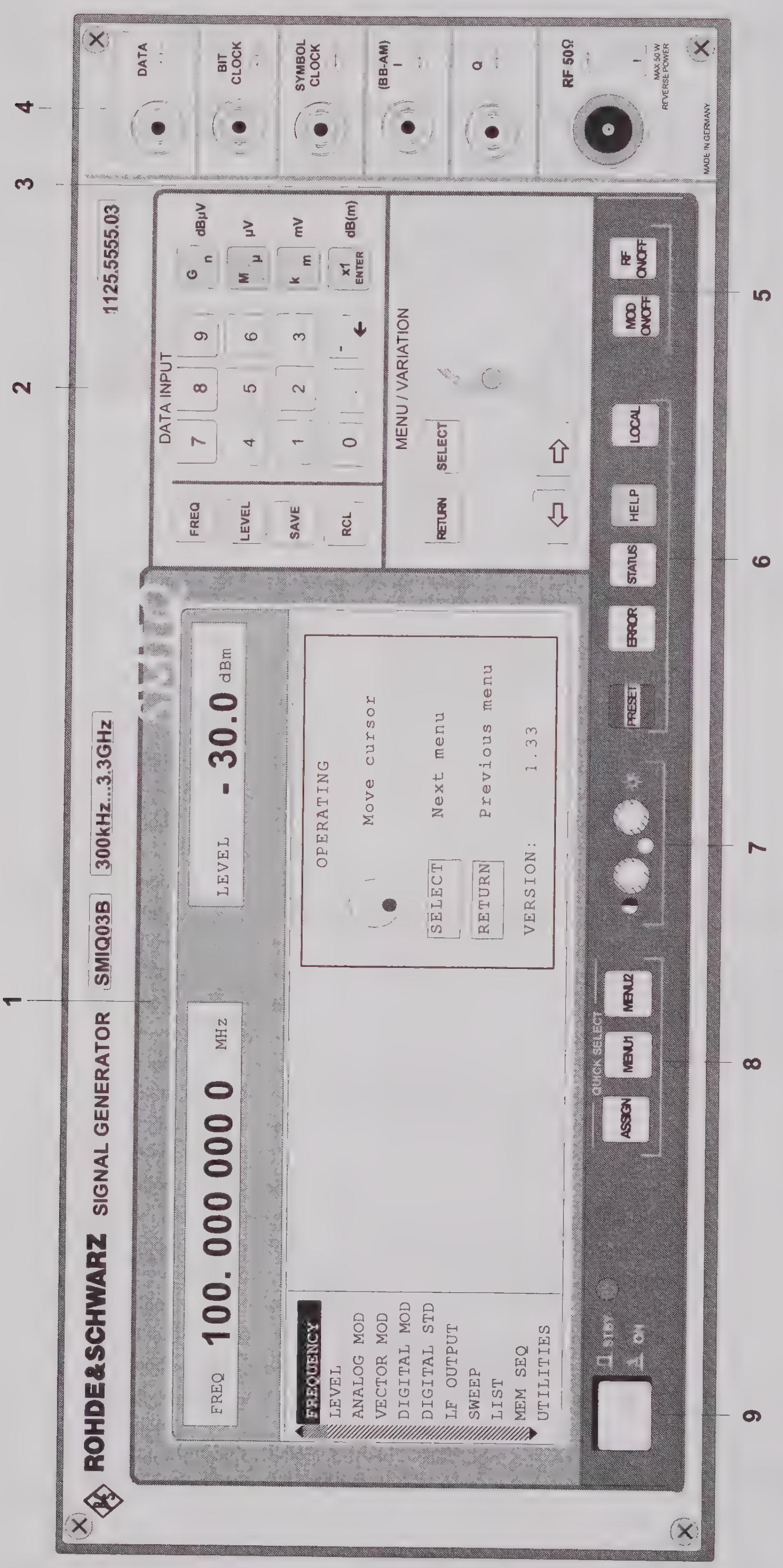
Chapter 2 Section "Help System"

Section "Status"

Section "Error Messages"

Chapter 3, "Remote Control"

* Exit the menus using the [RETURN] key.


1
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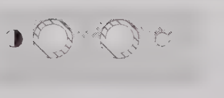
ROHDE&SCHWARZ
SIGNAL GENERATOR
SMIQ03B
300kHz...3.3GHz
1125.5555.03
FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
OPERATING
Move cursor
SELECT Next menu
RETURN Previous menu
VERSION: 1.33
DATA INPUT
FREQ
LEVEL
SAVE
RCL
7 8 9
4 5 6
1 2 3
0 . -
G n dBμV
M μ μV
k m mV
x1 ENTER dB(m)
MENU / VARIATION
RETURN
SELECT
DATA
BIT CLOCK
SYMBOL CLOCK
(BB-AM) I
Q
RF 50Ω
MAX 50 W REVERSE POWER
MADE IN GERMANY
RF ON/OFF
MOD ON/OFF
LOCAL
HELP
STATUS
ERROR
PRESET
QUICK SELECT
ASSIGN
MENU1
MENU2
STBY
ON

Fig. 2-1 Front panel view

7



Brightness and contrast of the display can be set using the rotary knobs.

◑	Contrast
☼	Brightness

See as well
Chapter 1
Section "Setting of Contrast and Brightness of the Display"

8 QUICK SELECT

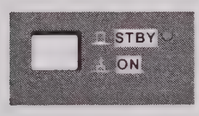
QUICK SELECT
ASSIGN
MENU1
MENU2

The menu-quick-selection keys permit fast access to two menus selected.

ASSIGN	Stores the current menu as menu1 when the MENU1 key is pressed afterwards or as menu2 when the MENU2 key is pressed afterwards.
MENU1	Activates menu1 stored.
MENU2	Activates menu2 stored.

See as well
Chapter 2
Section
"Basic Operating Steps"

9 Switching On/Off

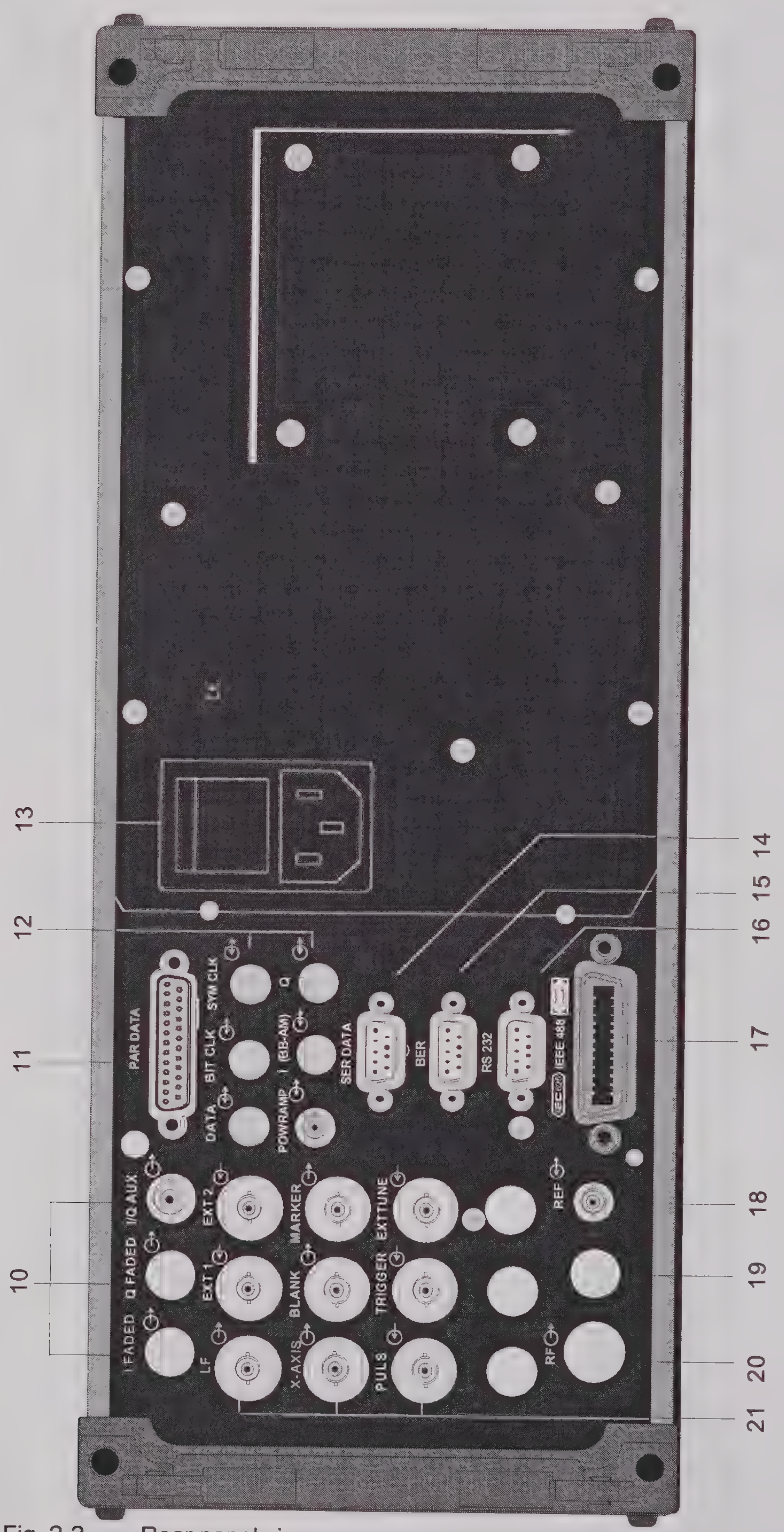


The On/Off switch switches the instrument from the standby mode to the ready-for-operation status. Prerequisite: The power switch at the rear of the instrument must be switched on.

STBY	LED is illuminated in the standby mode.

see as well
Chapter 1
Section "Switching On/Off the Instrument"

Chapter 2
Section "Elements of the Rear Panel, Power Switch"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21
PAR DATA
DATA
BIT CLK
SYM CLK
POWRAMP
I (BB-AM)
Q
SER DATA
BER
RS 232
IEEE 488
I FADED
Q FADED
I/Q AUX
LF
EXT 1
EXT 2
X-AXIS
BLANK
MARKER
PULS
TRIGGER
EXTTUNE
RF
REF

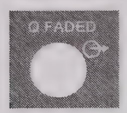
Fig. 2-2 Rear panel view

2.1.3 Elements of the Rear Panel

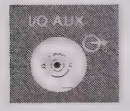
(Cf. Fig. 2-2, Rear panel view)

10

I FADED Faded I signal.



Q FADED Faded Q signal.

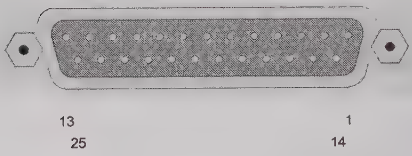


IQ AUX Output I/Q modulated subcarrier. Frequency 300 MHz, level -5 dBm, source resistance 50Ω

see as well Chapter 2, "Fading Simulation"

11

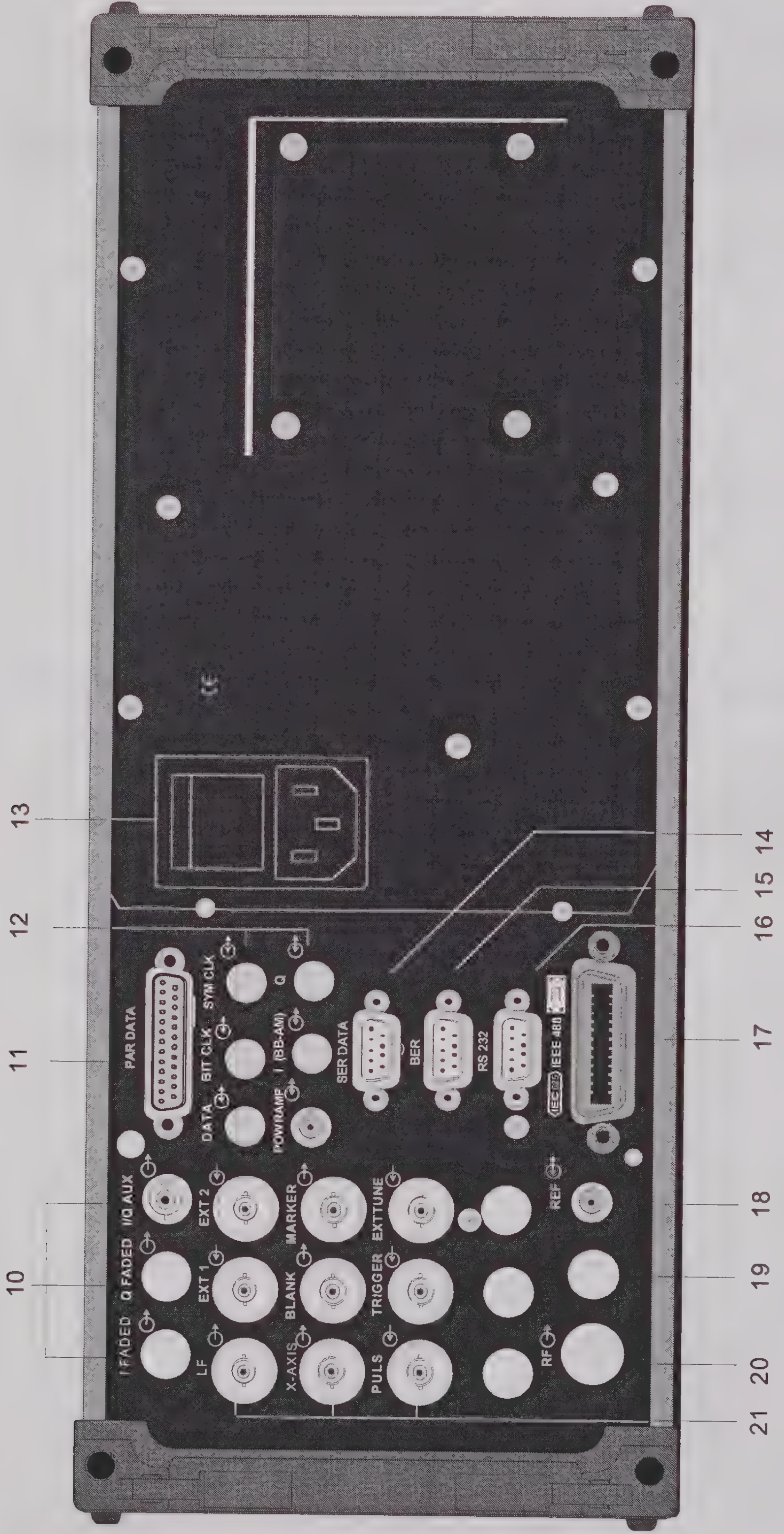
PAR DATA



An adapter between the PAR DATA connector and coaxial BNC connectors is available as an accessory (SMIQ-Z5, order no. 1104.8555.02).

PAR DATA

Pin	Description
1 - ⊥	Ground
2 - ⊥	Ground
3 - DATA-D6 4 - DATA-D4 5 - DATA-D2 6 - DATA-D0	Parallel data input/output D0 to D6 for digital modulation. Output: TTL signal. Input: Input resistance 1kΩ or 50Ω. Trigger threshold can be set from -2.5 to 2.5V, max. ± 15V, max. 40 mA
7 - SYMBCLK	Symbol clock input/output for synchronization of the data signal with modulation types with several bits per symbol. Output: TTL signal. Input: Input resistance 1kΩ or 50Ω. Trigger threshold can be set from -2.5 to 2.5V, max. ± 15V, max. 40 mA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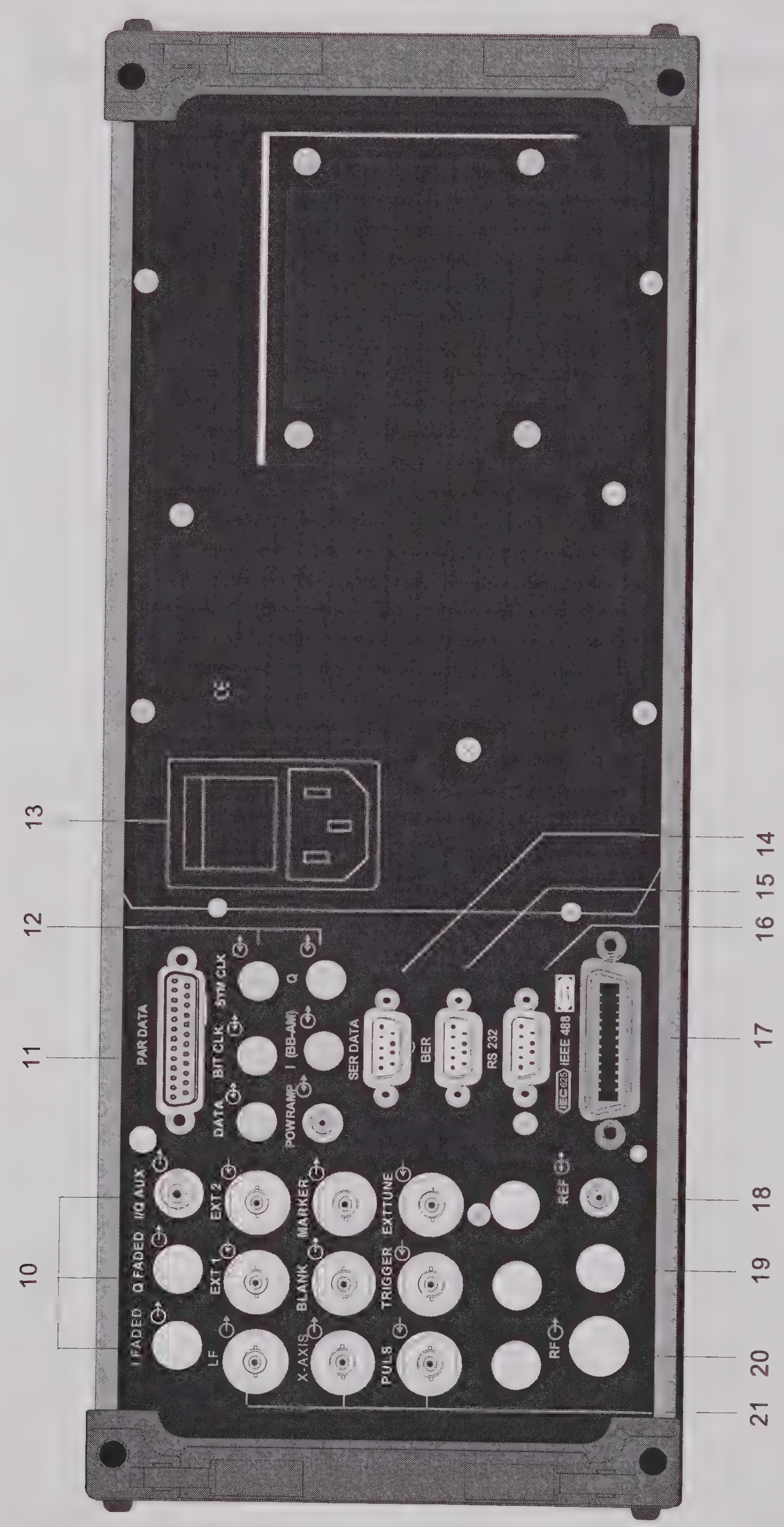
PAR DATA
DATA
BIT CLK
SYM CLK
POW RAMP
I (BB-AM)
Q
SER DATA
BER
RS 232
IEC 625 IEEE 488
I FADED
Q FADED
I/Q AUX
LF
EXT 1
EXT 2
X-AXIS
BLANK
MARKER
PULS
TRIGGER
EXTTUNE
RF
REF

Fig. 2-2 Rear panel view

11

PAR DATA

Pin	Description
8 - ⊥	Ground
9 - LEV-ATT	Signal input/output for controlling of level reduction. Output: TTL signal. Input: Input resistance 1kΩ or 50Ω. Trigger threshold can be set from -2.5 to 2.5V, max. ± 15V, max. 40 mA
10 - ⊥	Ground
11 - TRIGOUT 1	Output for triggering of external instruments. Output: TTL signal.
12 - ⊥	Ground
13 - ⊥	Ground
14 - TRIGIN	Input for triggering of frames, PRBS and data sequences. Input: Input resistance 1kΩ or 50Ω. Trigger threshold can be set from -2.5 to 2.5V, max. ± 15V, max. 40 mA
15 - DATA-D7	Parallel data input/output D1to D7 see pin 3 - 6
16 - DATA-D5	
17 - DATA-D3	
18 - DATA-D1	
19 - ⊥	Ground
20 - BITCLK	Output bit clock with operating mode internal. TTL signal
21 - CW	Signal input/output for controlling of modulation. Switches carrier to CW with FSK modulation. Output: TTL signal. Input: input resistance 1kΩ or 50Ω. Trigger threshold can be set from -2.5 to 2.5V, max. ± 15V, max. 40 mA
22 - BURST-GATE	Signal input/output for controlling of the burst profile. Output: TTL signal. Input: input resistance 1kΩ or 50Ω. Trigger threshold can be set from -2.5 to 2.5V, max. ± 15V, max. 40 mA
23 - TRIGOUT 2	Output for triggering and controlling of external instruments. Output: TTL signal.
24 - TRIGOUT 3	Output for triggering and controlling of external instruments. Output: TTL signal.
25 - HOP	HOP output provides control signal when internal frequency hopping is programmed. Output: TTL signal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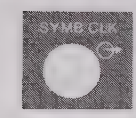
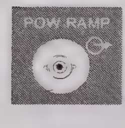
PAR DATA
DATA
BIT CLK
SYM CLK
POWRAMP
I (BB-AM)
Q
SER DATA
BER
RS 232
IEC625 IEEE 488
I FADED
Q FADED
I/Q AUX
LF
EXT 1
EXT 2
X-AXIS
BLANK
MARKER
PULS
TRIGGER
EXTTUNE
RF
REF
CE

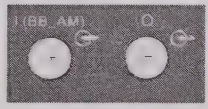
Fig. 2-2 Rear panel view

12



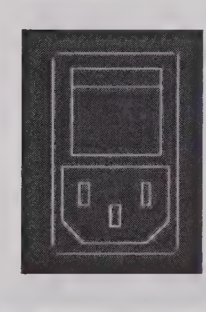


DATA	Cut-out, provided to relocate the DATA input/output at the front to the rear of the instrument.
BIT CLK	Cut-out, provided to relocate the BIT CLK input/output at the front to the rear of the instrument.
SYMB CLK	Cut-out, provided to relocate the SYMB CLK input/output at the front to the rear of the instrument.
POW RAMP	Signal input/output for power ramping. Input: accepts analog voltages from 0 to 1 V for envelope modulation. Input resistance 10kΩ Max. permissible overvoltage ±15 V Output provides modulation voltage of burst envelope with internal modulation. output resistance 10Ω Max. permissible overvoltage ±15 V



I (BB-AM)	Cut-out, provided to relocate the I- input at the front to the rear of the instrument.
Q	Cut-out, provided to relocate the Q- input at the front to the rear of the instrument.

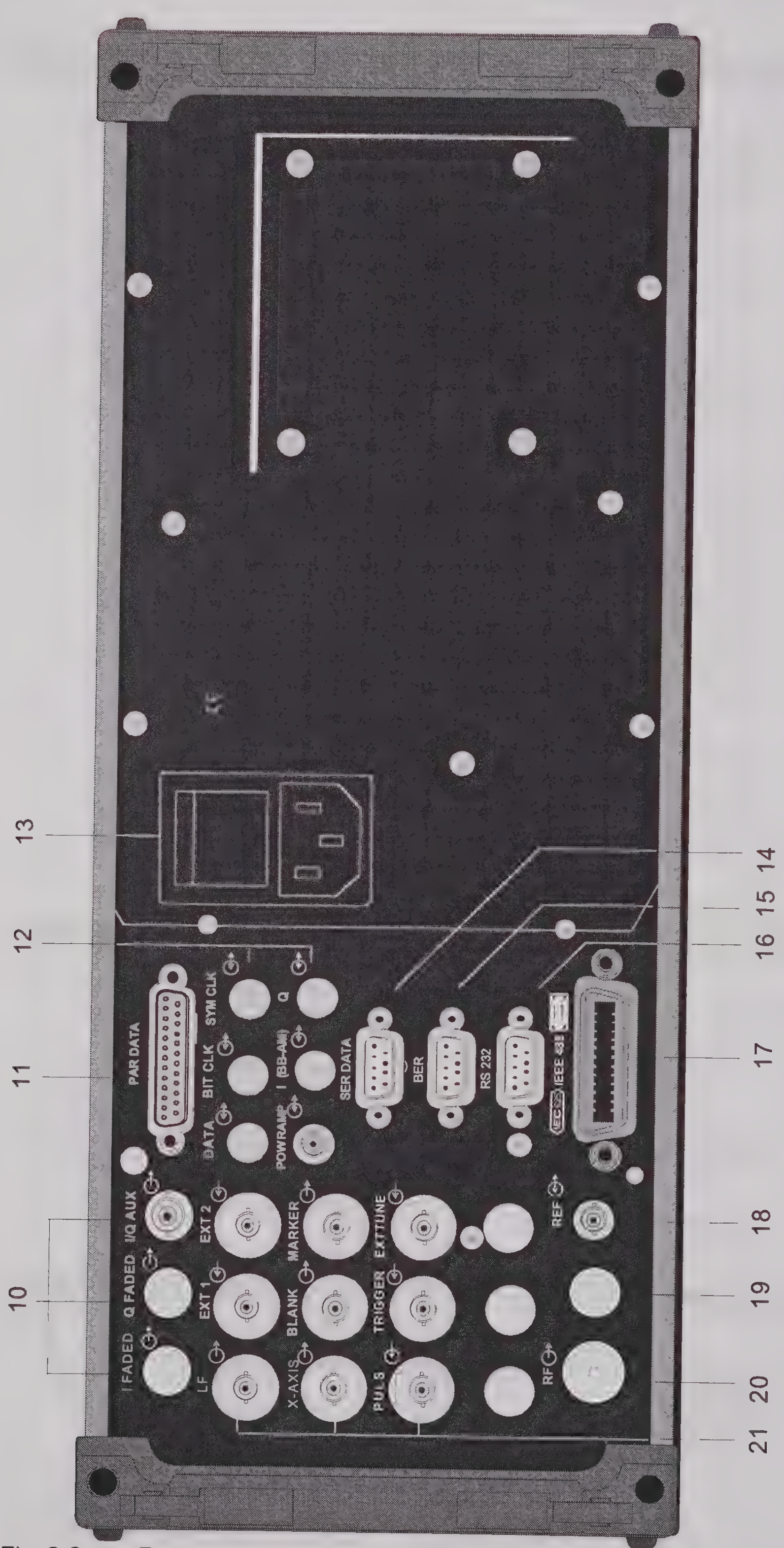
13



Power switch ON when pressed at the top ("I") Power supply connection	see as well Chapter 1, Section "Supply Voltage" Section "Switching On/Off the Instrument"

14

SER DATA	Asynchronous data input for digital modulation. Interface: RS232 up to 115 kbps	see as well Annex A "interfaces"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21
PAR DATA
DATA
BIT CLK
SYM CLK
POWRAMP
I (BB-AM)
Q
SER DATA
BER
RS 232
IEC 625 IEEE 488
I FADED
Q FADED
I/Q AUX
LF
EXT 1
EXT 2
X-AXIS
BLANK
MARKER
PULS
TRIGGER
EXTTUNE
REF
RF

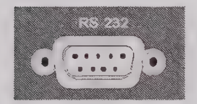
Fig. 2-2 Rear panel view

15

Interface for BER Test

See as well chapter 2, section "External Modulation Source AMIQ" and "Bit Error Rate Test"

16

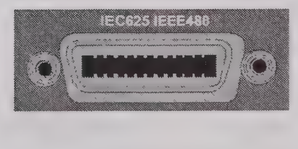


RS-232

RS-232 interface,
used for software update, the loading of calibration data, and remote control. The pin assignment corresponds to the pin assignment of a PC.

see as well
Chapter 3
Remote Control
and
Annex A
"Interfaces"

17



IEC 625
IEEE 488

IEC-Bus (IEEE 488)
Remote-control interface

See as well
Chapter 3,
"Remote Control"

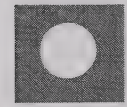
18

REF

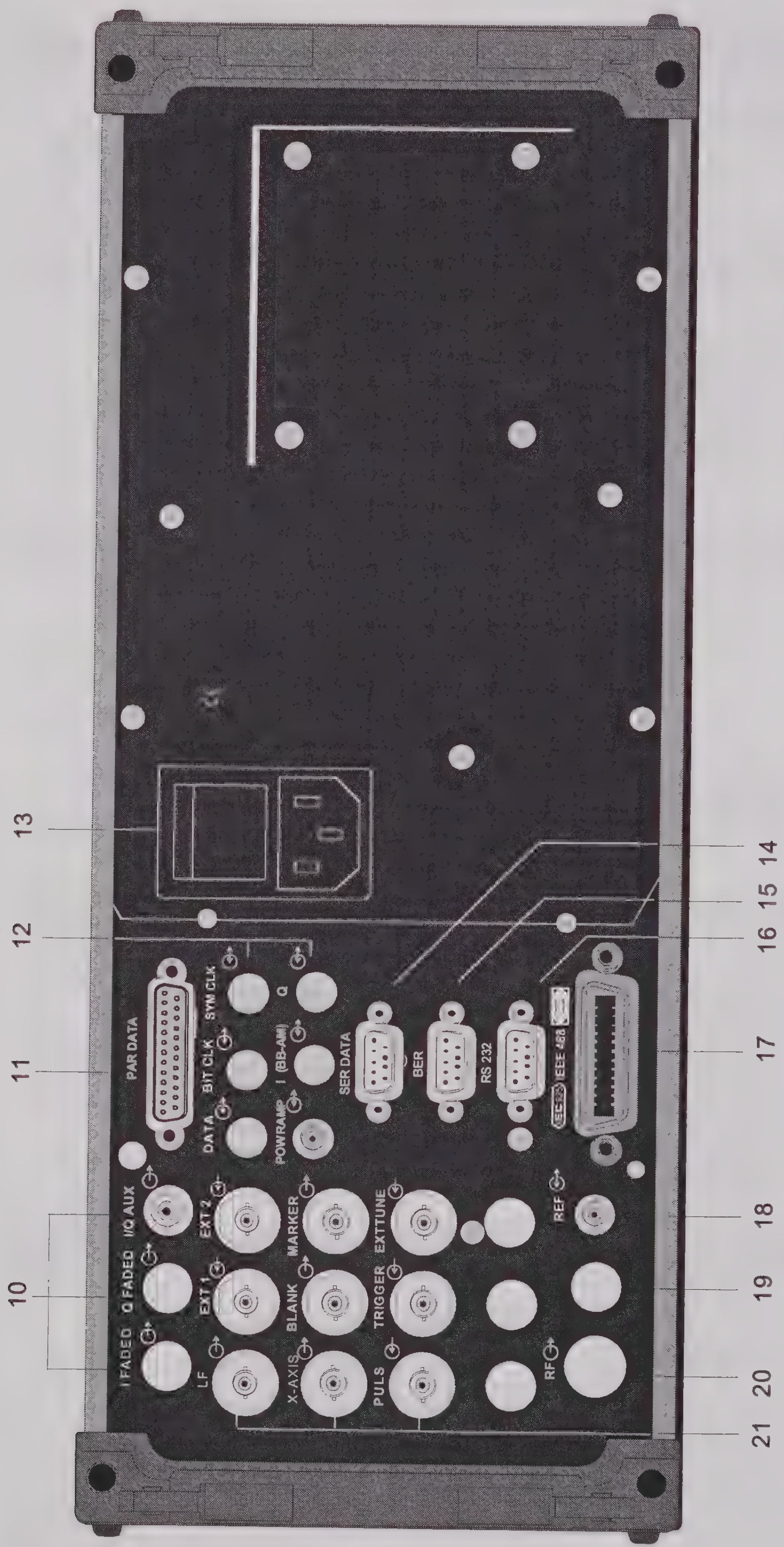
Output of the internal 10-MHz reference signal with reference internal.
Source resistance 50 Ω.
Input for external reference frequency with reference external. Adjustable to external reference frequencies from 1 to 16 MHz in 1-MHz steps.
Input resistance 200 Ω.

See as well
Chapter 2,
Section
"Reference Frequency int/ext"

19



4 optional cut-outs, provided for further connections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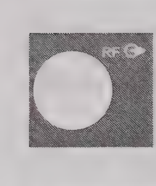
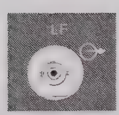
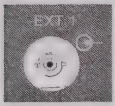
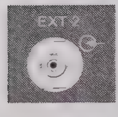
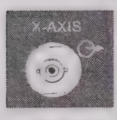
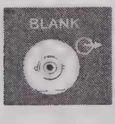
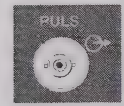
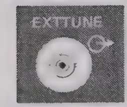
PAR DATA
DATA
BIT CLK
SYM CLK
POWRAMP
I (BB-AM)
Q
SER DATA
BER
RS 232
IEEE 488
I FADED
Q FADED
I/Q AUX
LF
EXT 1
EXT 2
X-AXIS
BLANK
MARKER
PULS
TRIGGER
EXTTUNE
RF
REF

Fig. 2-2 Rear panel view

20

	RF	Cut-out, provided to relocate the RF output at the front to the rear of the instrument

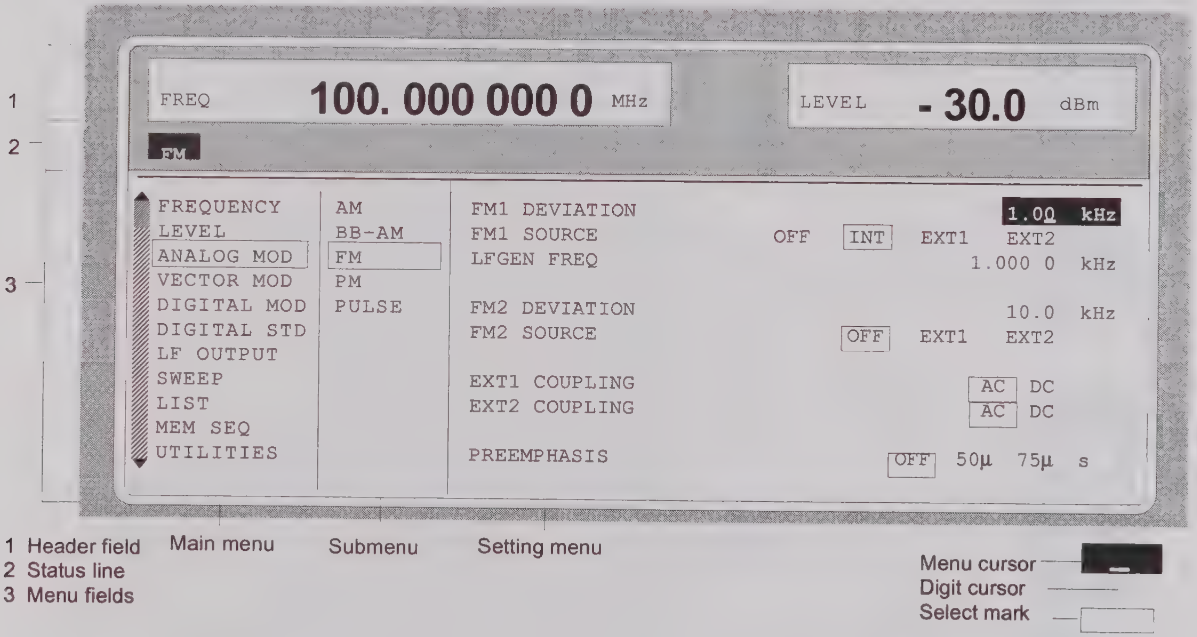
21

	LF	Output LF signal of the internal LF generator. Source resistance < 10 Ω.
	EXT1	Input external modulation signal, alternatively for AM or FM (PM). Input resistance >100 kΩ. Nominal voltage: $U_S = 1$ V Max. permissible overvoltage: ± 15 V
	EXT2	Input external modulation signal for FM (PM). Input resistance >100 kΩ. Nominal voltage: $U_S = 1$ V Max. permissible overvoltage: ± 15 V
	X-AXIS	Output supplies a voltage ramp of 0 to 10 V, when a sweep is switched on.
	BLANK	Output supplies a signal to blank the return sweep or the settling process in LIST mode Level: TTL
	MARKER	Output is active when the sweep reaches the marker or at the first step of the LIST mode. Level: TTL
	PULS	Input for pulse modulation. Level: TTL Input resistance 10Ω
	TRIGGER	Input to trigger sweep, LIST mode and Memory Sequence. Level: TTL
	EXTTUNE	Tuning input for the internal reference frequency. Voltage range ± 10 V, pulling range $\pm 1 \cdot 10^{-6}$

2.2 Basic Operating Steps

The operating principle is explained in this section. For better understanding, please read section "Sample Setting for First Users" (Section 2.2.10) in addition.
To operate the instrument, menus are called in the display. All setting possibilities and the current setting status are evident from the menus. All settings can be made by accessing the menus.
RF frequency and RF level can also be set without menu operation using keys [FREQ] and [LEVEL].
RF signal and modulation can also be switched on/off without menu operation using keys [RF ON/OFF] and/or [MOD ON/OFF].

2.2.1 Design of the Display


1
2
3
FM
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
AM
BB-AM
FM
PM
PULSE
1 Header field
2 Status line
3 Menu fields
Main menu
Submenu
Setting menu
Menu cursor
Digit cursor
Select mark

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
FM1 DEVIATION 1.00 kHz
FM1 SOURCE OFF INT EXT1 EXT2
LFGEN FREQ 1.000 0 kHz
FM2 DEVIATION 10.0 kHz
FM2 SOURCE OFF EXT1 EXT2
EXT1 COUPLING AC DC
EXT2 COUPLING AC DC
PREEMPHASIS OFF 50µ 75µ s

Fig. 2-3 Design of the display

Header field (1)The header field of the display indicates frequency and level of the RF output signal which considers the offset value. In the RF-sweep operating mode, the start and stop frequencies are displayed in two lines one above the other. The start and stop levels are indicated in the LEVEL-sweep operating mode correspondingly. A two-line level display appears for digital modulation or digital standard. The upper line indicates the average power (LEVEL), the lower line the peak envelope power (PEP) of the modulated RF output signal.

Status line (2) The status line below describes operating mode and operating state of the instrument. Error messages and notes for caution are also displayed in the status line.

Menu fields (3)The indication fields below the header field are reserved for the menu representations. The image contents of these fields change as a function of the menu selected. The field at the left-hand display margin is occupied with the main menu, the topmost level of the menu structure. The main menu is always faded in.

Each further field adjacent at the right contains submenus.
The field ending with the right-hand display margin shows the setting menu. In this menu all setting values and setting states connected with the menu selected are indicated. When accessing submenus, the higher-order menus remain in the display. The current menu path is evident through the select marks.

Menu cursor	The menu cursor shows the user at which position in the menu he is. The position of the menu cursor is evident from the inverse notation of the term (white characters on a black background)
Digit cursor	As an underscore, the digit cursor marks the position which can be varied by means of the rotary knob in a value indication.
Select mark	The frame around a term marks current menus or valid settings in the setting menu.

2.2.2 Calling the Menus

Accessing the menus is effected using rotary knob [VARIATION], [SELECT] key and [RETURN] key.

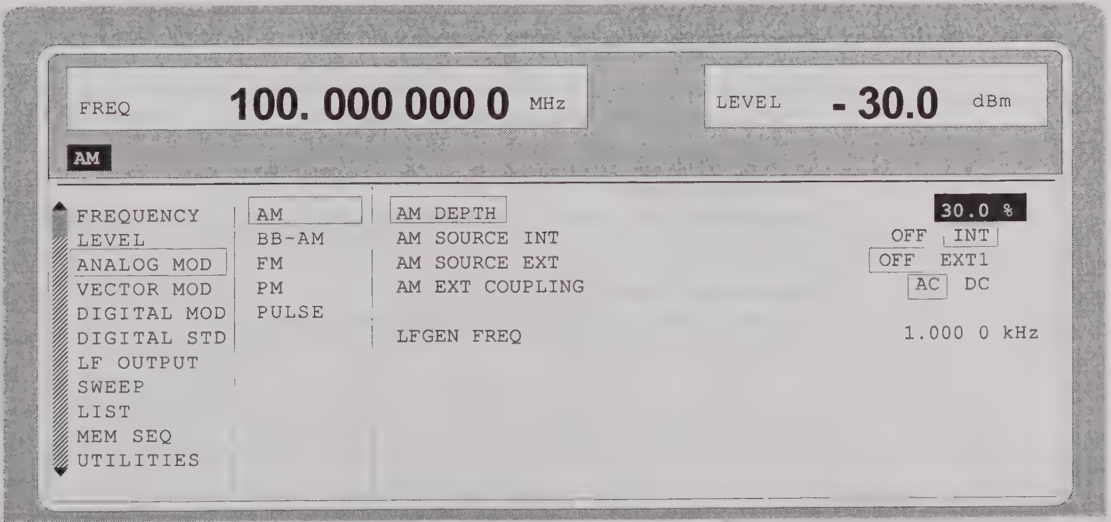
Rotary knob Rotary knob [VARIATION] moves the menu cursor over the positions of a menu level to be selected. If a scrollbar is visible at the left-hand margin of a menu, the menu is larger than the screen window. If the menu cursor is moved to the margin of the screen window, the covered lines become visible.

[SELECT] key The [SELECT acknowledges the selection marked by means of the menu cursor.

[RETURN] key The [RETURN] key

- returns the menu cursor to the next higher menu level. The menu cursor is shifted to the left into the preceding column of the menu structure.
- resets the menu cursor from frequency or level value indication in the header field into the menu field to the menu called last.
- closes the display pages called using keys [STATUS], [HELP] and [ERROR] again.

Settings are accessed in the setting menus ending with the right-hand display margin.



FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
AM DEPTH 30.0 %
AM SOURCE INT OFF INT
AM SOURCE EXT OFF EXT1
AM EXT COUPLING AC DC
LFGEN FREQ 1.000 0 kHz

Fig. 2-4 MODULATION-AM menu

2.2.3 Selection and Change of Parameters

Select parameter

- Set the menu cursor to the name of the parameter desired using the rotary knob, e.g. to AM DEPTH in the AM menu, Fig. 2.4.

Change setting value

- Via value input or using rotary knob.

via value inputs

- Press the first digit of the new value or minus sign.
  The old value is deleted, the entry is indicated in the marked field.
- Enter further digits.
- Terminate the input using a unit key or, in the case of inputs in the base unit or in the case of inputs without unit, using the [1x/Enter] key.
  The menu cursor wraps back to the appropriate parameter.

using rotary knob

- Press the [SELECT] key.
- The menu cursor changes from the parameter selected in the left-hand column of the setting menu to the setting value on the right, e.g. from AM DEPTH to 30%, Fig. 2-4.
- Set the underscore to the position of the setting value to be varied using keys [⇨] [⇦].
- Turn the rotary knob.

  The position underscored is varied in steps of 1.

  Note: *RF frequency and RF level can also be varied in a step width which can be defined arbitrarily using the rotary knob. In the respective setting menu (FREQUENCY or LEVEL) the step width is entered as KNOB STEP USER and the KNOB STEP set from DECIMAL to USER. To point to the fact that the step width has been converted to the value programmed, the underscore as a symbol of the digit cursor disappears in the respective value indication.*

1-out-of-n selection

- Select parameters.
- Press the [SELECT] key.
  The menu cursor changes from the parameter selected in the left-hand column of the setting menu to the current selection on the right, e.g. from AM SOURCE EXT to OFF, Fig. 2-4.
- Set the menu cursor to the position desired within the 1-out-of-n selection using the rotary knob or cursor keys [⇐] [⇒].
- Press the [SELECT] key.
  The setting is made.
  The selection mark which has marked the setting valid up to now wraps to the new position.
- Press the [RETURN] key.
  The menu cursor wraps back to the respective parameter.

Quick selection of a parameter

The quick selection of a parameter reduces the number of operating steps if several parameters are set successively. The menu cursor can directly be set further from line to line in the column of the setting values by pressing the [SELECT] key.

The menu cursor wraps from the setting value of a parameter to the setting value of the parameter in the next line.

The column of the setting values can be exited at each position by pressing the [RETURN] key.

2.2.4 Triggering Action

Lines in the setting menu which are marked with the "▶" symbol at the end of the line qualify an action which can be carried out. Instruction SEARCH ONCE ▶ in the LEVEL-ALC menu, e.g., switches on level control for level calibration for a short period of time.

Trigger action

- ➢ Set the menu cursor to the respective instruction.
- ➢ Press the [SELECT] key.
  The action is triggered.
  While the action is carried out, the instruction remains framed by the selection mark.

2.2.5 Quick Selection of Menu (QUICK SELECT)

The keys of the QUICK SELECT control field are used to call selected menus quickly by one keystroke.

Store menus

- ➢ Establish the desired operating status of the current menu.
- ➢ Press the [ASSIGN] key.
- ➢ Press key [MENU1] or [MENU2].
  The current menu is stored as menu1 or menu2. That is to say, 2 menus can be stored in total.

Call menus

- ➢ Press key [MENU1] or [MENU2].
  Menu1 or menu2 stored is displayed. Exactly the operating status which was current at the point of time of storing is reconstructed.

2.2.6 Use of [FREQ] and [LEVEL] Keys

RF frequency and RF level can be set without menu operation as well using direct keys [FREQ] and [LEVEL]. The input value considers the offset, see Sections 2.4 and 2.5.

Key [FREQ]/ [LEVEL]

- ➢ Press the [FREQ] or [LEVEL] key.

  The frequency and/or the level indication in the header field of the display is marked.

  The current menu at the display is maintained.
- ➢ Alter the value via a value input or the rotary knob.
- ➢ Press the [RETURN] key.

  The menu cursor wraps to the position marked last in the menu.

2.2.7 Use of [RF ON/OFF] and [MOD ON/OFF] Keys

RF signal and modulation can be switched on/off without menu operation as well using direct keys [RF ON/OFF] and/or [MOD ON/OFF] (see Sections [RF ON/OFF] key and [MOD ON/OFF] key as well).

Key [RF ON/OFF]

- ➢ Press the [RF ON/OFF] key.

  The RF output signal is switched on/off.

  IEC-bus-short command: `:OUTP OFF`

Key [MOD ON/OFF]

- ➢ Press the [MOD ON/OFF] key.

  The modulation is switched on/off.

An IEC-bus command is not available. The modulations have to be switched on and off in the respective modulation sub menus

2.2.8 [ENTER] Key – Special Toggle Function

This additional function of the [ENTER] key facilitates parameter changes.

Some selection parameters can now be selected using the [ENTER] key, and no longer have to be activated beforehand with [SELECT].

2.2.9 Changing Unit of Level

For the level, the unit of the value set can be changed without a new value input.

Change level unit

- ➢ Activate LEVEL parameter.
  - Press the [LEVEL] key or
  - set the menu cursor in the LEVEL menu to the setting value of the AMPLITUDE parameter.
- ➢ Press the unit key with the desired level unit.

  The level is indicated in the desired unit.

2.2.10 Correction of Input

Digital entries can be corrected by one of the following keys before terminating the input:

Key [-/←]	The backspace key deletes the value entered digit by digit. When the last digit is deleted, the previous value is displayed.
Key [RETURN]	Pressing the [RETURN] key deletes the entire entry and results in the previous value being indicated again. For a subsequent new input in the setting menu, the first digit of the new value is to be entered. For a subsequent new input via the [FREQ] or [LEVEL] keys, the respective key has to be pressed again.
Key [FREQ]/ [LEVEL]	In the case of a frequency or level input by means of the [FREQ] or [LEVEL] keys, pressing the [FREQ] and/or [LEVEL] key again deletes the entire input.

2.2.11 Sample Setting for First Users

First users most quickly become familiar with the operation of the instrument if they execute the pattern setting of this section.

First frequency and level of the RF output signal are set via keys [FREQ] and [LEVEL] in the DATA INPUT field:

- Frequency	250 MHz
- Level	10 dBm

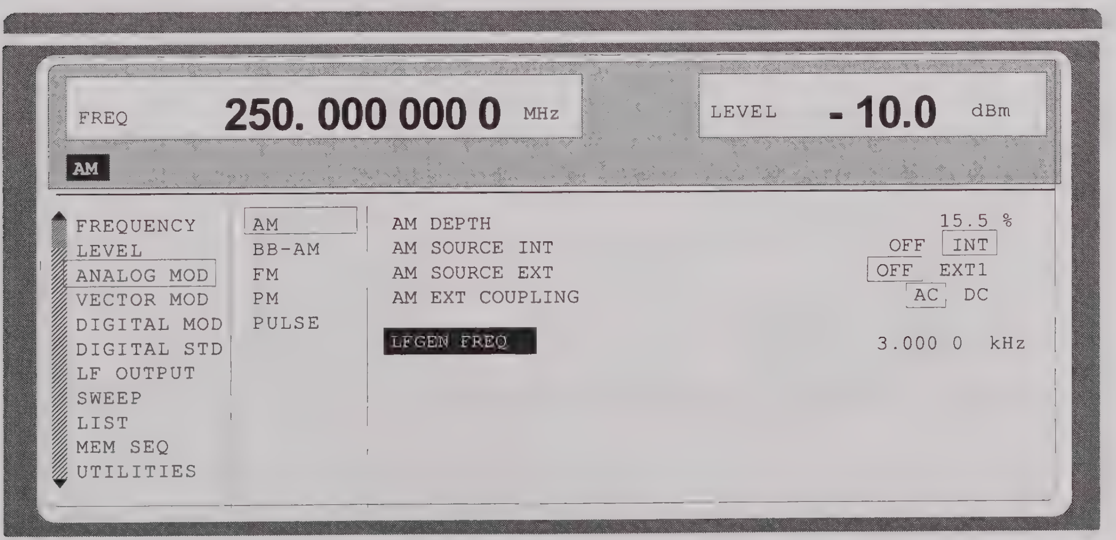
Operating steps	Explanations
PRESET	Reset the instrument to the defined state.
DATA INPUT FREQ 2 5 0 M/µ	Set the frequency to 250 MHz. The menu cursor marks the permanent frequency indication.
DATA INPUT LEVEL 1 0 x1 ENTER	Set the level to 10 dBm. The menu cursor marks the permanent level indication.
RETURN	Reset the menu cursor to the menu field.

The output signal is to be amplitude-modulated next.

- AM modulation depth 15.5 %
- Modulation frequency 3 kHz

Operating steps	Explanations
MENU / VARIATION ANALOG MOD **MENU / VARIATION** SELECT	Select ANALOG MOD menu. ➢ Set menu cursor to ANALOG MOD using the rotary knob and subsequently press [SELECT] key. The submenu is displayed.
MENU / VARIATION AM **MENU / VARIATION** SELECT	Select AM submenu The AM setting menu is displayed.
MENU / VARIATION AM DEPTH	Select AM DEPTH parameter. The menu cursor marks the setting value.
DATA INPUT 1 5 . 5 x1 ENTER	Enter modulation depth 15.5 % and press [x1 ENTER]. The menu cursor is reset to AM DEPTH.
MENU / VARIATION AM SOURCE INT **MENU / VARIATION** SELECT	Select AM SOURCE INT. The menu cursor marks the current 1-out-of-n selection.

Operating steps	Explanations
MENU / VARIATION INT **MENU / VARIATION** SELECT	Select INT 1 as internal modulation source. The selection mark marks INT. AM is faded in the status line as a hint that AM is switched on.
RETURN	Reset menu cursor to AM SOURCE INT.
MENU / VARIATION LFGEN FREQ	Select parameter LFGEN FREQ.
DATA INPUT 3 k / m	Enter frequency 3 kHz and press unit key. The indications on the display are represented in Fig. 2-5. The AM modulation setting is completed.



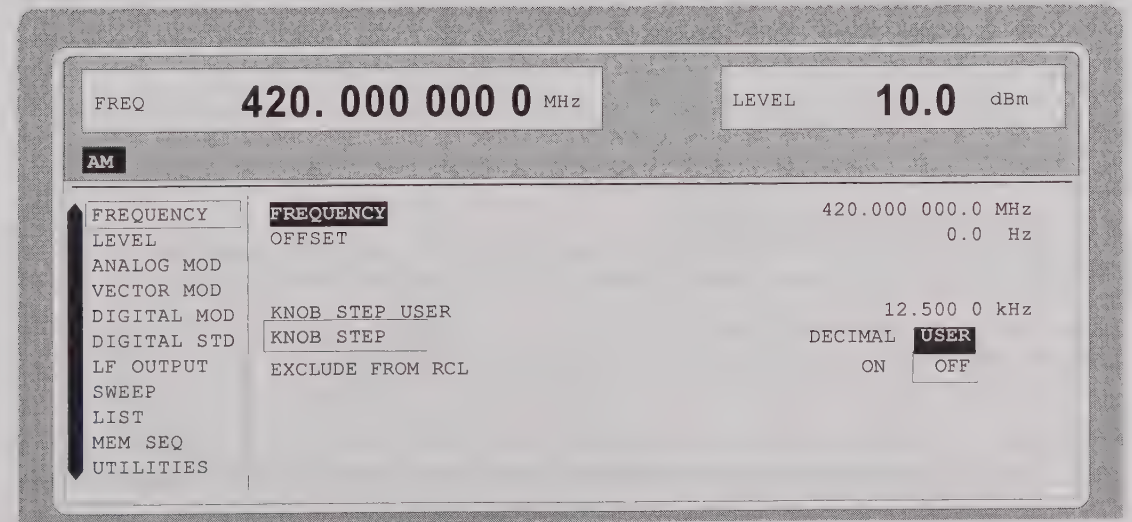
```
FREQ      250. 000 000 0  MHz          LEVEL    - 10.0   dBm
AM

FREQUENCY     AM        AM DEPTH                           15.5 %
LEVEL         BB-AM     AM SOURCE INT                 OFF   INT
ANALOG MOD    FM        AM SOURCE EXT                 OFF   EXT1
VECTOR MOD    PM        AM EXT COUPLING                  AC   DC
DIGITAL MOD   PULSE
DIGITAL STD             LFGEN FREQ                    3.000 0  kHz
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
```

Fig. 2-5 Display after AM setting

Subsequently to the above setting, 420 MHz as new RF frequency and 12.5 kHz as the step width for the RF frequency variation are set in the following. Parameter quick select is used, which reduces the number of operating steps.

Operating steps	Explanations
RETURN RETURN	Reset the menu cursor to the main menu in 2 steps.
MENU / VARIATION FREQUENCY MENU / VARIATION SELECT	Select FREQUENCY menu. The frequency setting menu is displayed.
MENU / VARIATION FREQUENCY	Select FREQUENCY parameter. The menu cursor marks the setting value.
DATA INPUT 4 2 0 M/µ	Enter frequency 420 MHz and press unit key.
MENU / VARIATION KNOB STEP USER	Set menu cursor to parameter KNOB STEP USER.
DATA INPUT 1 2 . 5 k/m	Enter step width 12.5 kHz. Press unit key.

Operating steps	Explanations
MENU / VARIATION KNOB STEP	Set menu cursor to parameter KNOB STEP.
SELECT	Set menu cursor to the current KNOB STEP selection.
MENU / VARIATION USER **MENU / VARIATION** **SELECT**	Select USER (user-defined step width). The selection mark marks USER. This results in step width 12.5 kHz being used in the case of variation using the rotary knob.
RETURN **RETURN**	Reset the menu cursor to the main menu in 2 steps.


AM
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES

```
FREQ   420. 000 000 0 MHz          LEVEL   10.0 dBm

FREQUENCY                                420.000 000.0 MHz
OFFSET                                         0.0  Hz

KNOB STEP USER                              12.500 0 kHz
KNOB STEP                            DECIMAL  USER
EXCLUDE FROM RCL                          ON   OFF
```

Fig. 2-6 Display after pattern setting

2.2.12 List Editor

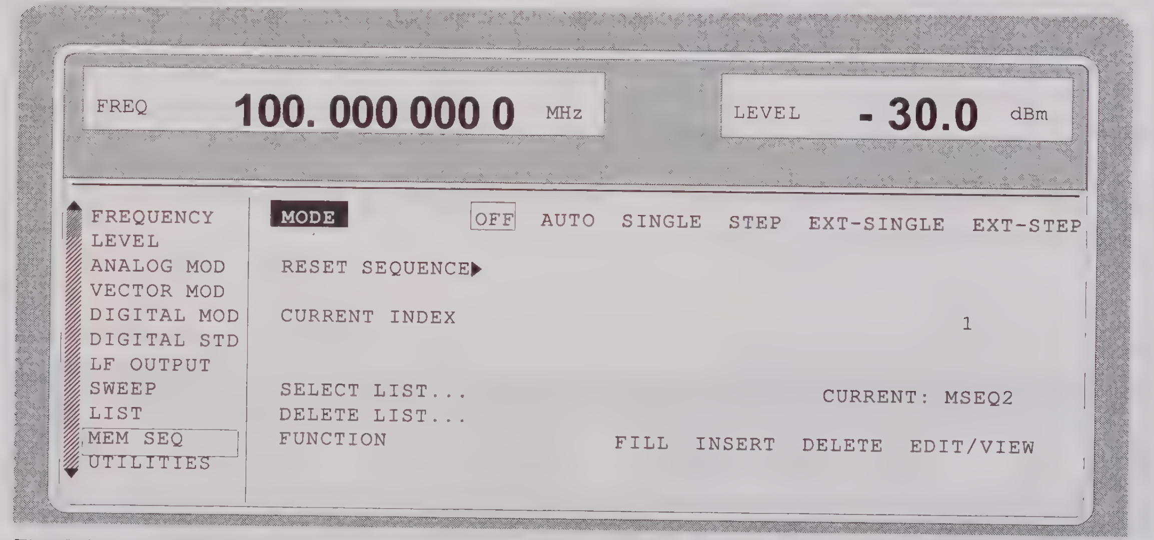
The SMIQ offers the possibility to generate lists. Lists are used for setting sequences LIST mode or (memory sequence), as data source for digital modulations or for level correction which can be defined by the user (UCOR). They consist of elements which are defined by an index and at least one parameter per index. Each list is marked by a separate name and can be selected via this name. The lists are accessed in the menus assigned in each case, e.g. to the settings sequences of instrument settings in the MEM SEQ menu. However, the lists are always generated and processed in the same way and the procedures are hence explained in detail by the example of the memory sequence mode (menu MEM SEQ) in this section. A pattern setting at the end of this section allows the user to become familiar with the operation of the list editor.

Setting menus providing list processing are structured in two pages:

The first page, called OPERATION page in the following contains the general configuration parameters for processing a list. Further, the general list functions such as selecting and deleting the list as well as calling an editing mode are provided. The second page, the EDIT page, is automatically displayed when calling an edit function and serves to enter and modify the parameters of the list.

The OPERATION page has a similar arrangement with all list editors. As an example, the OPERATION page of the MEM SEQ menu is shown:

Menu selection: MEM SEQ


FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

MODE: OFF
CURRENT INDEX 1
SELECT LIST... CURRENT: MSEQ2

Fig. 2-7 OPERATION page of the MEM SEQ menu

The settings for MODE, CURRENT INDEX, etc. are irrelevant for the general description of the list editors and are described in greater detail in Section 2.10, MEMORY SEQUENCE mode.

The last three menu lines of the OPERATION page always exist and are reserved for selecting and deleting lists as well as for calling the edit functions (and hence the EDIT page).

SELECT LIST Opens a selection window in which a list can be selected from the existing lists or a new, empty list can be generated. In this line the active list is always displayed.

DELETE LIST Opens a selection window in which the list to be deleted can be selected.

FUNCTION Selection of the edit function for processing the lists. The EDIT page is automatically called through the selection (cf. Section 2.2.11.3).

FILL	Filling a list with elements.
INSERT	Insertion of elements into a list.
DELETE	Deletion of elements of a list.
EDIT/VIEW	Editing the single elements.

2.2.12.1 Select and Generate - SELECT LIST

SELECT LIST opens a selection window in which either an existing list can be selected or a new, empty list can be generated (cf. Fig. 2-8). By pressing the [RETURN] key, the selection window is closed without changing the setting.

Select list

- ➢ Mark the list desired using the rotary knob.
- ➢ Press [SELECT] key.

  The selected list is included in the instrument setting. The selection window is closed. The selected list is displayed under CURRENT.

Generate list

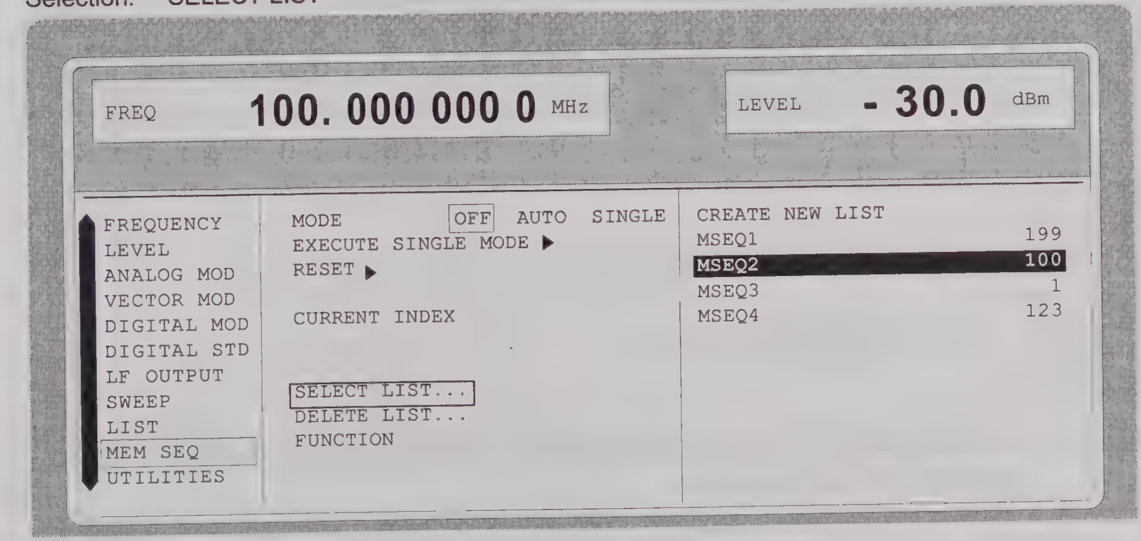
- ➢ Mark CREATE NEW LIST ▶ using rotary knob.
- ➢ Press [SELECT] key.

  A new empty list is automatically generated which can be filled using functions FILL or EDIT. The selection window is closed. The new list is displayed under CURRENT.

No modification of the setting

- ➢ Press [RETURN] key.

Selection: SELECT LIST



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

MODE OFF AUTO SINGLE
EXECUTE SINGLE MODE ▶
RESET ▶
CURRENT INDEX
SELECT LIST...
DELETE LIST...
FUNCTION

CREATE NEW LIST	
MSEQ1	199
MSEQ2	100
MSEQ3	1
MSEQ4	123

Fig. 2-8 SELECT-LIST-selection window

CREATE NEW LIST ▶

Generating a new list. The name of the list cannot be selected freely in the case of manual control. A definite list name is automatically generated in the following form:

MSEQ<n>, with <n> ∈ {0..9}, e.g. MSEQ1 (with Memory Sequence)

This applies correspondingly to the other operating modes. In the case of level correction mode, UCOR1 would be generated for example. If a list is created via IEC bus, an arbitrary list name can be given (cf. Section 3). Unrestricted access is also possible by means of the selection window.

MSEQ2 100

The list currently set is marked in the selection window by means of the selection mark, here MSEQ2. In addition to the list name, the length of the list is given, here 100 elements.

2.2.12.2 Deletion of Lists - DELETE LIST

DELETE LIST opens a selection window in which the list to be deleted can be selected. The lists are represented together with their name and their length (cf. Fig. 2-9). By pressing the [RETURN] key the selection window is exited without deleting a list.

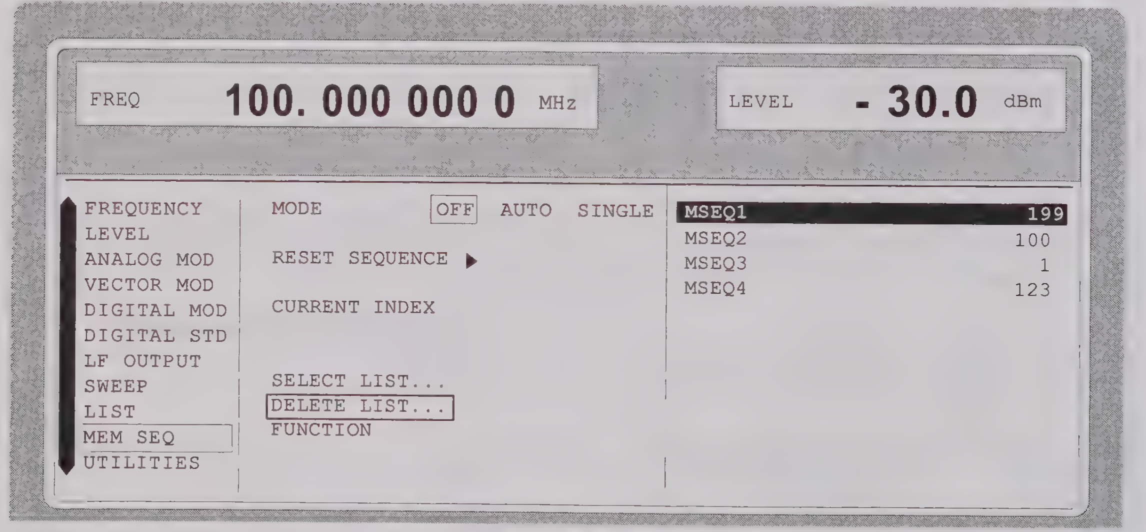
Delete list

- ➢ Mark desired list using the rotary knob.
- ➢ Press [SELECT] key.

  The prompt "enter [SELECT] to delete list/sequence?" is displayed
- ➢ Press [SELECT] key.

  The list is deleted. If the prompt is acknowledged with the [RETURN] key, however, the list is not deleted. The selection window is automatically closed due to the acknowledgment of the prompt.

Selection: DELETE LIST


FREQ
100. 000 000 0
MHz
LEVEL
- 30.0
dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
MODE
OFF
AUTO
SINGLE
RESET SEQUENCE ▶
CURRENT INDEX
SELECT LIST...
DELETE LIST...
FUNCTION

MSEQ1	199
MSEQ2	100
MSEQ3	1
MSEQ4	123

Fig. 2-9 DELETE-LIST selection window

2.2.11.3 Edition of Lists

Due to the selection of an edit mode on the OPERATION page the EDIT page is automatically activated. When the EDIT/VIEW function is selected, the largest possible section of the list is displayed (cf. Fig. 2-10). In the case of block functions FILL, INSERT and DELETE, an input window is additionally displayed (cf. Fig. 2-11 to 2-13).

Functions SELECT LIST and FUNCTION are available on the EDIT page as on the OPERATION page.

Return to the OPERATION page is effected by pressing the [SELECT] key twice.

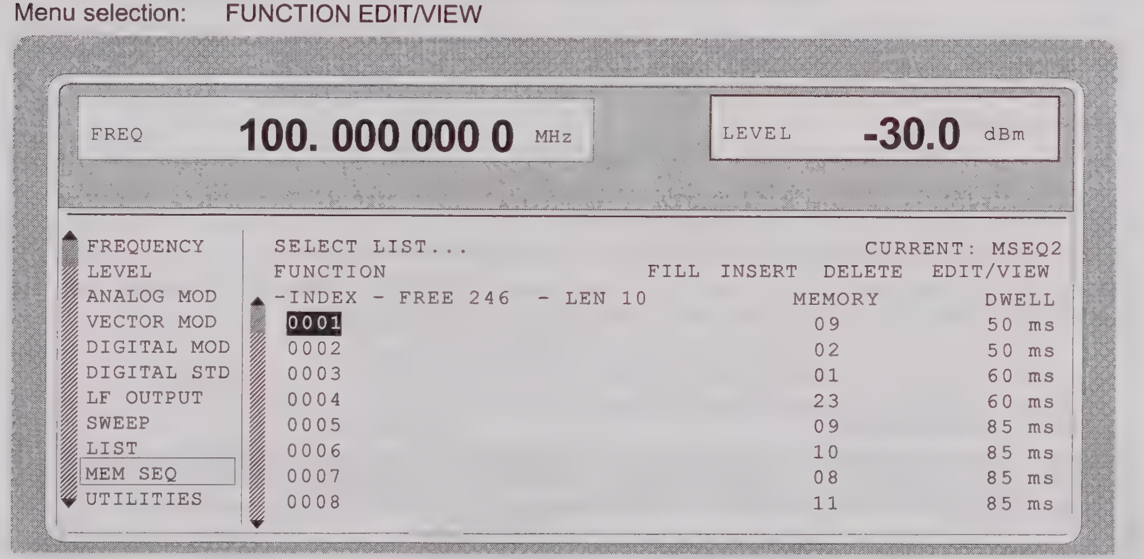
Single-value function EDIT/VIEW

By selecting the EDIT/VIEW function, the entire list can be viewed or modifications of single values be carried out.

If the cursor marks a value in the INDEX column of the list, the EDIT mode is exited by pressing the [RETURN] key. The menu cursor then marks FUNCTION again.

There is no separate function for storing the list. This means that every modification of the list is transferred to the internal data set and has an effect on exiting the EDIT/VIEW function.

Menu selection: FUNCTION EDIT/VIEW



```
FREQ   100. 000 000 0 MHz              LEVEL   -30.0 dBm

SELECT LIST...                                  CURRENT: MSEQ2
FUNCTION                        FILL  INSERT  DELETE  EDIT/VIEW
-INDEX - FREE 246  - LEN 10            MEMORY         DWELL
 0001                                    09           50 ms
 0002                                    02           50 ms
 0003                                    01           60 ms
 0004                                    23           60 ms
 0005                                    09           85 ms
 0006                                    10           85 ms
 0007                                    08           85 ms
 0008                                    11           85 ms
```

Fig. 2-10 Edit function EDIT/VIEW

INDEX	Position in the list
FREE	Space available. FREE 256 means that space for 256 parameter elements is available in the list memory in total.
LEN	Occupied space. LEN 0 means that the current list occupies 0 elements in the list memory.
MEMORY DWELL	Identification of the column below. The number and name of parameter columns is different for the various list editors.

Select parameters

- ➢ Mark the index associated to the parameter using the rotary knob or directly enter the value of the index via the numeric keys.
- ➢ Press [SELECT] key.
  Parameter MEMORY is marked. If the second parameter DWELL is to be marked, press the [SELECT] key again.

Modify parameters

- ➢ Vary the value of the parameter selected using the rotary knob or enter the value directly using numeric keys.

  Note: *The binary data of the digital modulations which cannot be varied are an exception. Further, all numeric keys except for "0" and "1" are ineffective in these cases.*

- ➢ Press the [ENTER] key or unit keys.
  The value is included in the data set. The menu cursor marks the value of the next column. In the last column, the menu cursor then marks the next line of column MEMORY.
- ➢ Press the [RETURN] key.
  The menu cursor wraps back to the INDEX column. The EDIT mode is exited by repeatedly pressing the [RETURN] key (cf. Section 2.2.11.4).

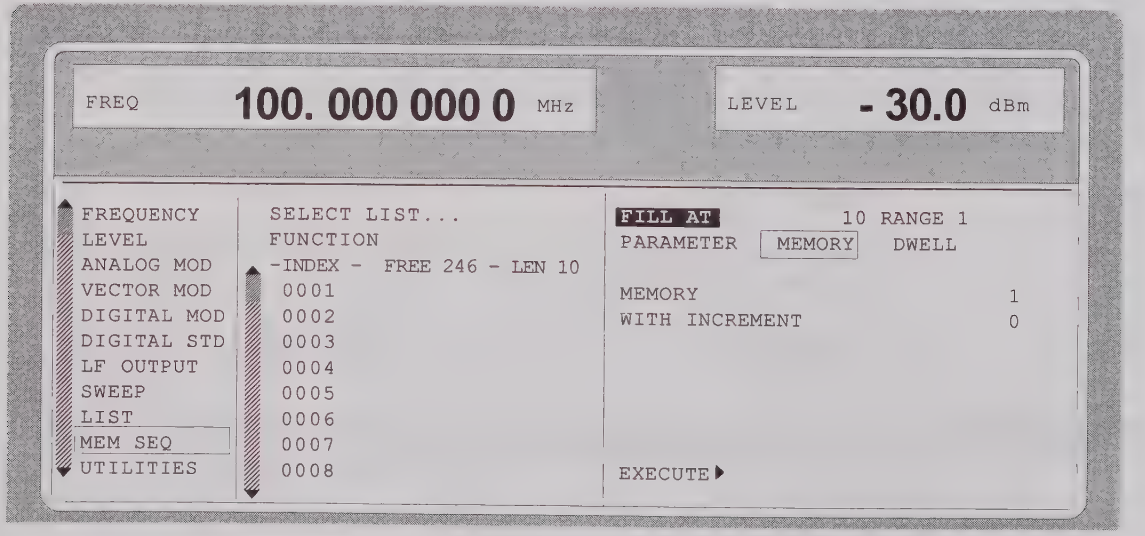
Block function FILL

Using function FILL, a parameter, e.g. MEMORY, is overwritten with constant or linearly increasing/decreasing values within a defined range. The input window is exited by pressing the [RETURN] key without a modification being carried out.
If the filler range exceeds the end of the list, the list is automatically extended.

The list entry, in the example for MEMORY, with index [AT +n] is calculated as follows from the information AT, RANGE, starting value (MEMORY) and WITH INCREMENT:

MEMORY[AT+n] = starting value (MEMORY)+ n · increment | ($0 \leq n \leq$ RANGE1)

Selection: FUNCTION-FILL



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

-INDEX - FREE 246 - LEN 10

0001
0002
0003
0004
0005
0006
0007
0008

FILL AT 10 RANGE 1

PARAMETER MEMORY DWELL

MEMORY 1

WITH INCREMENT 0

Fig. 2-11 Block function FILL: Input window

FILL AT

Setting the filling range.

AT	Lower limit (index)
RANGE	Number of the elements to be inserted

PARAMETER

Selection on which of the parameters the filling function is to have an effect. This menu option is eliminated if the list only includes elements with one parameter.

MEMORY or DWELL

Input of the starting value for the parameter selected. This option is only displayed if a selection has been made under PARAMETER MEMORY or DWELL.

WITH INCREMENT

Input of the increment between two successive values. If 0 is entered as increment, a filling procedure with constant values is achieved. This option is only displayed if a selection has been made under PARAMETER MEMORY or DWELL.

Note: *In the case of some types of lists, e.g. digital modulation data, indicating an increment is eliminated since there are binary data. In these cases line WITH INCREMENT is eliminated.*

EXECUTE ▶

Starts the filling sequence. After the function has been executed, the input window is automatically exited. The current index points to the first element after the processed range.

Filling a list

After selection of function FILL, the menu cursor marks FILL AT.

- Press the [SELECT] key.
  The menu cursor marks the value at AT.
- Vary index value using the rotary knob or enter using the numeric keys and the [ENTER] key.
- Press the [SELECT] key.
  The menu cursor marks the value at RANGE.
- Vary value using the rotary knob or enter using the numeric keys and the [ENTER] key.
- Press the [SELECT] key.
  The menu cursor marks MEMORY or DWELL in input line PARAMETER.
- Select MEMORY using the rotary knob (if not yet marked) and press the [SELECT] key.
  The menu cursor marks the value in input line MEMORY.
- Vary starting value for column MEMORY using the rotary knob or enter using the numeric keys and the [ENTER] key.
- Press the [SELECT] key
  The menu cursor marks the value in input line WITH INCREMENT.
- Vary the value of the increment desired using the rotary knob or enter using the numeric keys and the [ENTER] key.
- Press the [RETURN] key.
- Mark the action EXECUTE ▶
- Press the [SELECT] key.
  The filling sequence is initiated. After the function has been carried out, the input window is automatically exited. The menu cursor marks FUNCTION. The EDIT page shows the end of the range that has been filled right now.

Block function INSERT

Function INSERT inserts the desired number of elements with constant or linearly increasing/decreasing values before the element with the given starting index. All elements which had been stored from the starting index are shifted to the end of the range to be inserted.

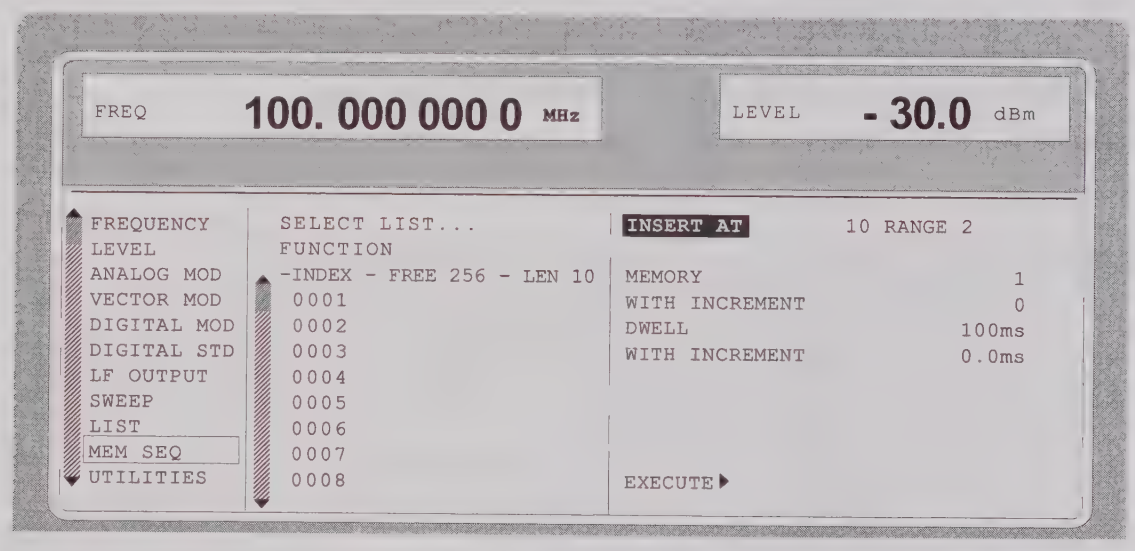
Input is effected analogously to filling a list.

By pressing the [RETURN] key the input window is exited without a modification being effected. The menu cursor then marks FUNCTION.

The list entry, in the example for MEMORY, with index [AT +n] is calculated as follows from the information AT, RANGE, starting value (MEMORY) and WITH INCREMENT:

MEMORY[AT+n] = starting value (MEMORY) + n · Increment | $(0 \leq n \leq \text{RANGE-1})$

Selection: FUNCTION INSERT



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

SELECT LIST...
FUNCTION
-INDEX - FREE 256 - LEN 10
0001
0002
0003
0004
0005
0006
0007
0008

INSERT AT 10 RANGE 2

MEMORY 1
WITH INCREMENT 0
DWELL 100ms
WITH INCREMENT 0.0ms

EXECUTE ▶

Fig. 2-12 Edit function INSERT: Input window

INSERT AT	Input of the starting index and the number of the elements to be inserted. AT Starting index before which the insert operation is to be effective. RANGE Number of the elements to be inserted
MEMORY	Input of the starting value for MEMORY.
DWELL	Input of the starting value for DWELL.
WITH INCREMENT	Input of the increment between two successive values for MEMORY or DWELL. If 0 is indicated as increment, constant values are achieved to be inserted RANGE times. ***Note:*** *In the case of some types of lists, e.g. digital modulation data, indicating an increment is eliminated since there are binary data. In these cases all lines WITH INCREMENT are eliminated.*
EXECUTE ▶	Starts the inserting sequence. After the function has been executed, the input window is automatically exited. The menu cursor marks FUNCTION. The EDIT page shows the beginning of the range that has moved forward.

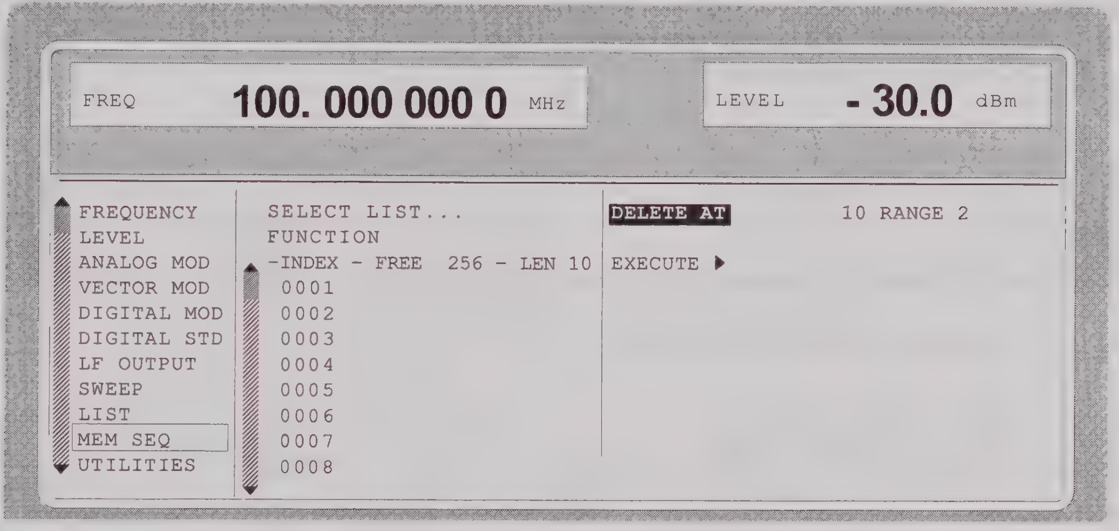
Block function DELETE

Function DELETE deletes the elements of the range indicated. This does not leave a gap in the list but the remaining elements move forward. If the given range exceeds the end of the list, deletion until the end of the list is effected.

Input is analog to filling a list.

By pressing the [RETURN] key, the input window is exited without a modification being carried out. The menu cursor then marks FUNCTION.

Selection: Function DELETE


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SELECT LIST...
FUNCTION
0001
0002
0003
0004
0005
0006
0007
0008
EXECUTE ▶

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

-INDEX - FREE 256 - LEN 10

DELETE AT 10 RANGE 2

Fig. 2-13 Edit function DELETE: Input window

DELETE AT	Input of the block of the list to be deleted	
	AT	Lower limit (INDEX)
	RANGE	Number of elements to be deleted.
EXECUTE ▶	Starts the deletion. After the function has been executed, the input window is automatically exited. The menu cursor marks FUNCTION. The EDIT page shows the beginning of the range that has moved forward.	

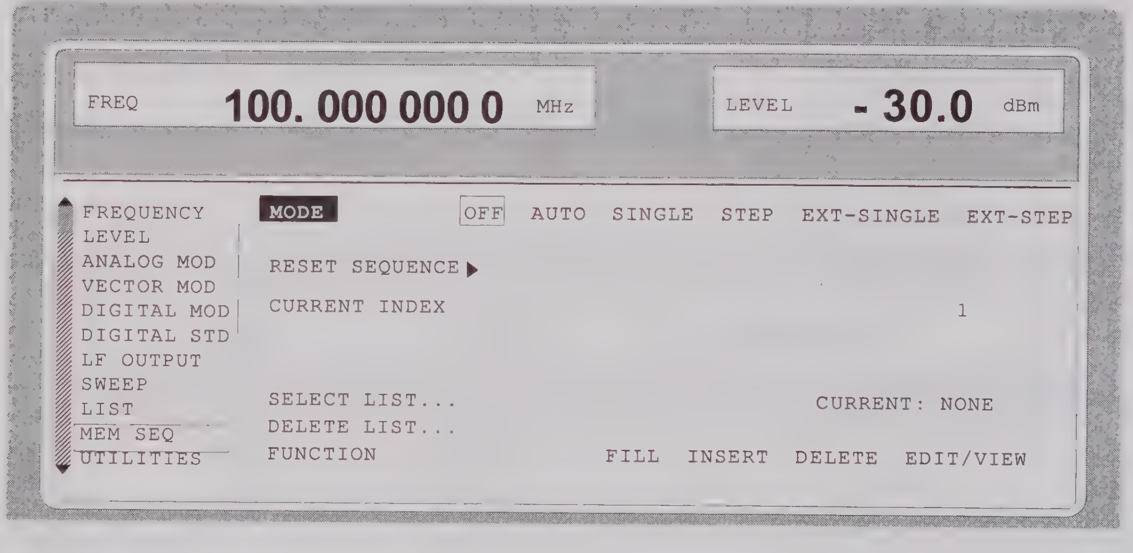
2.2.11.4 Pattern Setting to Operate the List Editor

The user can become familiar with the operation of the list editor by means of the following pattern setting in the MEM SEQ menu. A list has to be generated and filled with values by using the single-value function EDIT/VIEW:

• Memory location number of the first element	20
• Dwell time of the first element	15 s
• Memory location number of the second element	7.

When the setting has been terminated, return to the OPERATION page of the MEM SEQ menu.

At the beginning of the operation sequence, menu MEM SEQ is called. First a list MSEQ0 has to be generated and then activated. The menu cursor marks a parameter of the setting menu on the OPERATION page (c.f. Fig. 2-14).



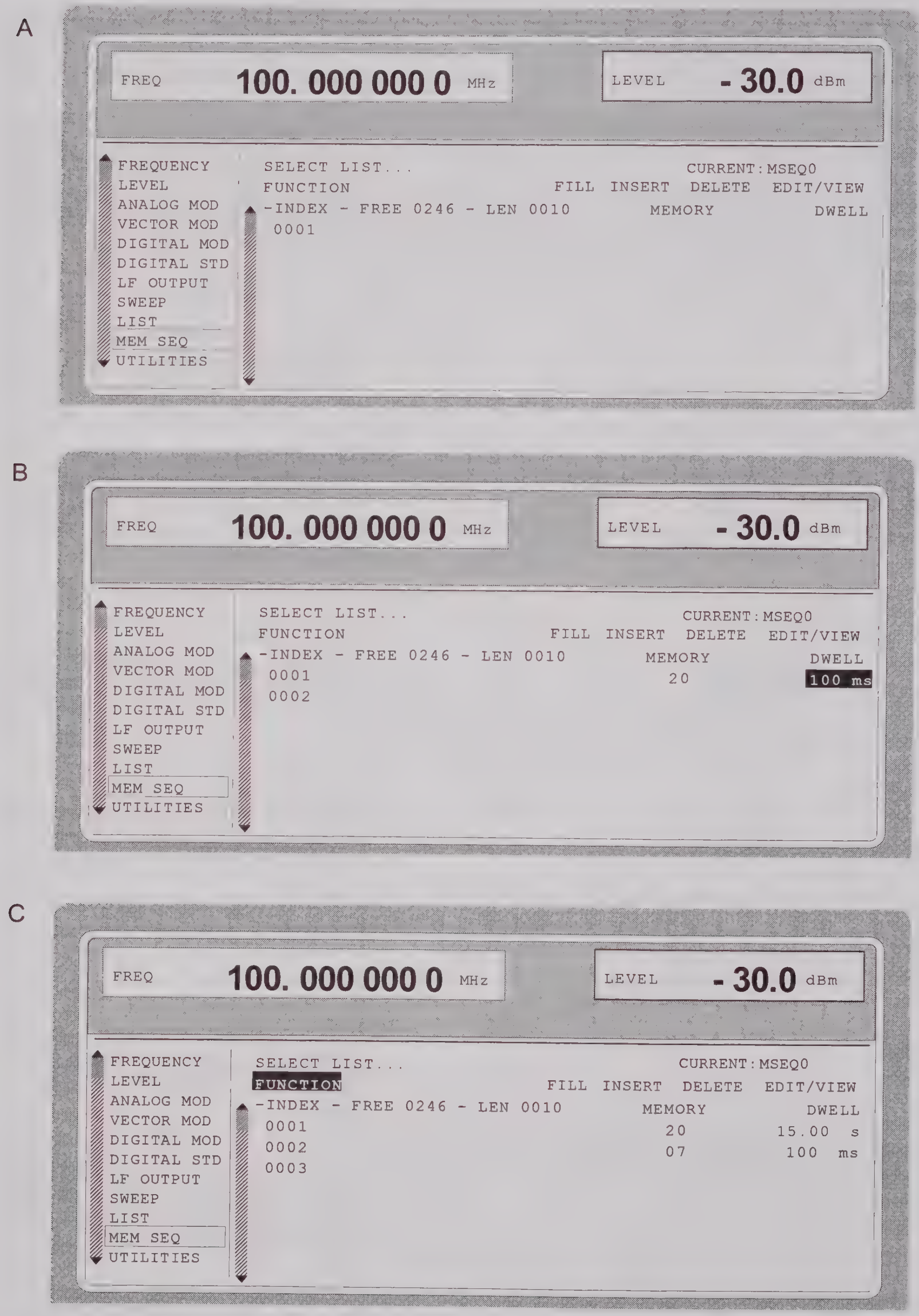
```
FREQ      100. 000 000 0  MHz             LEVEL     - 30.0  dBm

FREQUENCY   MODE          OFF  AUTO  SINGLE  STEP  EXT-SINGLE  EXT-STEP
LEVEL
ANALOG MOD  RESET SEQUENCE ▶
VECTOR MOD
DIGITAL MOD CURRENT INDEX                                  1
DIGITAL STD
LF OUTPUT
SWEEP
LIST        SELECT LIST...                     CURRENT: NONE
MEM SEQ     DELETE LIST...
UTILITIES   FUNCTION           FILL  INSERT  DELETE  EDIT/VIEW
```

Fig. 2-14 Starting point of the pattern setting

Operating steps	Explanations
MENU / VARIATION **MENU / VARIATION** SELECT LIST... SELECT	Select the SELECT LIST menu item.
MENU / VARIATION **MENU / VARIATION** CREATE NEW LIST ▶ SELECT	A new list MSEQ0 is generated. The menu cursor is reset to SELECT LIST...
MENU / VARIATION **MENU / VARIATION** FUNCTION... SELECT	Select the FUNCTION menu item.

Operating steps	Explanations
MENU / VARIATION .EDIT VIEW. MENU / VARIATION SELECT	Select single-value function EDIT/VIEW. The EDIT page of the MEM SEQ menu is called. The menu cursor marks the index of the first element of list MSEQ0.
SELECT	Set the menu cursor to the memory location number value of the first element (c.f. Fig. 2-15,A).
DATA INPUT 2 0 x1 ENTER	Enter MEMORY 20. The menu cursor automatically wraps to the DWELL value of the first element (Fig. 2-15,B). The default value is 100 ms.
DATA INPUT 1 5 x1 ENTER	Enter DWELL 15 s. The menu cursor automatically wraps to the MEMORY value of the second element.
DATA INPUT 7 x1 ENTER	Enter MEMORY 7. The menu cursor automatically wraps to the DWELL value of the second element (default value is 100 ms).
RETURN	Reset the menu cursor to the index.
RETURN	Reset the menu cursor to the FUNCTION menu item of the EDIT page of menu MEM SEQ (c.f. Fig. 2-15,C).
RETURN	Reset the menu cursor to the FUNCTION menu item of the OPERATION page of menu MEM SEQ.

Note: *With the return to the OPERATION page the operation of the list editor is finished. In the list mode (menu LIST), function LEARN ▶ must be activated subsequently to ensure that the settings are transferred to the hardware*



A

```
FREQ      100. 000 000 0 MHz                LEVEL    - 30.0 dBm

FREQUENCY     SELECT LIST...                          CURRENT:MSEQ0
LEVEL         FUNCTION                     FILL  INSERT  DELETE  EDIT/VIEW
ANALOG MOD    -INDEX - FREE 0246 - LEN 0010      MEMORY            DWELL
VECTOR MOD     0001
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
```

B

```
FREQ      100. 000 000 0 MHz                LEVEL    - 30.0 dBm

FREQUENCY     SELECT LIST...                          CURRENT:MSEQ0
LEVEL         FUNCTION                     FILL  INSERT  DELETE  EDIT/VIEW
ANALOG MOD    -INDEX - FREE 0246 - LEN 0010      MEMORY            DWELL
VECTOR MOD     0001                                20             100 ms
DIGITAL MOD    0002
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
```

C

```
FREQ      100. 000 000 0 MHz                LEVEL    - 30.0 dBm

FREQUENCY     SELECT LIST...                          CURRENT:MSEQ0
LEVEL         FUNCTION                     FILL  INSERT  DELETE  EDIT/VIEW
ANALOG MOD    -INDEX - FREE 0246 - LEN 0010      MEMORY            DWELL
VECTOR MOD     0001                                20           15.00  s
DIGITAL MOD    0002                                07             100  ms
DIGITAL STD    0003
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
```

Fig. 2-15, a to c Pattern setting - Edition of a list

2.2.12 Save/Recall - Storing/Calling of Instrument Settings

50 complete instrument settings can be stored in memory locations 1 to 50.

Operating steps	Explanations
DATA INPUT SAVE 1 2 x1 ENTER	Store current instrument setting in memory location 12.
DATA INPUT RCL 1 2 x1 ENTER	Call instrument setting of memory location 12.

The digital display during a save or recall entry is faded in a window.

Memory location 0 has a special function. Here the instrument setting which was current prior to the last memory recall and prior to a preset setting is automatically stored. This permits the resetting of instrument settings which have inadvertently been deleted using Recall 0.

If an instrument setting is stored in which a sweep was switched on, the sweep is started using the recall.

The parameter EXCLUDE FROM RCL in the FREQUENCY and LEVEL-LEVEL menus determines whether the saved RF frequency and RF level are loaded when an instrument setting is loaded, or whether the current settings are maintained.

Store IEC-bus command: `"*SAV 12"`

Recall IEC-bus command: `"*RCL 12"`

Notes:

- *The contents of lists, as they are used for the Memory Sequence (MSEQ) or for user correction (UCOR), is not saved in the SAVE memory. It is stored under the respective list name and can be called. If instrument settings are called which go back to list data such as level setting using UCOR, the current list contents is used. If this has been altered, it is not identical to the list contents at the point of storing any more.*
- *The frame configurations (digital standards PHS, NADC, PDC, GSM, DECT) and the channel configurations (digital standards CDMA, W-CDMA, 3GPP W-CDMA) are not stored in the SAVE memory either. These settings can be stored and loaded via menu items SAVE/RCL FRAME and SAVE/RCL MAPPING in the corresponding DIGITAL STD menus.*
- *The instruments comprise the "Fast Restore" mode for very fast loading of stored device settings. This mode can be called up only during remote control (see section 3, "Fast Restore Mode").*

2.3 Menu Summary

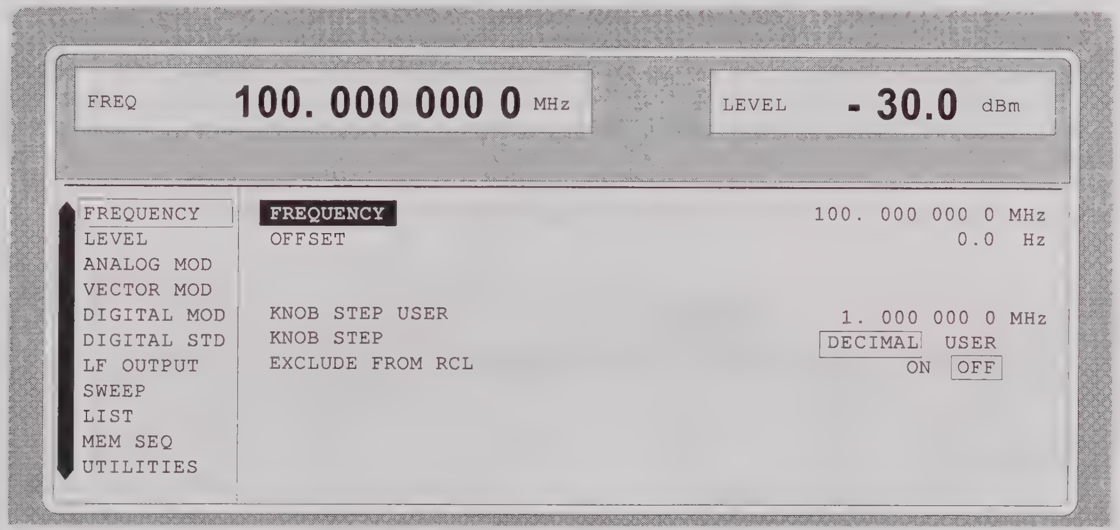
- FREQUENCY
- LEVEL
  - LEVEL
  - ALC
  - UCOR
  - EMF
- ANALOG MOD
  - AM
  - BB-AM
  - FM (option SM-B5)
  - PM (option SM-B5)
- VECTOR MOD (can be equipped with option SMIQB47)
- DIGITAL MOD (options SMIQB20, SMIQB11 and SMIQB12)
- DIGITAL STD (options SMIQB20, SMIQB11, SMIQB12 and digital standards incl. options SMIQB43, SMIQB45 and SMIQB48)
- ARB MOD (option SMIQB60)
- FADING SIM (options SMIQB14, SMIQB15 and SMIQB49)
- NOISE/DIST (option SMIQB17)
- LF OUTPUT
- BERT (option SMIQB21)
- SWEEP
  - FREQ
  - LEVEL
  - LFGEN
- LIST
- MEM SEQ
- UTILITIES
  - SYSTEM
    - GPIB
    - RS232
    - SECURITY
    - LANGUAGE
  - REF OSC
  - PHASE
  - PROTECT
  - CALIB
    - VCO SUM
    - VECTOR MOD
    - LEV PRESET
    - REF OSC
    - LEVEL
    - ALC TABLE
    - LEV ATT
    - LFGEN
  - DIAG
    - CONFIG
    - TPOINT
    - C/NMEAS
    - PARAM
  - TEST
  - MOD KEY
  - AUX I/O
  - BEEPER
- HELP

2.4 RF Frequency

The RF frequency can be set directly using the [FREQ] key (cf. Section 2.2.6) or by accessing menu FREQUENCY.
The frequency of the RF output signal is entered/indicated under FREQUENCY in the FREQUENCY menu.
The input value of frequency settings opened by means of the [FREQ] key and indicated in the header line considers the offset in calculation (cf. next Section). This offers the possibility of entering the desired output frequency of possibly series-connected instruments such as mixers in the menu.

Note:	*Further settings:*	*Frequency sweep*	*Menu SWEEP*
		LF frequency	*Menu ANALOG MOD* *Menu LF-OUTPUT*
		Int./ext. reference frequency	*Menu UTILITIES-REF OSC*
		Phase of the output signal	*Menu UTILITIES-PHASE*

Menu selection: FREQUENCY


FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

FREQUENCY 100. 000 000 0 MHz
OFFSET 0.0 Hz
KNOB STEP USER 1. 000 000 0 MHz
KNOB STEP DECIMAL USER
EXCLUDE FROM RCL ON OFF

Fig. 2-16 Menu FREQUENCY (preset setting)

FREQUENCY Input value of the RF frequency at the RF output connector.

IEC/IEEE-bus command `SOUR:FREQ 100E6`

OFFSET Input value of the frequency offset, e.g., of a series-connected mixer (cf. Section "Frequency Offset"). The status line indicates FREQ-OFFST.

IEC/IEEE-bus command `SOUR:FREQ:OFFS 0`

KNOB STEP USER Input value of the step width for frequency variation using the rotary knob. The RF frequency is varied in the step width entered if KNOB STEP is set to USER.

IEC/IEEE-bus command `SOUR:FREQ:STEP 1MHz`

KNOB STEP DECIMAL: Variation step width corresponding to the position of the digit cursor.

USER: "User Defined", variation step width as entered under KNOB STEP USER .

EXCLUDE FROM RCL	OFF	The saved frequency is also loaded when instrument settings are loaded with the [RCL] key or with a memory sequence. IEC/IEEE-bus command `SOUR:FREQ:RCL INCL`
	ON	The RF frequency is not loaded when instrument settings are loaded, the current frequency is maintained. IEC/IEEE-bus command `SOUR:FREQ:RCL EXCL`

2.4.1 Frequency Offset

The SMIQ offers the possibility of entering an offset (OFFSET) of possibly series-connected instruments in the FREQUENCY menu. The indication/input value of FREQ in the header field considers this input and represents the frequency value of the RF signal at the output of these instruments (cf. Fig. 2-17).

The entry values FREQUENCY and OFFSET in the menu FREQUENCY or FREQ in the header line are related to the RF output frequency as follows :

FREQ - OFFSET = output frequency (= FREQUENCY).

An offset entry causes no modification of the RF output frequency but only a modification of the display value in the FREQ field in the header line, ie FREQ in the header line indicates the offset-associated frequency and FREQUENCY in the menu FREQUENCY the RF output frequency. The status line indicates FREQ-OFFST.

The offset setting also remains effective with the frequency sweep.

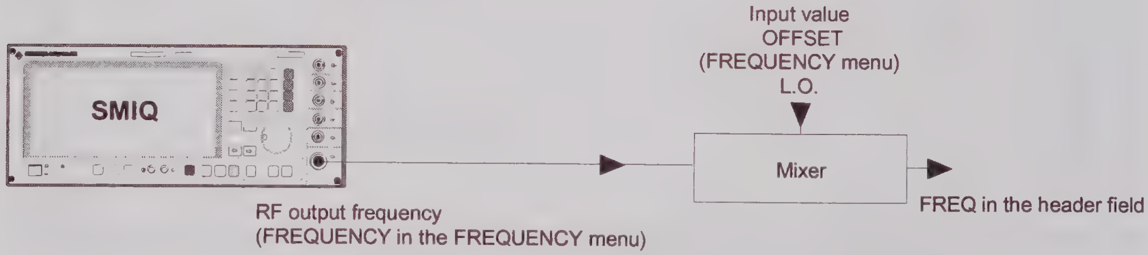


Fig. 2-17 Example of a circuit with frequency offset

2.5 RF Level

The RF level can be set directly using the [LEVEL] key (cf. Section 2.2.6) or by accessing the LEVEL menu.

In the LEVEL-LEVEL menu, the set RF output level is indicated under AMPLITUDE. A two-line level display appears for digital modulation or digital standard. The upper line indicates the average power (LEVEL), the lower line the peak envelope power (PEP) of the modulated RF output signal.

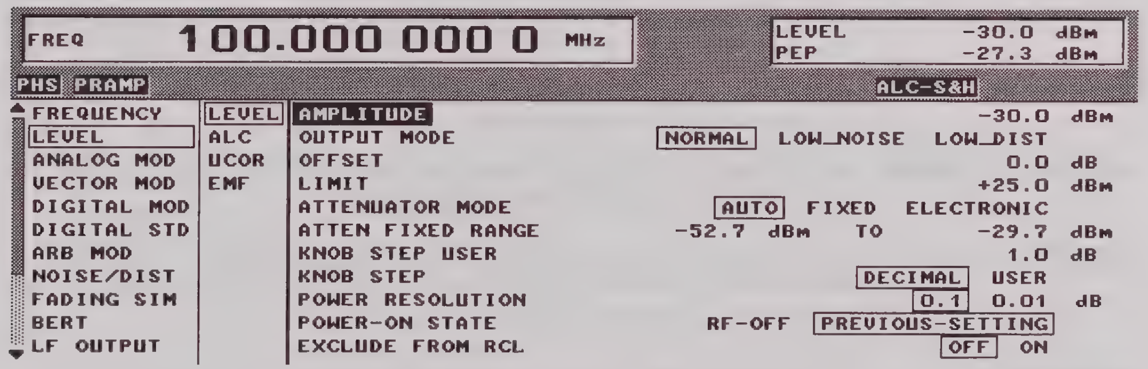
The input value of level settings effected in the LEVEL-LEVEL menu directly corresponds to the RF output level.

The input value of the level settings opened using the [LEVEL] key mathematically considers the offset of an attenuation/amplification element which is possibly series-connected (cf. Section 2.5.1). This offers the possibility of entering the desired level at the output of series-connected instruments, the SMIQ then alters the RF output level correspondingly. The offset can also be entered in the LEVEL-LEVEL menu.

dBm, dBµV, mV and µV can be used as level units. The 4 unit keys are directly labeled with these units. In order to change to another level unit, simply press the desired unit key.

Notes:
- *The message ERROR is displayed in the status line if the level set in the overrange is not reached.*
- *For digital modulation or digital standard, a WARNING message appears in the status line if the set LEVEL or the displayed PEP are overranged. If the set level cannot be generated as an overrange value, ERROR will be displayed.*
- *Further settings: Level sweep   menu SWEEP*

Menu selection: LEVEL - LEVEL



```
FREQ        100.000 000 0  MHz                  LEVEL          -30.0  dBm
                                                PEP            -27.3  dBm
PHS PRAMP                                                ALC-S&H
FREQUENCY      LEVEL  AMPLITUDE                                    -30.0  dBm
LEVEL          ALC    OUTPUT MODE             NORMAL  LOW_NOISE  LOW_DIST
ANALOG MOD     VCOR   OFFSET                                         0.0  dB
VECTOR MOD     EMF    LIMIT                                        +25.0  dBm
DIGITAL MOD           ATTENUATOR MODE             AUTO  FIXED  ELECTRONIC
DIGITAL STD           ATTEN FIXED RANGE     -52.7  dBm   TO        -29.7  dBm
ARB MOD               KNOB STEP USER                                 1.0  dB
NOISE/DIST            KNOB STEP                            DECIMAL  USER
FADING SIM            POWER RESOLUTION                         0.1  0.01  dB
BERT                  POWER-ON STATE          RF-OFF  PREVIOUS-SETTING
LF OUTPUT             EXCLUDE FROM RCL                           OFF  ON
```

Fig. 2-18   Menu LEVEL (preset setting) POWER RESOLUTION is set to 0.01 dB

AMPLITUDE Input value of the RF level at the RF output connector.

IEC/IEEE-bus command   `SOUR:POW -30`

OFFSET

Input value of the level offset of the RF output level compared to the input value of the RF level indicated in the LEVEL header field. Input in dB (cf. Section 2.5.1, Level Offset). The status line indicates LEV-OFFST.

IEC/IEEE-bus command `SOUR:POW:OFFS 0`

LIMIT

Input value of level limitation. This value indicates the upper limit of the level at the RF output connector. If a level above this limit is attempted to be set, a warning is displayed in the status line.

IEC/IEEE-bus command `SOUR:POW:LIM 16 dBm`

ATTENUATOR MODE

AUTO — Normal operation. The attenuator switching mechanically switches in steps of 5 dB, the switching points being fixed.

IEC/IEEE-bus command `:OUTP:AMOD AUTO`

FIXED — Level settings are made in a range of somewhat over 20 dB without switching the attenuator (see section 2.5.2, "Non-interrupting Level Setting"). The range of variation is fixed automatically upon selection of this operating mode. The range is indicated in the ATTEN FIXED RANGE menu. With level settings out of the indicated range, a warning is displayed.

IEC/IEEE-bus command `:OUTP:AMOD FIX`

ELECTRONIC

Level settings are made in a range of somewhat over 90 dB without switching the attenuator (see section 2.5.2, "Non-interrupting Level Setting"). The range of variation is fixed automatically upon selection of this operating mode. The range is indicated in the ATTEN FIXED RANGE menu. With level settings out of the indicated range, a warning is displayed.
This function is only available if the IQMOD module of version VAR 4 or higher is installed (indication in UTILITIES - DIAG - CONFIG menu, IQMOD Var 4 required).

IEC/IEEE-bus command `:OUTP:AMOD ELEC`

ATTEN FIXED RANGE

Indication of the level range in which the level is set without interruption in the "ATTENUATOR MODE FIXED" operating mode.

KNOB STEP USER

Input value of the step width for level variation using the rotary knob. The RF level is varied in the step width entered if KNOB STEP is set to USER.

IEC/IEEE-bus command `SOUR:POW:STEP 1`

KNOB STEP

DECIMAL — Variation step width according to the position of the digit cursor.

USER — User Defined, variation step width as entered under KNOB STEP USER.

POWER RESOLUTION

Selection of resolution of LEVEL display. For level range -99.9 dBm to +16 dBm the resolution for the level display can be set to 0.1 dB or 0.01 dB.

POWER-ON STATE

Selection of the state the RF output is to assume after power-on of the unit

RF OFF	Output is switched off
PREVIOUS SETTING	Same state as before switch-off
IEC/IEEE-bus command	`:OUTP:PON ON`

EXCLUDE FROM RCL	OFF	The saved RF level is also loaded when instrument settings are loaded with the [RCL] key or with a memory sequence. IEC/IEEE-bus command `SOUR:POW:RCL INCL`
	ON	The RF level is not loaded when instrument settings are loaded, the current level is maintained. IEC/IEEE-bus command `SOUR:POW:RCL EXCL`

2.5.1 Level Offset

The SMIQ offers the possibility of entering the offset (OFFSET) of a possibly series-connected attenuator/amplification element in the LEVEL-LEVEL menu. The indication/input value in the LEVEL header field considers this input (see below) and represents the level value of the signal at the output of the series-connected instrument (cf. Fig. 2-19).

The entry values AMPLITUDE and OFFSET in the menu LEVEL or LEVEL in the header line are related to the RF output level as follows:

LEVEL - OFFSET = output level (= AMPLITUDE)

An offset entry causes no modification of the RF output frequency but only a modification of the display value in the LEVEL field in the header line, ie LEVEL in the header line indicates the offset-associated level and AMPLITUDE in the menu LEVEL the RF output level. The status line indicates LEV-OFFST. The offset is to be entered in dB.

The offset setting also remains effective in the ATTENUATOR MODE FIXED operating mode and with level sweep.

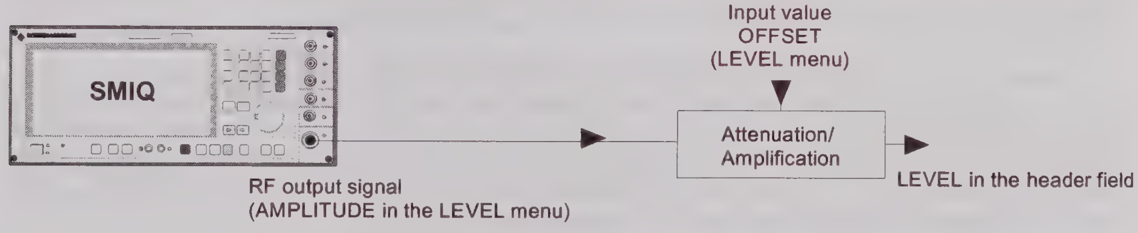


Fig. 2-19 Example of a circuit with level offset

2.5.2 Interrupt-free Level Setting

In the ATTENUATOR MODE FIXED and ATTENUATOR MODE ELECTRONIC operating modes, level settings are carried out without interruption. The attenuator is switched electronically rather than mechanically.
The MODE FIXED variation range is somewhat over 20 dB, the variation range of MODE ELECTRONIC over 90 dB. In case of over- or underranging of the normal variation range, level errors strongly increase and an under/overrange warning is displayed. At high attenuation values the spectral purity of the output signal is degraded.
The ATTENUATOR MODE ELECTRONIC mode is only possible with level control switched off. The ALC OFF mode is automatically set to TABLE.

Note: *To ensure highest level accuracy in ATTENUATOR MODE ELECTRONIC mode, the self calibration routines ALC TABLE and LEV ATT should be called up in the UTILITIES - CALIB menu after temperature variations of more than 5 degrees.*

The MODE ELECTRONIC cannot be used either simultaneously with the SLOT ATTENUATION function of the digital standards. This applies to all TDMA standards. The MODE ELECTRONIC cannot be used either simultaneously with the DIGITAL MOD - POWER RAMP CONTROL - ATTENUATION function.

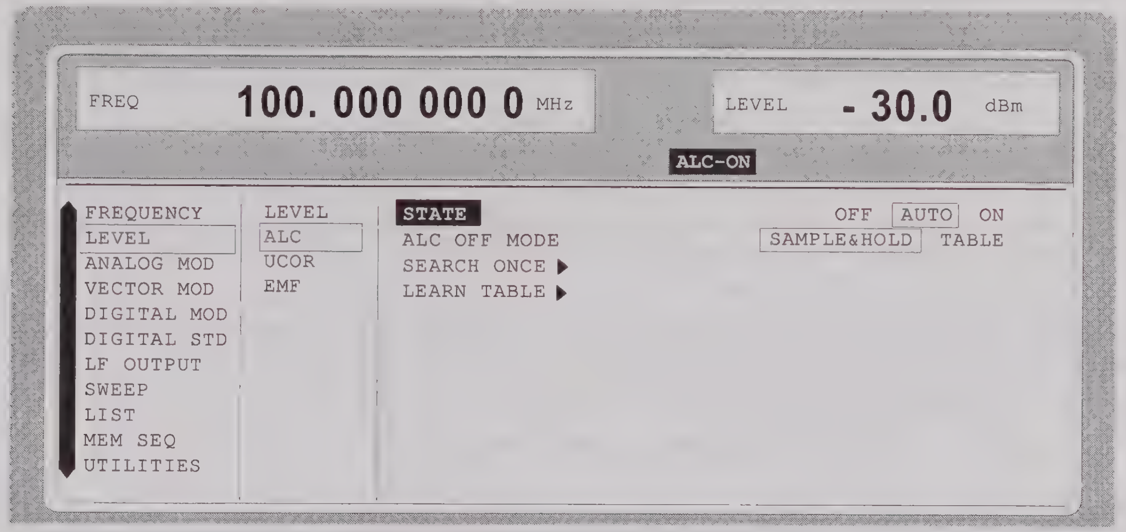
2.5.3 Switching On/Off Internal Level Control

The LEVEL - ALC menu allows the level control to be switched on and off for special applications. In the normal operating mode for CW, AM and FM(PM), level control is switched on so that an optimum level accuracy is obtained. For vector modulation or digital modulation, level control must normally be off. In this case the SAMPLE&HOLD or the TABLE mode can be selected instead.

In the SAMPLE&HOLD mode the level is recalibrated after each level or frequency setting. To do this, CW is selected for a short period of time, level control is switched on and the level control held at the value attained. If this calibration procedure is not desired, the TABLE mode (level control voltage taken from a table) can be selected. In this mode the correction values required after a frequency or level change are obtained from a table. With the LEARN TABLE ▶ function called up, a new table can be prepared without any additional measuring instruments being required.

The preset level control is AUTO. Level control in this mode is automatically adapted to the operating conditions. For special applications, level control can be held in the OFF or ON state. Level control OFF is useful for improving the intermodulation suppression in multi-signal measurements in the CW mode. The ON setting is recommended if vector modulation or digital modulation with a constant envelope curve is required.

Menu selection: LEVEL - ALC



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

ALC-ON

STATE OFF [✓ AUTO] ON

ALC OFF MODE [✓ SAMPLE&HOLD] TABLE

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
LEVEL
ALC
UCOR
EMF
SEARCH ONCE ▶
LEARN TABLE ▶

Fig. 2-20 Menu LEVEL - ALC (preset setting)

STATE	OFF	Internal level control is deactivated. In this state no AM is possible . IEC/IEEE-bus command `SOUR:POW:ALC OFF`
	AUTO	Normal state. The internal level control is automatically adjusted to the operating conditions. IEC/IEEE-bus command `SOUR:POW:ALC AUTO`
	ON	Internal level control is permanently switched on. IEC/IEEE-bus command `SOUR:POW:ALC ON`
ALC OFF MODE	SAMPLE&HOLD	Level recalibration in the ALC OFF mode after the level or frequency has been set. IEC/IEEE-bus command `SOUR:POW:ALC:SEAR ON`
	TABLE	In the ALC OFF mode correction values are taken from a table. IEC/IEEE-bus command `SOUR:POW:ALC:SEAR OFF`

SEARCH ONCE ▶ Manual short-time switching on of the level control for level calibration in the ALC OFF and SAMPLE&HOLD operating mode.

IEC/IEEE-bus command `SOUR:POW:ALC:SEAR ONCE`

LEARN TABLE ▶ Correction values for the ALC OFF MODE-TABLE function are regenerated (level control voltage obtained from a table).

IEC/IEEE-bus command `SOUR:POW:ALC:TABL?`

2.5.4 User Correction (UCOR)

Function "User Correction" can be used to create and activate lists in which arbitrary RF frequencies are assigned level correction values.

Up to 10 lists with a total of 160 correction values can be compiled. For frequencies which are not included in the list the level correction is determined by means of interpolation of the nearest correction values.

When user correction is switched on, the LEVEL indication is completed by the indication UCOR (User Correction) in the header field of the display. The RF output level is the sum of both values.

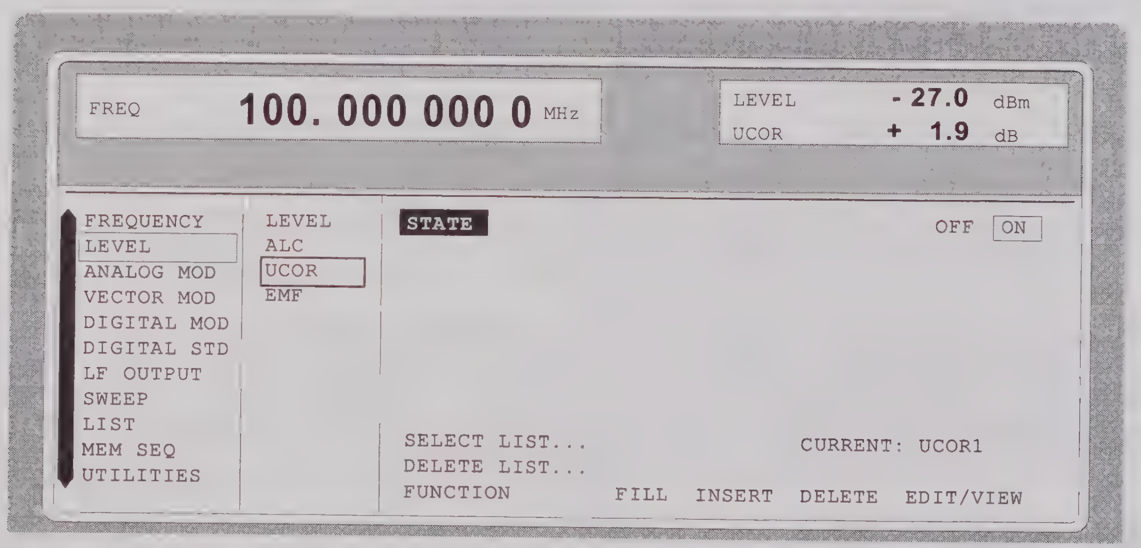
LEVEL + UCOR = output level

If the offset setting is used at the same time, the LEVEL indication value is the difference of the input values AMPLITUDE and OFFSET of the menu LEVEL.

AMPLITUDE - OFFSET = LEVEL

The user correction is effective in all operating modes if switched on.

Menu selection: LEVEL - UCOR



FREQ 100. 000 000 0 MHz

LEVEL - 27.0 dBm

UCOR + 1.9 dB

STATE OFF ON

CURRENT: UCOR1

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
LEVEL
ALC
UCOR
EMF
SELECT LIST...
DELETE LIST...
FUNCTION
FILL
INSERT
DELETE
EDIT/VIEW

Fig. 2-21 Menu LEVEL - UCOR - OPERATION side

STATE Switching on/off user correction.
IEC/IEEE-bus command `SOUR:CORR ON`

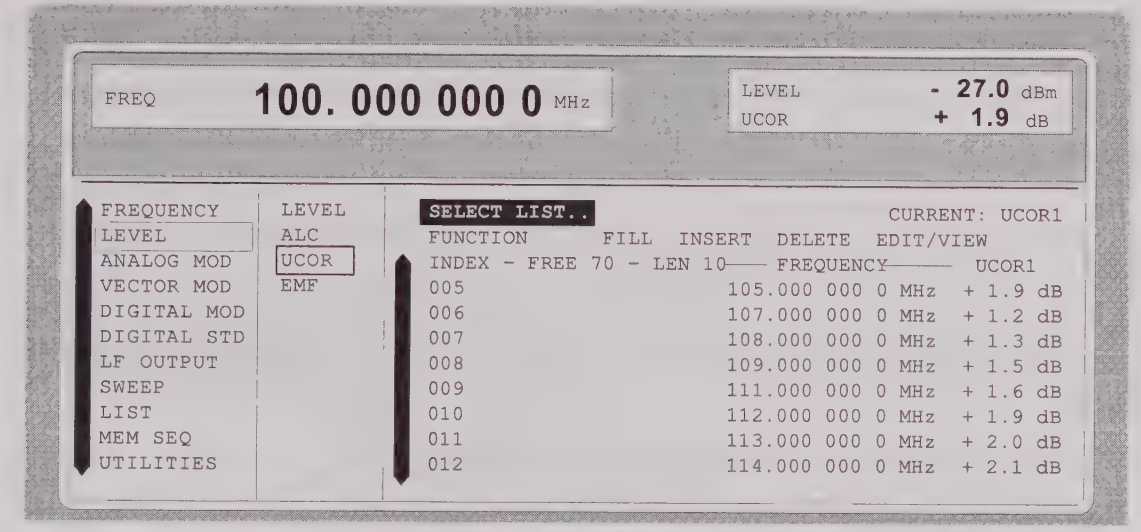
SELECT LIST... Selection of a list or generation of a new list (cf. Section 2.2.11, List Editor).
IEC/IEEE-bus command `SOUR:CORR:CSET "UCOR1"`

DELETE LIST... Deletion of a list (cf. Section 2.2.11, List Editor).
IEC/IEEE-bus command `SOUR:CORR:CSET:DEL "UCOR2"`

FUNCTION Selection of the editing mode to process the selected list (cf. Section 2.2.11, List Editor).
IEC-bus commands

```
SOUR:CORR:CSET:DATA:FREQ 100 MHz, 102 MHz,...
SOUR:CORR:CSET:DATA:POW 1dB,  0.8dB,...
```

Menu selection: LEVEL - UCOR


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
LEVEL
ALC
UCOR
EMF
SELECT LIST..
FUNCTION
FILL
INSERT
DELETE
EDIT/VIEW

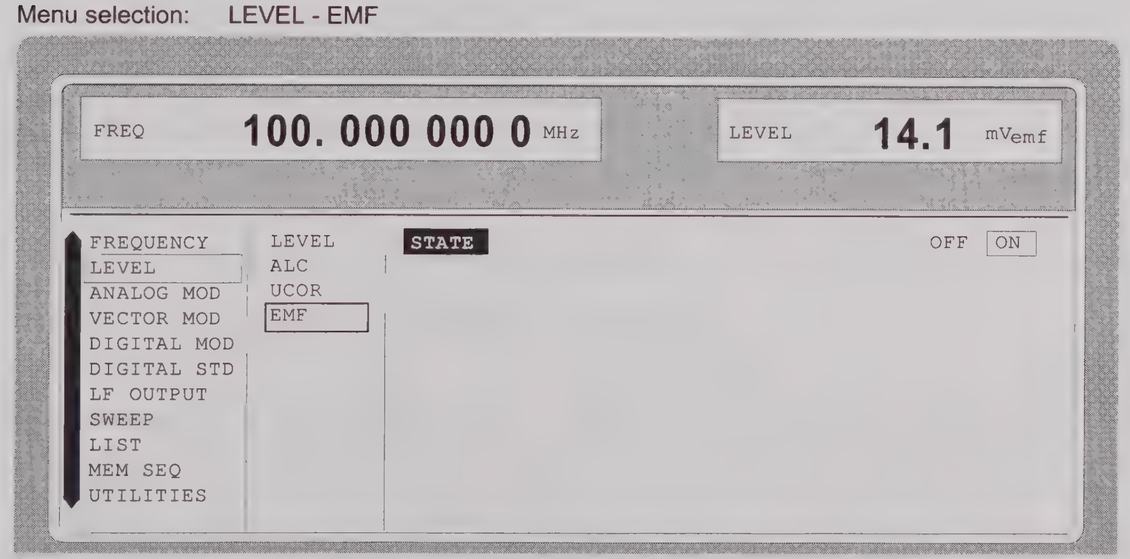
```
FREQ   100. 000 000 0 MHz        LEVEL   - 27.0 dBm
                                 UCOR    +  1.9 dB

SELECT LIST..                            CURRENT: UCOR1
FUNCTION          FILL   INSERT   DELETE   EDIT/VIEW
INDEX - FREE 70 - LEN 10── FREQUENCY────── UCOR1
005                   105.000 000 0 MHz   + 1.9 dB
006                   107.000 000 0 MHz   + 1.2 dB
007                   108.000 000 0 MHz   + 1.3 dB
008                   109.000 000 0 MHz   + 1.5 dB
009                   111.000 000 0 MHz   + 1.6 dB
010                   112.000 000 0 MHz   + 1.9 dB
011                   113.000 000 0 MHz   + 2.0 dB
012                   114.000 000 0 MHz   + 2.1 dB
```

Fig. 2-22 Menu UCOR - LEVEL-EDIT side

2.5.5 EMF

The signal level can also be set and indicated as the voltage of EMF (open-circuit voltage). EMF is displayed in the header field of the display after the unit of the level indication.

Menu selection: LEVEL - EMF


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
LEVEL
ALC
UCOR
EMF

```
FREQ   100. 000 000 0 MHz        LEVEL   14.1  mVemf

STATE                                  OFF  ON
```

Fig. 2-23 Menu LEVEL-EMF

STATE	ON	Voltage value of the level is the voltage of EMF.
	OFF	Voltage value of the level is voltage at 50 Ω (preset setting).

2.5.6 [RF ON / OFF]-Key

The RF output signal is switched on and off again using the [RF ON / OFF] key. This does not influence the current menu. When the output signal is switched off, the message "RF OFF" is displayed in the LEVEL indication of the header field. If RF OFF is displayed, the 50-Ω source resistance is maintained.

IEC/IEEE-bus command `:OUTP OFF`

2.5.7 Reset Overload Protection

The SMIQ is protected against overload by an external signal which is fed into the RF output. If an external signal is too high, the overload protection responds. This state is indicated by means of the message "RF OFF" in the LEVEL indication in the header field and the message "OVERLOAD" in the status line.

➢ Reset the overload protection by pressing the [RF ON / OFF] key.

IEC/IEEE-bus command `:OUTP:PROT:CLE`

2.6 Modulation - General

The SMIQ offers the following modulations and digital standards:

- Analog modulations
  - Amplitude modulation (AM)
  - Broadband AM (BB-AM)
  - Frequency modulation (FM; with option SM-B5 only)
  - Phase modulation (PM; with option SM-B5 only)
  - Pulse modulation (PULSE)
- Vector modulation (VECTOR MOD)
- Digital modulation (DIGITAL MOD; option SMIQB20)
- Digital standards (DIGITAL STD;-PHS; IS95/CDMA - option SMIQB42; NADC; PDC; GSM; DECT; W-CDMA - option SMIQB43; 3GPP W-CDMA - option SMIQB45; Enhanced Channels – option SMIQB48; options SMIQB20 and SMIQB11)

For AM, FM, PM and digital modulation internal or external modulation sources can be used.
For BB-AM, pulse and vector modulation only external modulation sources can be used.

2.6.1 Modulation Sources

Internal Modulation Sources

The internal modulation generator LF GEN is available for AM, FM and PM. The generator supplies sinusoidal signals in the frequency range from 0.1 Hz to 1 MHz. For a more detailed description, cf. Section "LF Generator".
The internal modulation coder (option SMIQB20) supplies PRBS signals, clock signals or modulation data for the digital modulations. The data generator (option SMIQB11) supplies modulation data and control signals. For a more detailed description, cf. Section "Digital Modulations".

External Modulation Sources

The appropriate input sockets to the different modulations in the case of external supply can be taken from Table 2-1.

Table 2-1 Input sockets for the different types of modulation

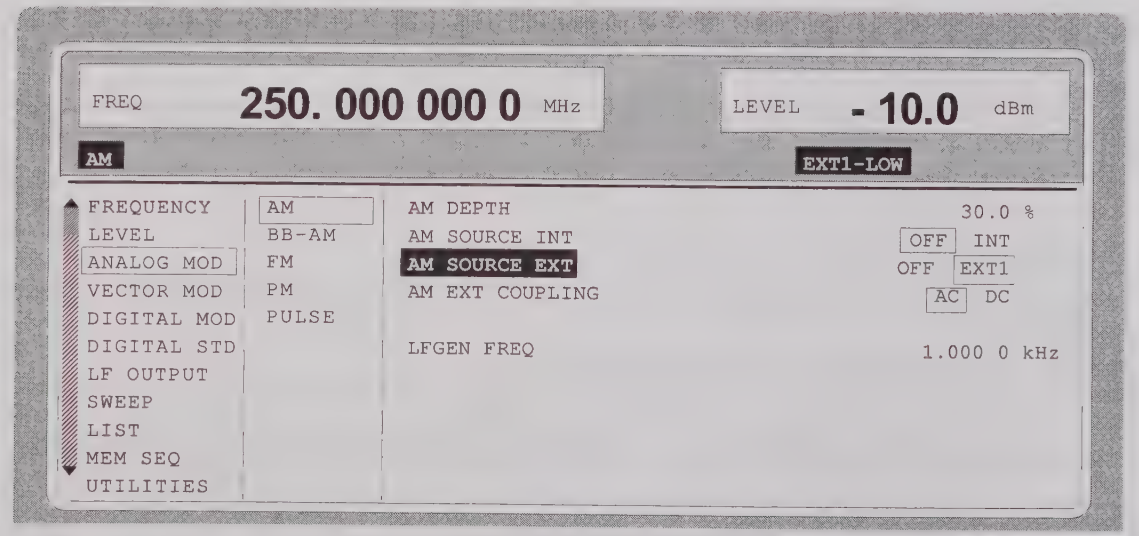
Modulation	Input									
	EXT1	EXT2	PULS	I	Q	DATA	PAR DATA	BIT CLOCK	SYMBOL CLOCK	POW RAMP
AM	X									
BB-AM				X						
FM1	X	X								
FM2	X	X								
PM1	X	X								
PM2	X	X								
PULSE			X							
VECTOR MOD				X	X					X
DIGITAL MOD						X	X	X	X	X

EXT1/EXT2-Inputs

The external modulation signal for AM, FM and PM at inputs EXT1 and EXT2 must show a voltage of V_s = 1 V (V_{eff} = 0.707 V) in order to maintain the modulation depth or deviation indicated. A monitoring circuit checks the input voltage in the frequency range 10 Hz to 100 kHz. Deviations of more than ±3 % are signaled in the status line by means of the following messages (cf. Table 2-2). The inputs EXT1 and EXT2 can be AC- or DC-coupled. Monitoring is only active if the inputs are AC-coupled.

Table 2-2 Status messages in the case of a deviation from the rated value at the external modulation inputs EXT1 and EXT2

Message	Deviation
EXT1-HIGH	Voltage at EXT1 too high
EXT1-LOW	Voltage at EXT1 too low
EXT2-HIGH	Voltage at EXT2 too high
EXT2-LOW	Voltage at EXT2 too low
EXT-HI/HI	Voltage at EXT1 and EXT2 too high
EXT-LO/LO	Voltage at EXT1 and EXT2 too low
EXT-HI/LO	Voltage at EXT1 too high and EXT2 too low
EXT-LO/HI	Voltage at EXT1 too low and EXT2 too high



FREQ 250. 000 000 0 MHz

LEVEL - 10.0 dBm

AM

EXT1-LOW

AM DEPTH 30.0 %

AM SOURCE INT OFF INT

AM SOURCE EXT OFF EXT1

AM EXT COUPLING AC DC

LFGEN FREQ 1.000 0 kHz

Fig. 2-24 Example: Status message "EXT1-LOW" in case of voltage at EXT1 too low

I/Q Inputs

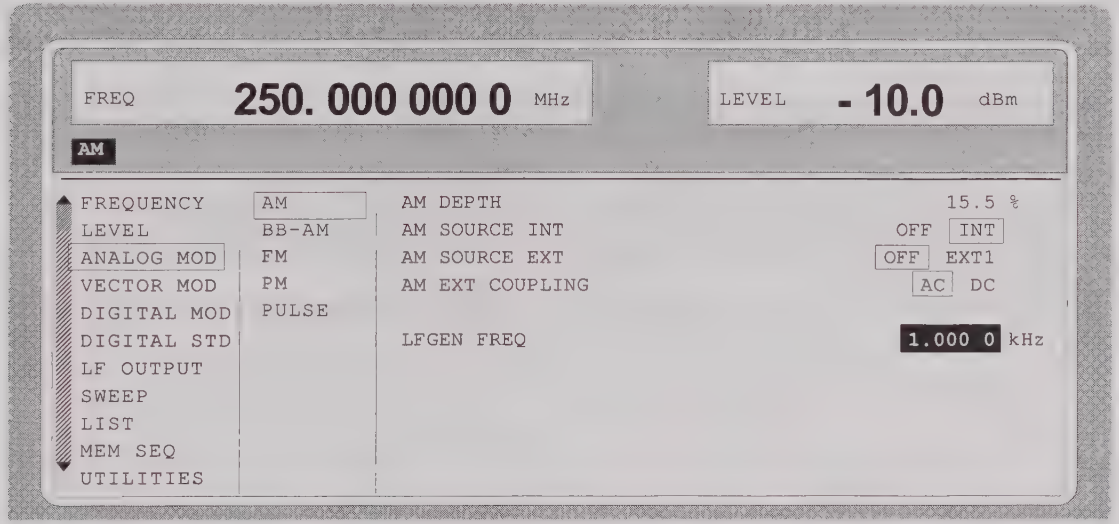
The nominal voltage at the I/Q-inputs for external vector modulation is U_S = 0.5 V. Input resistance is 50 Ω. For a more detailed description, cf. Section "Vector Modulation".

For external broadband AM the I-input is used. Input sensitivity is 0,25 V for 100% AM.

2.6.2 LF Generator

The SMIQ is equipped with a LF-generator as internal modulation source as a standard. The generator supplies sinusoidal signals in the frequency range from 0.1 Hz to kHz.

The frequency settings of the internal modulation signals can be made in one of the modulation menus (AM, FM, PM) as well as in the LF-output menu. Figure 2-25 shows the setting parameters using the AM menu as an example.



FREQ 250. 000 000 0 MHz

LEVEL - 10.0 dBm

AM DEPTH 15.5 %

AM SOURCE INT OFF INT

AM SOURCE EXT OFF EXT1

AM EXT COUPLING AC DC

LFGEN FREQ 1.000 0 kHz

Fig. 2-25 Example: Settings of the LF generator in the AM menu

2.6.3 Simultaneous Modulation

Combination of AM and FM as well as AM and vector modulation is possible. Instead of FM, phase modulation (PM) can be switched on as well.

Two-tone AM is possible by simultaneously switching on the external and internal source.

Two-tone FM or two-tone PM is possible by simultaneously switching on FM1 and FM2 or PM1 and PM2. For FM1 and FM2 (PM1 and PM2) separate deviations can be set and separate sources switched on.

Note: *With two-tone modulation please observe that the set deviation or modulation depth is valid for one signal and the sum deviation or sum modulation depth is determined by adding both signals. This results in overmodulation if the maximal value for deviation or modulation depth is exceeded.*

2.6.4 [MOD ON/OFF] Key

The modulations can directly be switched on/off using the key or by accessing the modulation menus. When switching on using the [MOD ON/OFF] key, the modulation sources which are set in the modulation menus are used.

The [MOD ON/OFF] key can either be effective for all modulations or for a selected modulation. The selection for which modulation the [MOD ON/OFF] key is effective is made in the UTILITIES-MOD KEY menu (cf. Section "Assigning Modulation to [MOD ON/OFF] Key").

When selecting a certain type of modulation, each pressing the [MOD ON/OFF] key switches on or off the modulation selected.

In the case of selection "all modulations", the [MOD ON/OFF] key has the following effect:

- At least one modulation is active:
  Pressing the [MOD ON/OFF] key switches off all active modulations. Which modulations were active is stored.
- No modulation is active:
  Pressing the [MOD ON/OFF] key switches on the modulations which were last switched off using the [MOD ON/OFF] key.

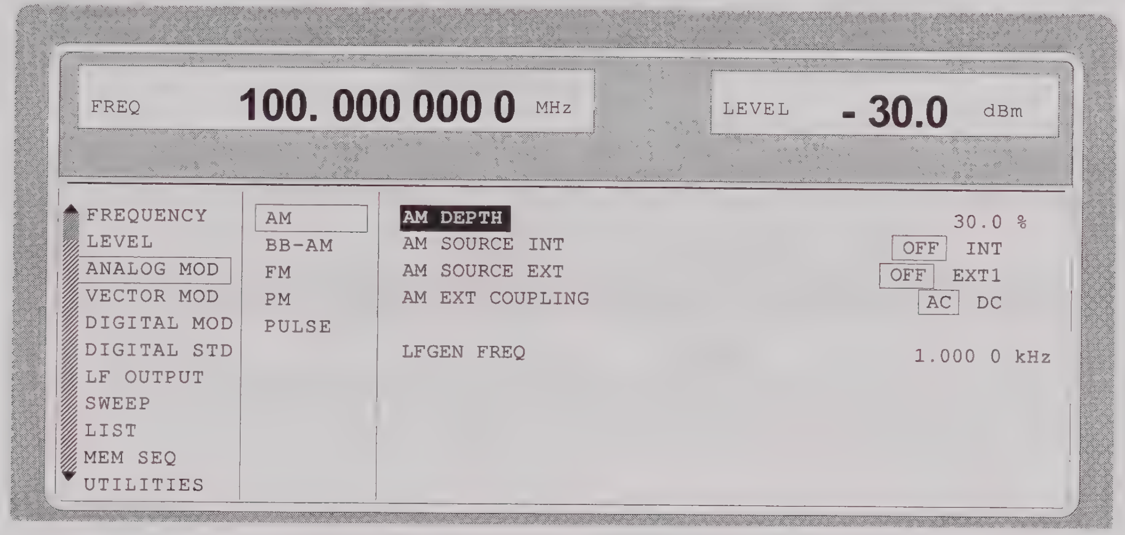
2.7 Analog Modulations

2.7.1 Amplitude Modulation

Menu ANALOG MOD-AM offers access to settings for amplitude modulation.

Notes:
- *The specifications for AM are only valid for the specified Level (PEP) range.*
- *For AM, setting LEVEL-ALC-STATE ON or AUTO is recommended.*

Menu selection: ANALOG MOD - AM



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

AM DEPTH 30.0 %

AM SOURCE INT OFF INT

AM SOURCE EXT OFF EXT1

AM EXT COUPLING AC DC

LFGEN FREQ 1.000 0 kHz

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
AM
BB-AM
FM
PM
PULSE

Fig. 2-26 Menu ANALOG MOD-AM (preset setting)

AM DEPTH	Input value of the modulation depth. IEC/IEEE-bus command `SOUR:AM 30PCT`
AM SOURCE INT	Selection of the internal source. IEC/IEEE-bus command `SOUR:AM:SOUR INT1; STAT ON`
AM SOURCE EXT	Selection of the external source. IEC/IEEE-bus command `SOUR:AM:SOUR EXT; STAT ON`
AM EXT COUPLING	Selection of the kind of coupling AC or DC with external supply (input EXT1). IEC/IEEE-bus command `SOUR:AM:EXT:COUP AC`
LFGEN FREQ	Input value of the frequency of the LF generator. IEC/IEEE-bus command `SOUR:AM:INT:FREQ 1kHz`

2.7.2 Broadband AM (BB-AM)

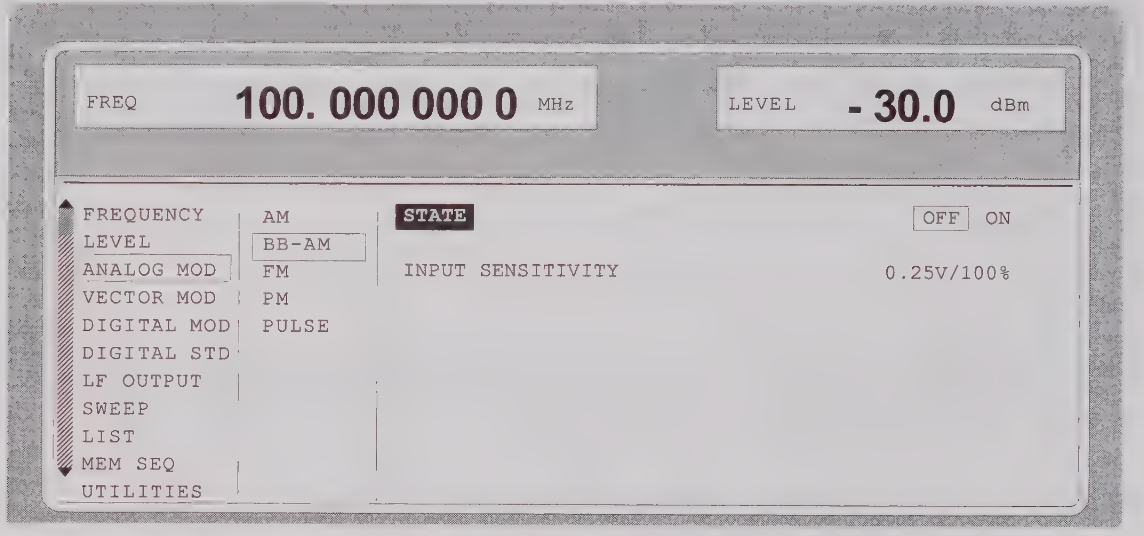
In the BB-AM mode the I/Q modulator is used for amplitude modulation. Level control should be set to AUTO or ON (see section, Switching On/Off Internal Level Control).

The modulation input (BB-AM) is identical with the I input of the I/Q modulator. The input impedance is 50 Ω. A signal of -0.25 V to +0.25 V corresponds to an amplitude modulation of -100% to +100%. Setting the modulation depth with BB-AM is not possible.

Notes:

- *For Broadband AM the value for the upper level limit is the same as for vector modulation. Depending on the modulation depth, PEP can exceed the displayed value by up to 6 dB.*
- *Broadband AM cannot be selected together with normal AM or vector modulation. These modulation deactivate one another*

Menu selection: ANALOG MOD - BB-AM


FREQ
100. 000 000 0
MHz
LEVEL
- 30.0
dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
AM
BB-AM
FM
PM
PULSE
STATE
OFF ON

INPUT SENSITIVITY 0.25V/100%

Fig. 2-27 Menu ANALOG MOD - BB-AM (preset setting)

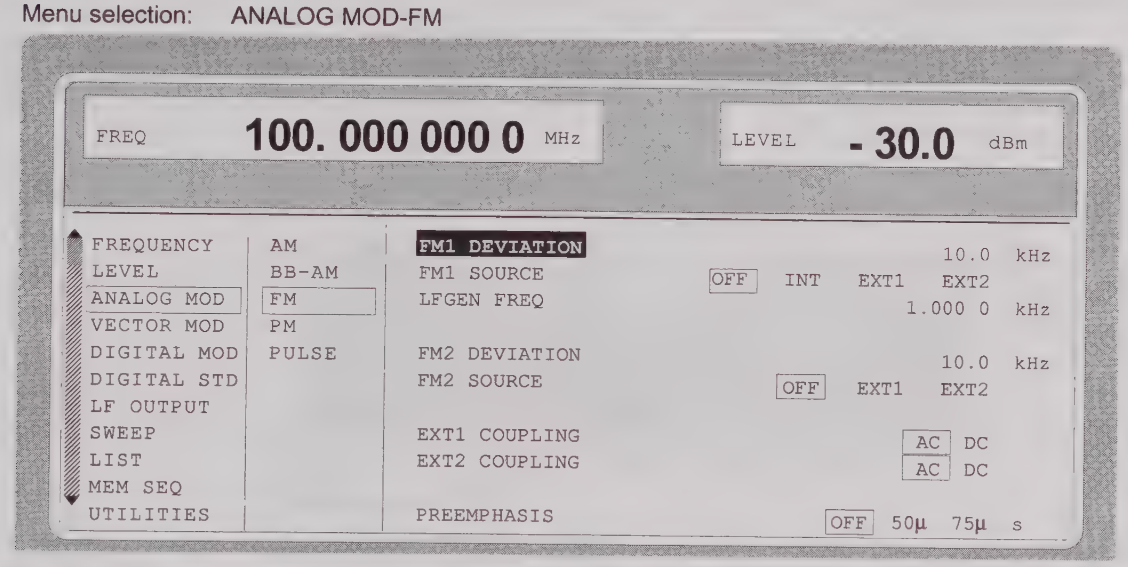
STATE	Switches BB-AM on and off. IEC/IEEE-bus command `SOUR:AM:BBAN ON`
INPUT SENSITIVITY	Display of the input sensitivity. The value cannot be changed.

2.7.3 Frequency Modulation

Menu ANALOG MOD-FM offers access to settings for frequency modulation.

Note: *The FM and PM modulations cannot be set simultaneously and deactivate one another:*

Menu selection: ANALOG MOD-FM



FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

FM1 DEVIATION 10.0 kHz
FM1 SOURCE OFF INT EXT1 EXT2
LFGEN FREQ 1.000 0 kHz
FM2 DEVIATION 10.0 kHz
FM2 SOURCE OFF EXT1 EXT2
EXT1 COUPLING AC DC
EXT2 COUPLING AC DC
PREEMPHASIS OFF 50µ 75µ s

Fig. 2-28 Menu ANALOG MOD-FM (preset setting), fitted with option SM-B5, FM/PM-modulator

FM1 DEVIATION	Input value of the deviation for FM1. IEC/IEEE-bus command `SOUR:FM1 10kHz`
FM1 SOURCE	Switching on and off FM1 and selection of the modulation source. IEC/IEEE-bus command `SOUR:FM1:SOUR INT; STAT ON`
LFGEN FREQ	Input value of the frequency of the LF generator. EC-bus short command `SOUR:FM1:INT:FREQ 1kHz`
FM2 DEVIATION	Input value of the deviation for FM2. EC-bus short command `SOUR:FM2 10kHz`
FM2 SOURCE	Switching on and off FM2 and selection of the modulation source. EC-bus short command `SOUR:FM2:STAT OFF`
EXT1 COUPLING	Selection of the type of coupling AC or DC for the external input EXT1. IEC/IEEE-bus command `SOUR:FM1:EXT1:COUP AC`
EXT2 COUPLING	Selection of the type of coupling AC or DC for the external input EXT2. IEC/IEEE-bus command `SOUR:FM1:EXT2:COUP AC`
PREEMPHASIS	Selection of the preemphasis IEC-bus short command `SOUR:FM1:PRE 50us`

2.7.3.1 FM Deviation Limits

The maximal deviation depends on the RF frequency set (cf. Fig. 2-29). It is possible to enter a deviation that is too high for a certain RF frequency or to vary the RF frequency to a range in which the deviation can no longer be set. In this case the maximally possible deviation is set and an error message is displayed.

In the RF range 450 MHz to 750 MHz and 1200 MHz to 1500 MHz a different synthesis range is selected depending on the deviation set. If the deviation is smaller than 500 kHz or 1000 kHz, the synthesizer is in the normal mode with optimal spectral purity. If the deviation set is larger the I/Q mode is automatically selected.

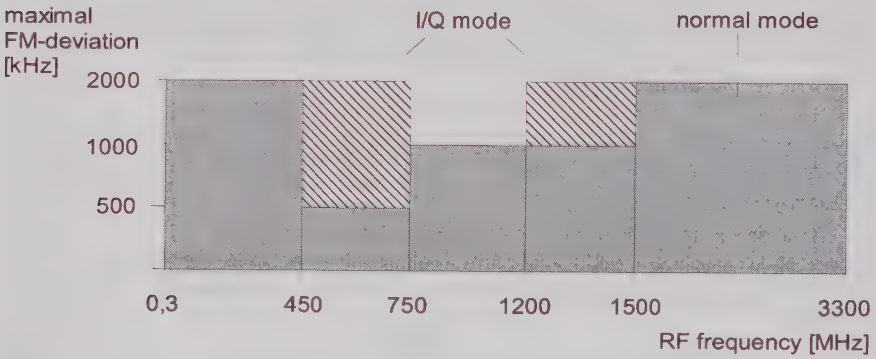


Fig. 2-29 Dependency of the FM maximal deviation on the RF frequency set

2.7.3.2 Preemphasis

Preemphasis results in a preemphasis of the modulation signal with time constants 50 µs or 75 µs. The higher frequencies of the modulation signal are preemphasized.

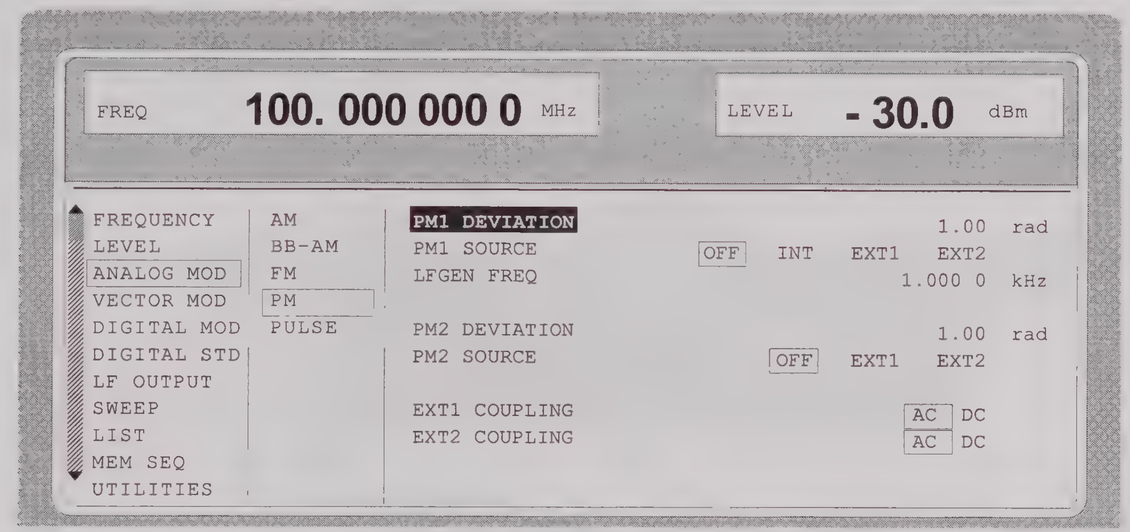
When preemphasis is switched on, only 1/4 of the maximal deviation is permissible. The highest permissible modulation frequency is 15 kHz. Exceeding the permissible modulation frequency can lead to overmodulation."

2.7.4 Phase Modulation

Menu ANALOG MOD-PM offers access to settings for phase modulation.

Note: *The PM and FM modulations cannot be set simultaneously and deactivate one another.*

Menu selection: ANALOG MOD - PM



```
FREQ   100. 000 000 0 MHz          LEVEL  - 30.0  dBm

PM1 DEVIATION                                 1.00  rad
PM1 SOURCE              OFF   INT   EXT1      EXT2
LFGEN FREQ                                 1.000 0  kHz

PM2 DEVIATION                                 1.00  rad
PM2 SOURCE                    OFF   EXT1      EXT2

EXT1 COUPLING                                 AC  DC
EXT2 COUPLING                                 AC  DC
```

Fig. 2-30 Menu ANALOG MOD - PM (preset setting), fitted with option SM-B5, FM/PM-modulator

PM1 DEVIATION	Input value of the deviation for PM1. IEC/IEEE-bus command `SOUR:PM1 1RAD`
PM1 SOURCE	Switching on and off PM1 and selection of the modulation source. IEC/IEEE-bus command `SOUR:PM1:SOUR:INT; STAT ON`
LFGEN1 FREQ	Input value of the frequency of the LF generator. IEC/IEEE-bus command `SOUR:PM1:INT:FREQ 1kHz`
PM2 DEVIATION	Input value of the deviation for PM2. IEC/IEEE-bus command `SOUR:PM2 1RAD`
PM2 SOURCE	Switching on and off PM2 and selection of the modulation source. IEC/IEEE-bus command `SOUR:PM2:SOUR INT; STAT ON`
EXT1 COUPLING	Selection of the type of coupling AC or DC with external supply for PM1 (input EXT1). IEC/IEEE-bus command `SOUR:PM:EXT1:COUP AC`
EXT2 COUPLING	Selection of the type of coupling AC or DC with external supply for PM2 (input EXT2). IEC/IEEE-bus command `SOUR:PM:EXT2:COUP AC`

2.7.4.1 PM Deviation Limits

The maximal deviation depends on the RF frequency set (cf. Fig. 2-31). It is possible to enter a deviation that is too high for a certain RF frequency or to vary the RF frequency to a range in which the deviation can no longer be set. In this case the maximally possible deviation is set and an error message displayed.

In the RF range 450 to 750 MHz and 1200 MHz to 1500 MHz a different synthesis range is selected depending on the deviation set. If the deviation is smaller than 5 rad or 10 rad, the synthesizer is in the normal mode with optimal spectral purity. If the deviation set is larger, the I/Q mode is automatically selected.

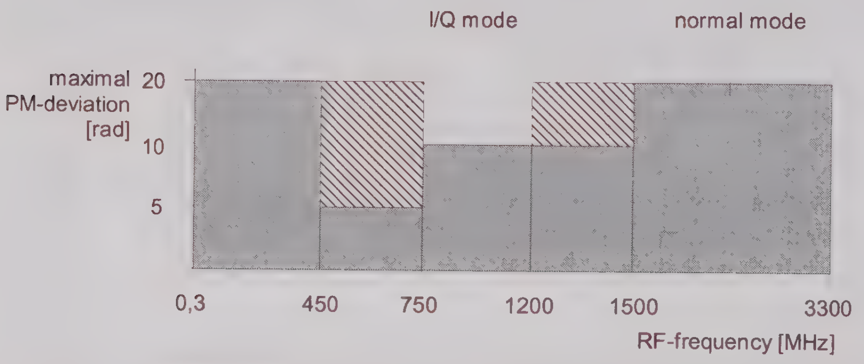


Fig. 2-31 Dependency of the PM maximal deviation on the RF frequency set

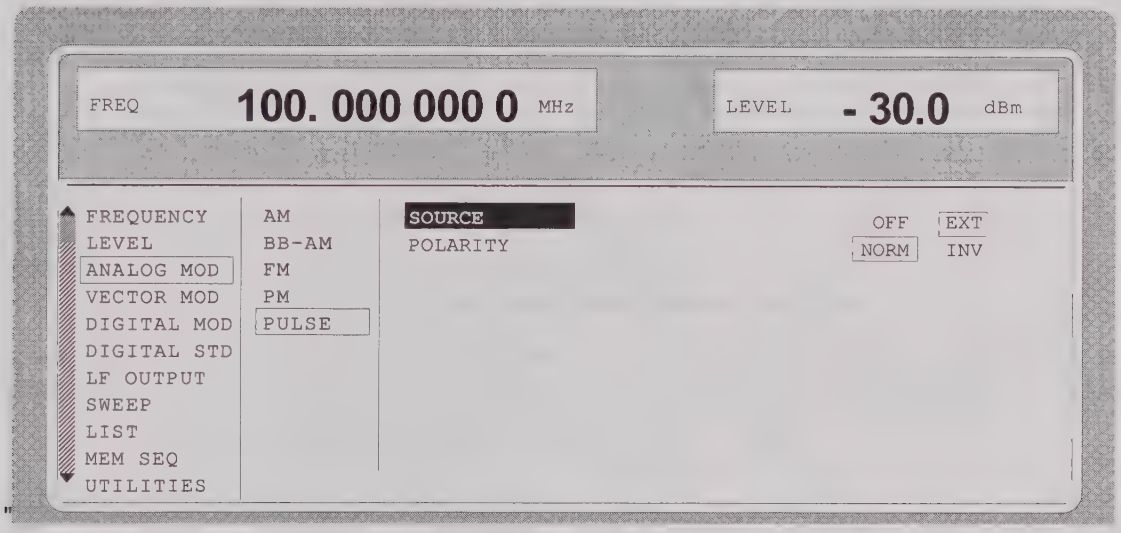
2.7.5 Pulse Modulation

The pulse modulator can be controlled by an external source at the PULSE input.

The polarity of the pulse modulation is selectable. With POLARITY = NORM, the RF level is on with HIGH level at modulation input PULSE.

Menu MODULATION-PULSE offers access to settings for pulse modulation

Menu selection: MODULATION - PULSE



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

SOURCE OFF EXT

POLARITY NORM INV

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
AM
BB-AM
FM
PM
PULSE

Fig. 2-32 Menu MODULATION-PULSE (preset setting), fitted with option SM-B3, pulse modulator, and option SM-B4, pulse generator

SOURCE	Switching on or off the external pulse modulation. IEC/IEEE-bus command `:SOUR:PULM:SOUR EXT; STAT ON`
POLARITY	Selection of the polarity of the modulation NORM The RF signal is on during high level. INV The RF signal is suppressed during high level. IEC/IEEE-bus command `:SOUR:PULM:POL NORM`

2.8 Vector Modulation

In the vector modulation mode (I/Q modulation) external modulation signals can be applied to modulation inputs I and Q for a complex modulation of the RF carrier.

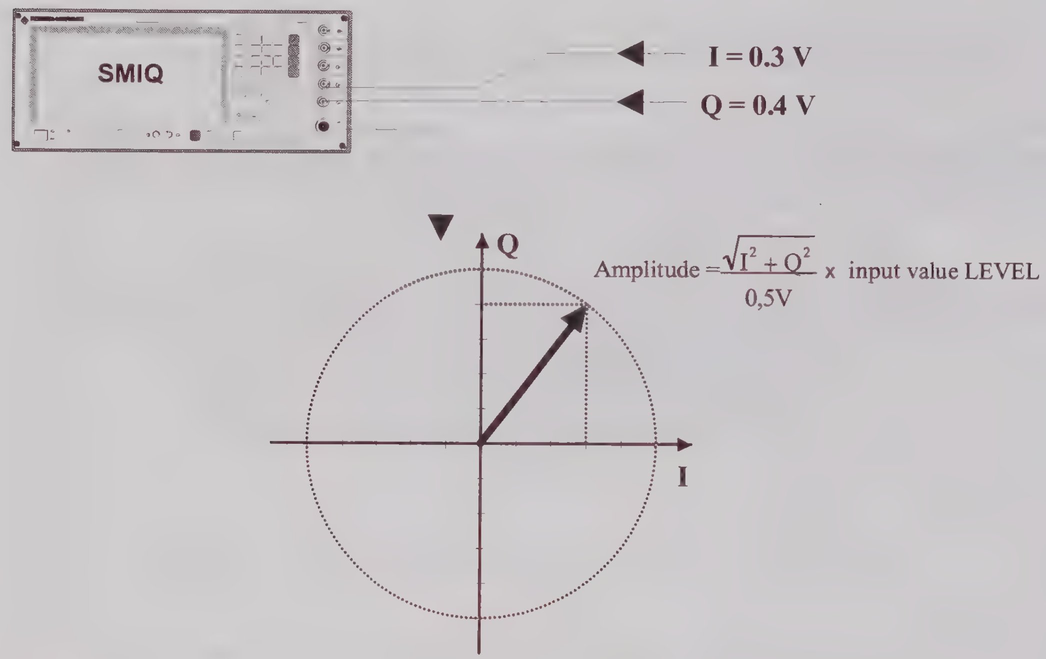

SMIQ
I = 0.3 V
Q = 0.4 V
Q
$\text{Amplitude} = \frac{\sqrt{I^2 + Q^2}}{0{,}5\text{V}} \times$ input value LEVEL
I

Fig. 2-33 Example: vector modulation

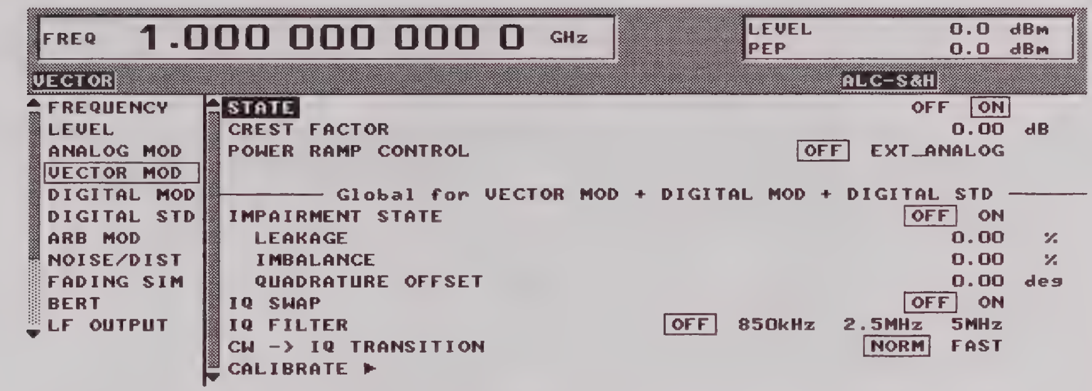
If the I/Q modulator is driven by a constant sum vector modulation of $\sqrt{I^2 + Q^2} = 0.5\text{V}$ the actual RF level corresponds to the displayed RF level. To avoid the I/Q modulator being overdriven, care should be taken that the sum vector never exceeds 0.5 V when digital modulation modes with amplitude modulation components such as QPSK are used. For full-scale input, the peak envelope power of the modulated RF signal is thus equal to the indicated LEVEL. The average power is smaller. The difference can be entered as an offset in the LEVEL menu.

Vector modulation settings are accessible in the VECTOR MOD menu, see following page.

Note:

- *Types of modulation VECTOR MOD and BB-AM cannot be set at the same time; they switch each other off.*
- *A selectable internal calibration of the I/Q modulator allows accurate and reproducible measurements to be made. The calibration routine should be called up with the CALIBRATE ▶ in the menu VECTOR MOD or UTILITIES-CALIB-VECTOR MOD after temperature changes of more than 5° C.*

Menu selection: VECTOR MOD



```
FREQ  1.000 000 000 0  GHz                    LEVEL        0.0 dBm
                                              PEP          0.0 dBm
VECTOR                                                 ALC-S&H
 FREQUENCY    STATE                                    OFF [ON]
 LEVEL        CREST FACTOR                              0.00 dB
 ANALOG MOD   POWER RAMP CONTROL               [OFF] EXT_ANALOG
[VECTOR MOD]
 DIGITAL MOD  ------- Global for VECTOR MOD + DIGITAL MOD + DIGITAL STD -------
 DIGITAL STD  IMPAIRMENT STATE                          [OFF] ON
 ARB MOD        LEAKAGE                                  0.00  %
 NOISE/DIST     IMBALANCE                                0.00  %
 FADING SIM     QUADRATURE OFFSET                        0.00  deg
 BERT         IQ SWAP                                   [OFF] ON
 LF OUTPUT    IQ FILTER              [OFF] 850kHz  2.5MHz  5MHz
              CW -> IQ TRANSITION                      [NORM] FAST
              CALIBRATE ►
```

Fig. 2-34 VECTOR MOD menu (preset settings), equipped with option SMIQB47 and IQMOD var. 8 or higher

STATE

Switches the vector modulation on and off.

IEC/IEEE-bus command `SOUR:DM:IQ:STAT ON`

POWER RAMP CONTROL

Switches the POW RAMP input for analog level control on and off. Thus an external control signal can be used for carrier envelope modulation in parallel to vector modulation.

IEC/IEEE-bus command `SOUR:DM:IQ:PRAM AEXT`

IMPAIRMENT STATE

Switches I/Q impairment on and off.

IEC/IEEE-bus command `SOUR:DM:IQ:IMP:STAT ON`

LEAKAGE

Value entered for residual carrier .

IEC/IEEE-bus command `SOUR:DM:LEAK 10PCT`

IMBALANCE

Value entered for imbalanced modulation of I and Q vectors.

IEC/IEEE-bus command `SOUR:DM:IQR -5PCT`

QUADRATURE OFFSET

Value entered for quadrature offset .

IEC/IEEE-bus command `SOUR:DM:QUAD:ANGL 4DEG`

IQ SWAP

Selection between normal and inverted I/Q modulation. Interchanging the I and Q signals inverts the modulation sidebands.

OFF	Normal I/Q modulation.
ON	I and Q signals interchanged.

IEC/IEEE-bus command `SOUR:DM:IQSW:STAT ON`

CW > IQ TRANSITION

Selection between normal and fast setting time during the transition to CW after vector modulation (including DIGITAL MOD, DIGITAL STD and ARB MOD). In the FAST mode, the CW-IQ mode is activated for CW and the poorer spectral characteristics of vector modulation apply. For this reason, the FAST mode should be activated only if very fast switchover between CW and VECTOR MOD is required.

IEC/IEEE-bus command `:SOUR:DM:IQ:TRAN:NORM|FAST`

IQ FILTER	Only available with option SMIQB47. Selection between no filter and a 850 kHz, a 2.5 MHz or a 5 MHz lowpass in the baseband. A filter of these types suppresses noise in the baseband above 900 kHz, 3 MHz or 6 MHz, which improves ACP (Adjacent Channel Power) for IS95 and W-CDMA. The frequency response of the filter is automatically compensated for DIGITAL MOD and DIGITAL STD in MCOD, which means that the EVM (Error Vector Magnitude) gets hardly worse. For VECTOR MOD and AMIQ with WinIQSIM as source, the frequency response is also compensated in WinIQSIM. For applications requiring a minimum EVM, the IQ filter should be set to OFF.

OFF	no filter
850 kHz	850 kHz lowpass
2.5 MHz	2.5 MHz lowpass
5 MHz	5 MHz lowpass

IEC/IEEE-bus commands

```
:SOUR:DM:IQ:FILT:STAT ON|OFF
:SOUR:DM:IQ:FILT:FREQ 2.5MHZ
```

2.8.1 I/Q Impairment

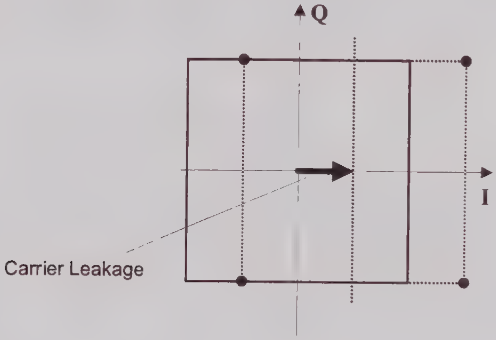
For simulating an impairment of the vector modulation, a residual carrier (LEAKAGE), imbalanced I and Q modulation (IMBALANCE) and a quadrature offset can be entered. The input values for LEAKAGE and IMBALANCE are with reference to the voltage.

Table 2-3 Parameter setting ranges

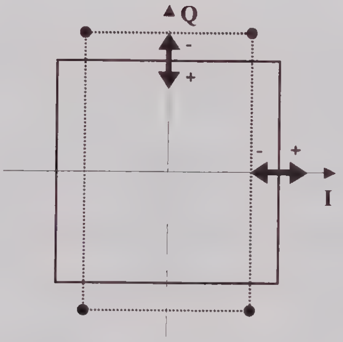
Parameter	Setting range	Resolution
LEAKAGE	0 ... 50 %	0.5 %
IMBALANCE	-12 ... +12 %	0.1 %
QUADRATURE OFFSET	-10 ... +10°	0.1°

The following figure shows the effect of I/Q impairment.

LEAKAGE:



IMBALANCE:



QUADRATURE OFFSET:

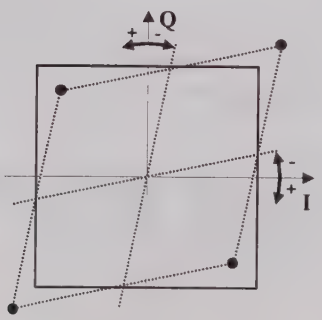


Fig. 2-35 Effect of I/Q impairment

2.9 Fading Simulation

By means of the option Fading Simulator SMIQB14, multipath fading signals with 6 independent transmission paths can be generated.

Important: **The Fading Simulator can only be operated with the complex baseband signals I and Q. Therefore, it is necessary to switch on either Vector Modulation or Digital Modulation.**

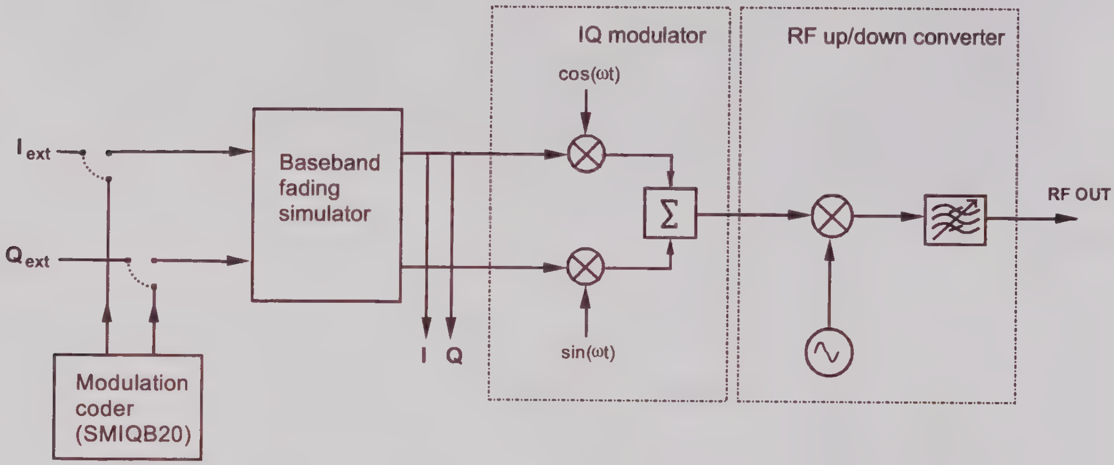


Fig. 2-36 Fading simulator in the SMIQ

The input signals for the fading simulator can either be applied to the modulation inputs I and Q or will be generated in the SMIQ by the optional Modulation Coder (SMIQB20). The output signals of the fading simulator will be passed to the IQ Modulator and then be mixed to the RF.

SMIQ can also be equipped with two Fading Simulators (SMIQB14 and SMIQB15). The second fading option provides another 6 transmission paths.
If only one fading simulator is built in, output signals I and Q are available at the I FADED and Q FADED connectors.
If two fading simulators are built in, the sum signal of the two fading options with 12 fading paths are available at the I FADED and Q FADED connectors.

Note: *If option SMIQB17 (NDSIM) is fitted, the faded, noisy and distorted I/Q signals are present at the I FADED and Q FADED connectors. If neither noise nor distortion is desired, set DISTORTION in the NOISE/DIST menu to ON and select the predefined TEST list under SELECT LIST to make sure that an output signal is present at the I FADED and Q FADED connectors.*

A selectable internal calibration of the fading simulator allows internal compensation of DC offset voltages. The calibration routine should be called up after temperature changes of more than 5 degree in menu UTILITIES-CALIB-VECTOR MOD.

2.9.1 Output Power with Fading

With a PATH LOSS setting of 0 dB, a single path of the fading simulator introduces an insertion loss between 12 dB and 18 dB for the IQ signals applied (with Insertion Loss Setting Mode = NORMAL). This insertion loss provides a headroom if several paths are superimposed on one another and also for the statistical influences to which a path is exposed. In the LOW ACP mode, the insertion loss is reduced to approx. 6 dB to 12 dB. This may lead to the rare case of clipping level being attained in the instrument. The IQ signals are in such case limited to the maximum level (clipping). The corresponding insertion loss is automatically corrected in the SMIQ. The output power is kept constant.

Displays:

LEVEL:	The rms value of the output signal is displayed.
PEP:	The (theoretically possible) peak value is displayed. This value is attained if clipping level is reached both for the I and the Q path. For signals with a high crest factor it is not likely that the PEP value is attained.

Due to the insertion loss introduced by the fading simulator, the maximum output power of the SMIQ is reduced by max. 18 dB.

2.9.2 Two-Channel Fading

For two-channel fading, an SMIQ with two fading options (SMIQB14 and SMIQB15) and a second SMIQ without fading options is required. Moreover, the SMIQ has to be recabled internally, see description in Chapter 1, Section "Option SMIQB15 – Second Fading Simulator". After reconnecting the internal cables, the I/Q baseband signals of the second channel (SMIQB15) are available at connectors I FADED and Q FADED on the rear panel of the unit. On the second unit, vector modulation has to be switched on.

Notes: *- Because of the insertion loss introduced by the fading simulator in the first SMIQ, the second SMIQ is not driven correctly. For the second SMIQ to be driven correctly, the current insertion loss of the first SMIQ must be determined and entered into the second SMIQ. This correction can be made by entering a CREST FACTOR in the vector modulation menu of the second SMIQ. The numerical value to be entered can be taken from the first SMIQ from the line SET SECOND SMIQ CREST FACTOR TO in the fading menu. After this value has been entered, the second SMIQ supplies the output level indicated in the menu.*

- For calibration of the two output levels (with Fading Simulator) it is necessary to leave the Fading Simulator in the signal path and to switch off fading. At this purpose, select setting "CALIBRATE" with Standard in the Fading Simulator menu.

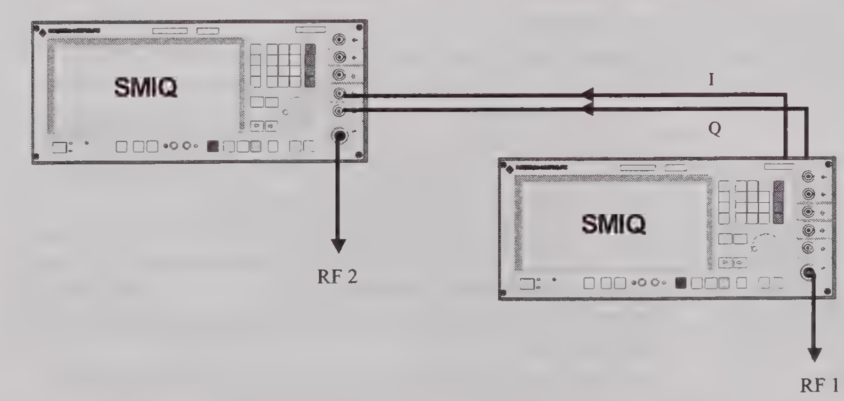


Fig. 2-37 Two-channel fading

2.9.3 Correlation between Paths

Fading processes of different paths normally do not depend on statistical processes. However, it is possible to set a correlation of paths 1 to 6 with paths 7 to 12 in pairs. To set the correlation, a synchronous signal processing is required for the two fading options which involves the following restrictions:

- The correlation is always reciprocal, ie if path 1 is correlated with path 7, path 7 is also correlated with path 1 (CORR PATH).
- The following parameters of the two paths have to correspond:

- Fading profile PROFILE

- Doppler parameter SPEED or DOPPLER FREQ

- Magnitude of correlation coefficient COEFF

- The following equation applies to the phase of the correlation coefficient and thus to the phase shift between the correlated paths:

$$\varphi_{12} = 360° - \varphi_{21}$$

- The parameters for Log Normal fading have to correspond for correlated paths.

All parameter adaptations due to the limitations mentioned before are automatically performed by SMIQ.

Note: *After setting all parameters, the two fading options have to be synchronized by RESET and then by RUN.*

2.9.4 Menu FADING SIM

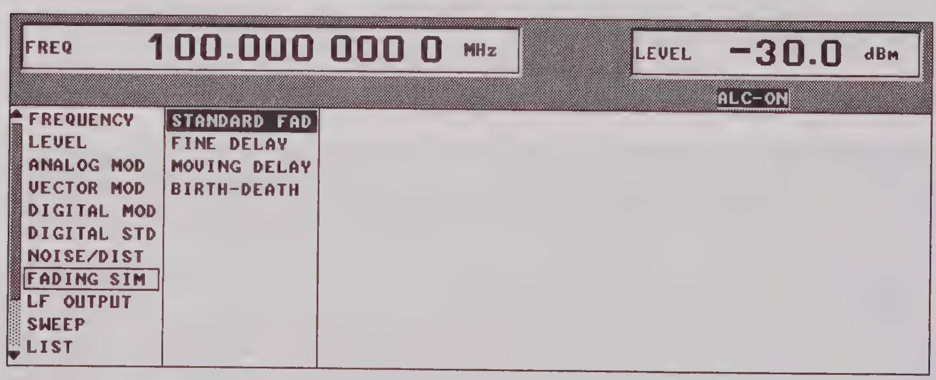


Fig. 2-38 Menu FADING SIM with submenus

FADING SIM Selection of fading simulator mode.

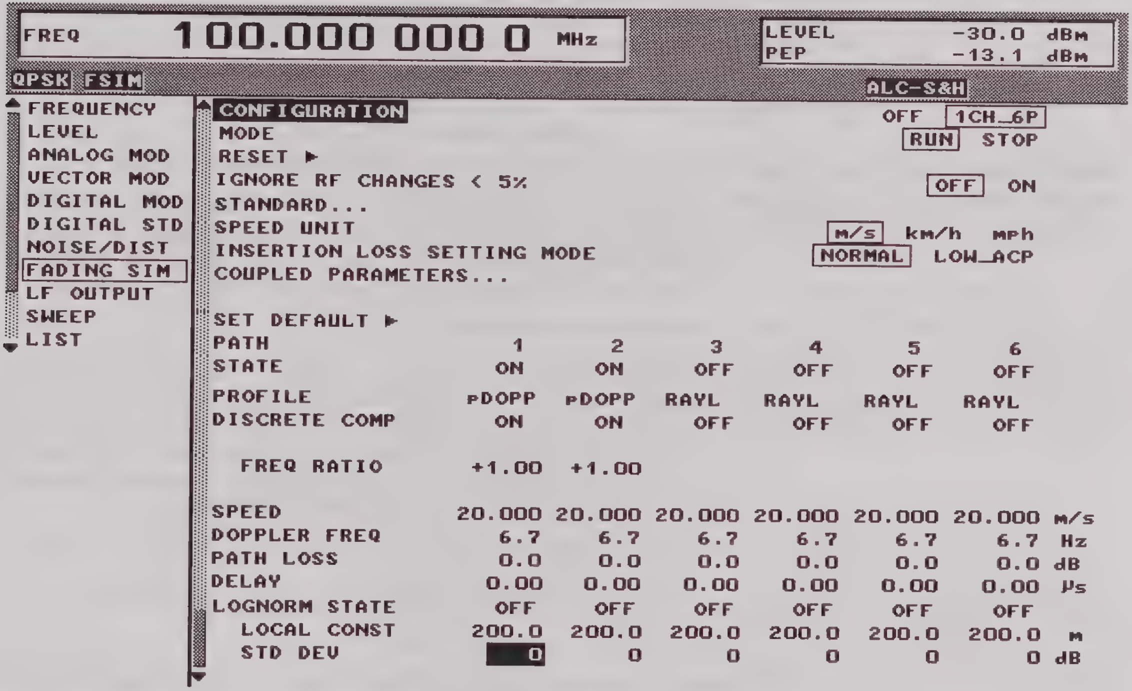
STANDARD FAD	"Normal" mode with 6 paths (with option SMIQB14) or 12 paths (with SMIQB14 and SMIQB15), time resolution 50 ns.
FINE DELAY	Mode with fine time resolution. Two paths are possible for each option (SMIQB14 / SMIQB15). The system bandwidth of the paths is limited to 4.6 MHz; this is sufficient for 3GPP with 3.84 Msymb/s. Time resolution is 1 ns.
MOVING DELAY	Mode with 2 paths, one with a fixed delay, the other with a delay varying sinusoidally. This corresponds to test case 3GPP, 25.104-320, Annex B3.
BIRTH-DEATH	Mode with 2 paths with a delay randomly varying within a time range.

Note: *The modes FINE DELAY, MOVING DELAY, BIRTH-DEATH are possible only if software option SMIQB49 is built in.*

2.9.4.1 Menu STANDARD FADING

The settings for fading simulation can be accessed via the FADING SIM menu.

Menu selection: FADING SIM



```
FREQ           100.000 000 0  MHz              LEVEL        -30.0  dBm
                                               PEP          -13.1  dBm
QPSK FSIM                                      ALC-S&H
CONFIGURATION                                             OFF   1CH_6P
MODE                                                      RUN   STOP
RESET ▶
IGNORE RF CHANGES < 5%                                    OFF   ON
STANDARD...
SPEED UNIT                                          m/s   km/h  mph
INSERTION LOSS SETTING MODE                      NORMAL   LOW_ACP
COUPLED PARAMETERS...

SET DEFAULT ▶
PATH                 1       2       3       4       5       6
STATE               ON      ON     OFF     OFF     OFF     OFF
PROFILE          PDOPP   PDOPP    RAYL    RAYL    RAYL    RAYL
DISCRETE COMP       ON      ON     OFF     OFF     OFF     OFF

  FREQ RATIO     +1.00   +1.00

SPEED           20.000  20.000  20.000  20.000  20.000  20.000 m/s
DOPPLER FREQ       6.7     6.7     6.7     6.7     6.7     6.7 Hz
PATH LOSS          0.0     0.0     0.0     0.0     0.0     0.0 dB
DELAY             0.00    0.00    0.00    0.00    0.00    0.00 µs
LOGNORM STATE      OFF     OFF     OFF     OFF     OFF     OFF
  LOCAL CONST    200.0   200.0   200.0   200.0   200.0   200.0 m
  STD DEV            0       0       0       0       0       0 dB
```

Fig. 2-39 Menu STANDARD FADING (two Fading Simulators installed)

CONFIGURATION Switching on fading simulation by selection of the number of active paths and channels.

1CH_6P	One channel with 6 paths (with option SMIQB14)
1CH_12P	One channel with 12 paths (with SMIQB14 and SMIQB15)
2CH_6+6	Two channels with 6 paths each (with SMIQB14 and SMIQB15)

IEC/IEEE-bus command `SOUR:FSIM:CONF S12P`
`SOUR:FSIM ON`

MODE Stopping (STOP) and starting (RUN) the fading process.

Following RESET the mode RUN starts the Pseudo Noise Generator at a defined starting point for generating the fading process.

IEC/IEEE-bus command `SOUR:FSIM:SEQ RUN`

RESET ▶ Resetting the Pseudo Noise Generator.
If RESET is set, the parameter MODE is automatically set to STOP. RESET also ensures a synchronization of the two fading options during 2-channel mode with the correlation switched on.

IEC/IEEE-bus command `:SOUR:FSIM:SEQ:RES`

IGNORE RF CHANGES < 5% ▶ In the ON state, frequency variations below 5% are ignored. This enables RF hopping faster than 3 ms. IEC/IEEE-bus command
`:SOUR:FSIM:IGN:RFCH ON`

STANDARD

Opens a window for selecting a standard setting of the fading paths. The parameter settings comply with the measurement specifications of mobile communication standards (e.g. GSM, CDMA, NADC).

With standards TETRA TYPICAL URBAN and TETRA HILLY TERRAIN, all 6 paths are used with these parameters instead of the stipulated 2 paths. This yields an improved residual carrier with the same output signal.

IEC/IEEE-bus command `SOUR:FSIM:STAN CDMA100`

SPEED UNIT

Selection of the speed unit.

IEC/IEEE-bus command `:SOUR:FSIM:SPE:UNIT KMPH`

INSERTION LOSS SETTING MODE

Selection of setting mode for the insertion loss of the fading simulator.

NORMAL The minimum insertion loss for a path of the fading simulator is fixed to 18 dB. The value has been chosen so that even with Log Normal-Fading switched on, the fading simulator will seldom be overdriven. This setting should be chosen for BER measurements.

IEC/IEEE-bus command `:SOUR:FSIM:ILOS:MODE NORM`

LOW_ACP The minimum insertion loss is between 6 and 12 dB. The value depends on the PATH LOSS setting of the selected paths. This mode is ideal for all STANDARD menu settings regarding signal/noise ratio and residual carrier suppression. This setting should be chosen for measurements which involve an adjacent-channel interfering signal generated by the SMIQ.

IEC/IEEE-bus command `:SOUR:FSIM:ILOS:MODE LACP`

SHOW PATHS

Switching from the display of paths 1 to 6 to the display of paths 7 to 12.

COUPLED PARAMETERS...

In this submenu, parameters SPEED, COEFF, LOCAL CONST and STD DEV can be coupled via the ON setting. If one of these parameters is modified afterwards in a particular path, the parameters of all other paths will be modified.

SPEED SETTING COUPLED

ON Parameters coupled for all paths.
OFF Parameter can be set individually.
IEC/IEEE `:SOUR:FSIM:COUP:SPE ON`

CORR COEFF SETTING COUPLED

ON Parameters coupled for all paths.
OFF Parameter can be set individually.
IEC/IEEE `:SOUR:FSIM:COUP:CORR:COEF ON`

LOCAL CONST SETTING COUPLED

ON Parameters coupled for all paths.
OFF Parameter can be set individually.
IEC/IEEE `:SOUR:FSIM:COUP:LOGN:LCON ON`

STD DEV SETTING COUPLED

ON Parameters coupled for all paths.
OFF Parameter can be set individually.
IEC/IEEE `:SOUR:FSIM:COUP:LOGN:CSTD ON`

SET SECOND SMIQ CREST FACTOR TO

Indication of crest factor which has to be set on the second SMIQ in the vector modulation menu so that the insertion loss caused by the fading simulator (in the first SMIQ) is compensated correctly on the second SMIQ.

IEC/IEEE-bus command `:SOUR:FSIM:CFAC:EXT?`

SET DEFAULT ▶

Default setting of path parameter with path 1 switched on and all other paths switched off.

IEC/IEEE-bus command `:SOUR:FSIM:DEF`

PATH The following parameter have to be set separately for each path.

STATE Switching on and off a path. If the cursor is placed onto a path in the diagram, it may be switched on and off by pressing one of the unit keys (toggle function).

IEC/IEEE-bus command `:SOUR:FSIM:PATH3:STAT ON`

PROFILE Selection of the fading profile.

pDOPP (Pure DOPpler) Simulation of a transmission path having a single direct connection from the transmitter to the moving receiver (discrete component). The Doppler Frequency shift is determined by the parameters DOPPLER FREQ and FREQ RATIO.

IEC/IEEE-bus command `:SOUR:FSIM:PATH4:PROF PDOP`

RAYL (RAYLeigh) Simulation of a radio field, where a multitude of broadly scattered partial waves hit upon a moving receiver. The receiving amplitude resulting therefrom is time-varying. The probability density function of the receiving amplitude is described by a Rayleigh distribution. The fading spectrum is a classical Doppler spectrum.

IEC/IEEE-bus command `:SOUR:FSIM:PATH1:PROF RAYL`

RICE Simulation of a radio field, where apart from a multitude of scattered partial waves, one strong direct wave (discrete component) hits upon a moving receiver. The probability density function of the receiving amplitude is described by a Rice distribution. The fading spectrum of an unmodulated signal is an overlapping of a classical Doppler spectrum with a discrete spectrum line.

IEC/IEEE-bus command `:SOUR:FSIM:PATH7:PROF RICE`

CPHAS A radio traffic area without direct and random waves is generated for this fading type (no discrete and no static component). The path is multiplied by a constant "pointer“ according to the path-specific parameter.

IEC/IEEE-bus command `:SOUR:FSIM:PATH3:PROF  CPH`

DISCRETE COMP Shows the status of the discrete component (ON or OFF).

IEC/IEEE-bus command `:SOUR:FSIM:PATH4:DCOM:STAT ON`

POWER RATIO Input value of the power ratio of the discrete component and distributed component with Ricean fading switched on. If POWER RATIO is changed the sum of both components remains constant.

IEC/IEEE-bus command `:SOUR:FSIM:PATH6:PRAT 3`

FREQ RATIO Input value of the ratio of the actual Doppler Frequency shift to the Doppler Frequency setting with Ricean fading or Pure Doppler switched on. The actual Doppler Frequency shift depends on the simulated angle of incidence of the discrete component.

IEC/IEEE-bus command `:SOUR:FSIM:PATH6:FRAT 1`

φ / deg	0	90	180
FREQ RATIO	-1	0	+1

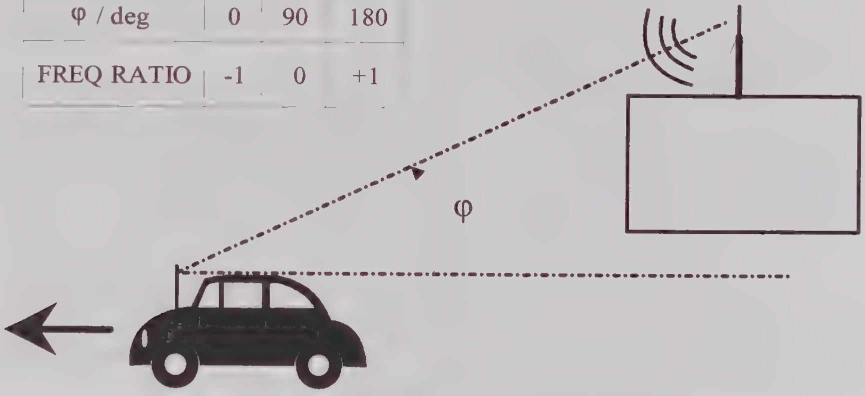


Fig. 2-40 Doppler Frequency shift with moving receiver

CONST PHASE Entry value of CONST PHASE with CPHAS fading switched on. The corresponding path is multiplied by this phase.

IEC/IEEE-bus command `:SOUR:FSIM:PATH6:CPH 20.0`

SPEED Input value of the speed v of the moving receiver.
The Doppler frequency f_D (DOPPLER FREQ) is calculated from the speed and the RF frequency f_{RF}. When changing SPEED the parameter DOPPLER FREQ will be automatically adjusted. This parameter can be coupled, see parameter COUPLED PARAMETERS...

Range: $v_{min} = \dfrac{0.03 \cdot 10^9 \, m/s^2}{f_{RF}}$ $v_{max} = \dfrac{479 \cdot 10^9 \, m/s^2}{f_{RF}}$

$v_{max} \leq 99999 km/h$

IEC/IEEE-bus command `:SOUR:FSIM:PATH1:SPE 100`

DOPPLER FREQ Input value of the amount of the maximum Doppler Frequency shift (cf. FREQ RATIO).
When changing the Doppler Frequency f_D the parameter SPEED will be automatically adjusted.

With $c = 2.998 \cdot 10^8 \, m/s$ it is $\dfrac{v}{c} = \dfrac{f_D}{f_{RF}}$

IEC/IEEE-bus command `:SOUR:FSIM:PATH2:FDOP 92.3`

PATH LOSS Input value of the attenuation of the path.

IEC/IEEE-bus command `:SOUR:FSIM:PATH3:LOSS 3`

DELAY Input value of the signal delay in the path.

IEC/IEEE-bus command `:SOUR:FSIM:PATH3:DEL 14.5`

CORR PATH

Switching on or off (NONE) a correlation with the selected path. This setting is accessible if both fading options SMIQB14 and SMIQB15 have been installed. Only a two by two correlation of paths 1 to 6 with paths 7 to 12 can be set.

IEC/IEEE-bus command `:SOUR:FSIM:PATH6:CORR:PATH 12`

COEFF

Input value of the amplitude of the complex correlation coefficient. This parameter can be coupled, see parameter COUPLED PARAMETERS...

IEC/IEEE-bus command `:SOUR:FSIM:PATH6:CORR:COEF 0.5`

PHASE

Input value of the phase of the complex correlation coefficient.

IEC/IEEE-bus command `:SOUR:FSIM:PATH6:CORR:PHAS 180`

LOGNORM STATE

Switching on or off the Log Normal-Fading.

With Log Normal-Fading set, an additional rather slow continuous changing of the receiving amplitude of a moving receiver is simulated. Log Normal-Fading has a multiplying effect on the path loss. The multiplication factor is time-varying and logarithmically normally distributed. If a Rayleigh profile is set simultaneously, this results in Suzuki-Fading.

IEC/IEEE-bus command `:SOUR:FSIM:PATH6:LOGN:STAT ON`

LOCAL CONST

Input value of the area constant L. This parameter can be coupled, see parameter COUPLED PARAMETERS...

The area constant L and the speed v of the moving receiver determine the corner frequency f_L of Log Normal-Fading:

$$f_L = v / L$$

The power density spectrum of an unmodulated carrier (CW) is an overlapping of a discrete spectrum line at f_{RF} with a frequency dependent continuous spectrum described by

$$S(f) = \text{const} \cdot e^{-0.5 \cdot \left(\frac{f - f_{RF}}{f_L}\right)^2}$$

The lower limit of the range is dependent on the RF frequency f_{RF} :

$$L_{min} = \frac{12 \cdot 10^9\, m/s}{f_{RF}}$$

IEC/IEEE-bus command `:SOUR:FSIM:PATH6:LOGN:LCON 150`

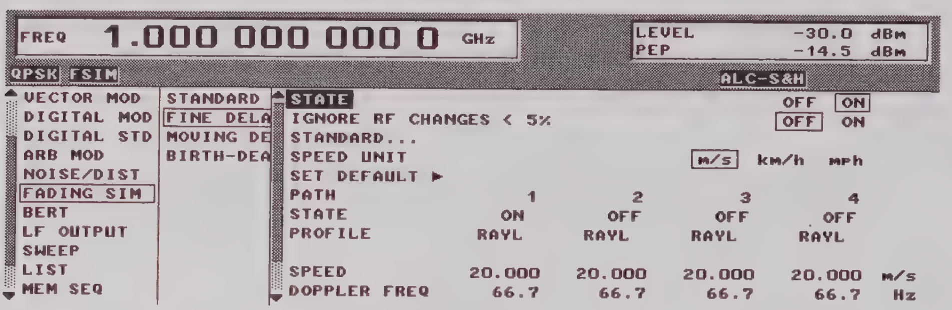
STD DEV

Input value of the standard deviation of the Log Normal-Fading. This parameter can be coupled, see parameter COUPLED PARAMETERS...

IEC/IEEE-bus command `:SOUR:FSIM:PATH6:LOGN:CSTD 6`

2.9.4.2 Menu FINE DELAY

With the FINE DELAY mode a better time delay resolution of the paths is obtained. Two paths are possible for each option (SMIQB14 / SMIQB15). The system bandwidth of these paths is limited to 4.6 MHz, which is sufficient for 3GPP with 3.84 Msymb/s. Time resolution is 1 ns.



```
FREQ   1.000 000 000 0 GHz          LEVEL   -30.0 dBm
                                    PEP     -14.5 dBm
STATE                                              OFF  [ON]
IGNORE RF CHANGES < 5%                            [OFF]  ON
STANDARD...
SPEED UNIT                                [m/s]  km/h   mph
SET DEFAULT ►
PATH                1         2         3         4
STATE              ON       OFF       OFF       OFF
PROFILE          RAYL      RAYL      RAYL      RAYL

SPEED          20.000    20.000    20.000    20.000  m/s
DOPPLER FREQ     66.7      66.7      66.7      66.7  Hz
```

Fig. 2-41 Menu FINE DELAY

STATE

Activating/deactivating fine delay simulation.

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:STAT ON | OFF`

IGNORE RF CHANGES < 5% ►

When switched on, frequency variations below 5% are ignored. Frequency hopping is thus faster than 3 ms. IEC/IEEE-bus command `:SOUR:FSIM:IGN:RFCH ON`

STANDARD

Opens a window for selecting a defined setting of fading paths.
Selection:

3GPP_BS__4.1.0_CASE1:		SPEED:	3 km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	1001 ns
		PATH LOSS	10 dB
3GPP_BS_4.1.0_CASE2:		SPEED:	3km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	1001 ns
		PATH LOSS	0 dB
	Pfad 3:	DELAY	20025 ns
		PATH LOSS	0 dB
3GPP_BS_4.1.0_CASE3:		SPEED:	120km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	285 ns
		PATH LOSS	3 dB
	Pfad 3:	DELAY	546 ns
		PATH LOSS	6 dB
	Pfad 4:	DELAY	806 ns
		PATH LOSS	9 dB

3GPP_3.3.1_CASE5:	SPEED:	50 km/h	
	Path 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Path 2:	DELAY	1001 ns
		PATH LOSS	10 dB

3GPP_BS_4.1.0_CASE4:		SPEED:	250 km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	285ns
		PATH LOSS	3 dB
	Pfad 3:	DELAY	546 ns
		PATH LOSS	6 dB
	Pfad 4:	DELAY	806 ns
		PATH LOSS	9 dB
3GPP_UE_4.1.0_CASE1:		SPEED:	3 km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	1001 ns
		PATH LOSS	10 dB
3GPP_UE_4.1.0_CASE2:		SPEED:	3km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	1001 ns
		PATH LOSS	0 dB
	Pfad 3:	DELAY	20025 ns
		PATH LOSS	0 dB
3GPP_UE_4.1.0_CASE3:		SPEED:	120km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	285 ns
		PATH LOSS	3 dB
	Pfad 3:	DELAY	546 ns
		PATH LOSS	6 dB
	Pfad 4:	DELAY	806 ns
		PATH LOSS	9 dB
3GPP_UE_4.1.0_CASE4:		SPEED:	3km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	1001 ns
		PATH LOSS	0 dB
3GPP_UE_4.1.0_CASE5:		SPEED:	50km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	1001 ns
		PATH LOSS	10 dB
3GPP_UE_4.1.0_CASE6:		SPEED:	250km/h
	Pfad 1:	DELAY	25 ns
		PATH LOSS	0 dB
	Pfad 2:	DELAY	285 ns
		PATH LOSS	3 dB
	Pfad 3:	DELAY	546 ns
		PATH LOSS	6 dB
	Pfad 4:	DELAY	806 ns
		PATH LOSS	9 dB

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:STAN G3C1`

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:STAN G3UECn ( n=1...6)`

Notes:

- The path delays correspond to those in 3GPP, TS 25.101 V4.1.0 (2001-06) und TS 25.141 V4.1.0 (2001-06). However, they include a basic delay of 25 ns of the Fading Simulator.
- All test cases requiring more than 2 paths are only possible with option SMIQB15.

SPEED UNIT

Selection of the unit required for the speed of parameter SPEED.

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:SPE:UNIT KMPH`

SET DEFAULT ▶

Sets the default setting of the path parameters with path 1 switched on and all other paths switched off.

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:DEF`

PATH

Indicates the paths for subsequent parameters. These parameters can be set individually for each path.

STATE

Switch-on/off of a path. If the cursor is placed onto a path, this path may be switched on or off by pressing one of the unit keys (toggle function).

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:PATH2:STAT ON | OFF`

PROFILE

Selection of a fading profile. See explanations under Standard Fading. Only the following setting is possible:

pDOPP

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:PATH2:PROF PDOP`

RAYL (This setting is provided in the 3GPP test cases)

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:PATH2:PROF RAYL`

FREQ RATIO

Entry value of the ratio of the actual Doppler frequency shift to the set Doppler frequency with FDOP fading switched on. (See explanation under Standard Fading).

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:PATH2:FRAT 1`

SPEED

Entry value of the speed v of the moving receiver (see explanation under Standard Fading).

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:PATH2:SPE 100`

DOPPLER FREQ

Entry value of the magnitude of the maximum Doppler frequency shift (see explanation under Standard Fading).

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:PATH2:FDOP 92.3`

PATH LOSS

Entry value of attenuation in path.
Value range: 0.0 to 50.0 dB.

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:PATH2:LOSS 3`

DELAY

Entry value of signal delay in path.
Value range: 25 ns to 1637 µs.

IEC/IEEE-bus command `:SOUR:FSIM:FDEL:PATH2:DEL 14.5E-6`

2.9.4.3 Menu MOVING DELAY

In the MOVING DELAY mode, the Fading Simulator simulates the dynamic propagation conditions according to test case 3GPP, 25.104-320, Annex B3.

2 paths are simulated; the delay of path 1 remains unchanged, the delay of path 2 slowly moves to and fro sinusoidally. The two paths have no fading profile (non-fading), have the same level, the same phase and have no Doppler shift.

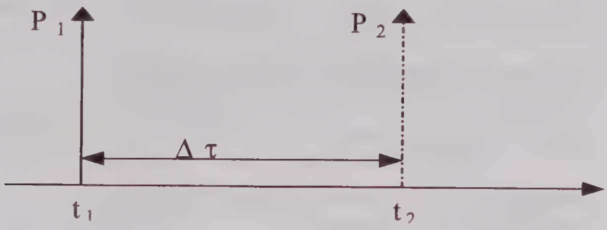


Fig. 2-42 Two paths with menu MOVING DELAY

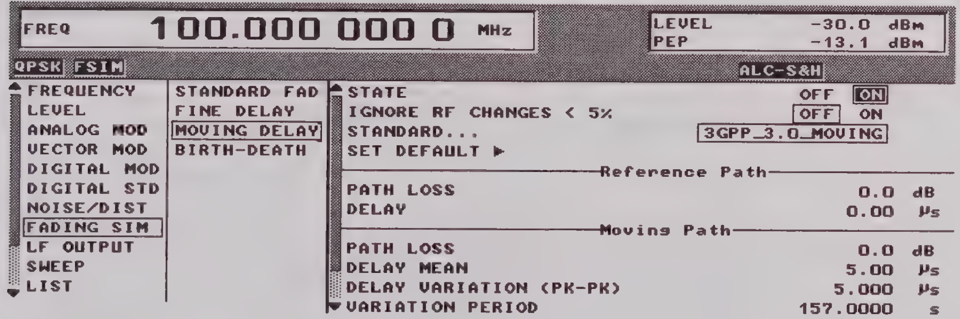
The delay of the moving path is obtained with the following equation:

$$\Delta\tau = \left(DELAY\_MEAN + \frac{DELAY\_VARIATION}{2}\left(1+\sin\left(\frac{2\pi\cdot t}{VARIATION\_PERIOD}\right)\right)\right)$$

The following settings are required to obtain the values suggested in Annex B3:

DELAY (path 1)	1µs
DELAY MEAN	= DELAY(path 1) + DELAY VARIATION / 2 = 3.5µs
DELAY VARIATION	5 µs
VARIATION PERIOD	157 s

For further tests, DELAY MEAN and the variation parameters can be modified. The two paths can be levelled differently.



```
FREQ    100.000 000 0 MHz          LEVEL   -30.0 dBm
                                   PEP     -13.1 dBm
QPSK FSIM                                  ALC-S&H
FREQUENCY      STANDARD FAD  STATE                        OFF  ON
LEVEL          FINE DELAY    IGNORE RF CHANGES < 5%       OFF  ON
ANALOG MOD     MOVING DELAY  STANDARD...         3GPP_3.0_MOVING
VECTOR MOD     BIRTH-DEATH   SET DEFAULT ▶
DIGITAL MOD                  ------------Reference Path------------
DIGITAL STD                  PATH LOSS                    0.0  dB
NOISE/DIST                   DELAY                       0.00  µs
FADING SIM                   -------------Movins Path-------------
LF OUTPUT                    PATH LOSS                    0.0  dB
SWEEP                        DELAY MEAN                  5.00  µs
LIST                         DELAY VARIATION (PK-PK)    5.000  µs
                             VARIATION PERIOD        157.0000   s
```

Fig. 2-43 Menu MOVING DELAY

STATE Activating/deactivating the moving delay simulation.
IEC/IEEE-bus command `:SOUR:FSIM:MDEL:STAT ON | OFF`

IGNORE RF CHANGES < 5% ▶ When switched on, frequency variations below 5% are ignored. RF hopping is thus faster than 3 ms. IEC/IEEE-bus command `:SOUR:FSIM:IGN:RFCH ON`

STANDARD Is now the same as SET DEFAULT.

SET DEFAULT ▶	Sets the default setting of the path parameters. IEC/IEEE-bus command `:SOUR:FSIM:MDEL:DEF`
PATH	Indicates the paths for subsequent parameters. These parameters can be set individually for each path.
PATH LOSS	Entry value of attenuation in path for the reference. Value range: 0.0 to 50.0 dB. IEC/IEEE-bus command `:SOUR:FSIM:MDEL:REF:LOSS 3`
DELAY	Entry value of signal delay in the reference path. Value range: 0.0 to 1638 µs. IEC/IEEE-bus command `:SOUR:FSIM:MDEL:REF:DEL 14.5E-6`
PATH LOSS	Entry value of attenuation in path for moving. Value range: 0.0 to 50.0 dB. IEC/IEEE-bus command `:SOUR:FSIM:MDEL:MOV:LOSS 3`
DELAY MEAN	Mean value of the moving path delay. See explanation above. Value range: 0.25 to 1637.8 µs. IEC/IEEE-bus command `:SOUR:FSIM:MDEL:MOV:DEL:MEAN 12.5E-6`
DELAY VARIATION (PK-PK)	Range for delay variation for the moving path. The peak-peak value is set. See explanations above. Value range: 0.3 to 100 µs. IEC/IEEE-bus command `:SOUR:FSIM:MDEL:MOV:DEL:VAR 2.1E-6`
VARIATION PERIOD	Speed of delay variation. After a VARIATION PERIOD has elapsed a complete cycle is swept. Only for the moving path. Value range: 10 to 500 s IEC/IEEE-bus command `:SOUR:FSIM:MDEL:MOV:VPER 13.4`

2.9.4.4 Menu BIRTH-DEATH

In the BIRTH-DEATH mode the Fading Simulator simulates the dynamic propagation conditions according to test case 3GPP, 25.104-320, Annex B4.

To do this, 2 paths are simulated which alternately appear (BIRTH) or disappear (DEATH) at random time positions. The time positions lie within a grid of [-5, -4, -3, -2, -1, 0, 1, 2, 3, 4, 5] µs. After a presettable time (HOPPING DWELL) a path disappears from a grid position and re-appears at another randomly selected grid position. During this hop, the second path remains stable at its grid position. After a further HOPPING DWELL has elapsed, the second path changes its position and the first path remains at its position, etc. The two paths never appear at the same time position (see Fig. 2-44).

According to Annex B4, each path has the same attenuation and phase and no Doppler shift. However, this can be set in the menu BIRTH-DEATH for further tests. The dwell period of 191 ms (in line with 3GPP) can be varied between 100 ms and 5 s.

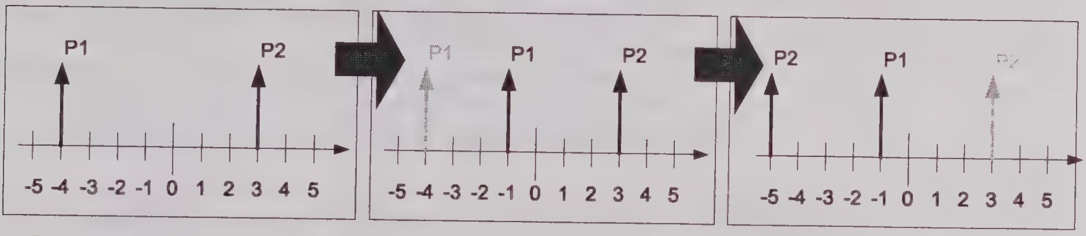
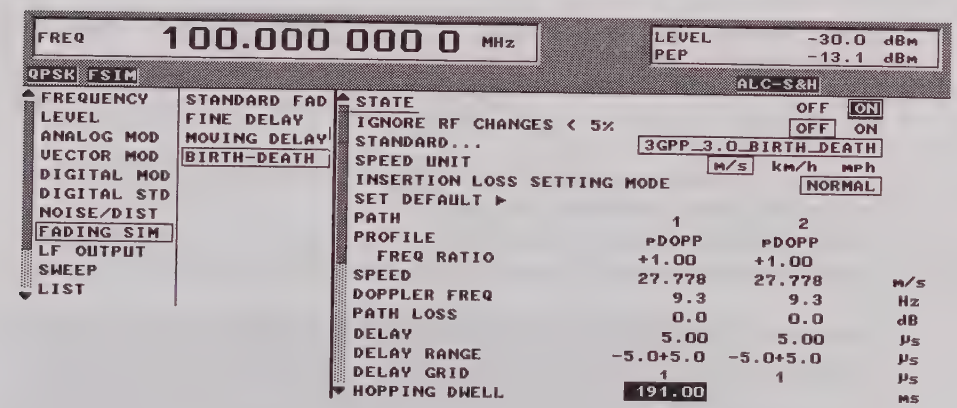

P1
P2
-5 -4 -3 -2 -1 0 1 2 3 4 5
P1
P1
P2
-5 -4 -3 -2 -1 0 1 2 3 4 5
P2
P1
P2
-5 -4 -3 -2 -1 0 1 2 3 4 5

Fig. 2-44 Example of hop sequence with BIRTH-DEATH fading


FREQ 100.000 000 0 MHz
LEVEL -30.0 dBm
PEP -13.1 dBm
QPSK FSIM
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
NOISE/DIST
FADING SIM
LF OUTPUT
SWEEP
LIST
STANDARD FAD
FINE DELAY
MOVING DELAY
BIRTH-DEATH

```
STATE                                              OFF  ON
IGNORE RF CHANGES < 5%                             OFF  ON
STANDARD...                           3GPP_3.0_BIRTH_DEATH
SPEED UNIT                                    m/s  km/h  mph
INSERTION LOSS SETTING MODE                          NORMAL
SET DEFAULT ▶
PATH                                1           2
PROFILE                           pDOPP       pDOPP
  FREQ RATIO                      +1.00       +1.00
SPEED                            27.778      27.778       m/s
DOPPLER FREQ                        9.3         9.3       Hz
PATH LOSS                           0.0         0.0       dB
DELAY                              5.00        5.00       µs
DELAY RANGE                   -5.0+5.0    -5.0+5.0       µs
DELAY GRID                            1           1       µs
HOPPING DWELL                    191.00                   ms
```

Fig. 2-45 Menu BIRTH-DEATH

STATE	Activation/deactivation of the BIRTH-DEATH simulation. IEC/IEEE-bus command `:SOUR:FSIM:BIRT:STAT ON	OFF`
IGNORE RF CHANGES < 5% ▶	When switched on, frequency variations below 5% are ignored. RF hopping is thus faster than 3 ms. IEC/IEEE-bus command `:SOUR:FSIM:IGN:RFCH ON`
STANDARD	Is now the same as SET DEFAULT.
SPEED UNIT	Selection of the unit required for the speed of parameter SPEED. IEC/IEEE-bus command `:SOUR:FSIM:BIRT:SPE:UNIT KMPH`

INSERTION LOSS SETTING MODE

Selection of a setting mode for the insertion loss of the fading simulator. See explanation under STANDARD FADING.

IEC/IEEE-bus command `:SOUR:FSIM:BIRT:ILOS:MODE NORM`
`:SOUR:FSIM:BIRT:ILOS:MODE LACP`

SET DEFAULT ▶

Sets the default setting of the path parameters.

IEC/IEEE-bus command `:SOUR:FSIM:BIRT:DEF`

PATH

Indicates the paths for subsequent parameters. These parameters can be set individually for each path.

PROFILE

Selection of a fading profile. Only pDOPP is available here; when FREQ RATIO = 0 is set a "non-fading" path is obtained.

pDOPP (pure DOPPler) Simulation of a transmission path having a single direct connection from the transmitter to the moving receiver (discrete component).
The Doppler frequency shift is determined by the parameters DOPPLER FREQ and FREQ RATIO.

IEC/IEEE-bus command `:SOUR:FSIM:BIRT:PATH1:PROF PDOP`

FREQ RATIO

Entry value of ratio of actual Doppler frequency shift. See explanation under STANDARD FADING. When FREQ RATIO = 0 is set, a "non-fading" path is obtained.
IEC/IEEE-bus command `:SOUR:FSIM:BIRT:PATH1:FRAT 1`

SPEED

Entry value of the speed v of the moving receiver. See explanations under STANDARD FADING.
All entries in path 1 are copied for path 2.

IEC/IEEE-bus command `:SOUR:FSIM:BIRT:PATH1:SPE 100`

DOPPLER FREQ

Entry value of the magnitude of the maximum Doppler frequency shift. See explanation under STANDARD FADING.
All entries in path 1 are copied for path 2.

IEC/IEEE-bus command `:SOUR:FSIM:BIRT:PATH1:FDOP 92.3`

PATH LOSS

Entry value of attenuation in path. Value range: 0.0 to 50.0 dB.

IEC/IEEE-bus command `:SOUR:FSIM:BIRT:PATH2:LOSS 3`

DELAY

Entry value of signal delay in path 1. All entries in path 1 are copied for path 2.
Value range: 5.0 to 1000.0 µs.

IEC/IEEE-bus command `:SOUR:FSIM:BIRT:PATH1:DEL 1000 E-3`

DELAY RANGE

The delay of the two paths is within this range.
Non-editable parameter.
For the two channels: -5.0 to +5.0 µs.

DELAY GRID

Time grid of the different carriers appearing at random. The carriers have delays which lie within the DELAY RANGE and coincide with n * DELAY GRID (with n –5 to +5).
Non-editable parameter. Value = 1 µs for the two channels.

HOPPING DWELL

Dwell period until the next BIRTH-DEATH action. After this dwell period the next path changes its delay at random.
Value range: 100.0 to 5000.0 ms.

IEC/IEEE-bus command `:SOUR:FSIM:BIRT:PATH1:HOPP:DWEL 1.2`

2.9.5 Test procedure

The following settings can be used to demonstrate how Option SMIQB49 functions:

Settings on SMIQ

General

Frequency	30 MHz
Level	-10 dB

Digital Modulation (DIGITAL MOD)

STATE	ON
SOURCE	Data List
LIST	1000 0000 0000 0000 0000 0000 0000 0000 (32 bits)
MODULATION	ASK
SYMBOL RATE	1 000 000 sym/s

Fading Simulation

MOVING DELAY	ON
Reference Path	
Path Loss	0 dB
Delay	0 µs
Moving Path	
Path Loss	6.0 dB
Delay Mean	5.0 µs
Delay Variation	5.0 µs
Variation Period	10.0 s

Settings on the oscilloscope (BW > 100 MHz)

Vertical	50 mV/div, at 50 Ω
Horizontal	2 µs /div
Trigger level	100 mV

The test result shows a high pulse (about 150 mV), and next to it a pulse which is half as high and moves from one side to the other accordingly to the setting *Delay Variation* or *Variation Period*.

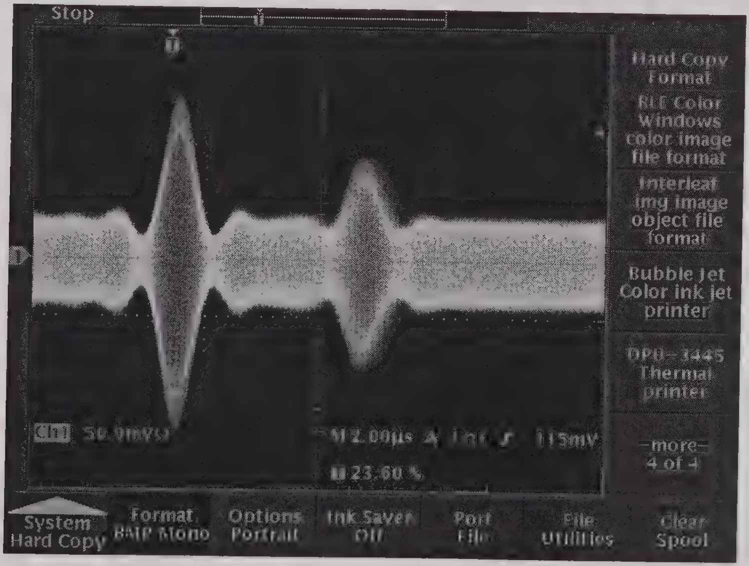

Stop
Hard Copy Format
RLE Color Windows color image file format
Interleaf img image object file format
Bubble Jet Color ink jet printer
DPU-3445 Thermal printer
-more- 4 of 4
Ch1 50.0mVΩ
M 2.00µs A Int 115mV
23.60 %
System Hard Copy
Format BMP Mono
Options Portrait
Ink Saver Off
Port File
File Utilities
Clear Spool

Fig. 2-46 Pulse on Oscilloscope

2.10 Digital Modulation

With option Modulation Coder (MCOD) SMIQB20 provided, SMIQ can generate digitally modulated output signals. Available modulation methods are ASK (amplitude shift keying), FSK (frequency shift keying), PSK (phase shift keying) as well as QAM (quadrature amplitude modulation). Baseband filtering and symbol rate can be freely set in a wide range.
The modulation coder generates the analog IQ signals for the I/Q modulator of SMIQ from the digital input signals.

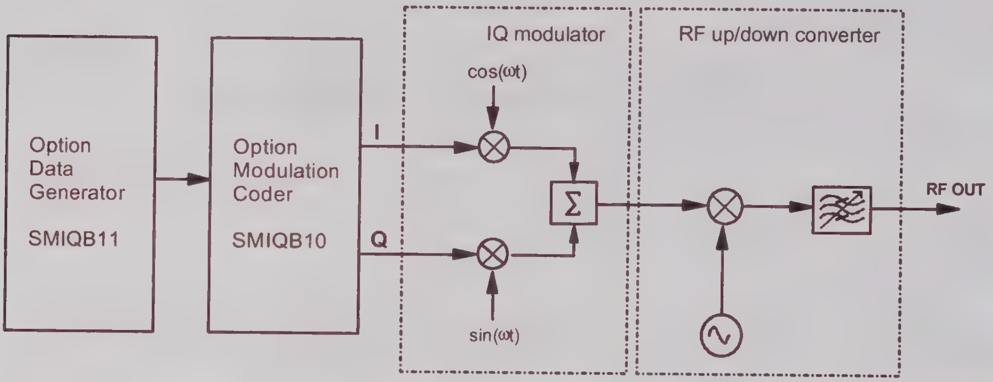

Option
Data
Generator
SMIQB11
Option
Modulation
Coder
SMIQB10
I
Q
IQ modulator
cos(ωt)
sin(ωt)
Σ
RF up/down converter
RF OUT

Fig. 2-47 Modulation coder in SMIQ

The modulation coder works with digital input signals such as

- clock signals (symbol clock, bit clock),
- modulation data or modulation symbols,
- control signals for envelope control and trigger signals.

The digital input signals may originate from the following sources:

- The modulation coder can generate clock signals such as PRBS data signals and simple data patterns.
- External clock signals, modulation data and signals for envelope control can be fed in via connectors at the front or rear panel of SMIQ.
- With option Data Generator SMIQB11 provided, an additional data source is available. This option has a 16-Mbit memory for modulation data and control signals. The memory can be extended to 48 Mbit or 80 Mbit by installing one or two SMIQB12 options (Memory Extension).

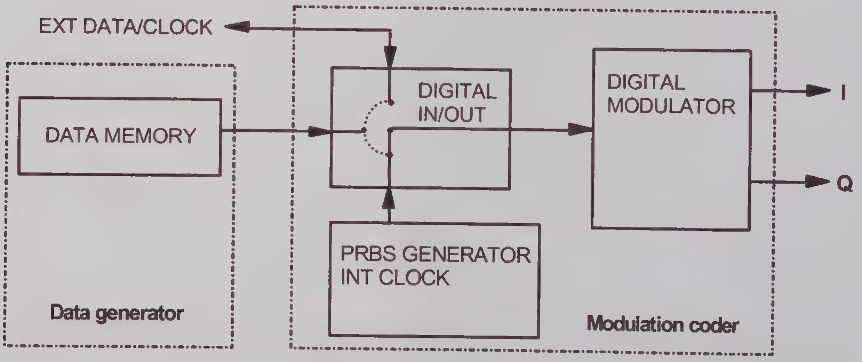

EXT DATA/CLOCK
DATA MEMORY
Data generator
DIGITAL
IN/OUT
PRBS GENERATOR
INT CLOCK
DIGITAL
MODULATOR
I
Q
Modulation coder

Fig. 2-48 Digital input signals of modulation coder

2.10.1 Digital Modulation Methods and Coding

The input sequence of modulation symbols d_n can be subject to different types of coding. I and Q values are assigned to the coded modulation symbols dc_n in the functional block MAPPING.

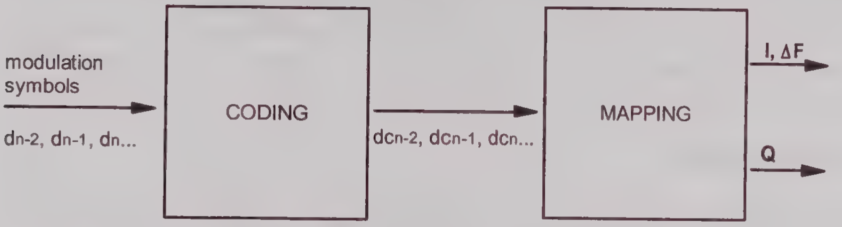

modulation
symbols
dn-2, dn-1, dn...
CODING
dcn-2, dcn-1, dcn...
MAPPING
I, ΔF
Q

Fig. 2-49 Functional blocks Coding and Mapping

2.10.1.1 PSK and QAM Modulation

PSK and QAM modulations can be explained on the basis of a rule of assignment (mapping) according to which each modulation symbol is represented by I and Q values as shown in the following IQ constellation diagrams. The constellation diagrams apply if **no** coding is switched on.

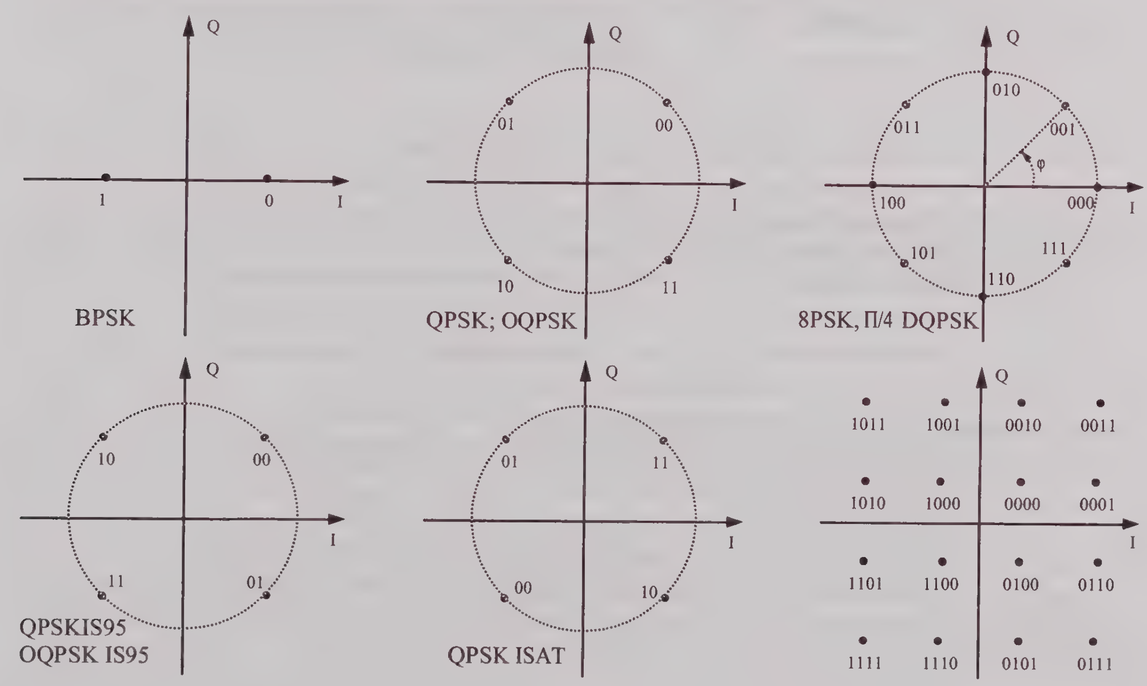

Q
1
0
I
BPSK
Q
01
00
10
11
I
QPSK; OQPSK
Q
010
011
001
φ
100
000
I
101
111
110
8PSK, Π/4 DQPSK
Q
10
00
11
01
I
QPSKIS95
OQPSK IS95
Q
01
11
00
10
I
QPSK ISAT
Q
1011
1001
0010
0011
1010
1000
0000
0001
1101
1100
0100
0110
1111
1110
0101
0111
I

Fig. 2-50 Constellation diagrams of BPSK, QPSK, 8PSK and 16QAM

For offset QPSK (OQPSK), the Q signal is delayed by half the symbol period with reference to the I signal.
QAM modulation methods 16QAM, 32QAM, 64QAM and 256QAM were implemented according to ETSI standard ETS 300429 for Digital Video Broadcasting (DVB).
All PSK and QAM modulation methods can be combined with COS and SQR_COS baseband filters as well as with IS-95 filters. A combination with GAUSS and BESSEL filters is not possible.

2.10.1.2 Modulation π/4DQPSK

With differential coding switched on at the same time, a constellation diagram is obtained for π/4DQPSK which is similar to that obtained for 8PSK. Phase shifts are however assigned to the individual modulation symbols. The following tables show the assignment of modulation symbols to phase shifts of IQ vectors at the selected coding.

Table 2-4 Phase shifts for π/4DQPSK without coding

Modulation symbol d_n (binary indication: MSB, LSB)	00	01	10	11
Phase shift φ	+ 45°	+135°	-135°	-45°

Table 2-5 Phase shifts for π/4DQPSK with coding NADC, PDC, PHS, TETRA or APCO25

Modulation symbol d_n (binary display: MSB, LSB)	00	01	10	11
Phase shift φ	+ 45°	+135°	-45°	-135°

Table 2-6 Phase shifts for π/4DQPSK with coding TFTS

Modulation symbol d_n (binary display: MSB, LSB)	00	01	10	11
Phase shift φ	- 135°	+135°	-45°	+45°

2.10.1.3 FSK Modulation

For FSK modulation, frequency shifts are assigned to the modulation symbols. The modulation index h of this digital frequency modulation is determined by

$$h = 2 \cdot \Delta f \; / \; f_{Symb}$$

The symbol rate f_{SYMB} can be freely set to a maximum of 2.5 Msymb/s for all FSK modulations. With GMSK selected, the frequency deviation Δf (FSK deviation) cannot be set since the modulation index is fixed to a value of h = 0.5. The following table shows the assignment of modulation symbols and frequency deviations for the different FSK methods.

Table 2-7 Frequency deviations for FSK methods

Mod. symbol	2FSK, GFSK	MSK, GMSK	4FSK	APCO
0	$-\Delta f$	$-\Delta f$	$-\Delta f$	$+\Delta f/3$
1	$+\Delta f$	$+\Delta f$	$-\Delta f/3$	$+\Delta f$
10	-	-	$+\Delta f/3$	$-\Delta f/3$
11	-	-	$+\Delta f$	$-\Delta f$

All FSK modulation methods can be combined with COS, SQR_COS, GAUSS and BESSEL baseband filters. A combination with IS-95 filters is not permissible.

2.10.1.4 Coding

Modulation symbols are coded directly before an assignment of I and Q values or frequency shifts. Coding is thus directly related with modulation methods which is the reason why codings are not freely combinable with modulation methods. The following table shows which combinations are possible.

Table 2-8 Possible combination of modulation method and coding

Coding	Selection CODING	Combinable with MOD TYPE	Example of use
Differential coding	DIFF	all except for 256QAM	
Gray + differential coding	GRAY+DIFF	all except for 256QAM	D8PSK for VDR
GSM differential coding	GSM	FSK, GFSK, GMSK	Mobile radio standard GSM
π/4DQPSK differential coding	NADC; PDC;..	π/4DQPSK	Mobile radio standards NADC, PDC, PHS, APCO25, TFTS, TETRA
Phase differential coding	INMARSAT	QPSK ISAT	Satellite system INMARSAT-M
Phase differential coding	PHASE DIFF	16 , 32 , 64 , 256 QAM	DVB in line with ETS 300429
VDL coding	VDL	8PSK	VHF Digital Link

The effect of differential coding on π/4DQPSK has been described in section 'Modulation π/4DQPSK'. Common coding types are listed in the following table.

Table 2-9 Coding algorithms

CODING	**Coding algorithm**	**Applicable for K bit/symbol**
NONE	$dc_n = d_n$	K = 1 to 8
DIFF	$dc_n = (d_n + dc_{n-1})$ modulo 2k	K = 1 to 7
GRAY+DIFF	Gray coding with additional differential coding	K = 1 to 7
GSM	dc_n = not (d_n exor d_{n-1}	K = 1
VDL	VDL standard	K = 3

Example 1: Differential coding for QPSK modulation with K = 2 bit/symbol
Decimal display; value range for modulation symbols
$d_n \in \{0;1;2;3\}$

Recursive coding is defined as follows: $dc_n = (d_n + d_{cn}\text{-}1)$ modulo 4.

Depending on the state of a preceding modulation symbol dc_{n-1} the coded modulation symbol dc_n is obtained for example from a modulation symbol $d_n = 2$ as follows:

$d_n = 2$	**dc_{n-1}**	**dc_n**
	0	2
	1	3
	2	0
	3	1

By means of differential coding, the assignment between modulation symbols and phase differences shown in the following table is generated:

Modulation symbol d_n (binary, MSB, LSB)	00	01	10	11
Phase difference $\Delta\varphi$	0°	90°	180°	270°

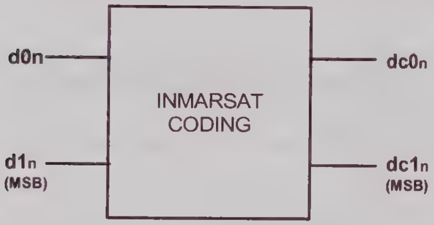
Example 2: Gray and differential coding for 8PSK modulation
First, a gray coding is performed according to the gray code. Afterwards, a differential coding is performed according to the recursive coding algorithm quoted above. By means of the mapping rule shown in the figure in Section 'PSK and QAM Modulation' above, IQ values are assigned to the re-coded modulation symbols. In summary, the assignment between modulation symbols and phase differences shown in the following table is generated:

Modulation symbol d_n (binary, MSB, LSB)	000	001	010	011	100	101	110	111
Phase difference $\Delta\varphi$	0°	45°	135°	90°	270°	315°	225°	180°

Differential coding according to VDL is shown in the following table:

Modulation symbol d_n (binary, MSB, LSB)	000	001	010	011	100	101	110	111
Phase difference $\Delta\varphi$	0°	45°	135°	90°	315°	270°	180°	225°

Phase differential coding INMARSAT and PHASE DIFF correspond to system standards Inmarsat-M and DVB according to ETS 300 429. The INMARSAT coding can generally be used for modulation types with 2 bit/symbol, such as QPSK. It uses the following algorithm.



$$\begin{aligned}
dc1_n = {} & [\text{NOT } (d1_n \text{ EXOR } d0_n) \text{ AND } (d1_n \text{ EXOR } dc1_{n-1})] \text{ OR} \\
& [(d1_n \text{ EXOR } d0_n) \text{ AND } (d1_n \text{ EXOR } dc0_{n-1})]
\end{aligned}$$

$$\begin{aligned}
dc0_n = {} & [\text{NOT } (d1_n \text{ EXOR } d0_n) \text{ AND } (d0_n \text{ EXOR } dc0_{n-1})] \text{ OR} \\
& [(d1_n \text{ EXOR } d0_{n-1}) \text{ AND } (d0_n \text{ EXOR } dc1_{n-1})]
\end{aligned}$$

2.10.1.5 Setting Conflicts

As already mentioned in the previous sections, the combination of the above modulation methods with modulation parameters such as symbol rate, filtering and coding is limited. This limitation inevitably causes setting conflicts if a parameter is changed which would lead to an impermissible combination.

Table 2-10 Examples of settings conflicts

	Original status	**Change by selection**	**Conflict**	**Solution**
1	MOD TYPE π/4DQPSK SYMBOL RATE 10 Msymb/s	MOD TYPE→ GMSK	GMSK is only possible for symbol rates up to 7.5 Msymb/s.	Reduction of symbol rate to a value of <7.5 MHz.
2	MOD TYPE 2FSK FILTER GAUSS FILTER PARAMETER 1.0	FILTER → COS	Filter COS 1.0 not possible, the maximum roll-off factor is 0.7.	Setting of FILTER PARAMETER to a value between 0.15 and 0.7.
3	SYMBOL RATE 270 ksymb/s POWER RAMP INT	SYMBOL RATE→ 5 Msymb/s	Envelope control is only possible for symbol rates up to 2.5 Msymb/s.	Switch-off of envelope control.

If SMIQ cannot resolve a setting conflict, error message SETTINGS CONFLICT is indicated in the status line of the display. In this case, SMIQ uses the user-defined setting in the display. However, the generated modulation signal does not correspond to this indication. The setting conflict can be eliminated by a change of parameters. Error message SETTINGS CONFLICT disappears as soon as a conflict-free setting is reinstated. For a list of possible setting conflicts and error messages for digital modulation see Annex B, thumbnail divider 7.

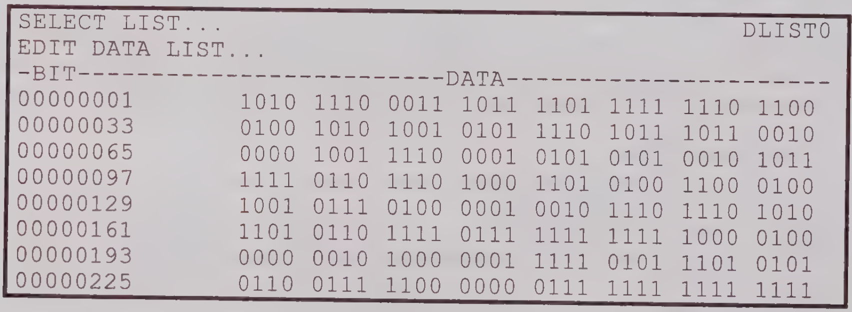
2.10.2 Internal Modulation Data and Control Signals from Lists

If SMIQ is equipped with option Data Generator SMIQB11, modulation data and control signals can be stored in a freely programmable data-generator memory. The storage capacity in the basic configuration is 16 Mbit but can be extended by 32 Mbit or 64 Mbit by fitting one or two SMIQB12 options.
The data are managed via so-called lists. 125 lists can be stored at maximum for modulation data or control signals. A list editor allows to select, copy, change and delete data lists. For a detailed description of the list editor see Section 2.2.11, List Editor.

There are two types of lists, the DATA LIST and the CONTROL LIST.
Lists, as a source for modulation data, can be selected in the menu by entering SOURCE -SOURCE-DATA_LIST and the active list by SOURCE-SELECT DATA LIST.
Lists, as a source for control signals, can be selected in the menu by entering SOURCE -CONTROL STATE ON and the active list by SOURCE-SELECT CONTROL LIST.

Note: *With DATA LIST selected as a source for internal modulation data, the control signals too have to come from a list. In this case, the setting POWER RAMP CONTROL- SOURCE EXTERN DIGITAL issues an error message*
With CONTROL LIST selected as a source for control signals, the modulation data too have to come from a list.

Data Lists:
The DATA LIST has a bit-by-bit layout. The length of the programmed data sequence and the available storage capacity are indicated in the status line of the display.

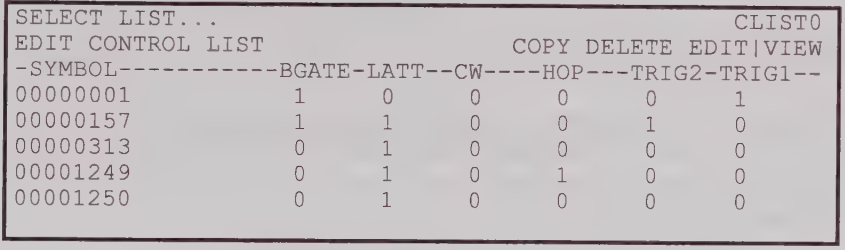


```
SELECT LIST...                                              DLIST0
EDIT DATA LIST...
-BIT-------------------------DATA----------------------
00000001        1010 1110 0011 1011 1101 1111 1110 1100
00000033        0100 1010 1001 0101 1110 1011 1011 0010
00000065        0000 1001 1110 0001 0101 0101 0010 1011
00000097        1111 0110 1110 1000 1101 0100 1100 0100
00000129        1001 0111 0100 0001 0010 1110 1110 1010
00000161        1101 0110 1111 0111 1111 1111 1000 0100
00000193        0000 0010 1000 0001 1111 0101 1101 0101
00000225        0110 0111 1100 0000 0111 1111 1111 1111
```

Fig. 2-51 DATA LIST for modulation data

Control Lists:

A CONTROL LIST can be created to generate control signals that have to be synchronous to the modulation symbols. The CONTROL LIST has a bit-by-bit layout. Six different control signals can be freely programmed. The CONTROL LIST can be created such that entries are only made at those symbol positions where a control signal is changed. The length of the CONTROL LIST is determined by the symbol number of the last entry and can differ from that of the DATA LIST.



```
SELECT LIST...                                            CLIST0
EDIT CONTROL LIST                      COPY DELETE EDIT|VIEW
-SYMBOL-----------BGATE-LATT--CW----HOP---TRIG2-TRIG1--
00000001             1     0     0     0     0     1
00000157             1     1     0     0     1     0
00000313             0     1     0     0     0     0
00001249             0     1     0     1     0     0
00001250             0     1     0     0     0     0
```

Fig. 2-52 CONTROL LIST for control signals

Signals BGATE (Burst Gate) and LATT (Level Attenuation) are used for envelope control. Envelope control with these signals is switched on by selecting SOURCE-CONTROL STATE ON and POWER RAMP CONTROL - SOURCE INT.

Digital modulation can be switched off by the CW (Continuous Wave) signal. HOP (Hopping) is a trigger signal for frequency hopping (compare with section 'List Mode').

TRIG 1 and TRIG 2 (Trigger Output 1 and 2) can be used to generate synchronization signals such as frame clocks, start markers of a PRBS sequence or a special modulation symbol.

Signals BGATE, LATT, HOP and CW are used internally by SMIQ but not signals TRIG 1 and TRIG 2. All the control signals are available at the PAR DATA interface at the rear of SMIQ.

The data sequences stored in the data generator can be run repetitively (TRIGGER MODE AUTO). Moreover, trigger signals can be used for synchronized sequences. Trigger signals can be fed in via the TRIGIN input at connector PAR DATA. A trigger event can be initiated manually by EXECUTE TRIGGER. A trigger signal is generated upon the trigger event at the output TRIGOUT 3.

After a trigger event, the data generator starts to output modulation data from the active list starting with bit 1. In case of external triggering, the start can be delayed by a selectable number of symbols (EXT TRIGGER DELAY). Retriggering (RETRIG) can be inhibited for a selectable number of symbols (EXT TRIGGER INHIBIT).

2.10.3 Internal PRBS Data and Pattern

The PRBS generators in the modulation coder provide pseudo random binary sequences (PRBS) of different length and period. They are called sequences of maximum length and are generated by means of feedback shift registers.
The following schematic shows the 9-bit generator with feedback from registers 4 and 0 (output).

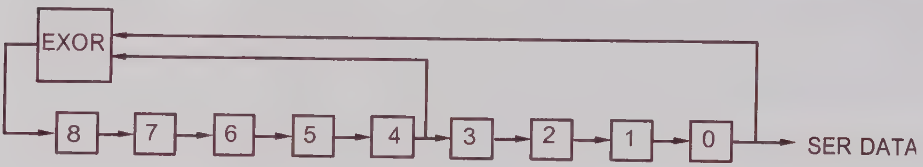


Fig. 2-53 9-bit PRBS generator

The pseudo random sequence of a PRBS generator is determined by the number of registers and the feedback. The following table describes all available PRBS generators:

Table 2-11 PRBS generators of modulation coder

PRBS generator	**Length in bit**	**Feedback to**
9 Bit	$2^9 - 1 = 511$	Register 4, 0
15 Bit	$2^{15} - 1 = 32767$	Register 1, 0
16 Bit	$2^{16} - 1 = 65535$	Register 5, 3, 2, 0
20 Bit	$2^{20} - 1 = 1048575$	Register 3, 0
21 Bit	$2^{21} - 1 = 2097151$	Register 2, 0
23 Bit	$2^{23} - 1 = 8388607$	Register 5, 0

PRBS data as a source for modulation data are selected in the menu via SOURCE - SOURCE PRBS. The period is determined by PRBS LENGTH.

Other internal modulation data are available as simple data patterns such as 0s or 1s. Selection is via SOURCE - SOURCE PATTERN.

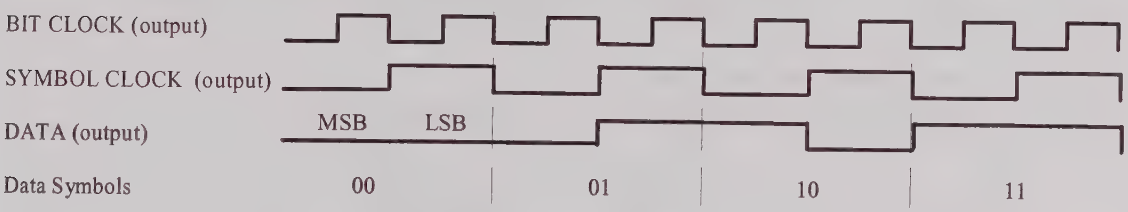
Note: *With PRBS data or Pattern selected as source for modulation data, only external control signals can be used. A combination with control signals from lists is not possible.*

2.10.4 Digital Data and Clock output Signals

2.10.4.1 Serial Interfaces DATA, BIT CLOCK and SYMBOL CLOCK

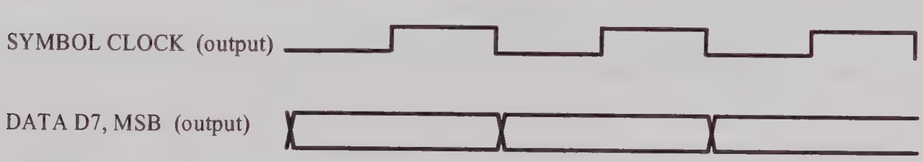
The following figure shows an example for the output signals at the serial interface for QPSK modulation (2 bits per symbol). A positive CLOCK EDGE is assumed to be set. The following list containing 4 symbols (8 bits) was used as a data source.

```
SELECT LIST...                                         DLIST00
EDIT DATA LIST...
-BIT--------------------------DATA----------------------
00000001        0001 1011
```



2.10.4.2 Parallel Interfaces DATA and SYMBOL CLOCK

The following figure shows an example for the output signals at the parallel interface. A positive CLOCK EDGE is assumed to be set.



2.10.5 External Modulation Data and Control Signals

Digital modulation signals such as data, clock and signals for envelope control can be externally applied to the modulation coder either via the parallel PAR DATA interface at the rear of SMIQ or via the serial interface with BNC connectors DATA, BIT CLOCK and SYMBOL CLOCK. Moreover, the asynchronous serial interface SERDATA can be used. For a detailed description of the interface hardware see Section Elements at the Rear Panel".

The data source is selected in menu DIGITAL MOD - SOURCE. The clock source is selected in menu DIGITAL MOD - CLOCK irrespective of the data source selection. The polarity of the active clock edge can be changed via DIGITAL MOD - EXT INPUTS - CLOCK SLOPE.

2.10.5.1 External Serial Modulation Data

Serial modulation data can be fed bit-by-bit via connector DATA. For modulation types with more than 1 bit/symbol, the MSB is applied first (MSB first). Either an external bit clock or symbol clock or the internal clock can be used. The symbol clock serves as strobe to mark the LSB of a symbol. If an external bit clock is applied, the data at the active clock edges have to be in a stable state. With external symbol clock, the bit clock for reading the data is internally generated in the modulation coder (internal data clock). The following figures show the timing at the interface. In all the cases shown, the active clock edge is assumed to be positive.

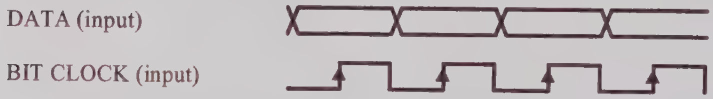

DATA (input)
BIT CLOCK (input)

Fig. 2-54 External serial data and bit clock
Data change should take place only on the negative clock edge.

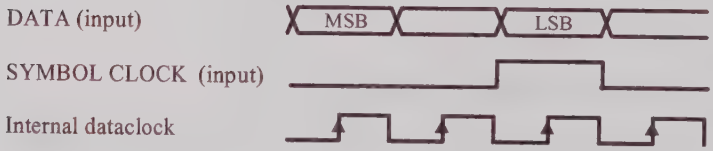

DATA (input)
MSB
LSB
SYMBOL CLOCK (input)
Internal dataclock

Fig. 2-55 External serial data and symbol clock, 3 bit/symbol
SYMBOL CLOCK = High marks the LSB. A status change of DATA and SYMBOL CLOCK should be performed synchronously.

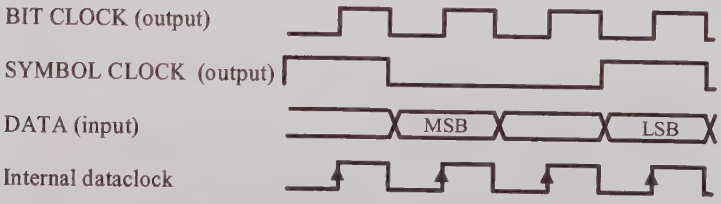

BIT CLOCK (output)
SYMBOL CLOCK (output)
DATA (input)
MSB
LSB
Internal dataclock

Fig. 2-56 External serial data, internal clock signals

External serial modulation data is selected in the menu by SOURCE-SOURCE-EXT_SER.

2.10.5.2 External Parallel Modulation Data

Parallel data can be fed as symbols via the PAR DATA interface (DATA-D7, -D6 to D0). Either an external symbol clock (SYMBCLK) or the internal symbol clock can be used. The data at the active edge of the symbol clock have to be in a stable state. The following figures show the timings at the interface. In the two examples shown below, the active clock edge is assumed to be positive.

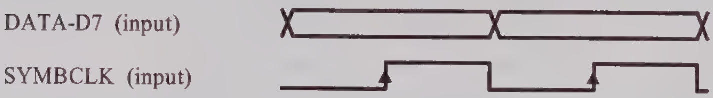


Fig. 2-57 External parallel data and symbol clock
Data change should take place only on the negative clock edge.

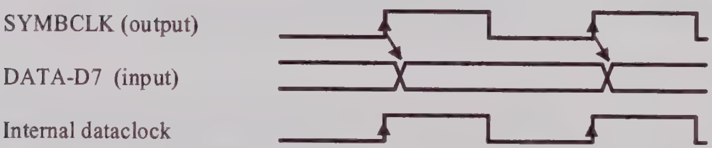


Fig. 2-58 External parallel data and symbol clock
SYMBOL CLOCK = High marks the LSB. A status change of DATA and SYMBOL CLOCK should be performed synchronously.

External serial modulation data is selected in the menu by SOURCE-SOURCE-EXT_SER.

For modulation types with less than 8 bit/symbol, line DATA-D7 is always the MSB. For QPSK modulation, for example (2 bit/symbol), data lines DATA-D7 and DATA-D6 are used.

Note: *BITCLK pin on the PARDATA interface is an output. Synchronization to an external bit clock is not possible in this mode.*

External parallel modulation data is selected in the menu by SOURCE-SOURCE-EXT_PAR.

2.10.5.3 Asynchronous Interface for External Modulation Data

The SERDATA interface on the rear of SMIQ serves for the asynchronous serial transmission of modulation data. The characteristics of this RS-232-C interface is described in Annex A.

For a defined start with specific modulation data it has to be made sure that the backup memories in the RS-232 transmitter and receiver are deleted. The following setting sequence in the menu is required:

1. Carry out desired settings for digital modulation in menu.
2. Select data source SERDATA using SOURCE - SOURCE SERDATA.
3. Make connection to external data source, but do not yet start external data source.
4. Switch off digital modulation using STATE OFF.
5. Set TRIGGER MODE ARMED_AUTO.
   In this state, SMIQ is ready for reception, but discards data that are read in via SERDATA.
6. Switch on digital modulation with STATE ON.
7. Start external data source.
   The read-in data are written into the receiving buffer. Only if this buffer is filled can SMIQ react to a trigger event.
8. Activate trigger event to start digital modulation.

Note: *The baud rate has to be selected at least 25% higher than the bit rate of the digital modulation. If SMIQ has not enough data, the error message "Data underrun" will be issued in the status line.*

2.10.5.4 External Control Signals

The external control signals for envelope control BURST GATE and LEV ATT can be applied via the SERDATA interface at the rear of SMIQ. The signal for CW control can also be applied to this interface.

Note: *The use of control signals from lists in combination with external modulation data is not possible.*

2.10.6 Envelope Control

For TDMA radio networks, in addition to digital modulation, a time-synchronous control of the envelope of the RF output signal is required. To this effect, SMIQ is equipped with an analog envelope modulator which can be driven via connector POWER RAMP. Instead of the analog control signal the digital signals BURST GATE and LEV ATT can be used to control the envelope modulator. In the modulation coder, a ramp with settable slope is obtained from data changes from high→low or low→high of the digital BURST GATE signal. The resulting analog signal is taken to the envelope modulator and is provided at connector POWER RAMP. The LEV ATT signal serves for a defined level reduction.

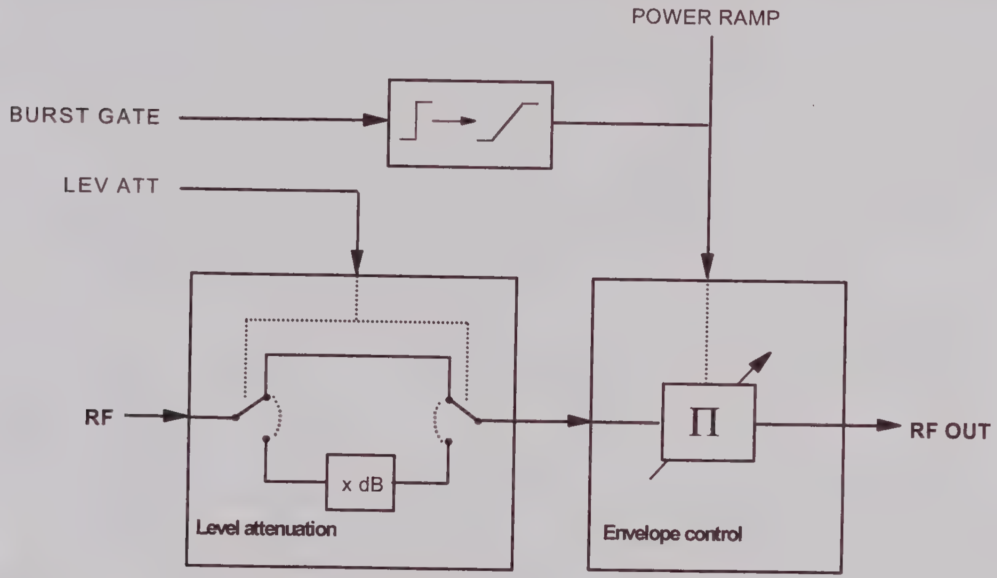


Fig. 2-59 Envelope control in SMIQ with modulation coder

The digital envelope control signals can be fed in externally via connector PAR DATA. With option Data Generator SMIQB11 provided, the signals can also be generated internally by programming them in the CONTROL LIST (see Section "Modulation Data from Lists").

The following table shows the logic function of the two signals BURST GATE and LEV ATT.

Table 2-12 Logic function of signals BURST GATE and LEVEL ATT

BURST GATE	LEV ATT	Remark
1	0	Full level
1	1	Level reduced by the value set in dB under POWER RAMP CONTROL - ATTENUATION
0	X	Maximum level reduction

The following figure illustrates the effect of the envelope control signals.

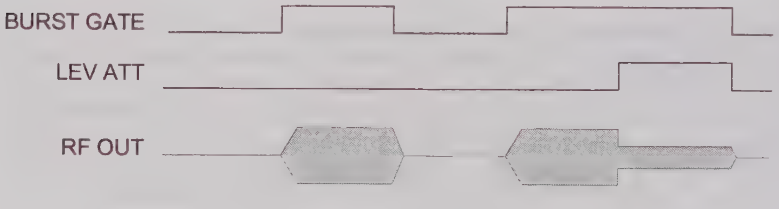


Fig. 2-60 Signal waveforms during envelope control

Note: Envelope control with digital input signals and edge shaping is only possible for symbol rates of maximum 2.5 Msymb/s.

Envelope control is switched on in the menu via:

analog	POWER RAMP CONTROL - SOURCE - EXT ANALOG.
external digital	POWER RAMP CONTROL - SOURCE - EXT DIGITAL.
internal digital	POWER RAMP CONTROL - SOURCE - INT and SOURCE CONTROL STATE ON.

2.10.7 Clock Signals

The symbol clock and the bit clock are generated in SMIQ by a clock synthesizer on the modulation coder. All clock signals are synchronized to the 10 MHz reference of the unit. The symbol clock is available at the SYMBOL CLOCK connector and the bit clock at the BIT CLOCK connector. If required, the clock synthesizer in SMIQ can synchronize to an externally applied symbol or bit clock.
Only during an operation with external parallel data is synchronization to one symbol clock possible. This symbol clock is applied via the PAR DATA interface. In all other cases, apply symbol and bit clock to the corresponding BNC connector.

The clock signal is selected in the menu via CLOCK-MODE SYMBOL/BIT and CLOCK-CLOCK SOURCE EXT.

To allow for a trouble-free synchronization of the clock synthesizer first apply the external clock and set the correct symbol rate at SMIQ. Then switch CLOCK SOURCE from INT to EXT.

Notes: *The set symbol rate should not differ by more than 1% from the symbol rate of the external signal.*

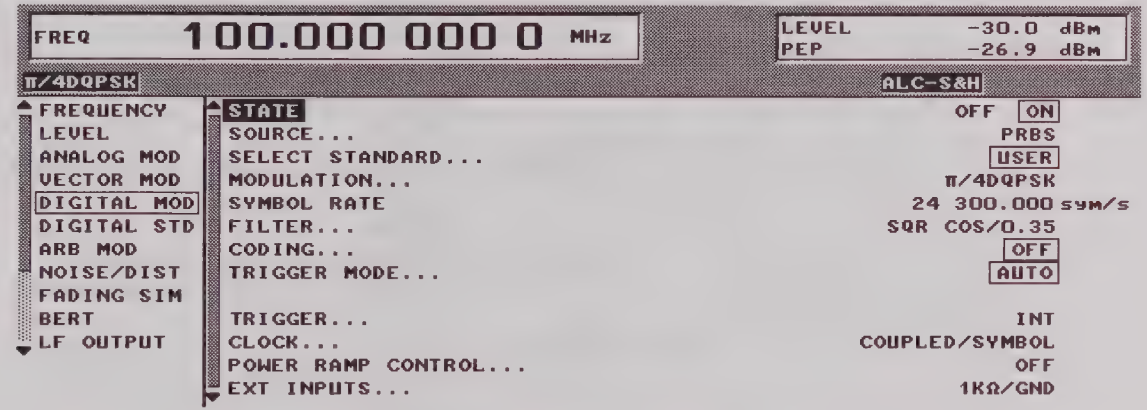
2.10.8 RF Level For Digital Modulation

With modulation switched on, a level display divided in half appears in the header of the display. The peak envelope power (PEP) of the modulated RF output level is displayed in addition to average power (LEVEL).
The difference between PEP and LEVEL depends on the modulation type and the filtering. The power of QAM signals is calculated on the assumption of a uniform distribution of modulation symbols. For the PEP calculation, it is always assumed that the most unfavourable case occurs in the sequence of modulation data. This is definitely the case for PRBS data with a long period (eg PRBS LENGTH 23 bit). For other data sequences it is possible that the indicated PEP is not attained.

2.10.9 Digital Modulation Menu

The DIGITAL MOD menu provides access to digital modulation settings.

Menu selection: DIGITAL MOD



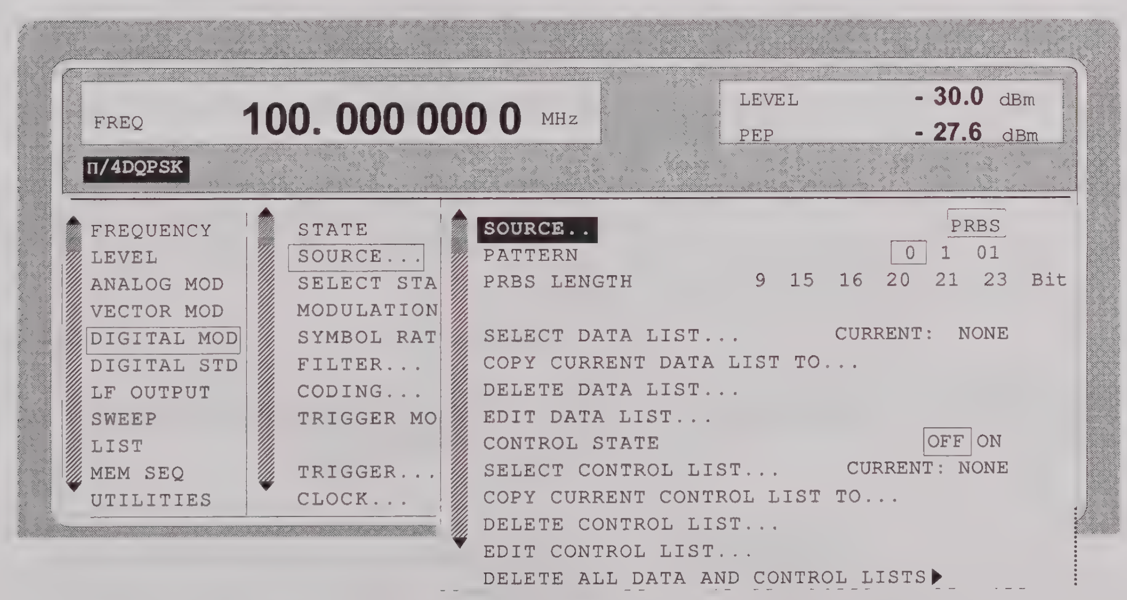
```
FREQ        100.000 000 0  MHz              LEVEL             -30.0  dBm
                                            PEP               -26.9  dBm
π/4DQPSK                                                  ALC-S&H
 FREQUENCY     STATE                                       OFF [ON]
 LEVEL         SOURCE...                                       PRBS
 ANALOG MOD    SELECT STANDARD...                            [USER]
 VECTOR MOD    MODULATION...                               π/4DQPSK
[DIGITAL MOD]  SYMBOL RATE                        24 300.000 sym/s
 DIGITAL STD   FILTER...                              SQR COS/0.35
 ARB MOD       CODING...                                      [OFF]
 NOISE/DIST    TRIGGER MODE...                               [AUTO]
 FADING SIM
 BERT          TRIGGER...                                       INT
 LF OUTPUT     CLOCK...                              COUPLED/SYMBOL
               POWER RAMP CONTROL...                            OFF
               EXT INPUTS...                                1kΩ/GND
```

Fig. 2-61 DIGITAL MOD menu, SMIQ equipped with option Modulation Coder SMIQB20 and option Data Generator SMIQB11

STATE Switch on/off of digital modulation.

IEC/IEEE-bus command `SOUR:DM:STAT ON`

SOURCE... Opens a window for defining the data source for digital modulation.
Menu selection depends on option SMIQB11. If option SMIQB11 is installed, the menu provides more selections and settings for modulation data from lists.



```
FREQ     100. 000 000 0  MHz                LEVEL      - 30.0  dBm
                                            PEP        - 27.6  dBm
π/4DQPSK
 FREQUENCY     STATE           SOURCE..                            PRBS
 LEVEL        [SOURCE...]      PATTERN                         [0]  1  01
 ANALOG MOD    SELECT STA      PRBS LENGTH          9  15  16  20  21  23  Bit
 VECTOR MOD    MODULATION
[DIGITAL MOD]  SYMBOL RAT      SELECT DATA LIST...          CURRENT:  NONE
 DIGITAL STD   FILTER...       COPY CURRENT DATA LIST TO...
 LF OUTPUT     CODING...       DELETE DATA LIST...
 SWEEP         TRIGGER MO      EDIT DATA LIST...
 LIST                          CONTROL STATE                       [OFF] ON
 MEM SEQ       TRIGGER...      SELECT CONTROL LIST...       CURRENT:  NONE
 UTILITIES     CLOCK...        COPY CURRENT CONTROL LIST TO...
                               DELETE CONTROL LIST...
                               EDIT CONTROL LIST...
                               DELETE ALL DATA AND CONTROL LISTS ▶
```

Fig. 2-62 DIGITAL MOD-SOURCE menu, SMIQ equipped with option Modulation Coder SMIQB20 and option Data Generator SMIQB11

(SOURCE...)

SOURCE...

Opens a window for selecting the source for modulation data.

EXT_PAR	The modulation data are fed in via the parallel PAR DATA interface at the rear of SMIQ. IEC -bus `:SOUR:DM:SOUR PAR`
EXT_SER	The modulation data are fed in serially at input DATA. IEC -bus `:SOUR:DM:SOUR SER`
PATTERN	A simple data pattern is continuously generated. IEC-bus `:SOUR:DM:SOUR PATT`
PRBS	A pseudo random bit sequence is generated and continuously repeated. IEC -bus `:SOUR:DM:SOUR PRBS`
SERDATA	The modulation data are fed in via the asynchronous SER_DATA interface. IEC -bus `:SOUR:DM:SOUR SDAT`
DATA LIST	Modulation data from lists IEC-bus `:SOUR:DM:SOUR DLIS`

PATTERN

Selection of a simple data pattern which is continuously generated.

0	0s are continuously generated. IEC -bus `:SOUR:DM:PATT ZERO`
1	1s are continuously generated. IEC -bus `:SOUR:DM:PATT ONE`
01	010 data changes are continuously generated. IEC -bus `:SOUR:DM:PATT ALT`

PRBS LENGTH

Setting of PRBS generator length. 9 bit, 15 bit, 16 bit, 20 bit, 21 bit and 23 bit can be selected. The period of the data sequence is then between 2^9-1 and 2^{23}-1 bit (see Section 2.10.3 "Internal PRBS Data and Pattern").

IEC/IEEE-bus command `:SOUR:DM:PRBS 15`

SELECT DATA LIST

Opens a window for selecting a stored data list or for generating a new list.

IEC/IEEE-bus `:SOUR:DM:DLIS:SEL ´name´`

COPY CURRENT DATA LIST TO

Stores the current data list under a different name.

IEC/IEEE-bus `:SOUR:DM:DLIS:COPY ´name´`

DELETE DATA LIST

Deletes a data list.

(SOURCE...)

EDIT DATA LIST

Opens a window for editing a data list bit-by-bit. The available storage capacity and the length of the current list is displayed in parameters FREE and LEN (see also Section List Editor).

COPY	Copies a list range
FILL	Fills the range with filler pattern
INSERT	Inserts a list range at a different position of the list
DELETE	Deletes a list range
EDIT/VIEW	Edits or views the list

CONTROL STATE

ON	The signals from the selected CONTROL LIST are effective.
OFF	The signals are not effective

IEC/IEEE-bus `:SOUR:DM:CLIS:CONT ON`

SELECT CONTROL LIST

Opens a window for selecting a stored list for control signals or for generating a new list for control signals.

IEC/IEEE-bus `:SOUR:DM:CLIS:SEL ´name´`

COPY CURRENT CONTROL LIST TO

Stores the current list for control signals under a different name.

IEC/IEEE-bus `:SOUR:DM:CLIS:COPY ´name´`

DELETE CONTROL LIST

Deletes a list for control signals.

EDIT CONTROL LIST

Opens a window for editing a list for control signals symbol-by-symbol. The available storage capacity and the length of the current list is displayed in parameters FREE and LEN (see also Section List Editor).

COPY	Copies a list range
DELETE	Deletes a list range
EDIT/VIEW	Edits or views the list

DELETE ALL DATA AND CONTROL LISTS ▶

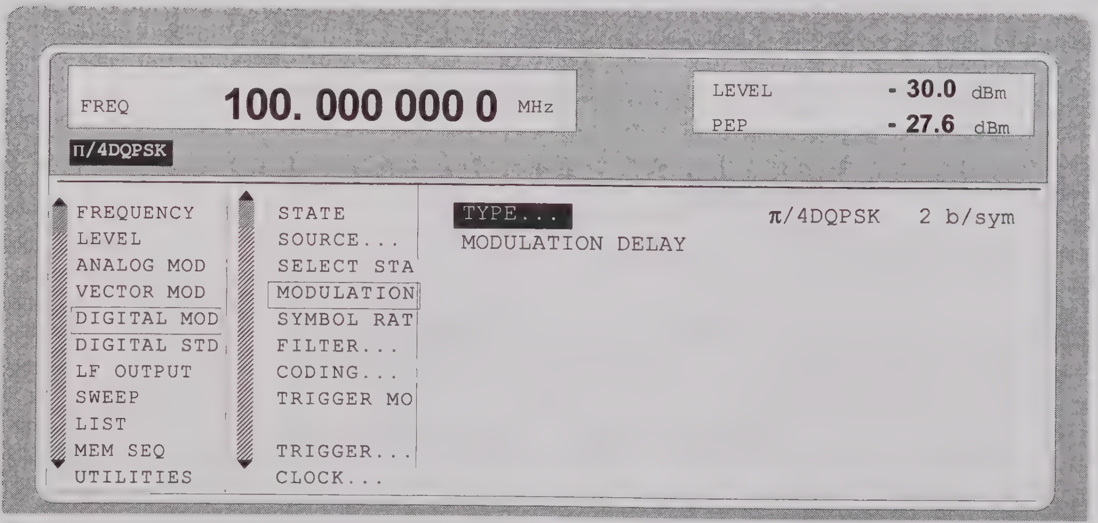
Deletes all data lists and lists for control signals.

IEC/IEEE-bus -

SELECT STANDARD... Opens a window for selecting the standard. After the selection, the modulation parameters MODULATION, SYMBOL RATE, FILTER and CODING are automatically adjusted to the standard. USER is indicated if the settings of these parameters do not correspond to the selected standard. The following standards are available:

APCO4FM	IEC\IEEE-bus command	`:SOUR:DM:STAN APCF`
APCOQPSK	IEC\IEEE-bus command	`:SOUR:DM:STAN APCQ`
ASK STD-T55	IEC\IEEE-bus command	`:SOUR:DM:STAN ASK`
BLUETOOTH	IEC\IEEE-bus command	`:SOUR:DM:STAN BLU`
CDPD	IEC/IEEE-bus command	`:SOUR:DM:STAN CDPD`
CT2	IEC/IEEE-bus command	`:SOUR:DM:STAN CT2`
DECT	IEC/IEEE-bus command	`:SOUR:DM:STAN DECT`
GSM	IEC/IEEE-bus command	`:SOUR:DM:STAN GSM`
GSM_EDGE	IEC/IEEE-bus command	`:SOUR:DM:STAN GSME`
IRIDIUM	IEC\IEEE-bus command	`:SOUR:DM:STAN IRID`
IS95 FWD	IEC\IEEE-bus command	`:SOUR:DM:STAN FIS95`
IS95 REV	IEC\IEEE-bus command	`:SOUR:DM:STAN RIS95`
NADC	IEC/IEEE-bus command	`:SOUR:DM:STAN NADC`
PDC	IEC/IEEE-bus command	`:SOUR:DM:STAN PDC`
PHS	IEC/IEEE-bus command	`:SOUR:DM:STAN PHS`
TETRA	IEC/IEEE-bus command	`:SOUR:DM:STAN TETR`
TFTS	IEC/IEEE-bus command	`:SOUR:DM:STAN TFTS`
PWT	IEC/IEEE-bus command	`:SOUR:DM:STAN PWT`
ICOBPSK	IEC\IEEE-bus command	`:SOUR:DM:STAN ICQB`
ICOGMSK	IEC\IEEE-bus command	`:SOUR:DM:STAN ICQG`
ICOQPSK	IEC\IEEE-bus command	`:SOUR:DM:STAN ICQQ`
WORLDSPACE	IEC\IEEE-bus command (only with option SMIQB17)	`:SOUR:DM:STAN WORL`
WCDMA QPSK	IEC/IEEE-bus command (only with option SMIQB47)	`:SOUR:DM:STAN QWCD`
ARIB STD-T55	IEC/IEEE-bus command	`:SOUR:DM:STAN AT55`

MODULATION... Opens a window for defining the modulation method. Moreover, the number of bits per modulation symbol is displayed. See also Section "Digital Modulation Methods and Coding".



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

π/4DQPSK

TYPE... π/4DQPSK 2 b/sym

MODULATION DELAY

Fig. 2-63 DIGITAL MOD - MODULATION... menu, SMIQ equipped with option Modulation Coder SMIQB20 and option Data Generator SMIQB11

(MODULATION...) **TYPE...**

Opens a window for selecting the modulation method. The following modulations can be selected.
User-defined mapping lists can be loaded by IEC/IEEE-bus. Then they can be selected by their list name (cf. chapter 3, `:SOURce:DM:MLISt`).

ASK	Amplitude Shift Keying IEC/IEEE-bus `:DM:FORM ASK`
BPSK	Binary Phase Shift Keying IEC/IEEE-bus `:DM:FORM BPSK`
QPSK	Quadrature Phase Shift Keying IEC/IEEE-bus `:DM:FORM QPSK`
QPSK IS95	Quadrature Phase Shift Keying with a mapping according to Interim Standard 95 for CDMA IEC/IEEE-bus `:DM:FORM QIS95`
QPSK ISAT	QPSK Modulation for INMARSAT-M IEC/IEEE-bus `:DM:FORM QINM`
QPSK ICO	QPSK Modulation for ICO IEC/IEEE-bus `:DM:FORM QICO`
WCDMA QPSK	QPSK Modulation for W-CDMA IEC/IEEE-bus `:DM:FORM QWCD` WCDMA QPSK only with option SMIQB47
OQPSK	Offset Quadrature Phase Shift Keying IEC/IEEE-bus `:DM:FORM OPSK`
OQPSK IS95	Quadrature Phase Shift Keying with a mapping according to Interim Standard 95 for CDMA IEC/IEEE-bus `:DM:FORM OIS95`
π/4 QPSK	QPSK with π/4 rotation for each symbol step (use: NSTAR) IEC/IEEE-bus `:DM:FORM P4QP`
π/4 DQPSK	QPSK with differential coding IEC/IEEE-bus `:DM:FORM P4DQ`
8PSK	Phase Shift Keying with 8 points in the constellation diagram IEC/IEEE-bus `:DM:FORM PSK8`
8PSK EDGE	Phase Shift Keying with 8 points in the constellation diagram and 3 π/8 rotation per symbol. IEC/IEEE-bus `:DM:FORM PSKE8`
GMSK	Gaussian Minimum Shift Keying IEC/IEEE-bus `:DM:FORM GMSK`
GFSK	Gaussian filtered Frequency Shift Keying IEC/IEEE-bus `:DM:FORM GFSK`
2FSK, 4FSK	Frequency Shift Keying IEC/IEEE-bus `:DM:FORM FSK2`
4FSK APCO	Frequency Shift Keying acording to APCO25 IEC/IEEE-bus `:DM:FORM AFSK4`
QAM	Quadrature Amplitude Modulation with 16, 32, 64 or 256 points in the constellation diagram IEC/IEEE-bus `:DM:FORM QAM16`

(MODULATION)

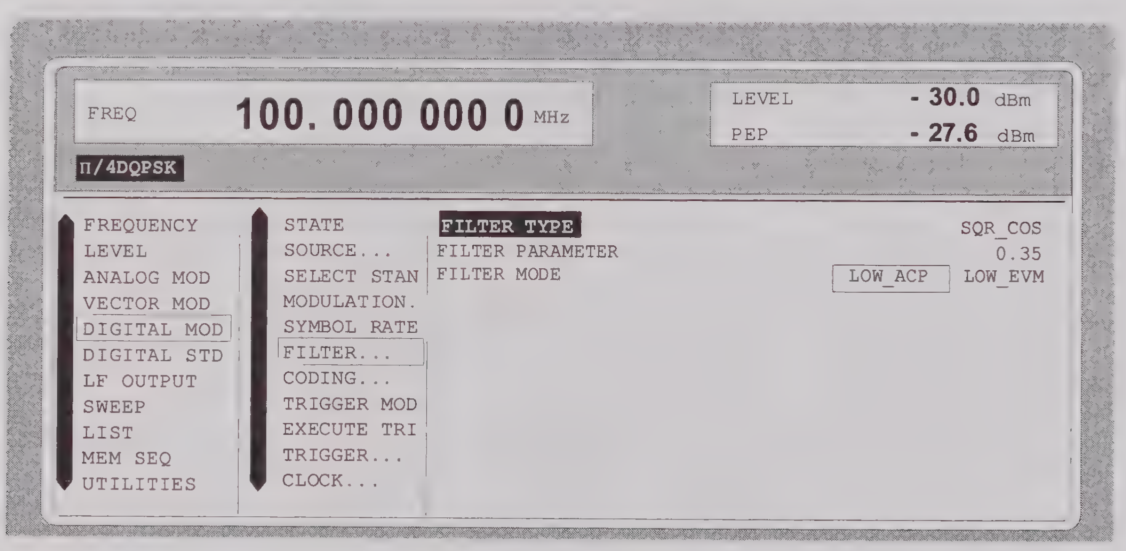
FSK DEVIATION Input value of deviation with FSK or GFSK selected.
IEC/IEEE-bus `:SOUR:DM:FSK:DEV 100 KHZ`

ASK DEPTH Input value for ASK modulation depth.
IEC/IEEE-bus `:SOUR:DM:ASK:DEPT 10`

MODULATION DELAY Value for time delay of digital modulation between data input/output and RF output of .
IEC/IEEE-bus `:SOUR:DM:MDEL?`

SYMBOL RATE Input value of symbol rate.
IEC/IEEE-bus command `:SOUR:DM:SRAT 1.2288 MHZ`

FILTER... Opens a window for setting the baseband filtering.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

FILTER TYPE SQR_COS

FILTER PARAMETER 0.35

FILTER MODE LOW_ACP LOW_EVM

Π/4DQPSK
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
STATE
SOURCE...
SELECT STAN
MODULATION.
SYMBOL RATE
FILTER...
CODING...
TRIGGER MOD
EXECUTE TRI
TRIGGER...
CLOCK...

Fig. 2-64 DIGITAL MOD -FILTER... menu, SMIQ equipped with option Modulation Coder SMIQB20 and option Data Generator SMIQB11

(FILTER...)

FILTER TYPE

Opens a window for selecting a type of filter. The following filters can be selected:

SQR_COS	Square Root RaisedCosine IEC `:SOUR:DM:FILT:TYPE SCOS`
COS	Cosine IEC `:SOUR:DM:FILT:TYPE COS`
GAUSS	Gaussian filter IEC `:SOUR:DM:FILT:TYPE GAUS`
GAUSS LINEAR	Linearized Gaussian filter for GSM_EDGE. IEC `:SOUR:DM:FILT:TYPE LGA`
BESSEL	Bessel filter with B×T = 1.25 or 2.5 IEC `:SOUR:DM:FILT:TYPE BESS1`
IS95	Filter for CDMA mobile station according to Interim Standard 95 IEC `:SOUR:DM:FILT:TYPE IS95`
IS95 EQUAL	Filter for CDMA base station with equalizer according to IS-95 IEC `:SOUR:DM:FILT:TYPE EIS95`
APCO C4FM	Cosine filter with si(x)-compensation according to APCO25 standard for 4FSK modulation. IEC `:SOUR:DM:FILT:TYPE APCO`
TETRA	Filter for PSK/QAM modulation. IEC `:SOUR:DM:FILT:TYPE TETR`
WCDMA0.22	Filter for PSK modulation. Square Root Raised Cosine filter with Roll Off = 0.22, optimized for W-CDMA IEC `:SOUR:DM:FILT:TYPE WCDM`
RECTANGLE	Filter with a rectangular impulse answer of 1 symbol length. IEC `:SOUR:DM:FILT:TYPE RECT`
SPLIT PHASE	Filter for ASK modulation IEC `:SOUR:DM:FILT:TYPE SPH`
USER	User defined filter. The lists can be generated via `:DM:FLISt:SEL` and filled via `:DM:FLISt:DATA`. IEC `:SOUR:DM:FILT:TYPE USER`

FILTER PARAMETER

Opens a window for setting the filter parameter. The selection depends on the selected filter type:

Parameter	**Filter Type**
Roll-off factor	SQR COS, COS, or APCO:
Normalized band width $B\times T_{Symb}$	GAUSS .

IEC/IEEE-bus `:SOUR:DM:FILT:PAR 0.2`

FILTER MODE

Selection of filter mode.

LOW_ACP	Filter for minimum Adjacent Channel Power IEEE `:SOUR:DM:FILT:MODE LACP`
LOW_EVM	Filter for minimum vector error IEEE `:SOUR:DM:FILT:MODE LEVM`

CODING...

Opens a window for setting the modulation coding. See also Section " Digital Modulation Methods and Coding. The following codings can be selected:

OFF	No coding IEC-bus command `:SOUR:DM:COD OFF`
DIFF	Differential coding IEC-bus command `:SOUR:DM:COD DIFF`
PHASE_DIFF	Phase differential coding IEC-bus command `:SOUR:DM:COD DPHS`
GRAY+DIFF	Gray and differential coding IEC-bus command `:SOUR:DM:COD DGR`
GSM	Differential coding according to GSM standard IEC-bus command `:SOUR:DM:COD GSM`
NADC	Differential coding for π/4 DQPSK according to NADC standard IEC-bus command `:SOUR:DM:COD NADC`
PDC	Differential coding according to PDC standard IEC-bus command `:SOUR:DM:COD PDC`
PHS	Differential coding according to PHS standard IEC-bus command `:SOUR:DM:COD PHS`
TETRA	Differential coding for π/4 DQPSK according to TETRA standard IEC-bus command `:SOUR:DM:COD TETR`
APCO25	Differential coding according to APCO25 standard IEC-bus command `:SOUR:DM:COD APCO25`
PWT	Differential coding for π/4 DQPSK according to PWT standard IEC-bus command `:SOUR:DM:COD PWT`
TFTS	Differential coding for π/4 DQPSK according to TFTS standard IEC-bus command `:SOUR:DM:COD TFTS`
INMARSAT	Phase differential coding for INMARSAT-M QPSK modulation IEC-bus command `:SOUR:DM:COD INM`
VDL	Differential coding according to VDL (VHF Digital Link) for 8PSK modulation. IEC-bus command `:SOUR:DM:COD VDL`

TRIGGER MODE

Selection of trigger mode.

This selection is only available when option SMIQB11 is installed.

AUTO	The data sequences from the selected DATA LIST and CONTROL LIST are continuously repeated. IEC/IEEE-bus command `:SOUR:DM:SEQ AUTO`
RETRIG	The data sequences are continuously repeated. A trigger event causes a restart from symbol 1. IEC/IEEE-bus command `:SOUR:DM:SEQ RETR`
ARMED_AUTO	A start of data sequences from symbol 1 is caused by a trigger event. The unit is then automatically set to AUTO and can no longer be triggered. IEC/IEEE-bus command `:SOUR:DM:SEQ AAUT`
ARMED_RETRIG	A start of data sequences from symbol 1 is only caused by a trigger event. The unit is then automatically set to RETRIG. Each new trigger event causes a restart of the data sequences. IEC/IEEE-bus command `:SOUR:DM:SEQ ARET`
SINGLE	A trigger event causes a single data sequence run. IEC/IEEE-bus command `:SOUR:DM:SEQ SING`

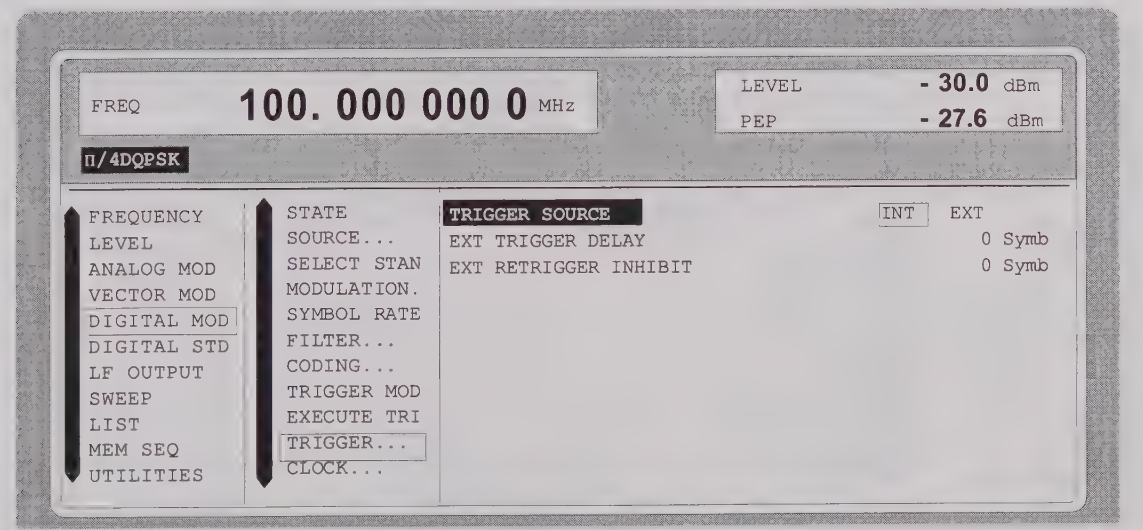
EXECUTE TRIGGER ▶

Executes a trigger event to start a data sequence.

IEC/IEEE-bus command `:TRIG:DM:IMM`

TRIGGER...

Opens a window for setting the different types of trigger and for setting the time delay of the trigger signal. The menu is only available if SMIQ is equipped with option Data Generator SMIQB11.


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
STATE
SOURCE...
SELECT STAN
MODULATION.
SYMBOL RATE
FILTER...
CODING...
TRIGGER MOD
EXECUTE TRI
TRIGGER...
CLOCK...

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

π/4DQPSK

TRIGGER SOURCE INT EXT

EXT TRIGGER DELAY 0 Symb

EXT RETRIGGER INHIBIT 0 Symb

Fig. 2-65 DIGITAL MOD - TRIGGER menu, SMIQ equipped with option Modulation Coder SMIQB20 and option Data Generator SMIQB11

(TRIGGER...)

TRIGGER SOURCE

Selection of trigger source

EXT An external trigger signal can be fed in at TRIGIN of connector PAR DATA at the rear of SMIQ. With the active edge, a data sequence is started from the data generator memory.

INT With INT selected, a trigger event can be manually executed by EXECUTE TRIGGER.

IEC/IEEE-bus `:SOUR:DM:TRIG:SOUR EXT`

EXT TRIGGER DELAY

Input value of number of symbols by which an external trigger signal is delayed before it starts the data sequence in the data generator. A synchroneity with the DUT or other units can thus be achieved.

IEC/IEEE-bus `:SOUR:DM:TRIG:DEL 3`

EXT RETRIGGER INHIBIT

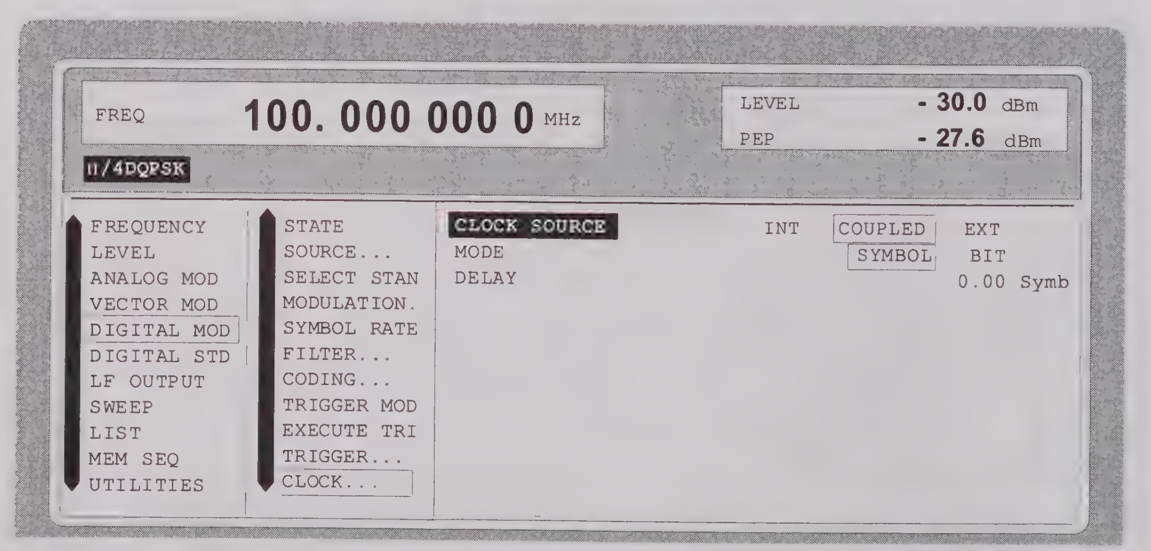
Input of number of symbols for which each new trigger event is inhibited during MODE RETRIG after a trigger signal.

During MODE RETRIG, each new trigger signal restarts the data sequence in the data generator. This restart can be inhibited for the entered number of symbols. The entry of 1250 symbols, for example, causes new trigger signals to be ignored for the duration of 1250 symbols after execution of a trigger event.

IEC/IEEE-bus `:SOUR:DM:TRIG:INH 1250`

CLOCK...

Opens a window for selecting the clock source and for setting a delay.


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
STATE
SOURCE...
SELECT STAN
MODULATION.
SYMBOL RATE
FILTER...
CODING...
TRIGGER MOD
EXECUTE TRI
TRIGGER...
CLOCK...

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

π/4DQPSK

CLOCK SOURCE INT COUPLED EXT

MODE SYMBOL BIT

DELAY 0.00 Symb

Fig. 2-66 DIGITAL MOD - CLOCK, SMIQ equipped with option Modulation Coder SMIQB20 and option Data Generator SMIQB11

(CLOCK...)

CLOCK SOURCE

Selection of clock source

INT	The symbol and the bit clock in SMIQ are generated by a clock synthesizer on the modulation coder. All the clock signals are synchronized to the 10-MHz reference of the unit. IEEE-bus `:SOUR:DM:CLOC:SOUR INT`
COUPLED	The clock comes from the same source as the data. IEEE-bus `:SOUR:DM:CLOC:SOUR COUP`
EXT	An external clock signal is fed externally. The clock synthesizer on the modulation coder is synchronized to this clock. Parameter SYMBOL RATE has to be correctly set with an accuracy of ± 1 %. IEEE-bus `:SOUR:DM:CLOC:SOUR EXT`

MODE

Selection of clock for the external clock signal

SYMBOL	An externally fed clock has to be a symbol clock.
BIT	An externally fed clock has to be a bit clock.

IEC/IEEE-bus `:SOUR:DM:CLOC:MODE SYMB`

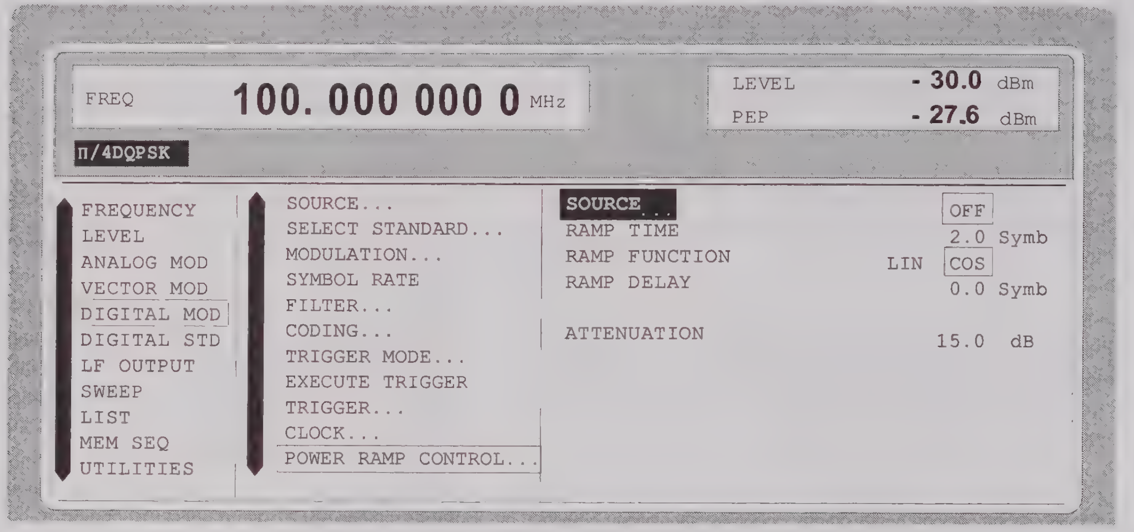
DELAY

Input value of delay of generated modulation signal compared with an externally fed clock. This can be used, for example, for synchronization with a second unit to achieve time synchroneity between the modulation signals of the two units. The displayed setting resolution of 1/100 symbol is only attained for symbol-clock frequencies below 100 kHz. The actual resolution is reduced with increasing frequency. The delay can no longer be set for 7 Msymb/s

IEC/IEEE-bus `:SOUR:DM:CLOC:DEL 0.5`

POWER RAMP CONTROL...

Opens a window for setting the envelope control.


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SOURCE...
SELECT STANDARD...
MODULATION...
SYMBOL RATE
FILTER...
CODING...
TRIGGER MODE...
EXECUTE TRIGGER
TRIGGER...
CLOCK...
POWER RAMP CONTROL...

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.6 dBm
π/4DQPSK

SOURCE... OFF
RAMP TIME 2.0 Symb
RAMP FUNCTION LIN COS
RAMP DELAY 0.0 Symb
ATTENUATION 15.0 dB

Fig. 2-67 DIGITAL MOD - POWER RAMP CONTROL menu, SMIQ equipped with option Modulation Coder SMIQB20 and option Data Generator SMIQB11

(POWER RAMP CONTROL...)

SOURCE

Selection between analog and digital envelope control and selection of source.

OFF Envelope control is switched off.

IEC/IEEE `:SOUR:DM:PRAM:STAT OFF`

INT Signals BGAT and LATT from the active CONTROL LIST (CLIST) are used for envelope control (only with option SMIQB11)

IEC/IEEE `:SOUR:DM:PRAM:STAT ON`
`:SOUR:DM:PRAM:SOUR CLIST`

EXT_ANALOG Envelope control is via an external analog signal that can be fed in via connector POWER RAMP.

IEC/IEEE-bus commands
`:SOUR:DM:PRAM:STAT ON`
`:SOUR:DM:PRAM:SOUR AEXT`

EXT_DIGITAL Envelope control is via external digital signals to be applied to BURST GATE and LEV ATT of connector PAR DATA.

IEC/IEEE-bus commands
`:SOUR:DM:PRAM:STAT ON`
`:SOUR:DM:PRAM:SOUR DEXT`

RAMP TIME

Input value of rise time of envelope after a transition from Low→High of signal BURST GATE and of fall time after a transition from High→Low.
The setting is a multiple of the symbol duration.

IEC/IEEE-bus `:SOUR:DM:PRAM:TIME 3.0`

(POWER RAMP CONTROL...)

RAMP FUNCTION

Determines the shape of the rising and falling edge during envelope control by means of the BURST GATE signal.

LIN	Selection of a linear ramp function.
COS	The edge is shaped according to a cosine function and a more favourable spectrum than that under setting LIN is obtained.

IEC/IEEE-bus `:SOUR:DM:PRAM:SHAP COS`

RAMP DELAY

Input value for a shift of the envelope characteristic to the modulated signal. A positive value causes a delay of the envelope. The values are set in the units of the symbol length.

IEC/IEEE-bus `:SOUR:DM:PRAM:DEL 0.1`

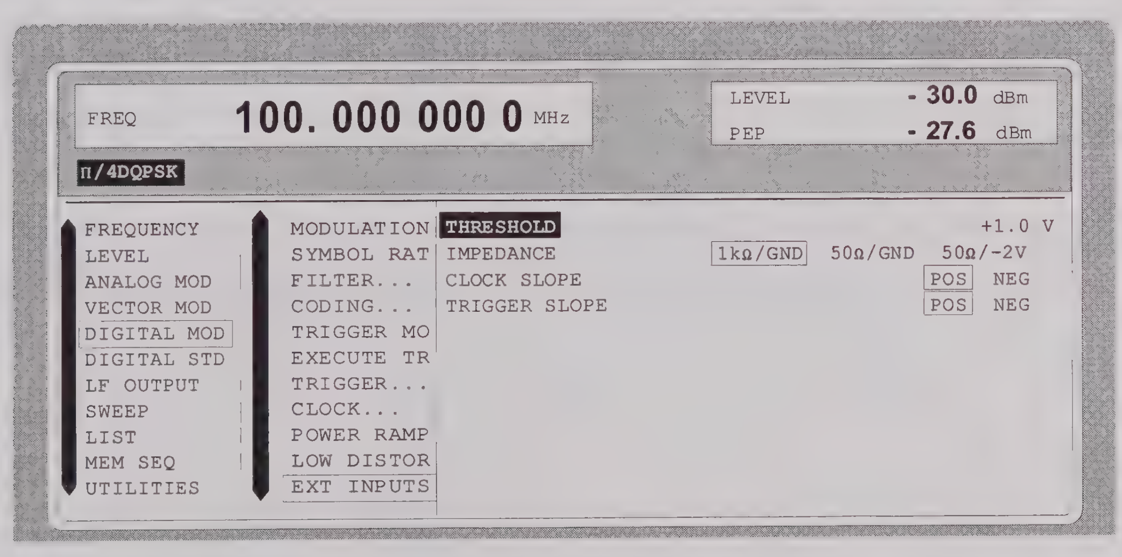
ATTENUATION

Input value of level reduction in dB with digital control signal LEV ATT used. This function is normally used - for TDMA frame structures - to generate a slot with reduced level.

IEC/IEEE-bus `:SOUR:DM:PRAM:ATT 0 dB`

EXT INPUTS

Opens a window for setting the trigger threshold, input impedance and polarity of the external digital modulation coder inputs, ie connector PAR DATA and inputs DATA, BIT CLOCK and SYMBOL CLOCK.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

π/4DQPSK

THRESHOLD +1.0 V

IMPEDANCE 1kΩ/GND

CLOCK SLOPE POS

TRIGGER SLOPE POS

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
MODULATION
SYMBOL RAT
FILTER...
CODING...
TRIGGER MO
EXECUTE TR
TRIGGER...
CLOCK...
POWER RAMP
LOW DISTOR
EXT INPUTS
50Ω/GND
50Ω/-2V
NEG
NEG

Fig. 2-68 DIGITAL MOD - EXT INPUTS menu, SMIQ equipped with option Modulation Coder SMIQB20 and option Data Generator SMIQB11

(EXT INPUTS...)

TRESHOLD

Input value of high/low threshold in Volt.

IEC/IEEE-bus command `:SOUR:DM:THR 1.0`

IMPEDANCE	Selection of input impedance and reference voltage. 50 Ω/GND should be selected for higher clock rates. Setting 50 Ω/-2V is suitable for sources with ECL output. Make sure to select a suitable setting for the high/low threshold under TRESHOLD. IEC/IEEE-bus command `:SOUR:DM:INP:IMP G1K`
CLOCK SLOPE	Selection of polarity of active edge of externally fed bit clock or symbol clock. ***Note:*** *In the internal clock mode, CLOCK SLOPE NEG inverts the clock output signals.* IEC/IEEE-bus `:SOUR:DM:CLOC:POL NORM`
TRIGGER SLOPE	Selection of polarity of active trigger edge for input TRIGIN. IEC/IEEE-bus `:SOUR:DM:TRIG:SLOP POS`

2.11 Digital Standard PHS

With the options Modulation Coder (SMIQB20) and Data Generator (SMIQB11) provided, modulation signals according to the Japanese PHS standard[1] can be generated. PHS is a TDMA standard for private and public cordless phones.

SMIQ can generate both the transmit signal of a cell station (CS) and the transmit signal of a personal station (PS). Transmission from CS to PS is called "downlink", "uplink" being used for transmission in the opposite direction.

Uplink and downlink are transmitted in the separate time slots of a frame using the time duplex method. Each frame consists of 8 slots. The data contents of each slot can be defined individually by SMIQ by means of a slot editor. SMIQ can generate a control physical slot and also a communication physical slot. Each slot can be switched on or off. A defined intermediate level can also be set.

A burst type has to be defined to configure a slot. The following burst types can be selected:

• TCHFULL	simulation of a communication channel for a speech coder rate of 32 kbit/s,
• TCHHALF	simulation of a communication channel for a speech coder rate of 16 kbit/s,
• VOX	simulation of a communication channel in non-speech intervals with uplink transmission only in every fourth frame,
• SYNC,	simulation of a sync channel and
• ALL_DATA	burst type for test purposes with freely programmable data contents in the selected slot.

The following internal modulation sources are available:

- different PRBS generators with a sequence length between 2^9-1 and 2^{23}-1 and
- data lists, ie freely programmable data sequences from the data generator memory.

For generating the PHS signals, SMIQ inserts the modulation data continuously (in real time) into the selected slots. Using a digital signal processor the data generator generates a data sequence with modulation data and control signals for envelope control.

The data generator in SMIQ generates a data stream which is converted into IQ signals in the modulation coder. According to the PHS standard, the modulation type is $\pi/4$ DQPSK at a symbol rate of 192 ksymbol/s and √COS filtering. Symbol rate and filtering can be changed in SMIQ.

---

[1] Personal Handy Phone System ARIB Standard (RCR STD-28)

2.11.1 Sync and Trigger Signals

The data generator generates a data sequence with modulation data, control signals for envelope control, and synchronization signals.

When TRIGGER MODE AUTO is selected, the PHS signal generation automatically starts.

This start can also be activated by an external trigger signal (TRIGGER MODE ARMED_AUTO) which allows a synchronous sequence for BER measurements to be carried out on receivers.

Trigger signals for synchronized sequences can be used for measuring the bit error rate of receivers. A trigger signal can be fed via the TRIGIN input at connector PAR DATA. The active slope of a trigger signal applied there executes a trigger event.
PHS signal generation at a frame limit is started after a trigger event. Data from data lists are inserted into the selected slots starting from the first bit. PRBS generators start with the set initialization status.

Signal generation either starts immediately after the active slope of the trigger signal or after a settable number of symbols (EXT TRIGGER DELAY). Retriggering (RETRIG) can be inhibited for a settable number of symbols (EXT RETRIGGER INHIBIT).

A trigger event can be executed manually or via the IEC/IEEE bus using EXECUTE TRIGGER.

When a trigger event is executed, a trigger signal is output at the TRIGOUT 3 output of SMIQ.

SMIQ also generates the following sync signals:

- a frame clock at TRIGOUT 1 output,
- a frame or multiframe clock at TRIGOUT 2 output with settable position in the frame,
- the symbol clock and the bit clock.

A clock synthesizer on the modulation coder generates the symbol clock and the bit clock in SMIQ. All the clock signals are synchronized to the 10-MHz reference of SMIQ. The symbol clock is available at connector SYMBOL CLOCK and the bit clock at connector BIT CLOCK. If required, the clock synthesizer in SMIQ can be synchronized to an external symbol or bit clock.

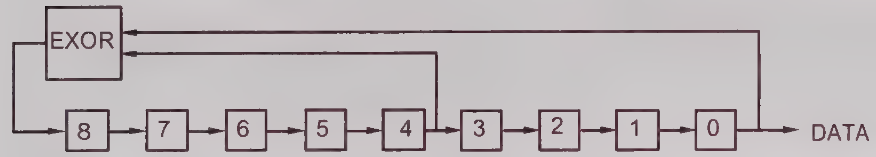
The clock signal is selected in the menu via CLOCK-CLOCK SOURCE EXT.

To allow for a trouble-free synchronization of the clock synthesizer first apply the external clock and set the correct symbol rate at SMIQ. Then switch CLOCK SOURCE from INT to EXT.

Notes: - *The set symbol rate should not differ by more than 1% from the symbol rate of the external signal.*

2.11.2 PN Generators as Internal Data Source

Independent PN generators (Pseudo Noise) can be selected for each slot as data source for data fields DATA and SACCH. These PN generators provide pseudo-random bit sequences of different length or period. That is why they are also called PRBS generators (Pseudo Random Binary Sequence).
Data sequences are sequences of maximum length which are generated by means of feedback shift registers.
The following figure gives an example of a 9 bit generator with feedbacks after register 4 and 0 (output).



The pseudo-random sequence of a PRBS generator is clearly defined by the number of registers and the feedback. The following table describes all PRBS generators available:

Table 2-13 PRBS generators for PHS

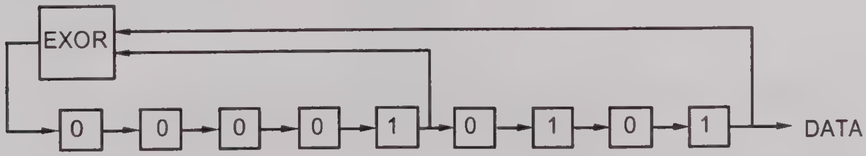
PRBS generator	**Length in bits**	**Feedback after**
9 bit	$2^9 - 1 = 511$	Register 4, 0
11 bit	$2^{11} - 1 = 2047$	Register 2, 0
15 bit	$2^{15} - 1 = 32767$	Register 1, 0
16 bit	$2^{16} - 1 = 65535$	Register 5, 3, 2, 0
20 bit	$2^{20} - 1 = 1048575$	Register 3, 0
21 bit	$2^{21} - 1 = 2097151$	Register 2, 0
23 bit	$2^{23} - 1 = 8388607$	Register 5, 0

PN generators PN9,11,15,20 and PN23 are configured according to CCITT Rec. 0.151/152/153. The output sequence is inverted for generators PN15 and PN23.

The start value of the PN generators is different in the slots and equals

$$\text{start value} = 1 + 14\,\text{hex} \times \text{slot number}$$

Example: PN9 generator in slot 1 with start value 15hex = 10101 binary.



The resulting output sequence is `10101000000101001010111110010` etc.

2.11.3 Lists as Internal Data Source

A freely programmable memory on the data generator serves as internal data source for the data fields of the slots. The data are managed in so-called lists. A list editor allows to select, copy, modify and delete data lists (DATA LIST).
The list editor is available via menu DIGITAL-MOD - SOURCE....

2.11.4 External Modulation Data

External data can (only) be applied via the SERDATA interface. A selection of SERDATA as data source is only possible for a single data field of a slot. For further information on the characteristics of the SERDATA interface see Annex A.

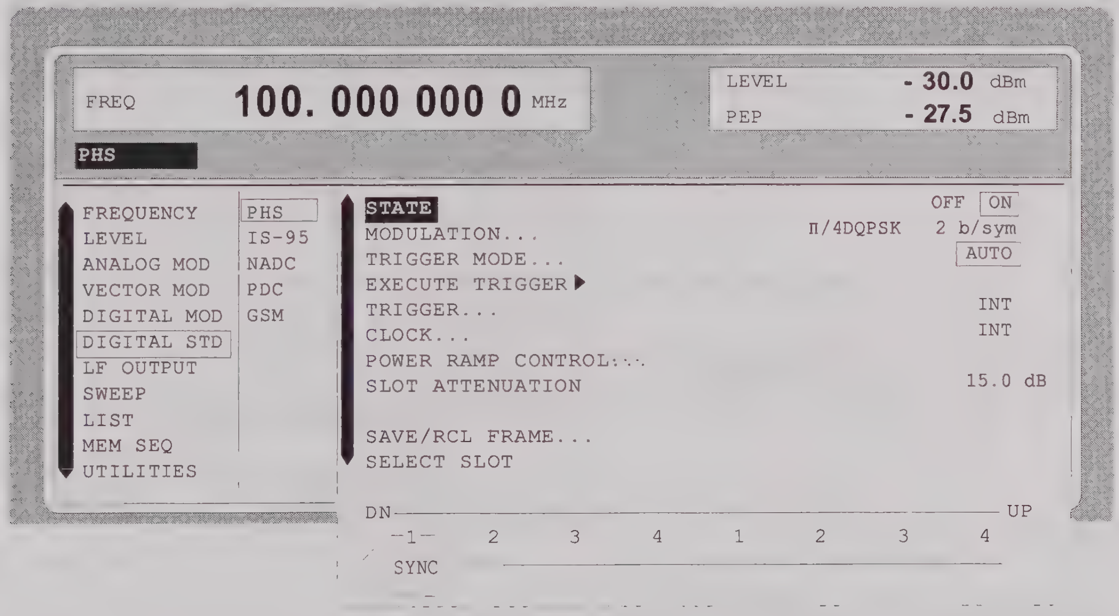
To ensure that the external data bits are assigned to specific positions in the data field of the selected slot and that they are reproducible, the buffer of the RS-232 transmitter and receiver has to be deleted. A triggered start has to follow.
The following setting sequence is required in the DIGITAL STD - PHS menu:

1. Carry out desired settings in menu.
2. Select data source SERDATA for the data field of the slot using SELECT SLOT - ....
3. Make connection to external data source, but do not yet start external data source.
4. Switch off digital standard using STATE - OFF.
5. Set TRIGGER MODE ARMED_AUTO.
   In this state, SMIQ is ready for reception, but discards data that are read in via SERDATA.
6. Switch on digital standard with STATE ON.
7. Start external data source.
   The read-in data are written into the receiving buffer. Only if this buffer is filled can SMIQ react to a trigger event.
8. Activate trigger event. Signal generation is thus started at a frame limit. The first bit received via SERDATA is put to the first bit position in the selected data field.

2.11.5 Menu DIGITAL STANDARD - PHS

Menu DIGITAL STD - PHS provides access to settings for generating PHS signals.

Menu selection: DIGITAL STD - PHS



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.5 dBm

STATE OFF ON

MODULATION... π/4DQPSK 2 b/sym

TRIGGER MODE... AUTO

TRIGGER... INT

CLOCK... INT

SLOT ATTENUATION 15.0 dB

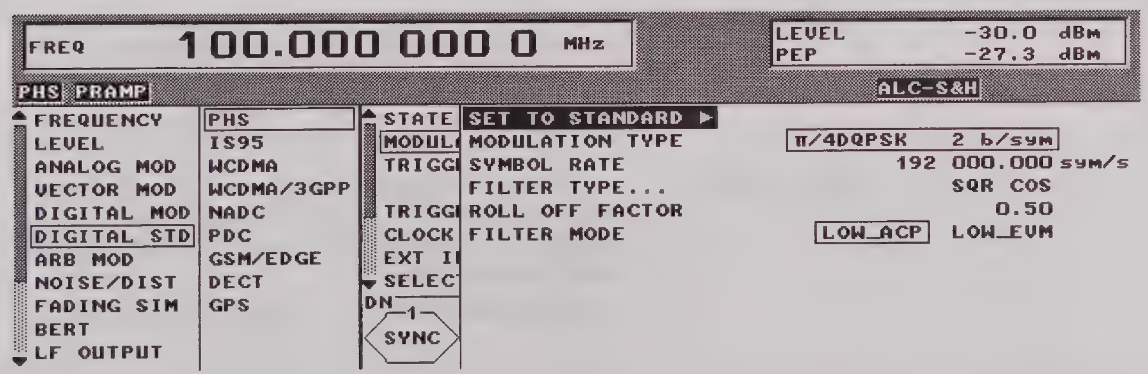
PHS
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
EXECUTE TRIGGER ▶
POWER RAMP CONTROL...
SAVE/RCL FRAME...
SELECT SLOT
DN
UP
1
2
3
4
1
2
3
4
SYNC

Fig. 2-69 Menu DIGITAL STD - PHS, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

STATE Switch on/off of Digital Standard PHS modulation. Vector Modulation or Digital Modulation will be switched off automatically.

IEC/IEEE-bus command `:SOUR:PHS:STAT ON`

MODULATION... Opens a window for setting the modulation parameters.



FREQ 100.000 000 0 MHz

LEVEL -30.0 dBm

PEP -27.3 dBm

MODULATION TYPE π/4DQPSK 2 b/sym

SYMBOL RATE 192 000.000 sym/s

FILTER TYPE... SQR COS

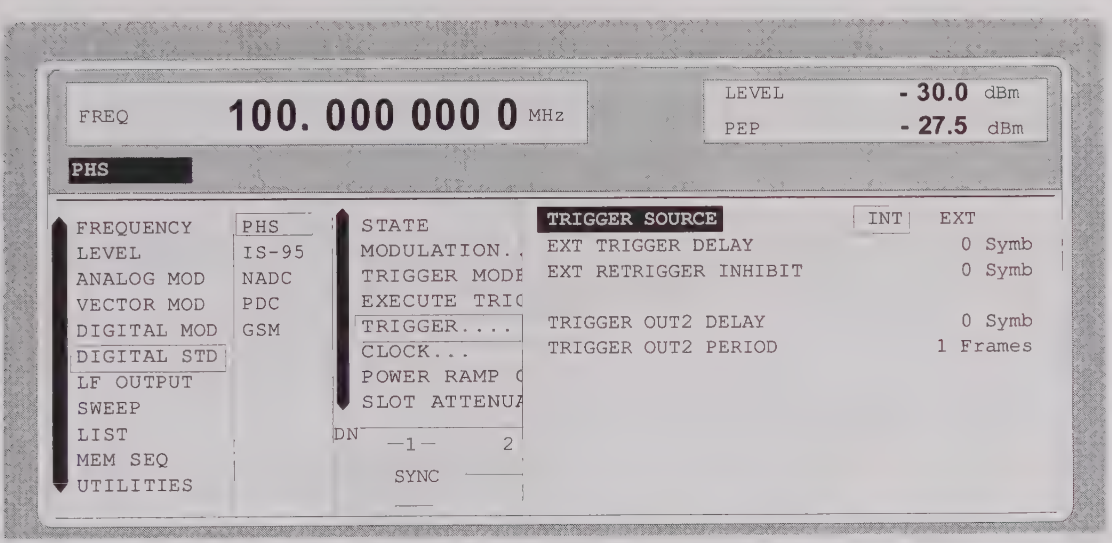
ROLL OFF FACTOR 0.50

FILTER MODE LOW_ACP LOW_EVM

PHS
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
NOISE/DIST
FADING SIM
BERT
LF OUTPUT
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
GPS
STATE
MODUL
TRIGG
TRIGG
CLOCK
EXT I
SELEC
DN
1
SYNC
SET TO STANDARD ▶

Fig. 2-70 Menu DIGITAL STD - PHS - MODULATION..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(MODULATION...)	**SET TO STANDARD ▶**	Sets the subsequent modulation parameters to the values predefined by the standard.
	MODULATION TYPE	Displays the modulation type.
	SYMBOL RATE	Input value for the symbol rate. 192 ksymbol/s are preset. IEC/IEEE-bus `:SOUR:PHS:SRAT 192.01 KHZ`
	FILTER	Selection of baseband filter. A selection between Nyquist filters COS, SQRCOS and a user-defined filter USER (cf. Section Digital Modulation) is possible. IEC/IEEE-bus `:SOUR:PHS:FILT:TYPE COS`
	ROLL OFF FACTOR	Input value for the roll-off factor. IEC/IEEE-bus `SOUR:PHS:FILT:PAR 0.50`
	FILTER MODE	Selection of filter mode. LOW_ACP Filter for minimum Adjacent Channel Power. IEC `:SOUR:PHS:FILT:MODE LACP` LOW_EVM Filter for minimum vector error. IEC `:SOUR:PHS:FILT:MODE LEVM`
TRIGGER MODE...		Opens a window for selecting the trigger mode.
	AUTO	The PHS signals are continuously transmitted in the activated slots. IEC/IEEE-bus command `:SOUR:PHS:SEQ AUTO`
	RETRIG	The PHS signals are continuously transmitted in the activated slots. A trigger event causes a restart. IEC/IEEE-bus command `:SOUR:PHS:SEQ RETR`
	ARMED_AUTO	The PHS signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the AUTO mode and can no longer be triggered. IEC/IEEE-bus command `:SOUR:PHS:SEQ AAUT`
	ARMED_RETRIG	The PHS signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the RETRIG mode. Each new trigger event causes a restart. IEC/IEEE-bus command `:SOUR:PHS:SEQ ARET`
EXECUTE TRIGGER ▶		Executes a trigger even to start the PHS signal generation. IEC/IEEE-bus command `:TRIG:DM:IMM`
TRIGGER...		Opens a window for selecting the trigger source, for configuring the trigger output signals and for setting the time delay of an external trigger signal.


PHS
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
STATE
MODULATION...
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
DN
—1—
2
SYNC

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm
PEP - 27.5 dBm

TRIGGER SOURCE INT EXT
EXT TRIGGER DELAY 0 Symb
EXT RETRIGGER INHIBIT 0 Symb

TRIGGER OUT2 DELAY 0 Symb
TRIGGER OUT2 PERIOD 1 Frames

Fig. 2-71 Menu DIGITAL STD - PHS_TRIGGER..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(TRIGGER...) **TRIGGER SOURCE**

Selection of trigger source.

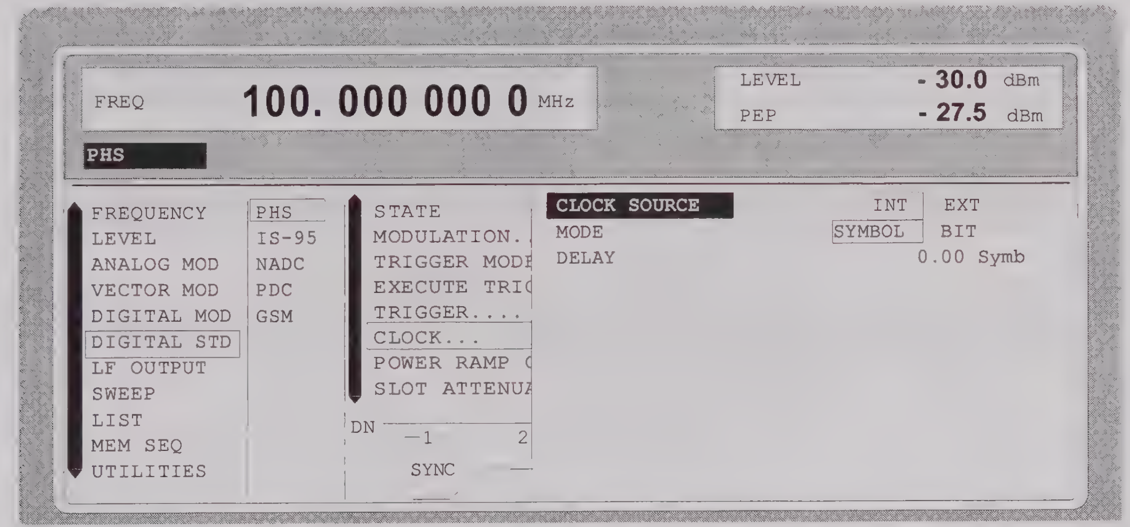
EXT The PHS signal generation is started by the active slope of an external trigger signal.
The polarity, the trigger threshold and the input resistance of the TRIGIN input can be modified in menu DIGITAL MOD - EXT INPUTS.

INT A trigger event can be executed by EXECUTE TRIGGER ▶.

IEC/IEEE-bus `:SOUR:PHS:TRIG:SOUR EXT`

(TRIGGER...)	**EXT TRIGGER DELAY**	Setting the number of symbols by which an external trigger signal is delayed before it starts the PHS signal generation. This is used for setting the time synchronization between the the SMIQ and the DUT. IEC/IEEE-bus command `:SOUR:PHS:TRIG:DEL 3`
	EXT RETRIGGER INHIBIT	Setting the number of symbols for which a restart is inhibited after a trigger event. With TRIGGER MODE RETRIG selected, each new trigger signal restarts the PHS signal generation. This restart can be inhibited for the entered number of symbols. Example: The entry of 1000 symbols causes new trigger signals to be ignored for the duration of 1000 symbols after a trigger event IEC/IEEE-bus `:SOUR:PHS:TRIG:INH 1000`
	TRIGGER OUT 2 DELAY	Input value of delay of trigger signal at TRIGOUT 2 output compared with beginning of frame. IEC/IEEE-bus `:SOUR:PHS:TRIG:OUTP:DEL 2`
	TRIGGER OUT2 PERIOD	Input value of output signal period at TRIGOUT 2 output given in frames. IEC/IEEE-bus `:SOUR:PHS:TRIG:OUTP:PER 1`

CLOCK... Opens a window for selecting the clock source and for setting a delay.


FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.5 dBm
PHS
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
STATE
MODULATION.
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
DN
—1
2
SYNC

CLOCK SOURCE INT EXT
MODE SYMBOL BIT
DELAY 0.00 Symb

Fig. 2-72 Menu DIGITAL STD - PHS - CLOCK..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(CLOCK...) **CLOCK SOURCE** Selection of clock source.

INT SMIQ uses internally generated clock signals.

EXT An external symbol clock or bit clock is fed in at connectors SYMBOL CLOCK or BIT CLOCK. The clock synthesizer on the modulation coder is synchronized to this clock.
The symbol rate has to be set with an accuracy of ± 1 %.
The polarity, the trigger threshold and the input resistance of the clock inputs can be modified in menu DIGITAL MOD - EXT INPUTS.

IEC/IEEE-bus command `:SOUR:PHS:CLOC:SOUR INT`

MODE Selection of clock for external clock signal.

SYMBOL The external clock has to be a symbol clock.

BIT The external clock has to be a bit clock.

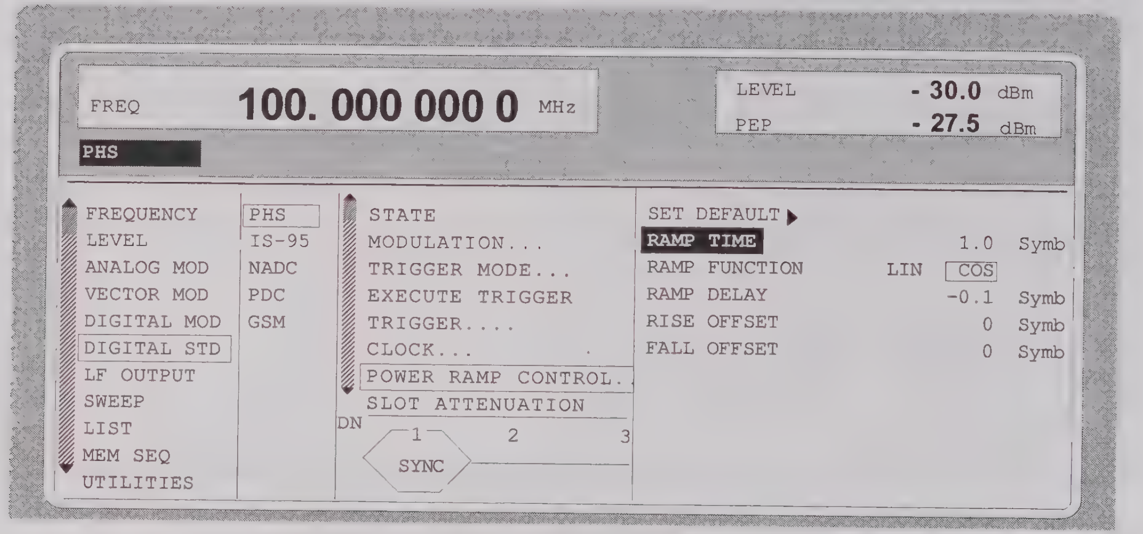
IEC/IEEE-bus command `:SOUR:PHS:CLOC:MODE SYMB`

DELAY Setting the delay of generated modulation signal to an external clock.
This can be used, for example, for synchronization with a second unit to achieve time synchronization between the modulation signals of the two units.

IEC/IEEE-bus command `:SOUR:PHS:CLOC:DEL 0.5`

POWER RAMP CONTROL...

Opens a window for setting the envelope control, especially for the rising and falling ramp at the beginning and end of a slot.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.5 dBm

PHS

SET DEFAULT ▶

RAMP TIME 1.0 Symb

RAMP FUNCTION LIN COS

RAMP DELAY -0.1 Symb

RISE OFFSET 0 Symb

FALL OFFSET 0 Symb

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
STATE
MODULATION...
TRIGGER MODE...
EXECUTE TRIGGER
TRIGGER....
CLOCK...
POWER RAMP CONTROL.
SLOT ATTENUATION
DN
1
2
3
SYNC

Fig. 2-73 Menu DIGITAL STD - PHS - POWER RAMP CONTROL... , SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(POWER RAMP CONTROL...)

SET DEFAULT ▶

Resets the subsequent parameters to the factory-set values.

IEC/IEEE-bus `:SOUR:PHS:PRAM:PRES`

RAMP TIME

Input value for the rise and fall time of the envelope at the beginning or end of a slot. The time is set in units of symbol period.

IEC/IEEE-bus `:SOUR:PHS:PRAM:TIME 0.25`

RAMP FUNCTION

Selection of shape of rising and falling ramp for envelope control.

LIN	Linear ramp function.
COS	Cosine function. A more favourable spectrum than that of the LIN function is obtained.

IEC/IEEE-bus `:SOUR:PHS:PRAM:SHAP LIN`

RAMP DELAY

Input value for a shift of the envelope characteristic to the modulated signal. A positive value causes a delay of the envelope. The values are set in the units of the symbol length.

IEC/IEEE-bus `:SOUR:PHS:PRAM:DEL 0.1`

RISE OFFSET

Input value for a positive or negative offset of the rising ramp of the envelope at the beginning of a slot.

IEC/IEEE-bus `:SOUR:PHS:PRAM:ROFF -1`

FALL OFFSET

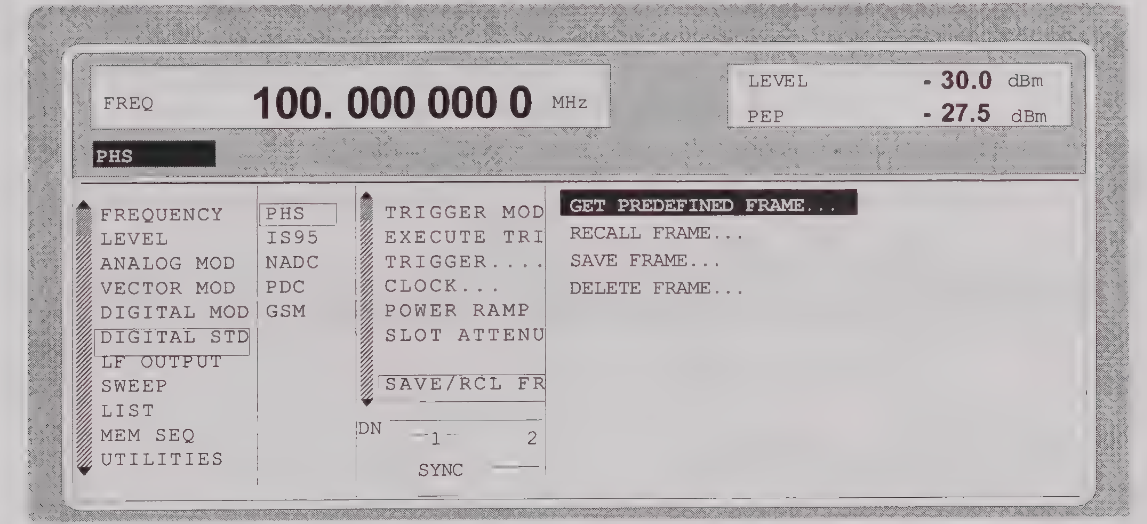
Input value for a positive or negative offset of the falling ramp of the envelope at the end of a slot.

IEC/IEEE-bus `:SOUR:PHS:PRAM:FOFF 1`

SLOT ATTENUATION Input value for the level reduction in dB of all active slots whose SLOT LEVEL was set to ATTEN. Menu SELECT SLOT allows the slots to be determined whose level is to be reduced.

IEC/IEEE-bus command `:SOUR:PHS:SLOT:ATT 40 DB`

SAVE/RCL FRAME... Opens a window for saving and loading a frame configuration. Loading a frame affects all parameters that can be set under SELECT SLOT.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.5 dBm

PHS
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
TRIGGER MOD
EXECUTE TRI
TRIGGER....
CLOCK...
POWER RAMP
SLOT ATTENU
SAVE/RCL FR
DN
1
2
SYNC
GET PREDEFINED FRAME...
RECALL FRAME...
SAVE FRAME...
DELETE FRAME...

Fig. 2-74 Menu DIGITAL STD - PHS - SAVE/RCL FRAME, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SAVE/RCL FRAME...)

GET PREDEFINED FRAME... Loads a factory-set frame configuration.

DN1_TCH	Downlink Traffic Channel in slot1
UP1_TCH	Uplink Traffic Channel in slot1
DN1_SYNC	Downlink Sync Channel in slot1

IEEE `:SOUR:PHS:FLIS:PRED:LOAD "DN1_TCH"`

RECALL FRAME... Loads a frame configuration saved by the user.

IEC/IEEE-bus `:SOUR:PHS:FLIS:LOAD "name"`

SAVE FRAME... Saves a user-defined frame configuration.

IEC/IEEE-bus `:SOUR:PHS:FLIS:STOR "name"`

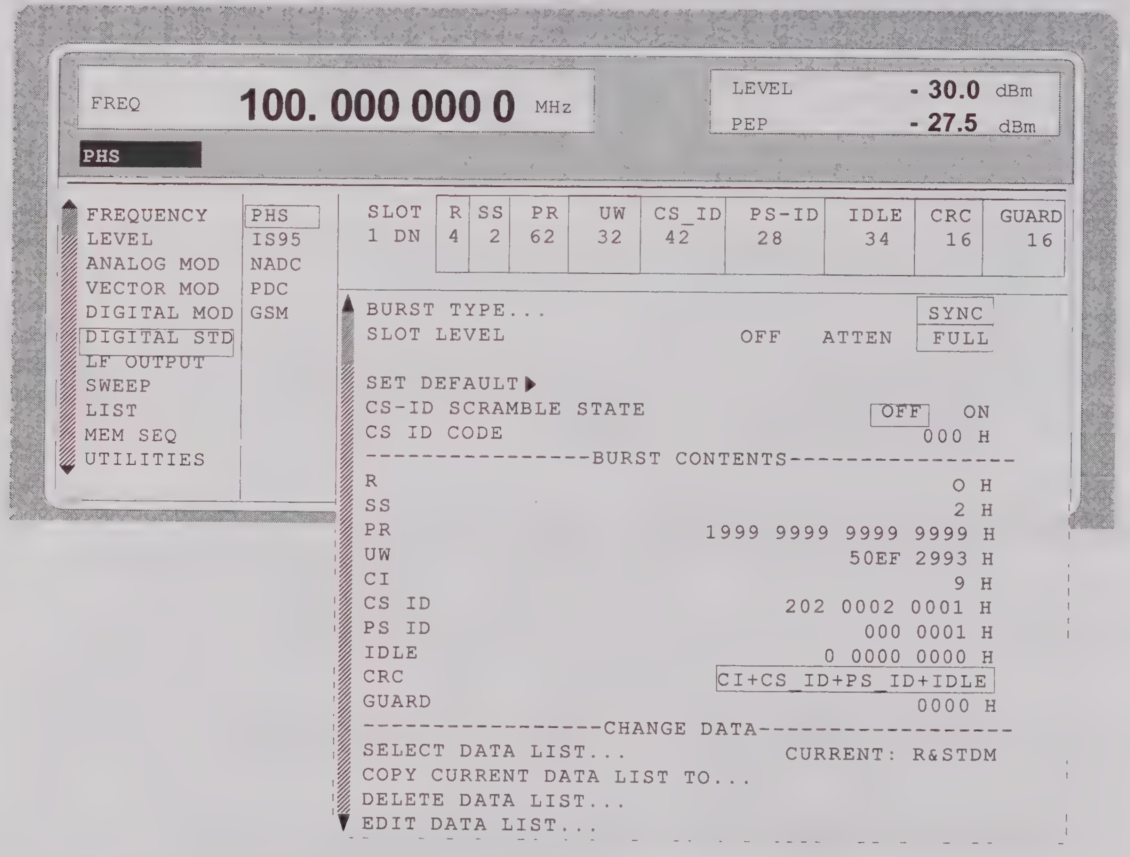
DELETE FRAME... Deletes a frame configuration saved by the user.

IEC/IEEE-bus `:SOUR:PHS:FLIS:DEL "name"`

SELECT SLOT... Selection of one of 8 possible slots. When selecting the slot, a window is opened in which the data contents belonging to this slot can be defined.
4 slots are available for uplink and downlink. They are designated as UP<i> and DN<i> in the display. For remote control, the slots are numbered from 1 to 8. The following assignment applies:

Slot	DN1	DN2	DN3	DN4	UP1	UP2	UP3	UP4
Slot number	1	2	3	4	5	6	7	8

If the cursor is placed onto a slot in the diagram, it may be switched on and off by pressing one of the unit keys (toggle function).


PHS
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.5 dBm

SLOT	R	SS	PR	UW	CS_ID	PS-ID	IDLE	CRC	GUARD
1 DN	4	2	62	32	42	28	34	16	16

```
BURST TYPE...                                   SYNC
SLOT LEVEL                   OFF    ATTEN       FULL

SET DEFAULT▶
CS-ID SCRAMBLE STATE                          OFF   ON
CS ID CODE                                       000 H
----------------BURST CONTENTS----------------
R                                                  0 H
SS                                                 2 H
PR                           1999 9999 9999 9999 H
UW                                         50EF 2993 H
CI                                                 9 H
CS ID                                  202 0002 0001 H
PS ID                                      000 0001 H
IDLE                                     0 0000 0000 H
CRC                            CI+CS_ID+PS_ID+IDLE
GUARD                                           0000 H
-----------------CHANGE DATA-----------------
SELECT DATA LIST...               CURRENT: R&STDM
COPY CURRENT DATA LIST TO...
DELETE DATA LIST...
EDIT DATA LIST...
```

Fig. 2-75 Menu DIGITAL STD - PHS - SELECT SLOT, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT)

BURST TYPE... Opens a window for the selection of the burst type used to configure the selected slot.

TCH_FULL Traffic channel configuration (rate 32 kbit/s)
IEEE-bus `:SOUR:PHS:SLOT2:TYPE TCHF`

TCH_HALF Traffic channel configuration (rate 16 kbit/s)
IEEE-bus `:SOUR:PHS:SLOT2:TYPE TCHH`

VOX VOX configuration. In slots 4 to 8 (uplink) transmission is only in every fourth frame.
IEEE-bus `:SOUR:PHS:SLOT2:TYPE VOX`

SYNC Sync burst
IEEE-bus `:SOUR:PHS:SLOT2:TYPE SYNC`

ALL DATA Burst type for testing with freely programmable data contents
IEEE-bus `SOUR:PHS:SLOT2:TYPE ADAT`

SLOT LEVEL Selection of level for selected slot.

OFF Maximum attenuation
IEEE bus `:SOUR:PHS:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.
IEEE bus `:SOUR:PHS:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.
IEEE bus `:SOUR:PHS:SLOT2:LEV ATT`

SET DEFAULT ▶ Resets the subsequent parameters to the factory-set values.
IEC/IEEE-bus command `:SOUR:PHS:SLOT8:PRES`

CS-ID SCRAMBLE STATE Switch on/off of CS-ID scrambling function.
IEC/IEEE-bus `:SOUR:PHS:SLOT2:SCR:STAT ON`

CS-ID CODE Input value in hexadecimal form for initializing the scramble generator.
IEEE-bus `:SOUR:PHS:SLOT2:SCR:CODE #H123`

ENCRYPTION STATE Switch on/off of encryption scrambling function. This parameter is only available for TCH burst types.
IEC/IEEE-bus `:SOUR:PHS:SLOT2:ENCR:STAT ON`

SECURITY KEY Input value in hexadecimal form for initializing the scramble generator. This parameter is only available for TCHburst types.
IEEE-bus `:SOUR:PHS:SLOT2:ENCR:KEY #H1234`

(SELECT SLOT)

R Display of data contents in the 4-bit data field "Ramp".

SS Display of data contents in the 2-bit data field "Start Symbol".

PR Display of "Preamble".

UW Input value for the "Unique Word" in hexadecimal form. The length of the hexadecimal value depends on the slot type (16 | 32 bit).

IEC/IEEE-bus `:SOUR:PHS:SLOT2:UWOR #H3D4C`

CI Display of the "Channel Identifier" data field in hexadecimal form.

SA Selection of data source for SACCH. This data field is only displayed for burst types TCH and VOX.

PN.. PRBS data according to CCITT V52 or Rec. 0.151 with periods between 2^9-1 and 2^{23}-1.
IEEE `:SOUR:PHS:SLOT3:SACCH PN9`

DLIST Data from a programmable data list.
IEEE `:SOUR:PHS:SLOT3:SACCH DLIS`

SERDATA Data from data input SER DATA
IEEE `:SOUR:PHS:SLOT3:SACCH SDAT`

TCH / VOX Selection of data source for TCH data field. This data field is only displayed for burst types TCH and VOX.

PN.. PRBS data according to CCITT V52 or Rec. 0.151 with periods between 2^9-1 and 2^{23}-1.
IEEE `:SOUR:PHS:SLOT3:TCH PN9`

DLIST Data from a programmable data list.
IEEE `:SOUR:PHS:SLOT3:TCH DLIS`

SERDATA Data from data input SER DATA
IEEE `:SOUR:PHS:SLOT3:TCH SDAT`

CS-ID Input value for the "Cell Station ID Code" field in hexadecimal form. This data field is only displayed for the burst type SYNC.

IEEE-bus `:SOUR:PHS:SLOT2:CSID #H20200020001`

(SELECT SLOT)

PS-ID Input value for the "Personal Station ID Code" field in hexadecimal form. This data field is only displayed for the burst type SYNC.

IEEE-bus `:SOUR:PHS:SLOT2:PSID #H0000001`

IDLE Display of data contents in the "Idle" field. This field is only displayed for the burst type SYNC.

CRC Indication of data fields for calculating the CRC code.

GUARD Display of data contents in the "Guard" field in hexadecimal form.

SELECT DATA LIST... Opens a window for selecting a stored data list or for generating a new list.

COPY CURRENT DATA LIST TO... Stores the current data list under a different name.

DELETE DATA LIST... Deletes a data list.

EDIT DATA LIST... Opens a window for editing a data list bit-by-bit. The available storage capacity and the length of the current list is displayed in parameters FREE and LEN (see also Section List Editor).

COPY	Copies a list range
FILL	Fills the range with filler pattern
INSERT	Inserts a list range at a different position of the list
DELETE	Deletes a list range
EDIT/VIEW	Edits or views the list

2.12 Digital Standard IS-95 CDMA

With the options Modulation Coder (SMIQB20), Data Generator (SMIQB11) and option Digital Standard CDMA (SMIQB42) provided, CDMA signals can be generated according to standard IS-95[1] as well as J-STD-008.

SMIQ can simulate both the transmit signal of a base station (forward link) and the transmit signal of a mobile station (reverse link). The forward link signal consists of up to 64 code channels. A reverse link signal can be generated in the full-rate or half-rate mode.

Simple bit patterns or pseudo random bit sequences (PRBS) can be selected as modulation data for forward link. Every code channel has a different PRBS. Modulation data are not subject to channel coding (convolution coding, interleaving).

The following figure shows the schematic of forward link signal generation.

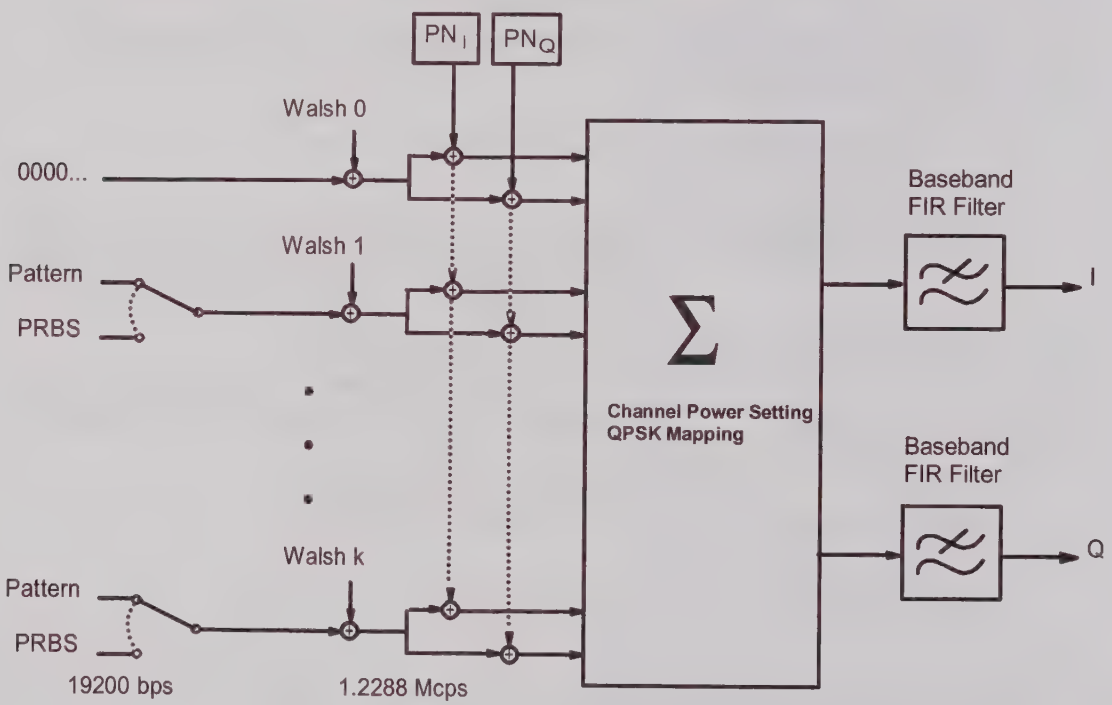

PN_I
PN_Q
Walsh 0
0000...
Baseband FIR Filter
Walsh 1
Pattern
PRBS
I
Σ
Channel Power Setting
QPSK Mapping
Baseband FIR Filter
Q
Walsh k
Pattern
PRBS
19200 bps
1.2288 Mcps

Fig. 2-76 Forward link signal generation

At a chip rate of 1.2288 Mcps, the modulation data rate for forward link is 19200 bp/s for all channels. The modulation type is QPSK. For baseband filtering, a FIR filter with equalizer is preset according to IS-95 (FILTER TYPE IS-95 EQUAL). Other filters can be set as well. The chip rate is preset to 1.2288 Mcps according to IS-95 but can be freely selected.

---

[1] TIA/EIA/IS-95, Mobile Station-Base Station Compatibility Standard for Dual-Mode Wideband Spread Spectrum Cellular Systems

To generate a reverse link signal, two different operating modes are available. The following figure shows the schematic of reverse link signal generation without channel coding.

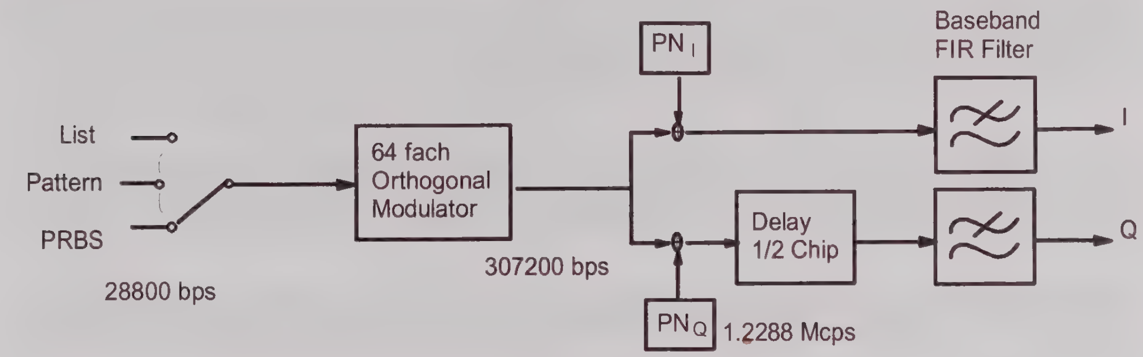

List
Pattern
PRBS
28800 bps
64 fach Orthogonal Modulator
307200 bps
PN I
PN Q
1.2288 Mcps
Delay 1/2 Chip
Baseband FIR Filter
I
Q

Fig. 2-77 Reverse link signal generation without channel coding

Simple bit patterns, PRBS and freely programmable data sequences in a data list are available as modulation data. At a chip rate of 1.2288 MHz, the modulation data rate of the reverse link is 28800 bps/s. The modulation type is offset-QPSK. A FIR filter is preset as baseband filter without equalizer according to IS-95 (FILTER TYPE IS-95).

The second reverse link mode also comprises channel coding. With this reverse link coded mode, both an access channel (4800 bps) and a traffic channel can be generated with all the data rates between 1200 bps and max. 14400 bps complying with IS-95 (Rate Set 1 and 2). The following figure gives an example of signal generation for the traffic channel with 9600 bps and shows the associated frame structure.

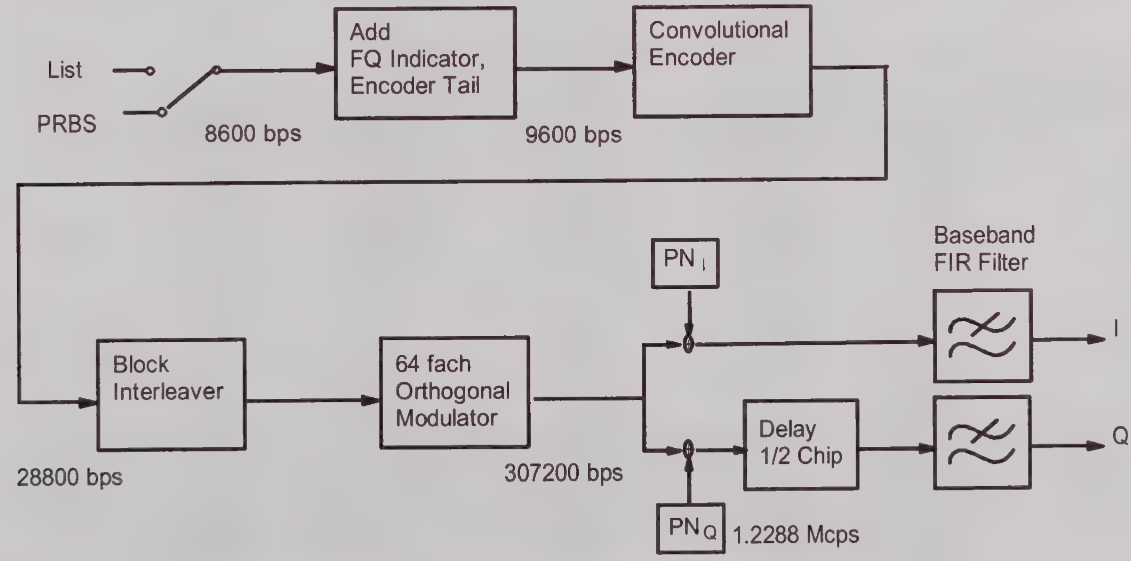

List
PRBS
8600 bps
Add FQ Indicator, Encoder Tail
9600 bps
Convolutional Encoder
Block Interleaver
28800 bps
64 fach Orthogonal Modulator
307200 bps
PN I
PN Q
1.2288 Mcps
Delay 1/2 Chip
Baseband FIR Filter
I
Q

Fig. 2-78 Traffic channel 9600 in "Reverse Link Coded" mode

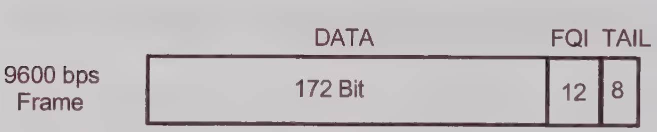


Fig. 2-79 Frame structure of traffic channel 9600 in "Reverse Link Coded" mode

PRBS of different lengths and a list of freely programmable data sequences are available as modulation data. In the reverse link coded mode, MIQ generates a test signal for BER and FER easurements at a base station receiver. A special ervice mode is required for the DUT since the long code is set to 0 in the SMIQ test signal.

To facilitate operation, the following tables show the frequency channels according to regulations IS-95 and J-STD-008. The tables are also available on the SMIQ display via the HELP function for IS-95 menu selection.

Table 2-14 CDMA: channel numbers and their frequencies

IS-95	**channel number**	**generator frequency /MHz**
Mobile station	$1 \le N \le 777$ $1013 \le N \le 1023$	0.03N + 825.0 0.03(N-1023)+825.0
Base station	$1 \le N \le 777$ $1013 \le N \le 1023$	0.03N + 870.0 0.03(N-1023)+870.0

J-STD-008	**channel number**	**generator frequency/MHz**
Mobile station	$1 \le N \le 1199$	0.050N + 1850.000
Base station	$1 \le N \le 1199$	0.050N + 1930.000

Table 2-15 Preferred CDMA-frequency channels according to J-STD-008

channel number	**generator-frequency/MHz**	
	mobile station	**base station**
25	1851.25	1931.25
50	1852.50	1932.50
75	1853.75	1933.75
100	1855.00	1935.00
125	1856.25	1936.25
150	1857.50	1937.50
175	1858.75	1938.75
200	1860.00	1940.00
225	1861.25	1941.25
250	1862.50	1942.50
275	1863.75	1943.75
325	1866.25	1946.25
350	1867.50	1947.50
375	1868.75	1948.75
425	1871.25	1951.25
450	1872.50	1952.50
475	1873.75	1953.75
500	1875.00	1955.00
525	1876.25	1956.25
550	1877.50	1957.50
575	1878.75	1958.75

channel number	**generator-frequency/MHz**	
	mobile station	**base station**
600	1880.00	1960.00
625	1881.25	1961.25
650	1882.50	1962.50
675	1883.75	1963.75
725	1886.25	1966.25
750	1887.50	1967.50
775	1888.75	1968.75
825	1891.25	1971.25
850	1892.50	1972.50
875	1893.75	1973.75
925	1896.25	1976.25
950	1897.50	1977.50
975	1898.75	1978.75
1000	1900.00	1980.00
1025	1901.25	1981.25
1050	1902.50	1982.50
1075	1903.75	1983.75
1100	1905.00	1985.00
1125	1906.25	1986.25
1150	1907.50	1997.50
1175	1908.75	1988.75

2.12.1 Sync and Trigger Signals

A CDMA sequence with a length of 98304 chips is calculated for the generation of forward link CDMA signals and stored in the memory of the data generator (option SMIQB11). This chip sequence can be run repetitively (TRIGGER MODE AUTO). During reverse link signal generation with channel coding, the modulation data are continuously processed in real time.
Trigger signals can be used for synchronized measurements on receivers.

A trigger signal can be fed via the TRIGIN input at connector PAR DATA. The chip sequence either starts immediately after the active slope of the trigger signal or after a settable number of chips (TRIGGER DELAY). Retriggering (RETRIG) can be inhibited for a settable number of chips (TRIGGER INHIBIT).

A trigger event can be executed manually or via the IEC/IEEE bus using EXECUTE TRIGGER. When a trigger event is executed, a trigger signal is output at the TRIGOUT 3 output of SMIQ.

SMIQ also generates the following sync signals:

- a 20-ms frame clock (traffic channel frame clock)
- a 80/3-ms clock (short sequence rollover)
- a 80-ms clock (super frame clock))
- a 2-s clock (even second clock)
- a PCG clock in reverse link at half rate, 1/4 rate and 1/8 rate

SMIQ can output two of the four signals via pins TRIGOUT 1 and 2 of connector PAR DATA.

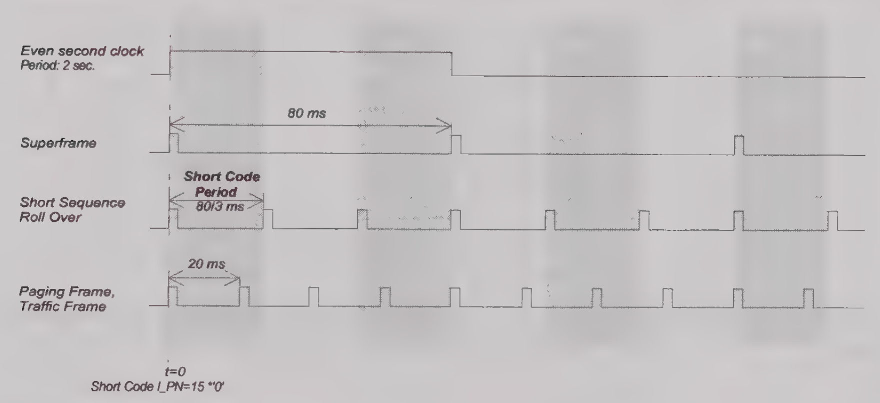


Fig. 2-80 CDMA sync signals

A clock synthesizer on the modulation coder generates the chip clock and a multifold chip clock in the SMIQ. All the clock signals are synchronized to the 10-MHz reference of the SMIQ. The chip clock is available at connector SYMBOL CLOCK and the multifold chip clock at connector BIT CLOCK. If required, the clock synthesizer in the SMIQ can be synchronized to an external chip clock which is fed in at connector SYMBOL CLOCK.

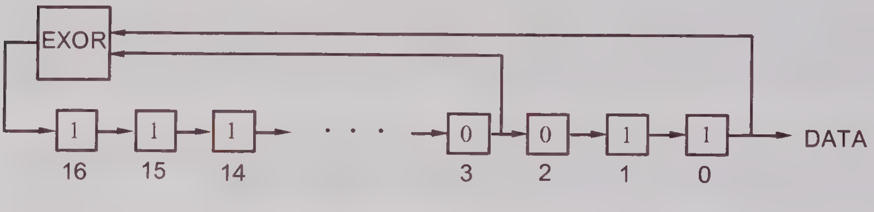
The clock signal is selected in the menu via CLOCK-CLOCK SOURCE EXT.

To allow for a trouble-free synchronization of the clock synthesizer first apply the external clock and set the correct symbol rate at SMIQ. Then switch CLOCK SOURCE from INT to EXT.

Note: *The set symbol rate should not differ by more than 1% from the symbol rate of the external signal.*

2.12.2 PRBS Data Source in Forward Link

A PN generator is used as PRBS data source for forward link modes. This PN generator provides a pseudo random bit sequence with a period of 2^{17}-1. The PRBS data sequence is a so-called sequence of maximum length that is generated by means of a feedback shift register. The following schematic shows the PN generator with feedback to registers 3 and 0 (output). The data of the start value are entered into the register.



The start value of the PN generator is `1100 0100 1110 0011 1`
The bit sequence after the start thus is:

```
         1100 0100 1110 0011 1111 0001 1111 1110...
     ... 0011 1111 0000 0111 1110 0011 1001 1100...
     ... 0111 1111 1011 1111 1100 0001 0010 0000...
     ... 1110 0100 0001 0011 1110 0010 0100 0110.........
```

The generated bits are assigned to the modulation data of the different forward link modes as follows:

Mode FWD_LINK_18

The PN bits are assigned to the different code channels as follows:

Modulation data bit 0:

```
1100 0100 1110 0011 1111 0001 1111 1110 ...
↑                                     ↑
c31, ch30, .........................ch0 ...
```

Modulation data bit 1:

```
0011 1111 0000 0111 1110 0011 1001 1100 ...
↑                                     ↑
ch31, ch30, ........................ch0 ...
```

etc.

The data bits of the unused code channels c18 to c31 and those of the deactivated code channels are discarded, ie not used.

Modus FWD_LINK_64

The PN bits are assigned to the different code channels as follows:

Modulation data bit 0:

```
11000100111000111111000111111110 00111111000001111110001110011100 ...
↑                              ↑ ↑                              ↑
c31, ch30, ..................ch0,ch63, ch62, ................ch32 ...
```

Modulation data bit 1:

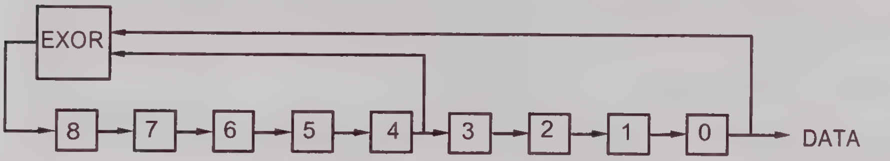
```
01111111101111111100000100100000.11100100000100111110001001000110 ...
↑                              ↑ ↑                              ↑
c31, ch30, ..................ch0,ch63, ch62, ................ch32 ...
```

etc.

The data bits of the deactivated code channels are discarded, ie not used.

2.12.3 PN Generators as Internal Data Source for Reverse Link

Different PN (Pseudo Noise) generators can be selected as data source for modulation data in the two reverse link modes. These PN generators provide pseudo random bit sequences of different lengths or periods which is why they are also called PRBS generators (Pseudo Random Binary Sequence).
The data sequences are so-called sequences of maximum lengths that are generated by means of feedback shift registers.
The following schematic shows the 9 bit generator with feedback to registers 4 and 0 (output).



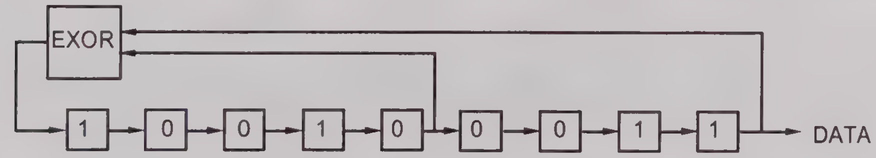
The pseudo random sequence of a PRBS generator is determined by the number of registers and the feedback. The following table describes all available PRBS generators:

Table 2-16 PN generators for IS-95 reverse link

PRBS generator	**Length in bits**	**Feedback to**	**Start value**
9 bits	$2^9 -1 = 511$	Register 4, 0	1100 0100 1
11 bits	$2^{11} -1 =2047$	Register 2, 0	1100 0100 111
15 bits	$2^{15} -1 = 32767$	Register 1, 0	1100 0100 1110 001
16 bits	$2^{16} -1 = 65535$	Register 5, 3, 2, 0	1100 0100 1110 0011
20 bits	$2^{20} -1 = 1048575$	Register 3, 0	1100 0100 1110 0011 1111
21 bits	$2^{21} -1 = 2097151$	Register 2, 0	1100 0100 1110 0011 1111 0
23 bits	$2^{23} -1 = 8388607$	Register 5, 0	1100 0100 1110 0011 1111 000

PN generators PN9,11,15,20 and PN23 are designed according to CCITT Rec. 0.151/152/153. The output sequence is inverted for generators PN15 and PN23.

Example: PN9 generator in slot 1 with start value 110001001 (binary)

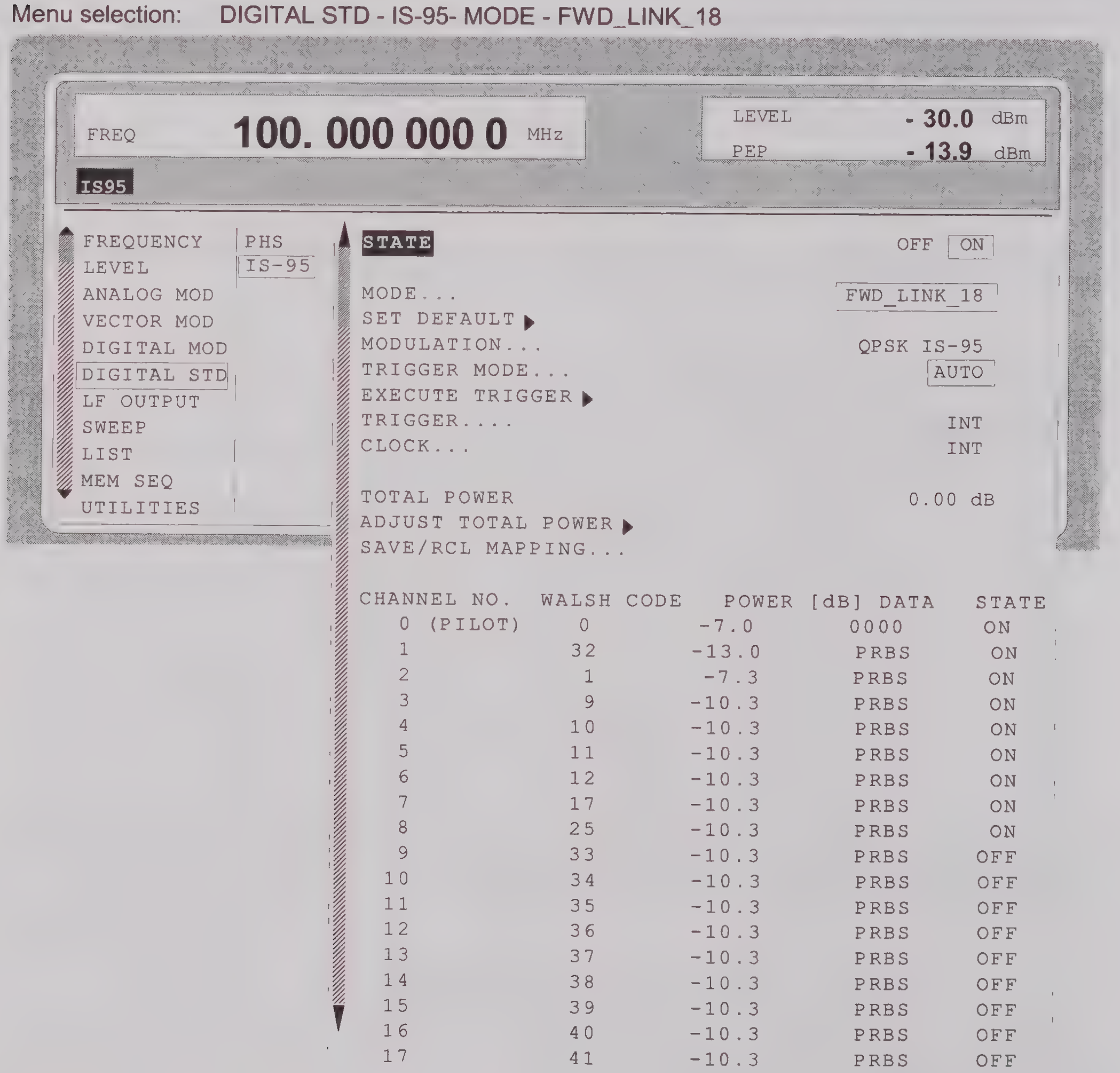


The resulting output sequence is `11000100110001000100 0` etc.

2.12.4 Menu IS-95 CDMA Standard - Forward Link Signal

Menu DIGITAL STD - IS-95 provides access to settings for IS-95 CDMA signal generation. The following figure shows the menu for generating the forward link signal (transmit signal of base station) in the FWD_LINK_18 mode. The menus for generating reverse link signals are described in the following section. Parameters that are identical for both modes are explained in the menu for forward link.

Menu selection: DIGITAL STD - IS-95- MODE - FWD_LINK_18



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 13.9 dBm

STATE OFF ON

MODE... FWD_LINK_18

SET DEFAULT ▶

MODULATION... QPSK IS-95

TRIGGER MODE... AUTO

EXECUTE TRIGGER ▶

TRIGGER.... INT

CLOCK... INT

TOTAL POWER 0.00 dB

ADJUST TOTAL POWER ▶

SAVE/RCL MAPPING...

CHANNEL NO.	WALSH CODE	POWER [dB]	DATA	STATE
0 (PILOT)	0	-7.0	0000	ON
1	32	-13.0	PRBS	ON
2	1	-7.3	PRBS	ON
3	9	-10.3	PRBS	ON
4	10	-10.3	PRBS	ON
5	11	-10.3	PRBS	ON
6	12	-10.3	PRBS	ON
7	17	-10.3	PRBS	ON
8	25	-10.3	PRBS	ON
9	33	-10.3	PRBS	OFF
10	34	-10.3	PRBS	OFF
11	35	-10.3	PRBS	OFF
12	36	-10.3	PRBS	OFF
13	37	-10.3	PRBS	OFF
14	38	-10.3	PRBS	OFF
15	39	-10.3	PRBS	OFF
16	40	-10.3	PRBS	OFF
17	41	-10.3	PRBS	OFF

Fig. 2-81 Menu DIGITAL STD - IS-95 - MODE - FWD_LINK_18, equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB42

STATE Switch on/off of modulation Digital Standard IS-95 CDMA. Vector modulation and digital modulation will be switched off automatically.

IEC/IEEE-bus command `SOUR:IS95:STAT ON`

MODE...

Selection between the different modes for generating a forward link signal or reverse link signals. The REV_LINK modes will be described in the following sections.

FWD_LINK_18	Activates the generation of a forward link signal with up to 18 code channels (channel No. 0 to 17). The Walsh code (WALSH CODE) and the source for modulation data (DATA) can be determined individually for each channel. Each channel can be switched on/off (STATE). The relative power (POWER) of channels 0 (pilot), 1 and 2 can be freely determined in range -30 dB to 0 dB. The power setting of channel 3 also determines the power setting of channels 4 to 17. This means that channels 3 to 17 all have the same power provided when the channels are switched on. IEC/IEEE-bus `:SOUR:IS95:MODE FLIN18`
FWD_LINK_64	Activates the generation of a forward link signal with up to 64 code channels. Walsh codes 0 to 63 (WALSH CODE) are assigned to the channels. The source for modulation data (DATA) can be determined individually for each channel. Each channel can be switched on/off (STATE). The relative power (POWER) of the pilot channel (Walsh 0) can be freely determined in range -30 dB to 0 dB. The power setting of channel 1 also determines the power setting of channels 2 to 63. This means that channels 1 to 63 all have the same power provided when the channels are switched on. IEC/IEEE-bus `:SOUR:IS95:MODE FLIN64`
REV_LINK	Activates the generation of a reverse link signal without channel coding. A selection between the full-rate and half-rate mode (RATE) is possible. IEC/IEEE-bus `:SOUR:IS95:MODE RLIN`
REV_LINK_CODED	Activates the generation of a reverse link signal with channel coding. A selection between the access channel and traffic channel with different rates is possible. IEC/IEEE-bus `:SOUR:IS95:MODE RLCO`

SET DEFAULT ▶

Provides the default setting for the channel configuration of the forward link modes.

For FWD_LINK_18:

- channels 0 to 8 are switched on
- the power of the pilot channel (channel 0) makes out 20% of the total power (-7 dB).
- channel 1 is set as sync channel by WALSH CODE 32
- channel 2 is set as paging channel
- the remaining 7 channels are configured as traffic channels

  The power setting was selected according to the "Base Station Test Model" of TIA standard IS-97.

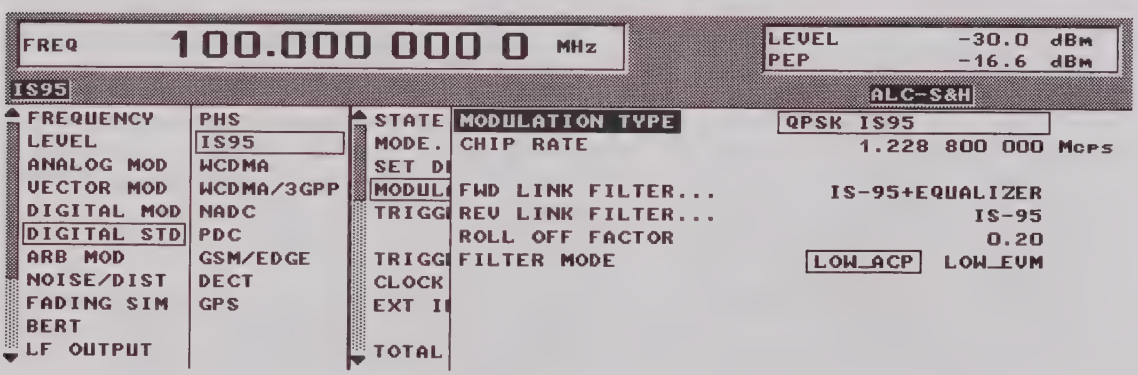
For FWD_LINK_64:

- all 64 channels are switched on
- the power of the pilot channel makes out 20% of the total power (-7 dB).
- the remaining 80% of the transmit power is distributed equally on the remaining 63 channels.

IEC/IEEE-bus command `:SOUR:IS95:PRES`

MODULATION...

Opens a window for setting the modulation parameters.



FREQ 100.000 000 0 MHz

LEVEL -30.0 dBm

PEP -16.6 dBm

MODULATION TYPE QPSK IS95

CHIP RATE 1.228 800 000 Mcps

FWD LINK FILTER... IS-95+EQUALIZER

REV LINK FILTER... IS-95

ROLL OFF FACTOR 0.20

FILTER MODE LOW_ACP LOW_EVM

IS95
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
NOISE/DIST
FADING SIM
BERT
LF OUTPUT
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
GPS
STATE
MODE.
SET DI
MODUL
TRIGG
TRIGG
CLOCK
EXT I
TOTAL

Fig. 2-82 Menu DIGITAL STD - IS-95 - MODULATION..., equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB42

(MODULATION...)	**MODULATION TYPE**	Indicates the modulation type.
	CHIP RATE	Setting value of chip clock frequency. Preset to 1.2288 Mcps. IEEE-bus command `:SOUR:IS95:CRAT 1.2 MHZ`
(MODULATION...)	**FWD LINK FILTER...**	Opens a window for selecting the baseband filters for the forward link. A selection between the FIR filters defined in IS-95, the Nyquist filters COS and SQRCOS or a user-defined filter USER (cf. Section Digital Modulation) is possible. A FIR filter is preset with equalizer according to IS-95. IEEE command `:SOUR:IS95:FILT:FTYP EIS95`

REV LINK FILTER...

Opens a window for selecting the baseband filter for the reverse link. A selection between the FIR filters defined in IS-95, the Nyquist filters COS and SQRCOS or a user-defined filter USER (cf. Section Digital Modulation) is possible. A FIR filter is preset according to IS-95.

IEEE command `:SOUR:IS95:FILT:RTYP IS95`

ROLL OFF FACTOR

Setting value of roll-off factor.
The setting is only possible provided that a COS or SQRCOS filter is selected under FILTER.

IEEE-bus command `:SOUR:IS95:FILT:PAR 0.22`

FILTER MODE

Selection of filter mode.

LOW_ACP	Filter for minimum Adjacent Channel Power. IEEE `SOUR:IS95:FILT:MODE LACP`
LOW_EVM	Filter for minimum vector error. IEEE `SOUR:IS95:FILT:MODE LEVM`

TRIGGER MODE... Opens a window for selecting the CDMA sequence.

AUTO — The calculated CDMA chip sequence is cyclically repeated.

IEC/IEEE-bus command `:SOUR:IS95:SEQ AUTO`

RETRIG — The CDMA chip sequence is continuously repeated. A trigger event causes a restart from frame 1.

IEC/IEEE-bus command `:SOUR:IS95:SEQ RETR`

ARMED_AUTO — The CDMA chip sequence cannot be started from frame 1 until a trigger event has occurred. The unit then automatically switches over to the AUTO mode and can no longer be triggered.

IEC/IEEE-bus command `:SOUR:IS95:SEQ AAUT`

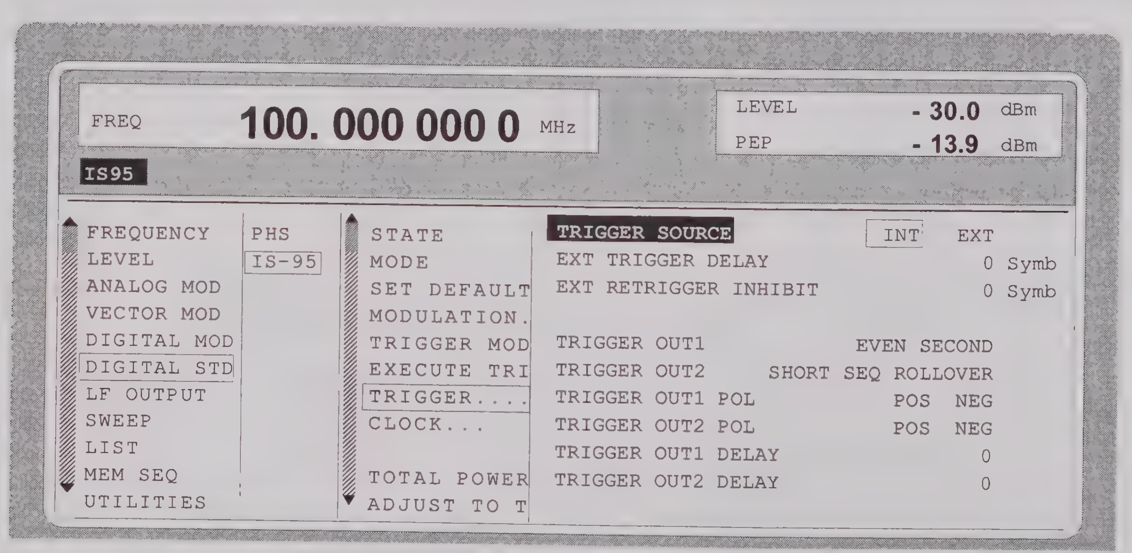
ARMED_RETRIG — The CDMA chip sequence cannot be started from frame 1 until a trigger event has occurred. each new trigger event causes a restart.

IEC/IEEE-bus command `:SOUR:IS95:SEQ ARET`

EXECUTE TRIGGER ▶ Executes a trigger event to start the CDMA chip sequence.

IEC/IEEE-bus command `:TRIG:DM:IMM`

TRIGGER... Opens a window for selecting the trigger source, for configuring the trigger output signals and for setting the time delay of an external trigger signal.


IS95
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
STATE
MODE
SET DEFAULT
MODULATION.
TRIGGER MOD
EXECUTE TRI
TRIGGER....
CLOCK...
TOTAL POWER
ADJUST TO T

```
FREQ       100. 000 000 0 MHz
LEVEL      - 30.0 dBm
PEP        - 13.9 dBm

TRIGGER SOURCE                          INT   EXT
EXT TRIGGER DELAY                             0 Symb
EXT RETRIGGER INHIBIT                         0 Symb

TRIGGER OUT1                      EVEN SECOND
TRIGGER OUT2              SHORT SEQ ROLLOVER
TRIGGER OUT1 POL                     POS   NEG
TRIGGER OUT2 POL                     POS   NEG
TRIGGER OUT1 DELAY                           0
TRIGGER OUT2 DELAY                           0
```

Fig. 2-83 Menu DIGITAL STD - IS-95 - TRIGGER..., equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB42

(TRIGGER...)

TRIGGER SOURCE

Selection of trigger source.

EXT	The CDMA chip sequence is started from frame 1 by means of the active slope of an external trigger signal. The polarity, the trigger threshold and the input resistance of the TRIGIN input can be modified in menu DIGITAL MOD - EXT INPUTS.
INT	A trigger event is manually executed by EXECUTE TRIGGER.

IEC/IEEE-bus `:SOUR:IS95:TRIG:SOUR EXT`

EXT TRIGGER DELAY

Setting the number of chips (symbols) by which an external trigger signal is delayed before it starts the CDMA chip sequence.

This is used for setting the time synchroneity between the DUT and other units.

IEC/IEEE-bus `:SOUR:IS95:TRIG:DEL 3`

EXT RETRIGGER INHIBIT

Setting the number of chips for which a restart is inhibited after a trigger event.
With TRIGGER MODE RETRIG selected, each new trigger signal restarts the CDMA chip sequence. This restart can be inhibited for the entered number of chips (symbols).

Example:
The entry of 98000 symbols, for example, causes new trigger signals to be ignored for the duration of 98000 chips after a trigger event.

IEC/IEEE-bus `:SOUR:IS95:TRIG:INH 16000`

TRIGGER OUT 1/2

Selection of signals for outputs TRIGOUT 1 and TRIGOUT 2 of connector PARDATA.
All the times indicated are valid for a chip rate of 1.2288 Mcps.

TRAFFIC FRAME	20-ms frame clock IEC `:SOUR:IS95:OUTP1 TFR`
SHORT SEQ ROLLOVER	80/3-ms clock IEC `:SOUR:IS95:OUTP1 SSR`
SUPER FRAME	80-ms clock IEC `:SOUR:IS95:OUTP1 SFR`
EVEN SECOND	2-s clock IEC `:SOUR:IS95:OUTP1 ESEC`
PCG	power control group rate IEC `:SOUR:IS95:OUTP1 GATE`

(TRIGGER...)

TRIGGER OUT 1/2 POL Selection of signal polarity at outputs TRIGOUT 1 and TRIGOUT 2 of the PARDATA connector.

IEC/IEEE-bus `:SOUR:IS95:OUTP1:POL NORM`

TRIGGER OUT 1/2 DELAY Setting the number of chips by which the selected trigger signal is delayed.

IEC/IEEE-bus `:SOUR:IS95:OUTP1:DEL -50`

CLOCK... Opens a window for selecting the clock source.
The CDMA chip clock in SMIQ corresponds to the symbol clock of the modulation. Therefore, the terms symbol clock and chip clock are synonymous.

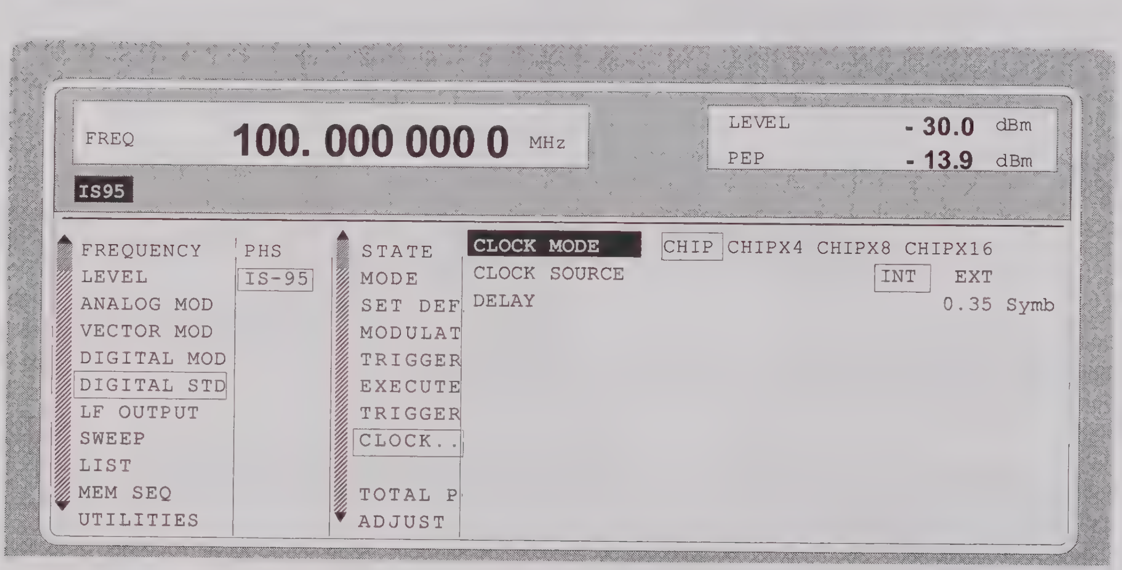

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 13.9 dBm
CLOCK MODE CHIP
CLOCK SOURCE INT
DELAY 0.35 Symb
IS95
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
STATE
MODE
SET DEF
MODULAT
TRIGGER
EXECUTE
TRIGGER
CLOCK..
TOTAL P
ADJUST
CHIP CHIPX4 CHIPX8 CHIPX16
INT EXT

Fig. 2-84 Menu DIGITAL STD - IS-95 - CLOCK..., equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB42

(CLOCK...)

CLOCK MODE Selection of the type of external clock signal.

CHIP	Chip clock
CHIPXn	Four-, eight- or sixteen-fold chip clock (n=4, 8 or 16).

IEC/IEEE-bus `:SOUR:IS95:CLOC:MODE CHIP`

(CLOCK...)

CLOCK SOURCE

Selection of clock source.

INT	SMIQ uses internally generated clock signals.
EXT	An external chip clock is fed in at connector SYMBOL CLOCK. Multifolds of the chip clock are fed in at connector BIT CLOCK . The clock synthesizer on the modulation coder is synchronized to this clock. Parameter CHIP RATE has to be set with an accuracy of ± 1 %. The polarity, the trigger threshold and the input resistance of the SYMBOL CLOCK input can be modified in menu DIGITAL MOD - EXT INPUTS.

IEC/IEEE-bus `:SOUR:IS95:CLOC:SOUR INT`

DELAY

Setting the delay of generated modulation signal to an external clock.

This can be used, for example, for synchronization with a second unit to achieve time synchroneity between the modulation signals of the two units.

The displayed setting resolution of 1/100 symbol is only attained for symbol-clock frequencies below 100 kHz.
The resolution is reduced with increasing frequency. At a chip rate of 1.2288 Mcps, the resolution equals the chip duration × 0.1.

IEC/IEEE-bus `:SOUR:IS95:CLOC:DEL 0.5`

TOTAL POWER

Display of the total power of all active code channels. The TOTAL POWER is calculated when the modulation is active (STATE = ON). It is the sum of the channel power of all active channels. If the value is not equal 0 dB then all active channel power were internally adjusted so that the total power equals 0 dB (the power relation between single code channels is not affected!) This is nessessary to keep the setted output power (LEVEL) constant. In addition to this average power (LEVEL) the peak envelope power (PEP) is also indicated in the header of the display. The value for PEP is calculated based on a worst case. The actual peak powers are mostly smaller.

IEC/IEEE-bus command `:SOUR:IS95:POW?`

ADJUST TOTAL POWER ▶

Changes the power of every code channel switched on. After this adjustment the total power is 0 dB. The power relation between single active code channels is not affected.

IEC/IEEE-bus command `:SOUR:IS95:POW:ADJ`

SAVE/RCL MAPPING...

Saves/calls up the set channel configuration.
This setting is only possible in the forward link mode.

For FWD_LINK_18, the following is saved for each channel number:
- selected WALSH CODE
- set POWER
- type of modulation data (DATA) and
- switch-on state.

For FWD_LINK_64, the following is saved for each channel number:
- set POWER
- modulation data (DATA) and
- switch-on state.

The channel configuration of the two forward-link modes is stored and loaded at the same time.

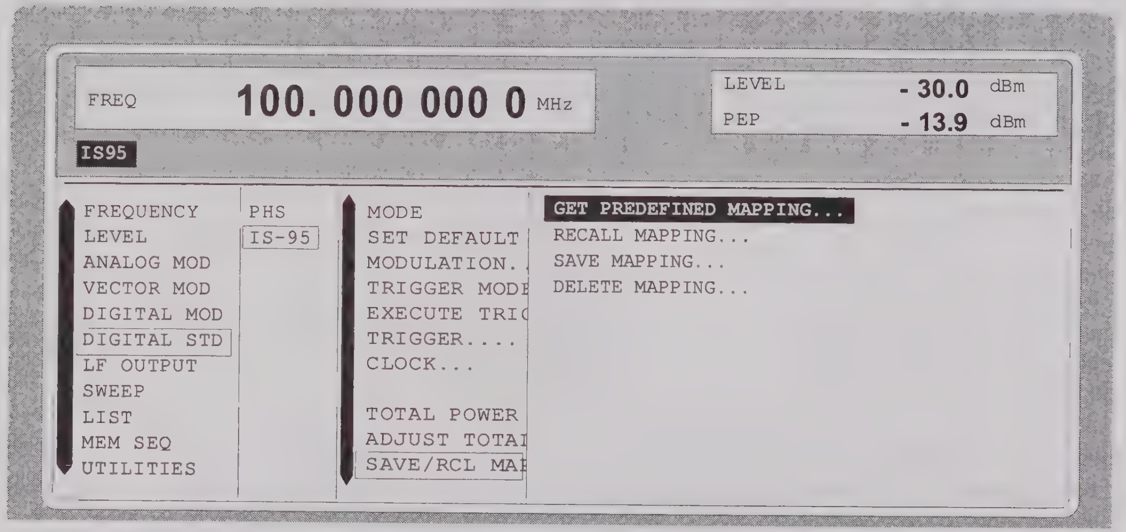

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 13.9 dBm
IS95
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
MODE
SET DEFAULT
MODULATION.
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
TOTAL POWER
ADJUST TOTAL
SAVE/RCL MAP
GET PREDEFINED MAPPING...
RECALL MAPPING...
SAVE MAPPING...
DELETE MAPPING...

Fig. 2-85 Menu DIGITAL STD - IS-95 - SAVE/RCL MAPPING..., equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB42

(SAVE/RCL MAPPING...)

GET PREDEFINED MAPPING...

Loads a factory-set channel configuration.

PILOT	A pilot signal is generated only.
09CHAN	A signal with 9-code channels is generated according to the Base Station Test Model (IS95).
18CHAN	A signal with 18-code channels is generated.
IEEE-bus	`:SOUR:IS95:MAPP:SEL:PRED "pilot"`

RECALL MAPPING...

Loads a stored channel configuration.

IEEE-bus `:SOUR:IS95:MAPP:LOAD "name"`

(SAVE/RCL MAPPING...)

SAVE MAPPING... Stores the channel configuration. For remote control, a name with 7 characters at max. can be used.

IIEEE-bus `:SOUR:IS95:MAPP:STOR "name"`

DELETE MAPPING... Deletes a stored channel configuration.

IEEE bus `:SOUR:IS95:MAPP:DEL "name"`

CHANNEL NO

Display of channel number.
This menu column is only displayed in the forward link mode.

WALSH CODE

Setting the Walsh code.
This menu column is only displayed in the forward link mode.

For FWD_LINK_18

- the Walsh code can be set individually for each channel.

  IEC/IEEE-bus command `:SOUR:IS95:CHAN1:WALS 32`

For FWD_LINK_64

- the Walsh code cannot be set. In this mode, the Walsh code corresponds to the channel number. All the 64 code channels are displayed.

POWER

Setting the power of channel.
This menu column is only displayed in the forward link mode.

POWER indicates the average power of the code channel relative to the power indicated in LEVEL display (code domain power).

For FWD_LINK_18

- the power of channels 0, 1, 2 is set individually.
- the other channels have the same power. The power for all the channels is set in channel 3.

For FWD_LINK_64

- the power of the pilot channel is set individually.
- the other channels have the same power. The power for all the channels is set in channel 1.

IEC/IEEE-bus command `:SOUR:IS95:CHAN1:POW -10 DB`

DATA

Setting the modulation data for the selected code channel.
This menu column is only displayed in the forward link mode.

The data rate for all forward link channels corresponds to the chip rate/64, ie 19200 bps at a chip rate of 1.2288 Mcps

0000 Continuous sequence of zeros

IEC/IEEE-bus command `SOUR:IS95:CHAN1:DATA ZERO`

1111 Continuous sequence of ones

IEC/IEEE-bus command `SOUR:IS95:CHAN1:DATA ONE`

1010 Alternating sequence of zeros and ones

IEC/IEEE-bus command `SOUR:IS95:CHAN1:DATA ALT`

PRBS Pseudo random data, different in every code channel

IEC/IEEE-bus command `SOUR:IS95:CHAN1:DATA PRBS`

STATE

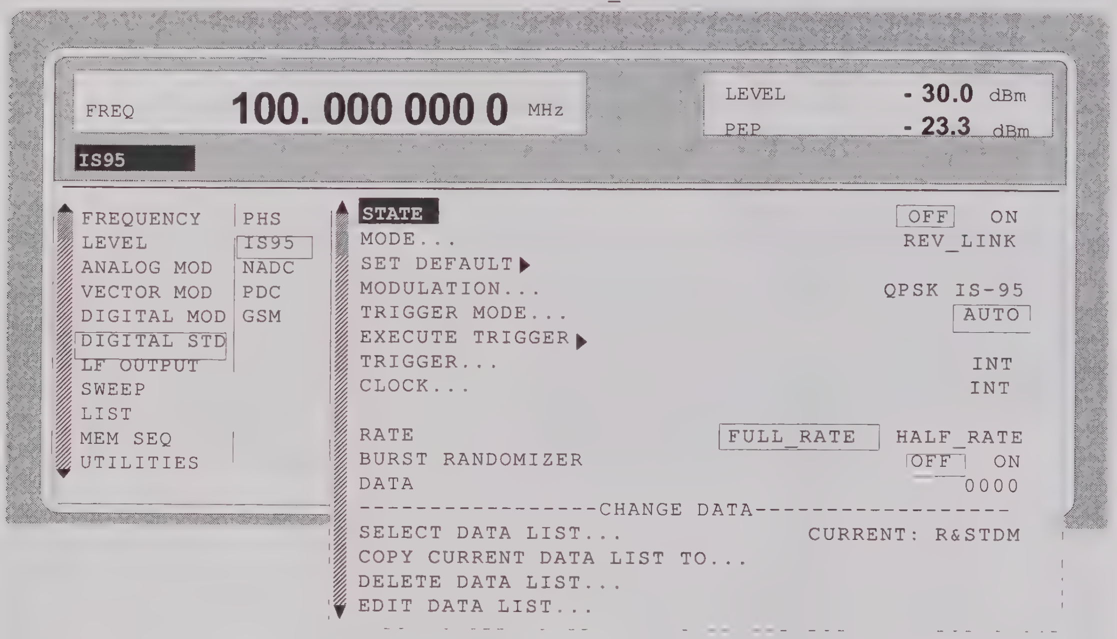
Switch on/off of assigned code channel.
This menu column is only displayed in the forward link mode.

IEC/IEEE-bus command `:SOUR:IS95:CHAN1:STAT ON`

2.12.5 Menu IS-95 CDMA Standard - Reverse Link Signal without Channel Coding

Menu DIGITAL STD - IS-95 provides access to settings for IS-95 CDMA signal generation. The following figure shows the menu for generating the reverse link signal (transmit signal of mobile station) without signal coding. The previous section showed the menu for generating the forward link signals as well as the parameters that are identical for both modes.

Menu selection: DIGITAL STD - IS-95- MODE - REV_LINK



```
FREQ   100. 000 000 0 MHz                 LEVEL   - 30.0 dBm
                                          PEP     - 23.3 dBm

STATE                                              OFF    ON
MODE...                                              REV_LINK
SET DEFAULT ▶
MODULATION...                                     QPSK IS-95
TRIGGER MODE...                                         AUTO
EXECUTE TRIGGER ▶
TRIGGER...                                               INT
CLOCK...                                                 INT

RATE                                    FULL_RATE  HALF_RATE
BURST RANDOMIZER                                   OFF    ON
DATA                                                    0000
-----------------CHANGE DATA-----------------
SELECT DATA LIST...                      CURRENT: R&STDM
COPY CURRENT DATA LIST TO...
DELETE DATA LIST...
EDIT DATA LIST...
```

Fig. 2-86 Menu DIGITAL STD - IS-95 - MODE - REV_LINK

STATE to CLOCK

see section "Menu IS-95 CDMA-Standard- Forward Link Signal"

RATE

Selection of full-rate or half-rate transmission of modulation data. This selection is only possible in the reverse link mode.
In the half-rate mode, only 8 of 16 power control groups of a frame are transmitted ("power gating").

IEC/IEEE-bus command `:SOUR:IS95:RATE HALF`

BURST RANDOMIZER

Switch on/off of burst randomizer. This selection is only possible during half-rate transmission in the reverse link mode.

OFF	Every second power control group is transmitted.
ON	A random algorithm determines which eight of the 16 power control groups of a frame will be transmitted.

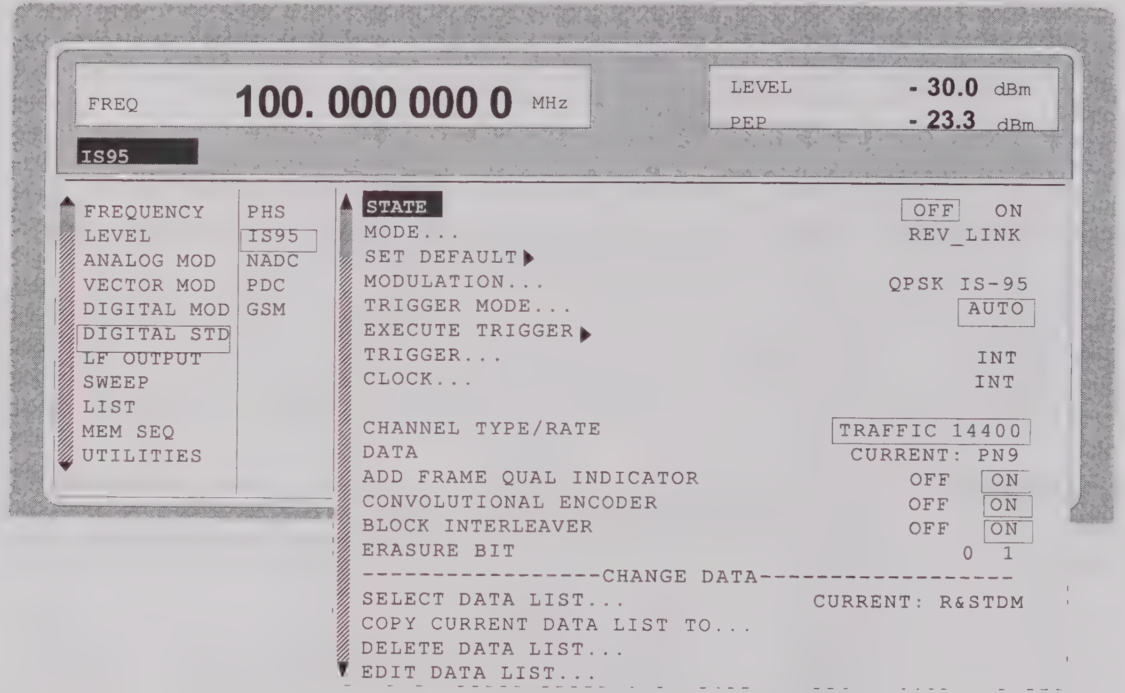
IEC/IEEE-bus command `:SOUR:IS95:RAND ON`

DATA	Selection of modulation data for the reverse link signal. The data rate corresponds to the chip rate x3/128, ie 28800 bps at 1.2288 Mcps.
	0000 Continuous sequence of zeros IEC/IEEE-bus `:SOUR:IS95:DATA ZERO`
	1111 Continuous sequence of ones IEC/IEEE-bus `:SOUR:IS95:DATA ONE`
	1010 Alternating zeros and ones IEC/IEEE-bus `:SOUR:IS95:DATA ALT`
	PRBS PRBS data. IEC/IEEE-bus `:SOUR:IS95:DATA PRBS`
	DLIST Data from a programmable data list. IEC/IEEE-bus `:SOUR:IS95:DATA DLIS`
SELECT DATA LIST...	Opens a window for selecting a stored data list or for generating a new list (only with Reverse Link or Reverse Link Coded modes).
COPY CURRENT DATA LIST TO...	Stores the current data list under a different name (only with Reverse Link or Reverse Link Coded modes).
DELETE DATA LIST...	Deletes a data list (only with Reverse Link or Reverse Link Coded modes).
EDIT DATA LIST...	Opens a window for editing a data list bit-by-bit. The available storage capacity and the length of the current list is displayed in parameters FREE and LEN (see also Section List Editor). Only with Reverse Link or Reverse Link Coded modes.
	COPY Copies a list range FILL Fills the range with filler pattern INSERT Inserts a list range at a different position of the list DELETE Deletes a list range EDIT/VIEW Edits or views the list

2.12.6 Menu IS-95 CDMA Standard - Reverse Link Signal with Channel Coding

Menu DIGITAL STD - IS-95 provides access to settings for generating IS-95 CDMA signals. The following figure shows the menu for generating a reverse link signals with channel coding. The section on the menu for forward link signal generation shows the parameters that are identical for both modes.

Menu selection: DIGITAL STD - IS-95- MODE - REV_LINK_CODED


IS95
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
SET DEFAULT
EXECUTE TRIGGER

```
FREQ      100. 000 000 0 MHz          LEVEL   - 30.0 dBm
                                      PEP     - 23.3 dBm

STATE                                            OFF   ON
MODE...                                          REV_LINK
SET DEFAULT
MODULATION...                                 QPSK IS-95
TRIGGER MODE...                                     AUTO
EXECUTE TRIGGER
TRIGGER...                                           INT
CLOCK...                                             INT

CHANNEL TYPE/RATE                          TRAFFIC 14400
DATA                                       CURRENT: PN9
ADD FRAME QUAL INDICATOR                        OFF   ON
CONVOLUTIONAL ENCODER                           OFF   ON
BLOCK INTERLEAVER                               OFF   ON
ERASURE BIT                                       0    1
-----------------CHANGE DATA------------------
SELECT DATA LIST...                     CURRENT: R&STDM
COPY CURRENT DATA LIST TO...
DELETE DATA LIST...
EDIT DATA LIST...
```

Fig. 2-87 Menu DIGITAL STD - IS-95 - MODE - REV_LINK_CODED

Parameter STATE to CLOCK... see section "Menu IS-95 CDMA Standard - Forward Link Signal".

CHANNEL TYPE/RATE

Selects the type of channel and the associated data rate. This selection is only possible in the reverse link mode. The selection also determines the structure of the channel coding and the number of data bits to be inserted into each frame.

TRAFFIC 14400	Traffic channel with 14400 bps. 267 bits are inserted into each frame
TRAFFIC 7200	Traffic channel with 7200 bps. 125 bits are inserted into each frame
TRAFFIC 3600	Traffic channel with 3600 bps. 55 bits are inserted into each frame
TRAFFIC 1800	Traffic channel with 1800 bps. 21 bits are inserted into each frame
ACCESS 4800	Access channel with 4800 bps. 88 bits are inserted into each frame
TRAFFIC 9600	Traffic channel with 9600 bps. 172 bits are inserted into each frame
TRAFFIC 4800	Traffic channel with 4800 bps. 80 bits are inserted into each frame
TRAFFIC 2400	Traffic channel with 2400 bps. 40 bits are inserted into each frame
TRAFFIC 1200	Traffic channel with 1200 bps. 16 bits are inserted into each frame
IEC/IEEE-bus	`:SOUR:IS95:RLC:CTYP TRAF7200` `:SOUR:IS95:RLC:CTYP ACC4800`

DATA SOURCE

Selects the modulation data for the reverse link signal with channel coding. The data are continuously inserted into the data field of the corresponding frame.

PN..	PRBS data to CCITT with period length between 2^9-1 and 2^{23}-1. IEC/IEEE-bus command `:SOUR:IS95:RLC:DATA PN1`
DLIST	Data from a previously programmed and stored data list. IEC/IEEE-bus command `:SOUR:IS95:RLC:DATA DLISt`

ADD FRAME QUAL INDICATOR

Switches the frame quality indicator (CRC calculation) on or off. With OFF selected, only zeros are sent instead of CRC bits.

IEC/IEEE-bus command `:SOUR:IS95:RLC:FQIN ON`

CONVOLUTIONAL ENCODER

Switches the convolutional encoder on or off. With OFF selected, the data rate is attained by repeating the symbol.

IEC/IEEE-bus command `:SOUR:IS95:RLC:CENC ON`

BLOCK INTERLEAVER

Switches the interleaver function on or off.

IEC/IEEE-bus command `:SOUR:IS95:RLC:BINT ON`

ERASURE BIT

Selects 1 or 0 for the erasure bit.

IEC/IEEE-bus command `:SOUR:IS95:RLC:EBIT 0`

Parameter **SELECT DATA LIST...** to **EDIT DATA LIST...** see section "Menu IS-95 CDMA Standard - Forward Link Signal"

2.13 Digital Standard W-CDMA (NTT DoCoMo/ARIB 0.0)

With the options Modulation Coder (SMIQB20), Data Generator (SMIQB11) and option Digital Standard W-CDMA (SMIQB43) provided, W-CDMA signals can be generated according to the Japanese experimental system NTT DoCoMo or the ARIB 0.0 standard[1].

SMIQ can simulate both the transmit signal of a base station (Downlink), and the transmit signal of a mobile station (Uplink) with up to 15 code channels.

Different physical channel types such as Perch, Common Control or Dedicated Physical Channel can be selected. For this purpose, SMIQ generates modulation data with the frame structure specified in the standard (framed data). Data fields with pilot symbols, TPC symbols (Traffic Power Control) or LMS symbols (Long Code Mask) are automatically generated. Freely programmable data lists or pseudo-random bit sequences (PRBS) can be used for the modulation symbols in the DATA fields. These modulation data are not subjected to any channel coding (convolution coding, interleaving). The user can, however, store channel-coded modulation data in a data list to generate a logic channel, eg a Dedicated Traffic Channel.

The following figure shows the schematic of forward link signal generation.

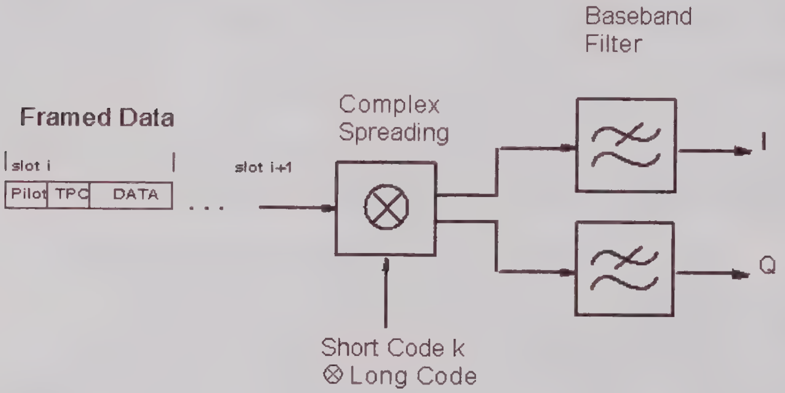


Fig. 2-88 Downlink DPCH signal generation for a code channel

The Perch Channel, DPCH (Dedicated Physical Channel) and CCPCH (Common Control Physical Channel) are available as channel types in the downlink. The data rate for modulation data (framed data) of the DPCH can be selected (32, 64, to 1024 ksymbol/s). The type of modulation is QPSK, but O-QPSK can also be set. A root cosine filter with roll-off factor 0.22 is preset for baseband filtering. Other filters can also be set (MODULATION FILTER). The chip rate is preset to 4.096 Mcps but can be modified within wide limits.

Two modes (LINK DIRECTION/MULTIPLEX) are available to generate an uplink signal. A mobile station transmitter of the Japanese W-CDMA experimental system is simulated in the UP mode. The frame structure of data corresponds to that of the downlink. DPCH and CCPCH are available as channel types.

---

[1] Association of Radio Industries and Businesses (ARIB), Specifications of Air-Interface for a 3 G Mobile System

A mobile station transmitter in line with the ARIB standard is simulated in the UP_IQ_MULT mode. Separate channel types and data sources for I and Q are available in the multiplex mode.

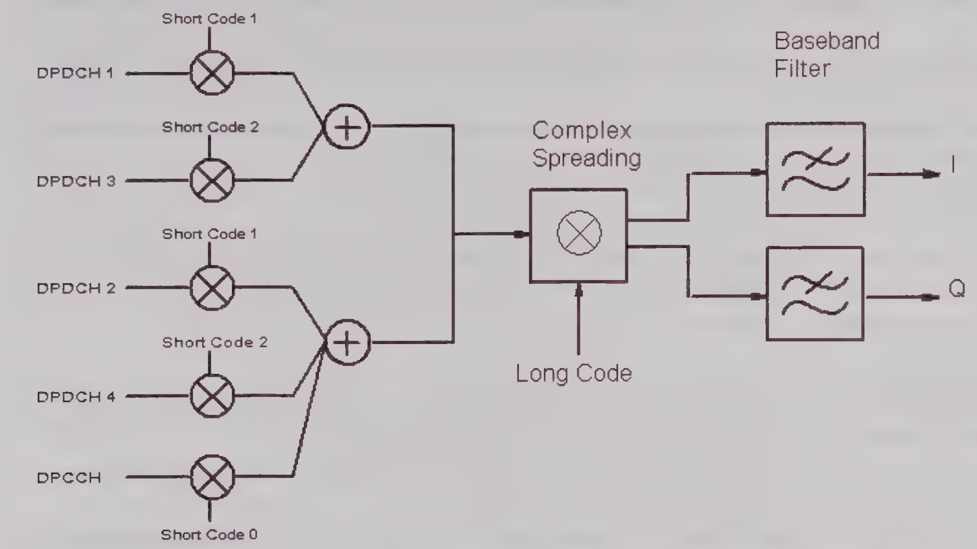

Short Code 1
DPDCH 1
Short Code 2
DPDCH 3
Short Code 1
DPDCH 2
Short Code 2
DPDCH 4
DPCCH
Short Code 0
Complex Spreading
Long Code
Baseband Filter
I
Q

Fig. 2-89 Uplink signal generation with IQ multiplex and several code channels

According to ARIB, DPDCH (Dedicated Physical Data Channel) and DCPCH (Dedicated Control Physical Channel) are available as channel types in the uplink. The data rate for modulation data (framed data) of the DPDCH can be selected (16, 32, 64, to 1024 ksymbol/s).

The modulation data are not calculated in real time in SMIQ. On the user request, a W-CDMA chip sequence is calculated (STATE = ON) and then stored in the data memory of the data generator (option SMIQ B11). The sequence length is settable (SEQUENCE LENGTH [FRAMES]), the maximum possible length depends on the memory capacity of the data generator.

A special multicode mode is available (MULTICODE) for LINK DIRECTION/MULTIPLEX DOWN and UP modes. The data fields Pilot and TPC have the same settings and are spread with the same spreading codes of the MASTER CHANNEL.

2.13.1 Sync and Trigger Signals

A chip sequence is calculated for the generation of W-CDMA signals and stored in the memory of the data generator (option SMIQB11). This chip sequence can be run repetitively (TRIGGER MODE AUTO).
Trigger signals can be used for synchronized measurements on receivers (TRIGGER MODE RETRIG, ARMED_AUTO or ARMED_RETRIG).

A trigger signal can be fed via the TRIGIN input at connector PAR DATA of SMIQ. The chip sequence either starts immediately after the active slope of the trigger signal or after a settable number of chips (EXT TRIGGER DELAY). Retriggering (RETRIG) can be inhibited for a settable number of chips (EXT RETRIGGER INHIBIT).

A trigger event can be executed manually or via the IEC/IEEE bus using EXECUTE TRIGGER.
When a trigger event is executed, a trigger signal is output at the TRIGOUT 3 output of SMIQ.

SMIQ generates the following sync signals:

- a 0.625 ms slot clock
- a 10 ms radio frame clock
- a marker signal for identifying the periodic repetition of the generated chip sequence

SMIQ can output two of the three signals via pins TRIGOUT 1 and 2 of connector PAR DATA.

A clock synthesizer on the modulation coder generates the chip clock in the SMIQ. All the clock signals are synchronized to the 10-MHz reference of the SMIQ. The chip clock is available at connector SYMBOL CLOCK. If required, the clock synthesizer in the SMIQ can be synchronized to an external chip clock which is fed in at connector SYMBOL CLOCK.

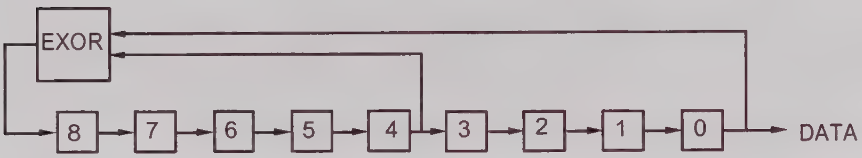
To allow for a trouble-free synchronization of the clock synthesizer first apply the external clock and set the correct chip rate at SMIQ (MODULATION - CHIPRATE VARIATION). Then switch CLOCK SOURCE from INT to EXT.

Note: *The set symbol rate should not differ by more than 1% from the symbol rate of the external signal.*

2.13.2 PN Generators as Internal Data Source

Different PN (Pseudo Noise) generators can be selected as data source for DATA fields. These PN generators provide pseudo random bit sequences of different lengths or periods which is why they are also called PRBS generators (Pseudo Random Binary Sequence).

The data sequences are so-called sequences of maximum lengths that are generated by means of feedback shift registers.
The following schematic shows the 9 bit generator with feedback to registers 4 and 0 (output).



The pseudo random sequence of a PRBS generator is determined by the number of registers and the feedback. The following table describes all available PRBS generators:

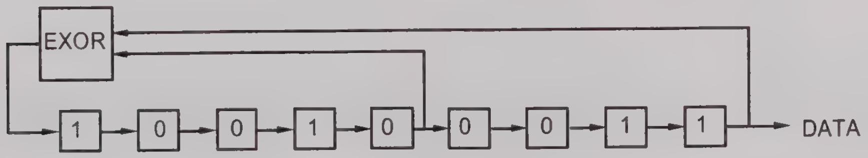
Table 2-17 PN generators for W-CDMA

PRBS generator	Length in bits	Feedback to	Start value (*)
9 bits	$2^9 -1 = 511$	Register 4, 0	Channel 0: 0000 0100 1 Channel 1: 1000 0100 1 Channel 2: 0100 0100 1 Channel 3: 1100 0100 1 ... Channel 15: 1111 0100 1
11 bits	$2^{11} -1 =2047$	Register 2, 0	Channel 0: 0000 0100 100 ... Channel 15: 1111 0100 100
15 bits	$2^{15} -1 = 32767$	Register 1, 0	Channel 0: 0000 0100 1000 000 ... Channel 15: 1111 0100 1000 000
16 bits	$2^{16} -1 = 65535$	Register 5, 3, 2, 0	Channel 0: 0000 0100 1000 0000 ... Channel 15: 1111 0100 1000 0000

*) the 5th start value bit of channels in Q phase is 1 (for example: channel 3: 1100 1100 1...)

PN generators PN9,11 and PN15 are designed according to CCITT Rec. 0.151/152/153. The output sequence is inverted for generator PN15.

Example: PN9 generator in channel 3 with start value 110001001 (binary)



The resulting output sequence is `11000100110001000100 0` etc.

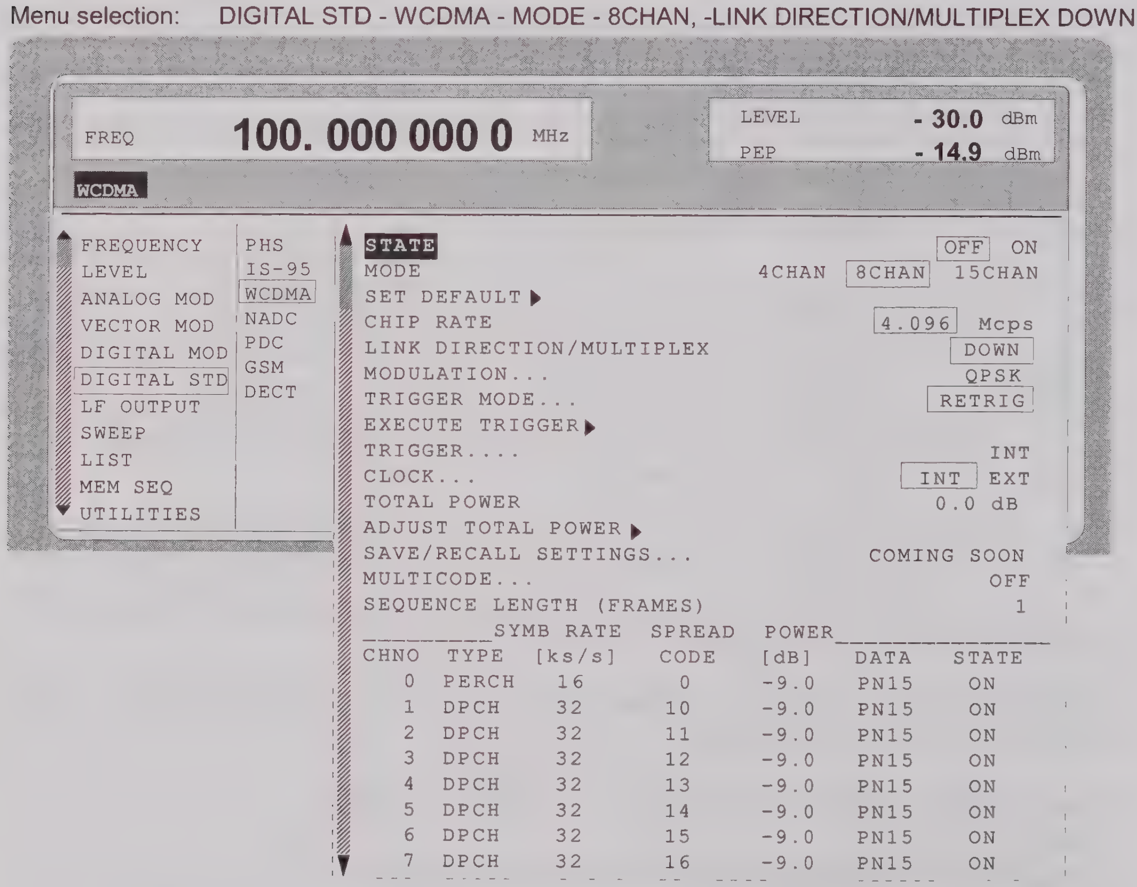
2.13.3 Lists as an Internal Data Source

A freely programmable memory can be used as a data source for DATA or TPC fields. The data are managed in lists. A list editor enables the data lists (DATA LIST) to be selected, copied, modified and erased. The list editor can be accessed via the menu DIGITAL MOD SOURCE...

2.13.4 Menu W-CDMA Standard - Downlink and Uplink Signals without IQ Multiplex

Menu DIGITAL STD - W-CDMA provides access to settings for W-CDMA signal generation.
The figure below shows an example of the menu for generating the downlink signal (transmit signal of the base station) in the 8CHAN mode. The menus for generating uplink signals without IQ multiplex have nearly the same structure. A menu for generating uplink signals with IQ multiplex is shown in the next section. Parameters that are identical for all modes are explained in this section.

Menu selection: DIGITAL STD - WCDMA - MODE - 8CHAN, -LINK DIRECTION/MULTIPLEX DOWN



FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 14.9 dBm

STATE OFF ON
MODE 4CHAN 8CHAN 15CHAN
SET DEFAULT ▶
CHIP RATE 4.096 Mcps
LINK DIRECTION/MULTIPLEX DOWN
MODULATION... QPSK
TRIGGER MODE... RETRIG
EXECUTE TRIGGER ▶
TRIGGER.... INT
CLOCK... INT EXT
TOTAL POWER 0.0 dB
ADJUST TOTAL POWER ▶
SAVE/RECALL SETTINGS... COMING SOON
MULTICODE... OFF
SEQUENCE LENGTH (FRAMES) 1

CHNO	TYPE	SYMB RATE [ks/s]	SPREAD CODE	POWER [dB]	DATA	STATE
0	PERCH	16	0	-9.0	PN15	ON
1	DPCH	32	10	-9.0	PN15	ON
2	DPCH	32	11	-9.0	PN15	ON
3	DPCH	32	12	-9.0	PN15	ON
4	DPCH	32	13	-9.0	PN15	ON
5	DPCH	32	14	-9.0	PN15	ON
6	DPCH	32	15	-9.0	PN15	ON
7	DPCH	32	16	-9.0	PN15	ON

Fig. 2-90 Menu DIGITAL STD - WCDMA - MODE - 8CHAN, LINK DIRECTION/MULTIPLEX - DOWN, equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB43

STATE	Switch on/off of modulation Digital Standard W-CDMA. Vector modulation and digital modulation will be switched off automatically. STATE = ON starts the calculation of a chip sequence based on the current settings. The lenght of the chip sequence and therefore the duration of calculation are determined by parameter SEQUENCE LENGTH. IEC/IEEE-bus command `:SOUR:WCDM:STAT ON`
MODE...	Opens a window for selecting from the different modes to determine the number of code channels. The physical channel type (TYPE), the symbol rate, some spreading parameters (SPREAD CODE) and the source for modulation data (DATA) can be separately defined for each channel. Each channel can be switched on or off (STATE).

4CHAN	Activates the generation of a signal with up to 4 code channels (channel No. 0 to 3). The relative power (POWER) of the channels can be freely determined in range -30 dB to 0 dB. IEC/IEEE-bus `:SOUR:WCDM:MODE CHAN4`
8CHAN	Activates the generation of a signal with up to 8 code channels (channel No. 0 to 7). The relative power (POWER) of channel 0 can be freely determined in range -30 dB to 0 dB. The power setting of channel 1 also determines the power setting of channels 2 to 7. This means that channels 1 to 7 all have the same power provided when the channels are switched on. IEC/IEEE-bus `:SOUR:WCDM:MODE CHAN8`
15CHAN	Activates the generation of a signal with up to 15 code channels (channel No. 0 to 14). The relative power of channels cannot be freely defined since all activated code channels have the same power. The relative power is displayed (POWER). IEC/IEEE-bus `:SOUR:WCDM:MODE CHAN15`

SET DEFAULT ▶

Provides the default setting for W-CDMA.

For LINK DIRECTION/MULTIPLEX DOWN

- in mode 8, channel 0 is a Perch channel, it is switched on and its symbol rate is 16 ksymbol/s
- all other channels are also switched on (STATE ON), channel type (TYPE) is DPCH, symbol rate is 32 ksymbol/s
- the channels have the same relative power (POWER)
- the data are pseudo random (PN15), except for Perch channel, a data offset of three times the channel number (3 • CHNO) is set.

For LINK DIRECTION/MULTIPLEX UP

- only channel 0 is switched on, channel type is DPCH, symbol rate is 32 ksymbol/s
- the data are pseudo random (PN15), no data offset is set.

For LINK DIRECTION/MULTIPLEX UP_IQ_MULT

- all channels are switched on except I-channel 0 in mode 8
- Q-channel 0 is configured as DPCCH, all the other channels are configured as DPDCH with a symbol rate of 16 ksymbol/s
- all channels have the same relative power (POWER)
- the data are pseudo random (PN15), no data offset is set.

IEC/IEEE-bus `:SOUR:WCDM:PRES`

CHIP RATE

Selection between 4.096 Mcps and 8.192 Mcps. Selecting 8.192 Mcps depends on the hardware configuration of the unit and is not offered yet. Selecting CHIP RATE has an effect on the generation of spreading codes and thus on the calculated chip sequence.

IEC/IEEE-bus `:SOUR: WCDM:CRAT R4M`

LINK DIRECTION/ MULTIPLEX

Opens a window for selecting the type of transmit signal from the different modes.

DOWN

Activates the generation of a downlink signal. In this mode, the transmit signal of a base station can be generated in line with the Japanese ARIB standard or according to NTTDoCoMo specifications for an experimental system.

IEC/IEEE-bus `:SOUR:WCDM:LINK DOWN`

UP

Activates the generation of an uplink signal. In this mode, the transmit signal of a mobile station can be generated in line with the NTTDoCoMo specifications for an experimental system.

IEC/IEEE-bus `:SOUR:WCDM:LINK UP`

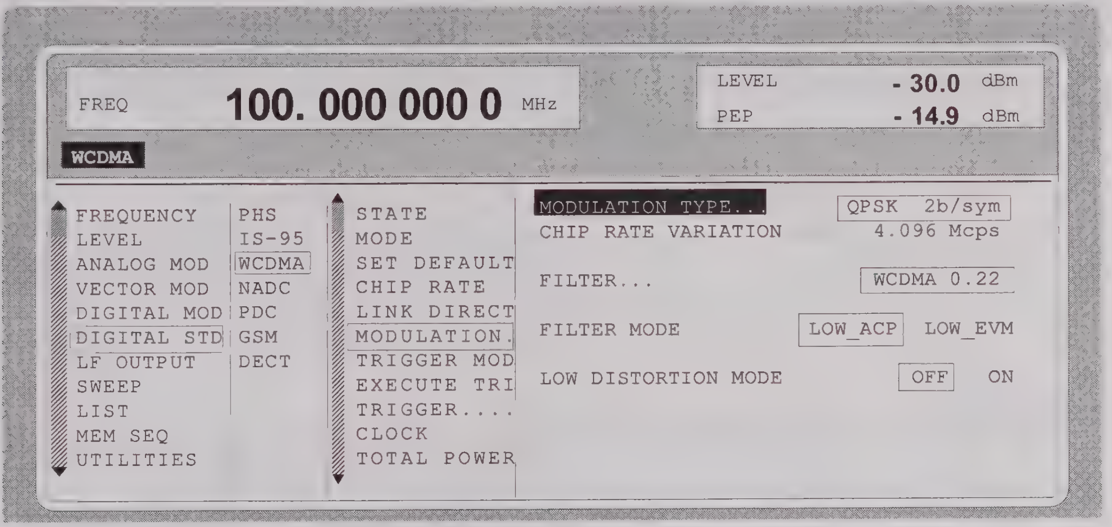
UP_IQ_MULT

Activates the generation of an uplink signal with a multiplex mode for the I and Q channel. Separate channel types and data sources are available for I and Q. In this mode, the transmit signal of a mobile station can be generated in line with the Japanese ARIB standard.

IEC/IEEE-bus `:SOUR:WCDMA:LINK UPM`

MODULATION... Opens a window for setting the modulation parameters.

Menu selection: DIGITAL STD - WCDMA - MODULATION...


FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 14.9 dBm

MODULATION TYPE... QPSK 2b/sym

CHIP RATE VARIATION 4.096 Mcps

FILTER... WCDMA 0.22

FILTER MODE LOW_ACP LOW_EVM

LOW DISTORTION MODE OFF ON

WCDMA
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
WCDMA
NADC
PDC
GSM
DECT
STATE
MODE
SET DEFAULT
CHIP RATE
LINK DIRECT
MODULATION.
TRIGGER MOD
EXECUTE TRI
TRIGGER....
CLOCK
TOTAL POWER

Fig. 2-91 Menu DIGITAL STD - WCDMA - MODULATION..., equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB43

(MODULATION...)

MODULATION TYPE Selection between the types of modulation QPSK and offset QPSK.

IEC/IEEE-bus `:SOUR:WCDM:FORM QPSK`

CHIP RATE VARIATION Setting value for the chip clock frequency. The value for CHIPRATE from the main menu is preset. CHIP RATE VARIATION modifies the output clock and the modulation bandwidth as well as the output synchronization signals. It has no effect on the calculated chip sequence.

IEC/IEEE-bus `:SOUR:WCDM:CRAT:VAR 4096001`

FILTER... Opens a window for selecting the baseband filter. Selection can be made between a filter optimized for W-CDMA, the standard Nyquist filters COS and SQRCOS or a user-defined filter USER (cf. Section Digital Modulation) is possible. The optimized root cosine filter WCDMA 0.22 is preset. Its roll-off factor is fixed to 0.22.

IEC/IEEE-bus `:SOUR:WCDM:FILT:TYPE WCDM`

ROLL OFF FACTOR Setting value of roll-off factor.
The setting is only possible provided that a COS or SQRCOS filter is selected under FILTER.

IEC/IEEE-bus `:SOUR:WCDM:FILT:PAR 0.22`

(MODULATION...) **FILTER MODE**

Selection of filter mode.

LOW_ACP Filter for minimum Adjacent Channel Power.

IEC/IEEE-bus command
`:SOUR:WCDM:FILT:MODE LACP`

LOW_EVM Filter for minimum vector error.

IEC/IEEE-bus command
`:SOUR:WCDM:FILT:MODE LEVM`

LOW DISTORTION MODE

Switch on/off of low-distortion mode.
After switch-on, the level of the IQ baseband signals is reduced by 3 dB. In some cases, this might reduce undesired intermodulation products. OFF is normally the more favourable setting.

IEC/IEEE-bus `:SOUR:WCDM:LDIS:STAT OFF`

TRIGGER MODE

Opens a window for selecting the W-CDMA sequence.

AUTO

The calculated W-CDMA chip sequence is cyclically repeated.

IEC/IEEE-bus command `:SOUR:WCDM:SEQ AUTO`

RETRIG

The W-CDMA chip sequence is continuously repeated. A trigger event causes a restart from frame 1.

IEC/IEEE-bus command `:SOUR:WCDM:SEQ RETR`

ARMED_AUTO

The W-CDMA chip sequence cannot be started from frame 1 until a trigger event has occurred. The unit then automatically switches over to the AUTO mode and can no longer be triggered.

IEC/IEEE-bus command `:SOUR:WCDM:SEQ AAUT`

ARMED_RETRIG

The W-CDMA chip sequence cannot be started from frame 1 until a trigger event has occurred. each new trigger event causes a restart.

IEC/IEEE-bus command `:SOUR:WCDM:SEQ ARET`

EXECUTE TRIGGER ▶

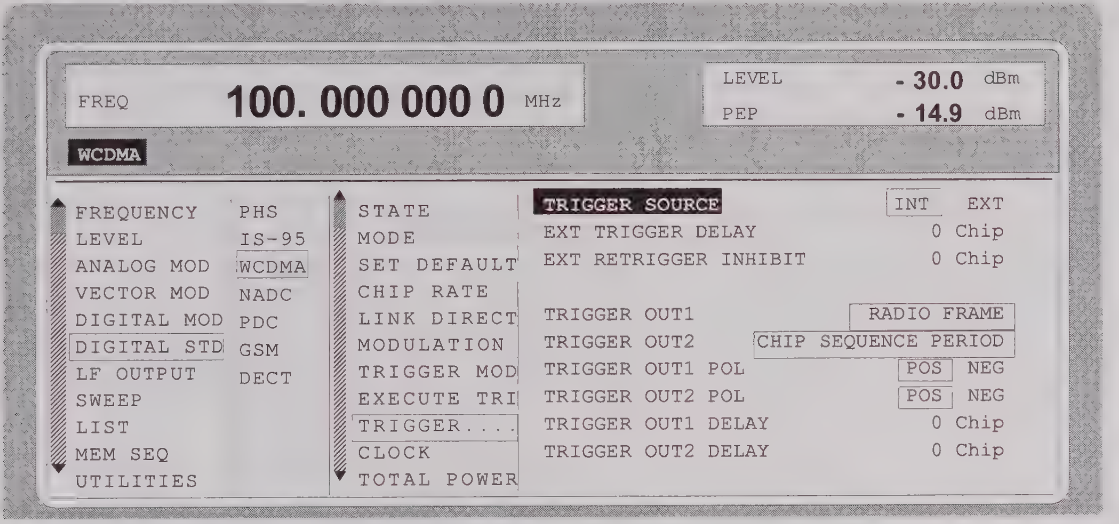
Executes a trigger event to start the W-CDMA chip sequence.

IEC/IEEE-bus command `:TRIG:DM:IMM`

TRIGGER...

Opens a window for selecting the trigger source, for configuring the trigger output signals and for setting the time delay of an external trigger signal.

Menu selection: DIGITAL STD - WCDMA - TRIGGER...


FREQ
MHz
WCDMA
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
WCDMA
NADC
PDC
GSM
DECT
STATE
MODE
SET DEFAULT
CHIP RATE
LINK DIRECT
MODULATION
TRIGGER MOD
EXECUTE TRI
TRIGGER....
CLOCK
TOTAL POWER

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 14.9 dBm

TRIGGER SOURCE INT EXT
EXT TRIGGER DELAY 0 Chip
EXT RETRIGGER INHIBIT 0 Chip
TRIGGER OUT1 RADIO FRAME
TRIGGER OUT2 CHIP SEQUENCE PERIOD
TRIGGER OUT1 POL POS NEG
TRIGGER OUT2 POL POS NEG
TRIGGER OUT1 DELAY 0 Chip
TRIGGER OUT2 DELAY 0 Chip

Fig. 2-92 Menu DIGITAL STD - WCDMA - TRIGGER..., equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB43

TRIGGER...

TRIGGER SOURCE

Selection of trigger source.

EXT The W-CDMA chip sequence is started from frame 1 by means of the active slope of an external trigger signal.
The polarity, the trigger threshold and the input resistance of the TRIGIN input can be modified in menu DIGITAL MOD - EXT INPUTS.

INT A trigger event is manually executed by EXECUTE TRIGGER.

IEC/IEEE-bus `:SOUR:WCDM:TRIG:SOUR EXT`

EXT TRIGGER DELAY

Setting the number of chips by which an external trigger signal is delayed before it starts the W-CDMA chip sequence.
This is used for setting the time synchroneity between the DUT and other units.

IEC/IEEE-bus `:SOUR:WCDM:TRIG:DEL 3`

EXT RETRIGGER INHIBIT

Setting the number of chips for which a restart is inhibited after a trigger event.
With TRIGGER MODE RETRIG selected, each new trigger signal restarts the W-CDMA chip sequence. This restart can be inhibited for the entered number of chips.

Example:
The entry of 82000 chips, for example, causes new trigger signals to be ignored for the duration of 82000 chips after a trigger event.

IEC/IEEE-bus `:SOUR:WCDM:TRIG:INH 82000`

(TRIGGER...)

TRIGGER OUT 1/2

Selection of signals for outputs TRIGOUT 1 and TRIGOUT 2 of connector PARDATA.
The time specifications are valid only if the frequency of the internal clock generation is not modified with the parameter CHIP RATE VARIATION.

SLOT	0.625 ms time slot clock IEC/IEEE-bus: `:SOUR:WCDM:TRIG:OUTP1 SLOT`
RADIO FRAME	10 ms frame clock IEC/IEEE-bus: `:SOUR:WCDM:TRIG:OUTP1 RFR`
CHIP SEQUENCE PERIOD	Marker signal for identifying the periodic repetition of the generated chip sequence IEC/IEEE-bus: `:SOUR:WCDM:TRIG:OUTP1 CSP`

TRIGGER OUT 1/2 POL

Selection of signal polarity at outputs TRIGOUT 1 and TRIGOUT 2 of the PARDATA connector.
IEC/IEEE-bus `:SOUR:WCDM:OUTP2:POL POS`

TRIGGER OUT 1/2 DELAY

Setting the number of chips by which the selected trigger signal is delayed.
IEC/IEEE-bus `:SOUR:WCDM:OUTP2:DEL 0`

CLOCK...

Enables the selection of the clock source of the chip clock.

INT	SMIQ uses internally generated clock signals.
EXT	A chip clock should externally be applied to connector SYMBOL CLOCK. Parameter CHIP RATE should be correctly set with a precision of ± 1 %. The polarity, the trigger threshold and the input resistance of the SYMBOL CLOCK input can be modified in menu DIGITAL MOD - EXT INPUTS.

IEC/IEEE-bus `:SOUR:WCDM:CLOC:SOUR INT`

TOTAL POWER

Display of the total power of all active code channels. The TOTAL POWER is calculated when the modulation is active (STATE = ON). It is the sum of the channel power of all active channels. If the value is not equal 0 dB then all active channel power were internally adjusted so that the total power equals 0 dB (the power relation between single code channels is not affected!) This is nessessary to keep the setted output power (LEVEL) constant. In addition to this average power (LEVEL) the peak envelope power (PEP) is also indicated in the header of the display. The value for PEP is calculated based on a worst case. The actual peak powers are mostly smaller.

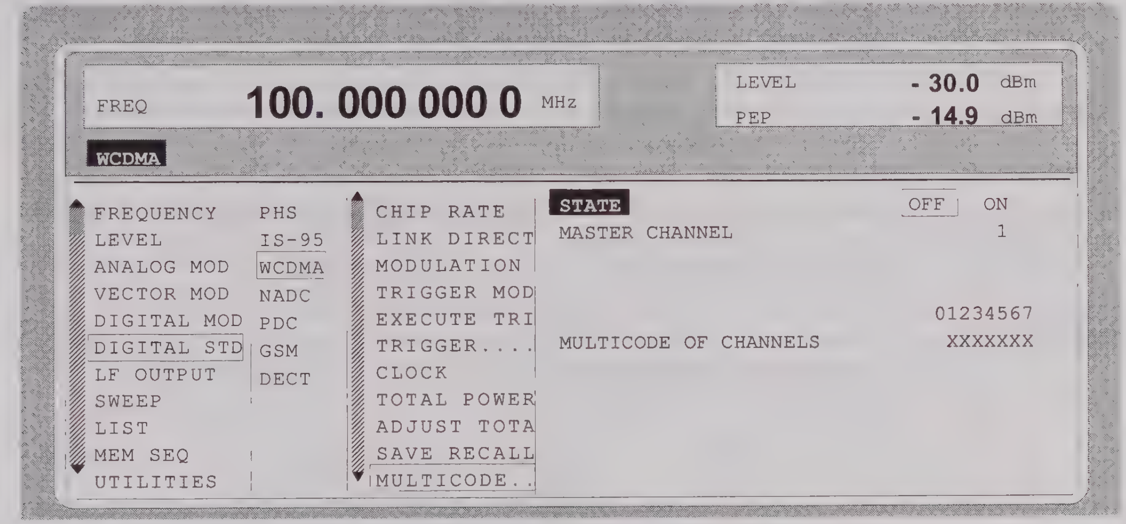
IEC/IEEE-bus command `:SOUR:WCDM:POW?`

ADJUST TOTAL POWER ►

Changes the power values of all activated code channels. After this adjustment the total power is 0 dB. The power relation between single active code channels is not affected.

IEC/IEEE-bus command `:SOUR:WCDM:POW:ADJ`

Menu selection: DIGITAL STD - WCDMA - MULTICODE...


WCDMA
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
WCDMA
NADC
PDC
GSM
DECT
CHIP RATE
LINK DIRECT
MODULATION
TRIGGER MOD
EXECUTE TRI
TRIGGER....
CLOCK
TOTAL POWER
ADJUST TOTA
SAVE RECALL
MULTICODE..

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 14.9 dBm

STATE OFF ON

MASTER CHANNEL 1

MULTICODE OF CHANNELS 01234567 XXXXXXX

Fig. 2-93 Menu DIGITAL STD - WCDMA - MULTICODE..., equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB43

MULTICODE... Opens a window for selecting multicode settings. The multicode mode cannot be activated in the LINK DIRECTION/MULTIPLEX UP-IQ_MULT mode. In the multicode mode, the data fields Pilot and TPC have the same settings and are spread with the same spread codes of the MASTER CHANNEL for all selected code channels.

STATE Switch on/off the multicode mode.

IEC/IEEE-bus `:SOUR:WCDM:MULT:STAT ON`

MASTER CHANNEL Selection of the master channel. This channel defines spread code and contents for data fields Pilot and TPC in all multicode channels. The selection also causes that parameters LONG CODE INIT, LONG CODE OFFSET, SYMBOL RATE and DATA OFFSET of the selected multicode channels are set to the same values as the master channel.

IEC/IEEE-bus `:SOUR:WCDM:MULT:MAST 1`

MULTICODE OF CHANNELS Selection of the channels for multicode transmission. The selected channels are marked with an X.

IEC/IEEE-bus `:SOUR:WCDM:MULT:CHAN #H1E`

SEQUENCE LENGTH [FRAMES] Defines the length of the calculated chip sequence in number of frames.

IEC/IEEE-bus command `:SOUR:WCDM:SLEN 4`

CHNO Column title for the display of the number of the channel for which the settings of the corresponding line are valid. The other columns can be selected with "=>" and "<=".

TYPE

Opens a window for selecting the channel type.

PERCH	Perch 1-channel with Pilot, DATA and LMS data field. Selection is possible with LINK DIRECTION - DOWN.
CCPCH	Common Control Physical Channel with Pilot and DATA field. Selection is possible with LINK DIRECTION - DOWN and UP.
DPCH	Dedicated Control Physical Channel with Pilot, TPC and DATA field. Selection is possible with LINK DIRECTION - DOWN and UP.
ALLD	All data channel type only with DATA field. Selection is possible with LINK DIRECTION - DOWN and UP.

IEC/IEEE-bus command `:SOUR:WCDM:CHAN4:DPCH`

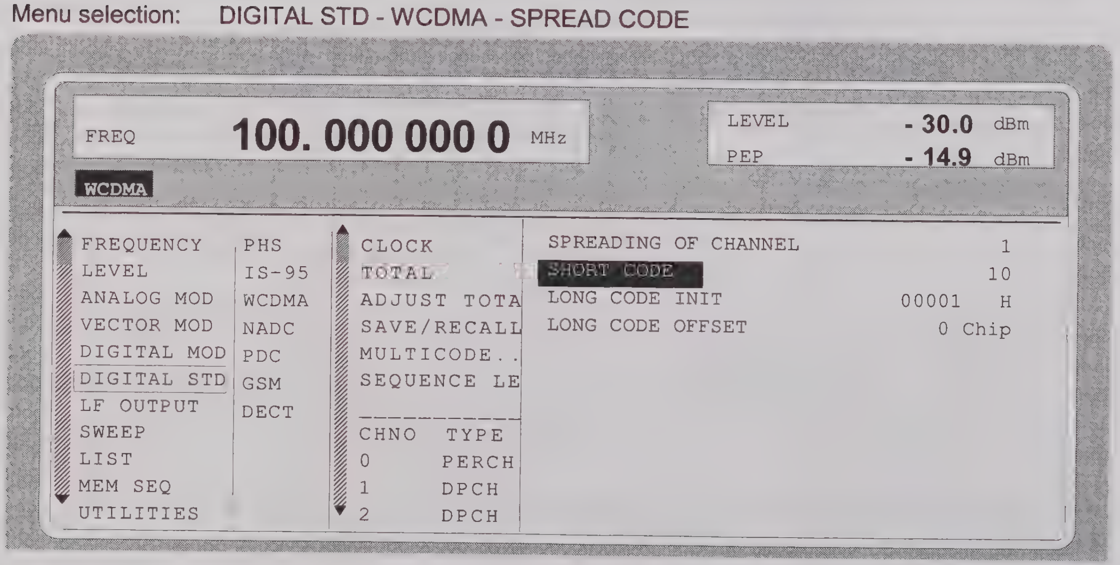
SYMBOL RATE

Opens a window for selecting the symbol rate. The admissible selection depends on the channel type selected (TYPE). The possible values are represented in the following table:

TYPE	SYMBOL RATE kS/s
PERCH	16
CCPCH	64
DPCH	32, 64, 128, 256, 512, 1024
DPDCH	16, 32, 64, 128, 256, 512, 1024
DPCCH	16
ALLD	16, 32, 64, 128, 256, 512, 1024

IEC/IEEE-bus command `:SOUR:WCDM:CHAN4:SRAT D64`

Menu selection: DIGITAL STD - WCDMA - SPREAD CODE


FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 14.9 dBm

SPREADING OF CHANNEL 1

SHORT CODE 10

LONG CODE INIT 00001 H

LONG CODE OFFSET 0 Chip

CHNO	TYPE
0	PERCH
1	DPCH
2	DPCH

WCDMA
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
WCDMA
NADC
PDC
GSM
DECT
CLOCK
TOTAL
ADJUST TOTA
SAVE/RECALL
MULTICODE..
SEQUENCE LE

Fig. 2-94 Menu DIGITAL STD - WCDMA - SPREAD CODE; equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB43

SPREAD CODE

Opens a window for the spread code settings. The first line of the window indicates the code channel for which the settings are done (SPREADING OF CHANNEL NO).

(SPREAD CODE)

SHORT CODE Entry value for the short code index. The upper limit depends on parameters CHIP RATE, SYMBOL RATE as well as on the channel type.

IEC/IEEE `:SOUR:WCDM:CHAN4:SCOD 12`

LONG CODE INIT Entry value for initializing the long code generator in hexadecimal notation.

IEC/IEEE `:SOUR: WCDM:CHAN4:LCOD #HFFF`

LONG CODE OFFSET Entry value for a time shift of the long code with respect to the data symbols in units of chip duration.

IEC/IEEE `:SOUR:WCDM:CHAN4:LCOD:OFFS 1`

LMS SHORT CODE Entry value for the short code index used for spreading the long code mask symbols (LMS) of the Perch channel.

IEC/IEEE `:SOUR:WCDM:CHAN4:SCOD:LMS #HFF`

POWER Input value for channel power.

POWER indicates the average power of the code channel relative to the power indicated in LEVEL display (code domain power).

For MODE - 4CHAN

- the power of channels 0, 1, 2 and 3 is set separately.

For MODE - 8CHAN

- the power of channel 0 is set separately.
- the other channels have the same power. The power for all the channels is set, for instant, in channel 1.

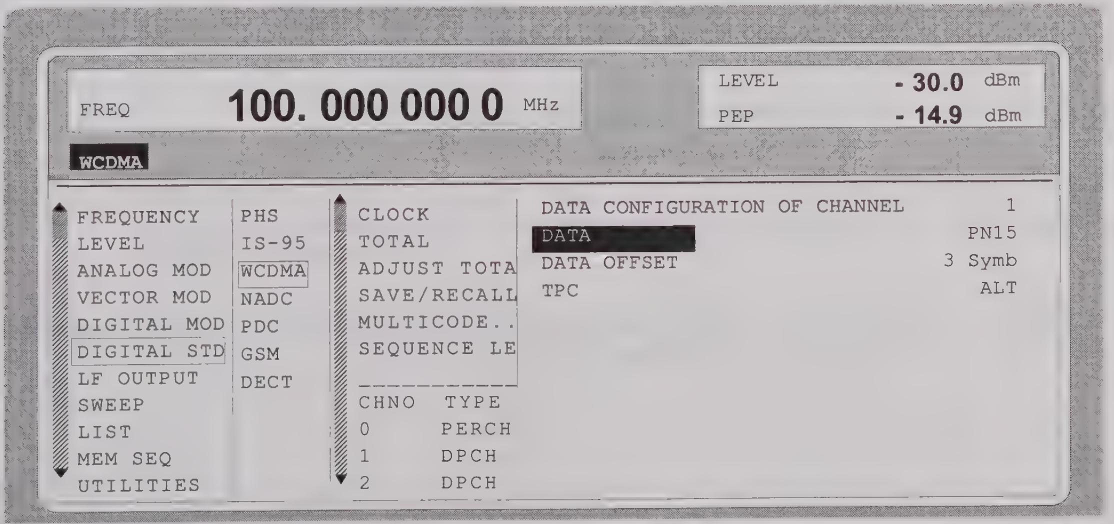
For MODE - 15CHAN

- Since all channels have the same power, they cannot be set. POWER is only a display parameter in this case.

When this value is modified, the value of TOTAL POWER is automatically adapted.

IEC/IEEE-bus command `:SOUR:WCDM:CHAN4:POW -3`

Menu selection: DIGITAL STD - WCDMA - DATA


WCDMA
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
WCDMA
NADC
PDC
GSM
DECT
CLOCK
TOTAL
ADJUST TOTA
SAVE/RECALL
MULTICODE..
SEQUENCE LE
CHNO TYPE
0 PERCH
1 DPCH
2 DPCH

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 14.9 dBm

DATA CONFIGURATION OF CHANNEL 1

DATA PN15

DATA OFFSET 3 Symb

TPC ALT

Fig. 2-95 Menu DIGITAL STD - WCDMA - DATA; equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB43

DATA...

Opens a window for selecting data sources and setting a data offset. The first line of the window indicates the code channel for which the settings are done (DATA CONFIGURATION OF CHANNEL NO).

DATA

Selection of the data source for the DATA field of the selected channel type. The data from the selected data source are permanently continued from slot to slot in the data field.

PN..	PRBS data to CCITT with period lengths between 29-1 and 216-1.
DLIST	Data from a list previously programmed and stored. Programming is performed in the menu DIGITAL MOD - SOURCE - EDIT DATA LIST

IEC/IEEE-bus `:SOUR:WCDM:CHAN4:DATA PN9`

DATA OFFSET

Entry value for a data offset in units of symbol duration. The admissible range is as large as the length of a radio frame. The entry of a data offset shifts in time the modulation data with respect to the spread code.

IEC/IEEE-bus `:SOUR:WCDM:CHAN4:DATA:OFFS 3`

TPC

Selection of the data source for the TPC field in channel types DPCH and DPCCh.

0	0 data are continuously generated
1	1 data are continuously generated
ALT	The TPC field is alternately assigned with 1 or 0 from slot to slot. (The first slot contains 1 data).
DLIST	Data from a list previously programmed and stored. Programming is performed in the menu DIGITAL MOD - SOURCE - EDIT DATA LIST

IEC/IEEE-bus `:SOUR:WCDM:CHAN4:TPC ZERO`

STATE

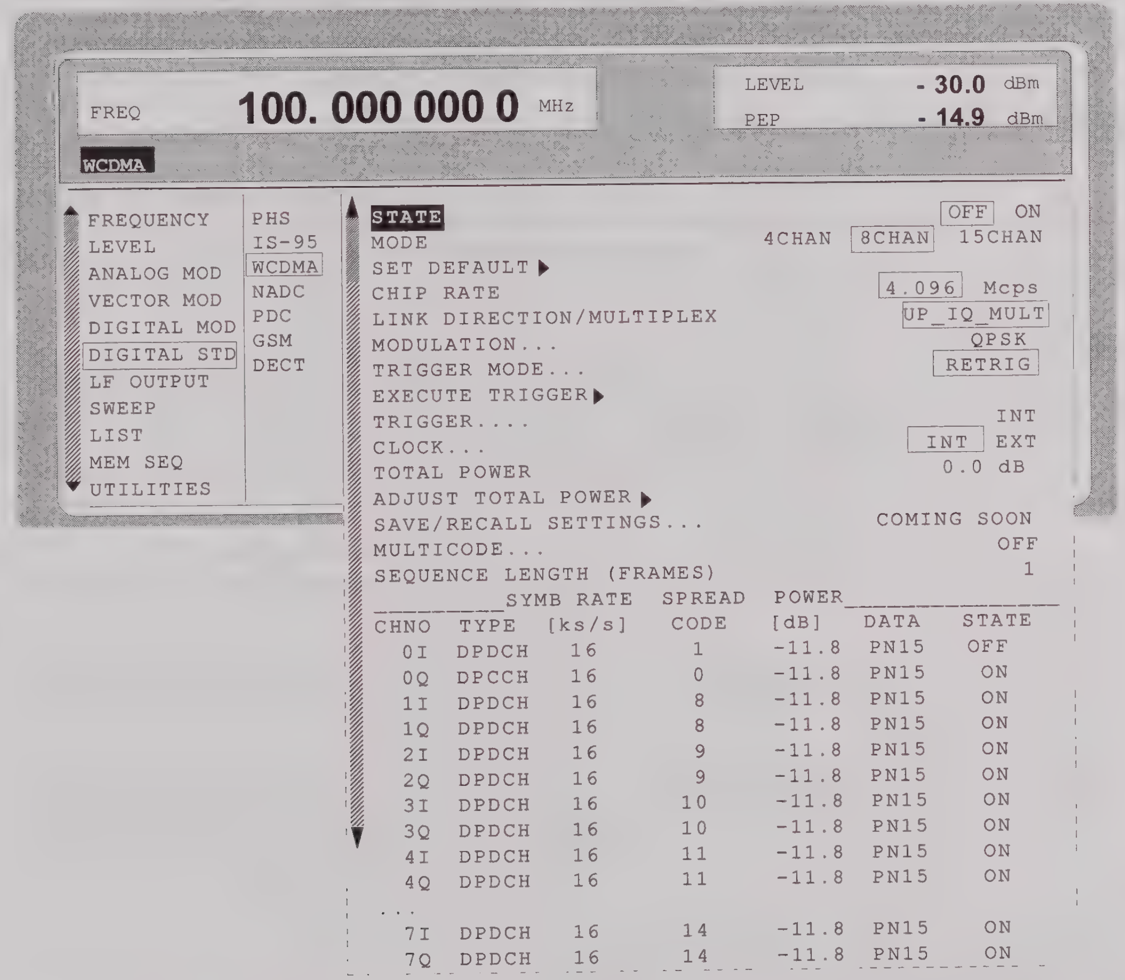
Switch on/off of assigned code channel.

IEC/IEEE-bus command `:SOUR:WCDM:CHAN4:STAT ON`

2.13.5 Menu W-CDMA Standard - Uplink Signals with IQ Multiplex

The settings for generating W-CDMA signals can be accessed via the menu DIGITAL STD - W-CDMA. The figure below shows the menu for generating the uplink signal with multiplex mode for the I and Q channel in the 8CHAN mode. The previous section shows the menu for generating the up and downlink signals without IQ multiplex as well as the parameters which are identical for all modes.

Selection: DIGITAL STD - WCDMA- MODE -8CHAN, -LINK DIRECTION/MULTIPLEX - UP_IQ_MULT



FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 14.9 dBm

STATE OFF ON
MODE 4CHAN 8CHAN 15CHAN
SET DEFAULT ▶
CHIP RATE 4.096 Mcps
LINK DIRECTION/MULTIPLEX UP_IQ_MULT
MODULATION... QPSK
TRIGGER MODE... RETRIG
EXECUTE TRIGGER ▶
TRIGGER.... INT
CLOCK... INT EXT
TOTAL POWER 0.0 dB
ADJUST TOTAL POWER ▶
SAVE/RECALL SETTINGS... COMING SOON
MULTICODE... OFF
SEQUENCE LENGTH (FRAMES) 1

CHNO	TYPE	SYMB RATE [ks/s]	SPREAD CODE	POWER [dB]	DATA	STATE
0I	DPDCH	16	1	-11.8	PN15	OFF
0Q	DPCCH	16	0	-11.8	PN15	ON
1I	DPDCH	16	8	-11.8	PN15	ON
1Q	DPDCH	16	8	-11.8	PN15	ON
2I	DPDCH	16	9	-11.8	PN15	ON
2Q	DPDCH	16	9	-11.8	PN15	ON
3I	DPDCH	16	10	-11.8	PN15	ON
3Q	DPDCH	16	10	-11.8	PN15	ON
4I	DPDCH	16	11	-11.8	PN15	ON
4Q	DPDCH	16	11	-11.8	PN15	ON
...						
7I	DPDCH	16	14	-11.8	PN15	ON
7Q	DPDCH	16	14	-11.8	PN15	ON

Fig. 2-96 Menu DIGITAL STD - WCDMA - MODE - 8CHAN, -LINK DIRECTION/MULTIPLEX - UP_IQ_MULT, equipped with options modulation coder SMIQB20, data generator SMIQB11 and SMIQB43

Parameters STATE to SEQUENCE LENGTH see section " Menu W-CDMA Standard -Downlink and Uplink Signals without IQ Multiplex"

CHNO

Column title for the display of channel numbers.

I	Line for the parameters of the I channel
Q	Line for the parameters of the Q channel

TYPE

Opens a window for selecting the channel type. The channel type can be separately set for the I and Q channel.

DPDCH	Dedicated Physical Data Channel with data field DATA
DPCCH	Dedicated Physical Control Channel with data field Pilot and TPC

IEC/IEEE-bus command `:SOUR:WCDM:CHAN4:I:TYPE DPCC`

SYMBOL RATE

Opens a window for selecting the symbol rate. The admissible selection depends on the channel type selected (TYPE). Possible values for DPDCH are 16, 32, 64, 128, 256, 512 and 1024 ksymbol/s (ksymbols per second). The DPDCH type has a fixed symbol rate of 16 ksymbol/s.
The symbol rate can be separately set for the I and Q channel.

IEC/IEEE-bus command `:SOUR:WCDM:CHAN4:I:SRAT D16`

SPREAD CODE

Opens a window for the spread code settings. The first line of the window indicates the code channel for which the settings are done (SPREADING OF CHANNEL NO).
The long code settings are valid in common for the I and Q channel. For the short code of I and Q separate settings are possible within a channel number line.

SHORT CODE

Entry value for the short code index.
The upper limit depends on parameters CHIP RATE, SYMBOL RATE.

IEC/IEEE `:SOUR:WCDM:CHAN4:I:SCOD 9`

LONG CODE INIT

Entry value for initializing the long code generator in hexadecimal notation.

IEC/IEEE `:SOUR:WCDM:CHAN4:I:LCOD #H1`

LONG CODE OFFSET

Entry value for a time shift of the long code with respect to the data symbols in units of chip duration.

IEC/IEEE `:SOUR:WCDM:CHAN4:I:LCOD:OFFS 5`

POWER

Input value for channel power

POWER indicates the average power of the I or Q code channel component in relation to the power indicated in the LEVEL display (Code Domain Power).

The setting values for the I and Q code channels having the same channel number should always be identical.

For MODE - 4CHAN

- the power of channels 0, 1, 2 and 3 is set separately.

For MODE - 8CHAN

- the power of channel 0 is set separately.
- the other channels have the same power. The power for all the channels is set, for instant, in channel 1.

For MODE - 15CHAN

- Since all channels have the same power, they cannot be set. POWER is only a display parameter in this case.

When this value is modified, the value of TOTAL POWER is automatically adapted if STATE = ON.

IEC/IEEE-bus command `:SOUR:WCDM:CHAN4:I:POW -6`

DATA...

Opens a window for selecting data sources and setting a data offset. The first line of the window indicates the code channel for which the settings are done (DATA CONFIGURATION OF CHANNEL NO).
The data source, the data offset and TPC can be separately set for the I and Q channel.

DATA

Selection of the data source for the DATA field of a DPDCH channel.

PN..	PRBS data to CCITT with period lengths between 2^9-1 and 2^{16}-1.
DLIST	Data from a list previously programmed and stored. Programming is performed in the menu DIGITAL MOD - SOURCE - EDIT DATA LIST

IEC/IEEE-bus `:SOUR:WCDM:CHAN4:I:DATA PN15`

DATA OFFSET

Entry value for a data offset in units of symbol duration. The admissible range is as large as the length of a radio frame. The entry of a data offset shifts in time the modulation data with respect to the spread code.

IEC/IEEE-bus `:SOUR:WCDM:CHAN4:I:DATA:OFFS 2`

(DATA...) **TPC**

Selection of the data source for the TPC field of a DPDCH channel.

0	0 data are continuously generated
1	1 data are continuously generated
ALT	The TPC field is alternately assigned with 1 or 0 from slot to slot. (The first slot contains 1 data).
DLIST	Data from a list previously programmed and stored. Programming is performed in the menu DIGITAL MOD - SOURCE - EDIT DATA LIST

IEC/IEEE-bus `:SOUR:WCDM:CHAN4:I:TPC ZERO`

STATE

Activation or deactivation of the I or Q component of the assigned code channel. The I or Q component of channels with common power-up should always have the same state ON or OFF.

IEC/IEEE-bus command `:SOUR:WCDM:CHAN4:I:STAT ON`

2.14 Digital Standard 3GPP W-CDMA (FDD)

The SMIQ can generate 3GPP W-CDMA signals provided that the SMIQ is equipped with the following options: Modulation Coder (SMIQB20), Data Generator (SMIQB11) and Digital Standard 3GPP W-CDMA (SMIQB45). Section 2.14.1 gives an overview of this mobile transmission method. Section 2.14.2 describes the associated operating functions. Section 2.14.3 provides in-depth information about the generation of 3GPP W-CDMA signals.

Chapter 2.15 describes all enhanced functions for digital standard 3GPP W-CDMA (option SMIQB48).

2.14.1 Description of Mobile Radio Transmission Method 3GPP W-CDMA

W-CDMA (wideband CDMA) designates a group of mobile radio transmission methods which differ in numerous details. The SMIQ provides a version developed by the 3 GPP standard organization ("3rd Generation Partnership Project").
This version can be regarded as an amalgamation of the IMT-2000 proposals by

- Association of Radio Industries and Businesses (ARIB)
- The European Telecommunications Standards Institute (ETSI)

and is thus also the successor of versions to 3GPP 2.0.0, 2.1.0, 3.1.1,3.2.0 and 3.4.0 implemented in former firmware versions.

The current version (SIMQ firmware version 5.70 or higher) supports W-CDMA according to 3GPP in version 4.1.0 (FDD mode). It is compatible with "Release 1999".

References:

[1] 3GPP TS 25.211 Version 4.1.0 Physical channels and mapping of transport channels onto physical channels (FDD)
[2] 3GPP TS 25.213 Version 4.1.0 Spreading and modulation (FDD)
[3] 3GPP TS 25.141 Version 4.1.0 Base station conformance testing (FDD)

The SMIQ simulates W-CDMA 3GPP at the physical channel layer. The following description is therefore limited to this layer.

Table 2-18 Parameters of W-CDMA system

Chip rate	3.84 Mcps
Channel types	Downlink : • Primary Common Pilot Channel (P-CPICH) • Secondary Common Pilot Channel (S-CPICH) • Primary Sync Channel (P-SCH) • Secondary Sync Channel (S-SCH) • Primary Common Control Phys. Channel (P-CCPCH) • Secondary Common Control Phys. Channel (S-CCPCH) • Page Indication Channel (PICH) • Acquisition Indication Channel (AICH) • Access Preamble Acquisition Indication Channel (AP-AICH) • Phys. Downlink Shared Channel (PDSCH) • Dedicated Physical Control Channel (DL-DPCCH) • Dedicated Phys. Channel (DPCH) Uplink : • Phys. Random Access Channel (PRACH) • Phys. Common Packet Channel (PCPCH) • Dedicated Physical Control Channel (DPCCH) • Dedicated Physical Data Channel (DPDCH)
Symbol rates	7.5 ksps, 15 ksps, 30 ksps to 960 ksps depending on type of channel
Number of channels	4 base stations in the downlink with up to 128 DPCHs and 11 special channels each 4 mobile stations in the uplink either with PRACH or PCPCH or DPDCH and up to 6 DPDCHs.
Frame structure	Timeslot: 0.667 ms, Radio Frame: 15 timeslots = 10 ms, the structure of the frames depends on the symbol rate.
Scrambling code	Downlink: 18 bit M sequence Uplink: 25 bit M sequence in long mode and 8 bit M sequence in short mode
Channelization code for DPCH, DPDCH and DPCCH	"Orthogonal Variable Spreading Factor Code (OVSF)" Orthogonal matrix of *chip rate/symbol rate*

2.14.1.1 System Components

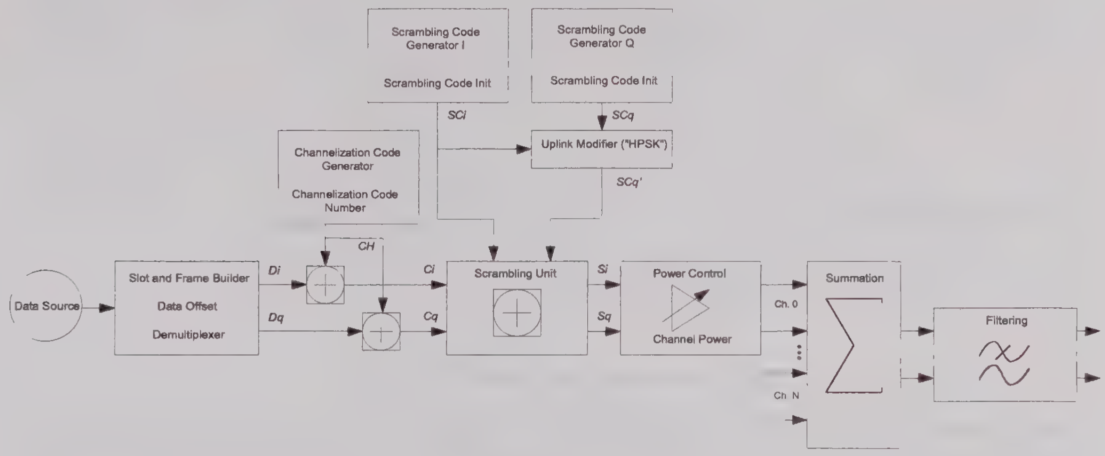

Scrambling Code Generator I
Scrambling Code Init
Scrambling Code Generator Q
Scrambling Code Init
SCi
SCq
Uplink Modifier ("HPSK")
SCq'
Channelization Code Generator
Channelization Code Number
CH
Data Source
Slot and Frame Builder
Data Offset
Demultiplexer
Di
Dq
Ci
Cq
Scrambling Unit
Si
Sq
Power Control
Channel Power
Ch. 0
Ch N
Summation
Filtering

Fig. 2-97 Components of 3GPP W-CDMA transmission system

The individual functional blocks are described below in detail.

Scrambling code generator

The scrambling code generator (formerly long code generator) is used to scramble the chip sequence as a function of the transmitter. The structure and initialization rule of the generator differ depending on the link direction and the mode (long or short).

1. Downlink scrambling code generator

This generator consists of a pair of shift registers from which the binary sequences for in-phase and quadrature components of the scrambling code are determined. Fig. 2-98 shows that the I component results from an EXOR link of the LSB outputs whereas for the Q component the register contents are first output masked and then EXORed.

Note: *As an alternative, the Q component can be determined via a second pair of registers with the same structure, which idle for a certain number of cycles before the scrambling code bits are output. This I/Q offset is 131.072 in the downlink and 16.777.232 in the similarly designed uplink long scrambling code generator. For reasons of simplicity, the implementation shown in Fig. 2-98 is the better choice.*

Generator polynomials of downlink scrambling code generators

Shift register 1	$x^{18}+x^{7}+1$
Shift register 2	$x^{18}+x^{10}+x^{7}+x^{5}+1$

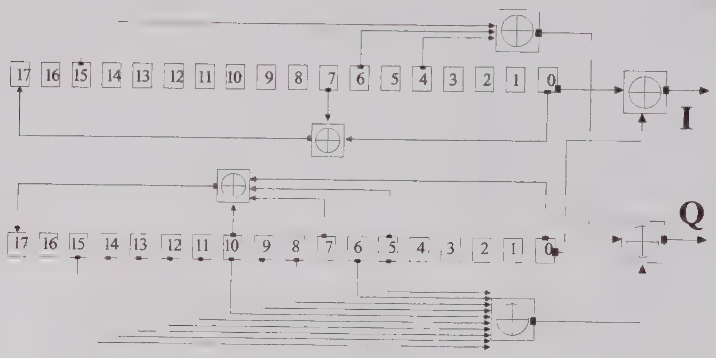


Fig. 2-98 Structure of the downlink scrambling code generator

The shift registers are initialized by loading shift register 1 with "0 to 01" and shift register 2 with all "1". In addition, shift register 1 is run forward by n cycles, n being the scrambling code number or in short the "scrambling code" (SC).

After a cycle time of one radio frame the generators are reset, i.e. the above initialization is carried out again.

2. Uplink scrambling code generator

In the uplink, distinction is made between two modes of the SC. There is on the one hand the long SC which can be used for all types of channel. On the other hand, there is the short SC which can be used for all channels except PRACH and PCPCH as an alternative to the long SC.

a) Uplink long scrambling code generator

The code generator of the long SC in the uplink has basically the same structure as the SC in the downlink. However, the generator polynomials of the shift registers and the type of initialization differ.

Table 2-19 Generator polynomials of uplink long scrambling code generators

Shift register 1	$x^{25}+x^3+1$
Shift register 2	$x^{25}+x^3+x^2+x+1$

The shift registers are initialized by assigning 1 to bit number 24 in shift register 1 and the binary form of the scrambling code number n to bits 23 to 0. Shift register 2 is loaded with all "1".
The readout positions for the Q component are such that they correspond to an IQ offset of 16.777.232 cycles.
After a cycle time of one radio frame the generators are reset, i.e. the above initialization is carried out again.

b) Uplink short scrambling code generator

The code generator of the short SC in the uplink comprises altogether 3 coupled shift registers.

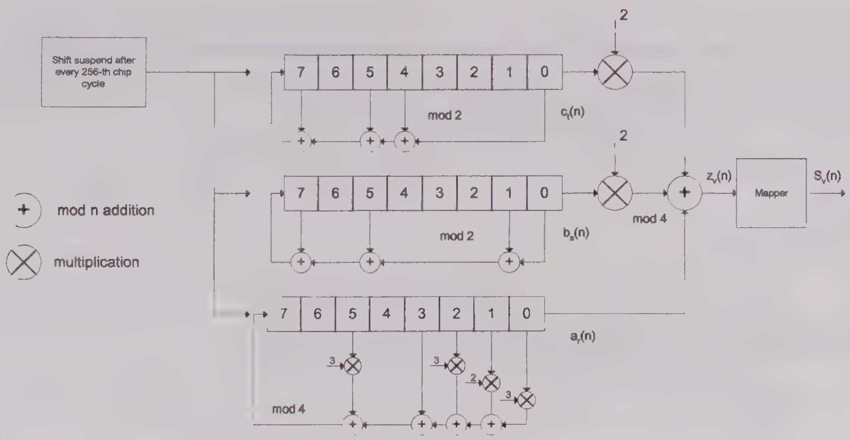


Fig. 2-99 Structure of the uplink short scrambling code generator

Table 2-20 Generator polynomials of uplink short scrambling code generators

Shift register 1 (binary)	$x^8+x^7+x^5+x^4+1$
Shift register 2 (binary)	$x^8+x^7+x^5+x+1$
Shift register 3 (quaternary)	$x^8+x^5+3x^3+x^2+2x1$

The output sequences of the two binary shift registers are weighted with factor 2 and added to the output sequence of the quaternary shift register (Modulo 4 addition). The resulting quaternary output sequence is mapped into the binary complex level by the mapper block.

For initialization of the three 8-bit shift registers (in a modified way) the binary form of the 24-bit short SC number n is used, for details see section 4.3.2.3 in [2].

Table 2-21 Mapping of the quaternary output sequence into the binary IQ level

zv(n)	Sv(n)
0	+1 + j1
1	-1 + j1
2	-1 - j1
3	+1 - j1

c) Preamble scrambling code generator

When generating the preambles of the PRACH and PCPCH a special SC is used. It is based on the long SC described under a), however only the I component is taken and subsequently a pointer ($e^{j(PI/4 + PI/4 * k)}$, k=0 to 4095) modulated upon it.

Modification of the long and short scrambling code output sequence

The scrambling code sequence of the Q component is modified as standard to reduce the crest factor of the signal. Zero-crossings can then be avoided for every second chip using this method. (This method is often called HPSK).
For details see [2], section 4.3.2.1. The SMIQ uses a decimation factor of 2.

Scrambling Unit

In the scrambling unit, the output of the scrambling code generator and the spread symbols are combined. If the input signal and the scrambling code signal are interpreted as complex numbers (C_i , C_q, SC_i , SC_q' $\in$ $\{-1, +1\}$), the output signal is a complex multiplication of the two signals:

$$S_i + j\,S_q = (C_i + j\,C_q) * (SC_i + j\,SC_q')$$

This gives:

$$S_i = C_i SC_i - C_q SC_q'$$
$$S_q = C_i SC_q' + C_q SC_i$$

The signal obtained in this way has a constellation diagram like a QPSK signal:

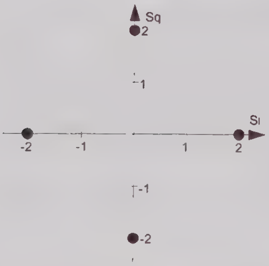


Fig. 2-100 Constellation diagram of a channel with 0 dB power

Note: *There are auxiliary conditions for some types of channels that may result in different constellation diagrams.*
If, for instance, symbols of SCH or preamble part of PRACH are coded, a BPSK constellation is obtained without the scrambling unit. For an explanation of the different constellation diagrams see section 2.14.3.

Channelization code generator

The channelization code generator cyclically outputs a channel-specific bit pattern. The length of the cycle corresponds to the duration of the source symbol to be spread, i.e. the number of bits corresponds to the spread factor. The spreading sequence for the I and Q branches is identical (real). Spreading itself is a simple EXOR operation.

Two different channelization code generators are used depending on the type of channel:

Channelization code generator for all channels except SCH

Due to this channelization code the channel separation takes place in the sum signal. The channelization code number is the row in an orthogonal spreading matrix which is generated by an iterative process (OVSF).

Channelization code generator SCH

This generator replaces the one described above if the synchronization code symbol of the SCH channels is spread.

The spreading matrix is replaced by a method that forms the spreading sequence from a Hadamard sequence and a statistical sequence. For details see [2], section 5.2.3.1.

Data source

The data source used in the SMIQ is implemented at the physical layer. There is neither mapping of logical channels to physical channels nor is inner coding/outer coding performed. If the data source is to be implemented at a higher layer, this can be done with the aid of a file interface (see section 5.6 in the operating manual).

Slot and frame builder
The bits from the data source are first entered into a frame structure. The frames are made up of two hierarchical levels:

Table 2-22 Hierarchical structure of 3GPP W-CDMA frames

Hierarchy	Length in ms	Remarks
Time slot	0.667	
Radio frame	10	After a radio frame, pilot symbols are repeated. A radio frame consists of 15 time slots. This is also the length of a scrambling code cycle. Frames are the basic unit in the SMIQ. The sequence length is stated in radio frames.

The configuration of the time slots depends on the channel type and symbol rate.
The following components are distinguished:

- Pilot sequence
  The pilot sequence characterizes the time slot position within the radio frame and also depends on the symbol rate, transmit diversity and the pilot length parameter.
  Channel types DPCH, S-CCPCH, DL-DPCCH, DPCCH, PRACH and PCPCH have a pilot sequence.
  The pilot sequence cannot be changed by the user.
- Synchronization code symbol
  The synchronization code symbol is the only symbol of the SCH.
  It is fixed to “11“.
- TPC symbol
  This symbol is used to control the transmit power. It is used in DPCH, DL-DPCCH and DPCCH. A bit pattern for the sequence of TPC symbols can be indicated as a channel-specific pattern.
- Data symbols
  These symbols carry the user information and are fed from the data source. They are used in DPCH, P-CCPCH, S-CCPCH,PDSCH, DL-DPCCH, DPDCH, PRACH and PCPCH.
- Signature
  The signature is used in PRACH and PCPCH. 16 fix bit patterns are defined of which the user may select one.
- TFCI
  The “Transport Format Combination Indicator“ is used in DPCH/DPCCH if the corresponding button of the interface (Base Station Configuration, see section 2.14.2.5/ Mobile Station Configuration, see section 2.14.2.6) is activated. In this case, a code sequence with the length of 30 is defined using this value and distributed among 15 subsequent time slots. In PRACH and PCPCH, the TFCI field is provided as standard.
- FBI
  Feedback indication bits are only used in DPCCH and PCPCH.

Timing offset
The symbol stream can be shifted in time relative to the other channels. For this purpose a timing offset can be entered into the channel table, stating the range of shifting in multiples of 256 chips. Since the SMIQ does not generate infinite symbol streams like a realtime system, this offset is implemented as a rotation.

Example for DPCH 30 ksps, 1 time slot,
Timing offset = 2 ➔ 2 x 256 chips = 512 chip offset ➔ 4 data symbols shifting at a symbol rate of 30 ksps (1 symbol corresponds to 3.84 Mcps / 30 ksps = 128 chips).

previously:

11	11	11	11	00	01	10	11	00	10	01	11	11	01	00	01	10	11	01	00

afterwards:

10	11	01	00	11	11	11	11	00	01	10	11	00	10	01	11	11	01	00	01

The use of the timing offset usually causes a reduction of the crest factor of the total signal, since it is not always the same spreading chips (channelization chips) CH and scramble chips SC_i/SC_q' that are applied to the pilot sequences of the channels.

Demultiplexer
In the downlink, the symbol stream is divided into two bit streams D_i and D_q prior to processing in the spreading unit. The symbol stream is divided by allocating bits 1, 3, 5, to 2n-1 to the in-phase bit stream D_i, and bits 2, 4, 6, 2n to the quadrature bit stream D_q.
For the above example with timing offset:

$$D_i = 1\,1\,0\,0\,1\,1\,1\,1\,0\,0\,1\,1\,0\,1\,0\,1\,1\,0\,0\,0$$
$$Dq = 0\,1\,1\,0\,1\,1\,1\,1\,0\,1\,0\,1\,0\,0\,1\,1\,1\,1\,0\,1$$

(lefthand bit is always the first one in the time sequence)

In the uplink, independent data are used for the two paths.

PRACH/PCPCH:	Preamble: signature parallel to I and Q Message part : data to I, pilot and TFCI to Q
DPCCH:	All bits to I, Q always unused
DPDCH:	All bits are always to I **or** Q (dependent on channel number), the other path is unused.

Power control
After spreading and scrambling, a channel-specific power factor p is applied to the signal. A value of –6 dB therefore results in half the level (or ¼ power) and the following diagram (DPCH):

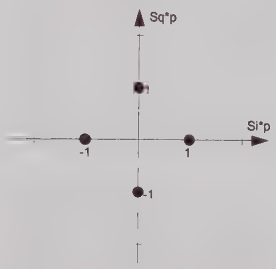


Fig. 2-101 Constellation diagram of a channel with –6 dB power

Summation
After application of the channel power, the components of the individual channels are summed up.
The constellation diagram of the sum signal is obtained by superposition of the diagrams of the individual channels. If the signal consists of two channels with a power of -6 dB and -12 dB and each channel contains independent source data (DPCH), the following constellation diagram is obtained:

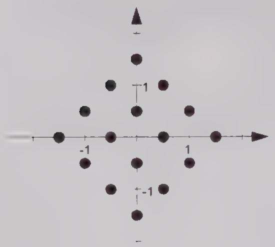


Fig. 2-102 Constellation diagram of a 3GPP W-CDMA signal with two DPCH channels

Filtering
An unfiltered spread signal is obtained after summation. Due to filtering the number of samples is increased by the oversampling factor and band-limiting is performed.

Multi code
3GPP W-CDMA supports multi-code transmission for downlink-dedicated physical channels (DPCH) .
This form of transmission is used for channels intended for the same receiver, i.e. those receivers that belong to a radio link. The first channel of this group is used as a master channel.
Shared parts (pilot, TPC and TCFI) are spread for all channels using the spreading code of the master channel.

Note: *Instead of changing the spreading code within a slot several times, the master code rather than the shared parts can be sent at higher power. The other channels then have to be blanked out correspondingly.*

2.14.2 Generation of 3GPP W-CDMA Signals

The DIGITAL STD - WCDMA/3GPP menu is used to configure W-CDMA signals to the 3GPP standard (FDD). Up to 512 channels (distributed amongst up to four base or mobile stations) can be simulated in the downlink. The operation in the uplink was changed due to the extension to 3GPP version 3.1.1. In contrast to the 3GPP versions 2.0.0 and 2.1.0, there is no longer a large channel table with 128 DPDCHs. A mobile station has now 6 DPDCHs at maximum, the parameters of which are prescribed to a large extent by the standard. In order to obtain clear and simple operation, distinction is now made between 3 modes in the uplink (PRACH only, PCPCH only and DPCCH + DPDCH). In each mode, only the relevant parameters are displayed. But it is still possible to simulate a signal scenario of up to 4 mobile stations.
The menu is hierarchical with several levels. This organizes the large number of parameters in a clear way.
There are also other menus with parameters that modify the W-CDMA signal.

The following figure overviews all menus relevant for the W-CDMA signal and refers to related sections in the manual.

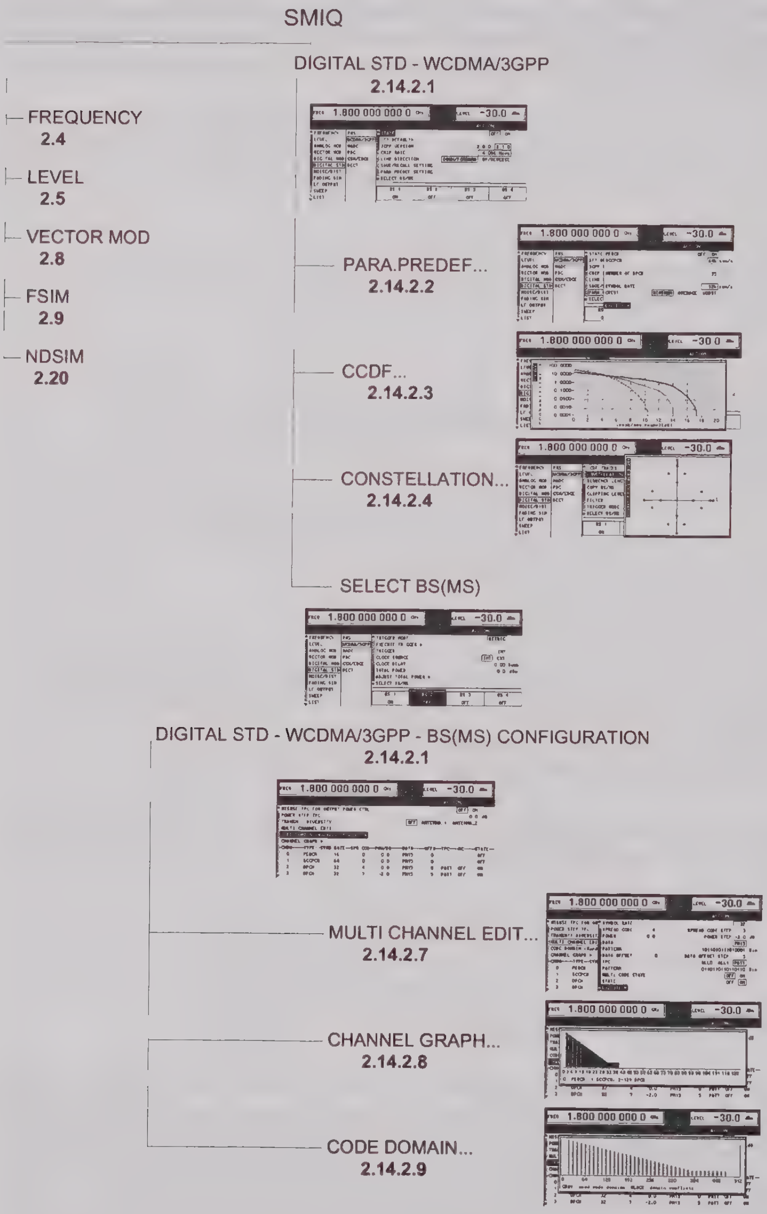
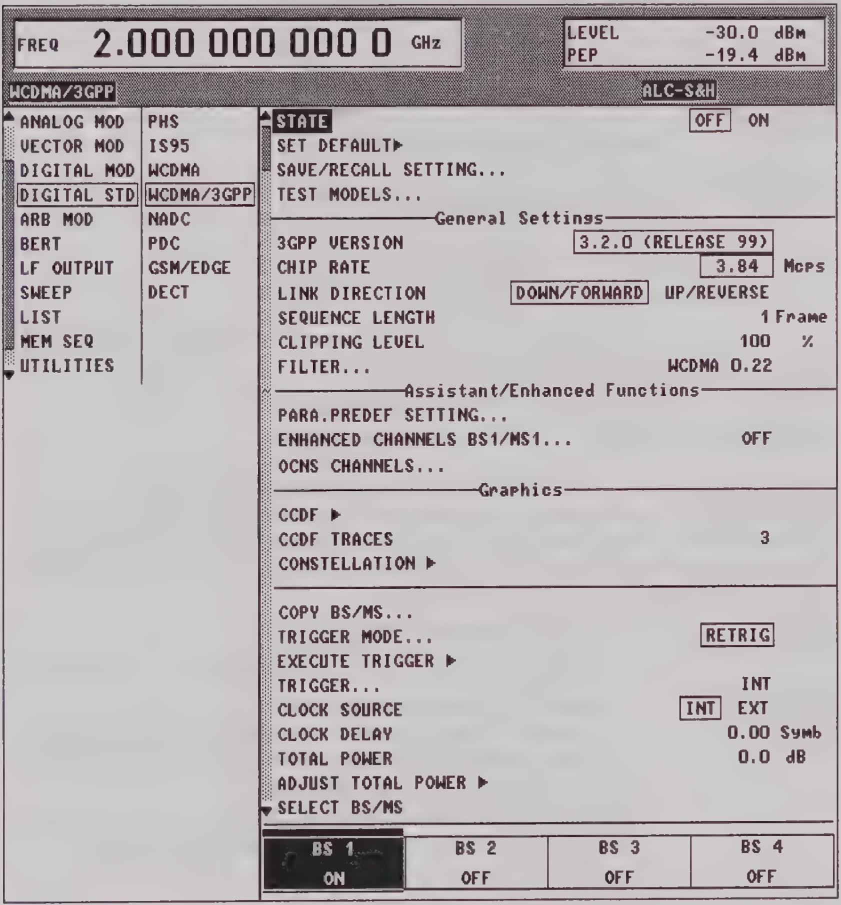

SMIQ
DIGITAL STD - WCDMA/3GPP
2.14.2.1
FREQUENCY
2.4
LEVEL
2.5
VECTOR MOD
2.8
FSIM
2.9
NDSIM
2.20
PARA.PREDEF...
2.14.2.2
CCDF...
2.14.2.3
CONSTELLATION...
2.14.2.4
SELECT BS(MS)
DIGITAL STD - WCDMA/3GPP - BS(MS) CONFIGURATION
2.14.2.1
MULTI CHANNEL EDIT...
2.14.2.7
CHANNEL GRAPH...
2.14.2.8
CODE DOMAIN...
2.14.2.9

Fig. 2-103 Overview of DIGITAL STD – 3GPP WCDMA/3GPP menu structure

Note: *The parts of the DIGITAL STD – WCDMA/3GPP menu that are below the SELECT BS(MS) function are not required if a signal is parameterized by means of the PARA. PREDFEF function (see section 2.14.2.2).*

2.14.2.1 Menu WCDMA/3GPP

The following figure shows the menu for generating a downlink signal (transmit signal of base station).



```
FREQ  2.000 000 000 0 GHz                      LEVEL      -30.0 dBm
                                               PEP        -19.4 dBm
WCDMA/3GPP                                     ALC-S&H
ANALOG MOD    PHS         STATE                          OFF  ON
VECTOR MOD    IS95        SET DEFAULT▶
DIGITAL MOD   WCDMA       SAVE/RECALL SETTING...
DIGITAL STD   WCDMA/3GPP  TEST MODELS...
ARB MOD       NADC        ---------General Settings---------
BERT          PDC         3GPP VERSION           3.2.0 (RELEASE 99)
LF OUTPUT     GSM/EDGE    CHIP RATE                      3.84 Mcps
SWEEP         DECT        LINK DIRECTION   DOWN/FORWARD  UP/REVERSE
LIST                      SEQUENCE LENGTH                   1 Frame
MEM SEQ                   CLIPPING LEVEL                   100   %
UTILITIES                 FILTER...                   WCDMA 0.22
                          ----Assistant/Enhanced Functions----
                          PARA.PREDEF SETTING...
                          ENHANCED CHANNELS BS1/MS1...      OFF
                          OCNS CHANNELS...
                          -------------Graphics-------------
                          CCDF ▶
                          CCDF TRACES                        3
                          CONSTELLATION ▶
                          ----------------------------------
                          COPY BS/MS...
                          TRIGGER MODE...                RETRIG
                          EXECUTE TRIGGER ▶
                          TRIGGER...                       INT
                          CLOCK SOURCE                 INT  EXT
                          CLOCK DELAY                 0.00 Symb
                          TOTAL POWER                   0.0 dB
                          ADJUST TOTAL POWER ▶
                          SELECT BS/MS
                          BS 1     BS 2     BS 3     BS 4
                          ON       OFF      OFF      OFF
```

Fig. 2-104 DIGITAL STD - WCDMA/3GPP - Downlink menu

For clarity, associated parameters are combined to subgroups by horizontal lines.

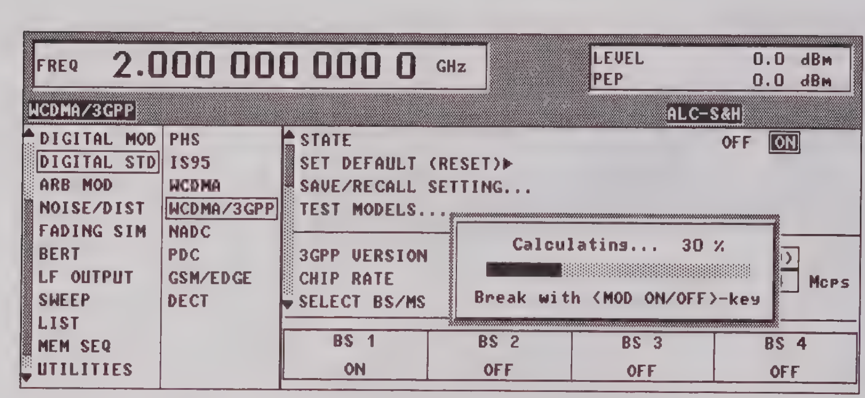
STATE Switch-on/off of modulation - digital standard 3GPP W-CDMA. If vector modulation, digital modulation or another digital standard are on, they are automatically switched off.

STATE = ON starts the calculation of a chip sequence based on current settings. The length of the chip sequence, and so the duration of the calculation, is determined by the SEQUENCE LENGTH parameter.

IEC/IEEE-bus command `:SOUR:W3GP:STAT ON`

Note: *When a parameter is changed, STATE is automatically switched to OFF to ensure that the set values and the output signal are consistent. After changing all the values, STATE can manually be set to ON. This is possible via menu parameter STATE and with the MOD ON/OFF key below the rollkey.*

A progress bar is displayed while the chip sequence is calculated. The progress of the calculation can also be read via the remote control interfaces.
IEC/IEEE-bus command `:SOUR:W3GP:CAL:PROG?`


WCDMA/3GPP
ALC-S&H
DIGITAL MOD
DIGITAL STD
ARB MOD
NOISE/DIST
FADING SIM
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
STATE
OFF ON
SET DEFAULT (RESET)▶
SAVE/RECALL SETTING...
TEST MODELS...
3GPP VERSION
CHIP RATE
Mcps
SELECT BS/MS
BS 1
BS 2
BS 3
BS 4

FREQ 2.000 000 000 0 GHz

LEVEL 0.0 dBm
PEP 0.0 dBm

Calculating... 30 %

Break with <MOD ON/OFF>-key

BS 1 ON · BS 2 OFF · BS 3 OFF · BS 4 OFF

Fig. 2-105 DIGITAL STD - WCDMA/3GPP menu – progress bar

SET DEFAULT ▶

Sets the default setup for 3GPP W-CDMA.
For further information about the default setting see section 2.14.2.12.

IEC/IEEE-bus command `:SOUR:W3GP:PRES`

SAVE/RECALL...

The SMIQ supports two parallel SAVE/RECALL memory systems: The SAVE and RCL hardkeys are used to handle the frequency, level and state in the 3GPP W-CDMA menu. Menu selection SAVE/REALL, however, manages all the other parameters in the 3GPP W-CDMA menu. This also includes station-specific parameters such as the channel table.

RECALL SETTING Recalling a saved configuration.

IEC/IEEE bus `:SOUR:W3GP:SETT:LOAD "MYSETUP"`

SAVE SETTING Saving the current configuration.
An existing setting can be overwritten or a new setting can be created. The name of the new setting is assigned automatically, e.g. WCDMASEQ1.
If the setting is created via the IEC/IEEE bus, the name is user-selectable and the usual restrictions apply.

IEC/IEEE bus `:SOUR:W3GP:SETT:STOR "MYSETUP"`

DELETE SETTING Deleting a configuration.

IEC/IEEE bus `:SOUR:W3GP:SETT:DEL "MYSETUP"`

TEST MODELS

Selection of one of the test models defined in the 3GPP standard [3]. This menu item is available only in the downlink. The following test scenarios are available:

Test Model	**Application/measurement**
Test1_16 TEST MODEL 1 (16 channels)	Spectrum emission mask ACLR Spurious emissions Transmit intermodulation Modulation accuracy Peak code domain error
Test1_32 TEST MODEL 1 (32 channels)	Spectrum emission mask ACLR Spurious emissions Transmit intermodulation Modulation accuracy

Test1_64 TEST MODEL 1 (64 channels)	Spectrum emission mask ACLR Spurious emissions Transmit intermodulation Modulation accuracy
Test2 TEST MODEL 2	Output power dynamics
Test3_16 TEST MODEL 3 (16 channels)	Peak code domain error
Test3_32 TEST MODEL 3 (32 channels)	Peak code domain error

IEC/IEEE-bus command `:SOUR:W3GP:SETT:TMOD "TEST1_16"`

TEST MODELS (NOT STANDARDIZED)

No test models are defined for the uplink in the 3GPP standard. To be able to generate appropriate test signals by a keystroke nevertheless, the SMIQ offers predefined test signals also for the uplink.

Test model	Description
C+D60 k	Mobile station 1 is activated in the DPCCH + DPDCH mode. 60 ksps is selected as overall symbol rate and the power of DPCCH and DPDCH is set to 0 dB.
C+D960 k	Mobile station 1 is activated in the DPCCH + DPDCH mode. 960 ksps is selected as overall symbol rate and the power of DPCCH and DPDCH is set to 0 dB.

IEC/IEEE bus `:SOUR:W3GP:SETT:TMOD "C+D60 k"`

----------------------**General Settings**---

3GPP VERSION

Display of current 3GPP version 3.2.0.

IEC/IEEE-bus command `:SOUR:W3GP:GPP3:VERS?`

CHIP RATE

Display of fixed chip rate for a 3GPP W-CDMA of 3.84 Mcps.

IEC/IEEE-bus command `:SOUR:W3GP:CRAT?`

LINK DIRECTION

Selecting the link direction:

DOWN/FORWARD Activates downlink-signal generation. In this mode, the base-station transmit signal can be generated.

IEC/IEEE-bus command `:SOUR:W3GP:LINK FORW`

UP/REVERSE Activates uplink-signal generation. In this mode, the mobile-station transmit signal can be generated.

IEC/IEEE-bus command `:SOUR:WCDM:LINK REV`

SEQUENCE LENGTH (FRAMES)

The sequence length can be set.

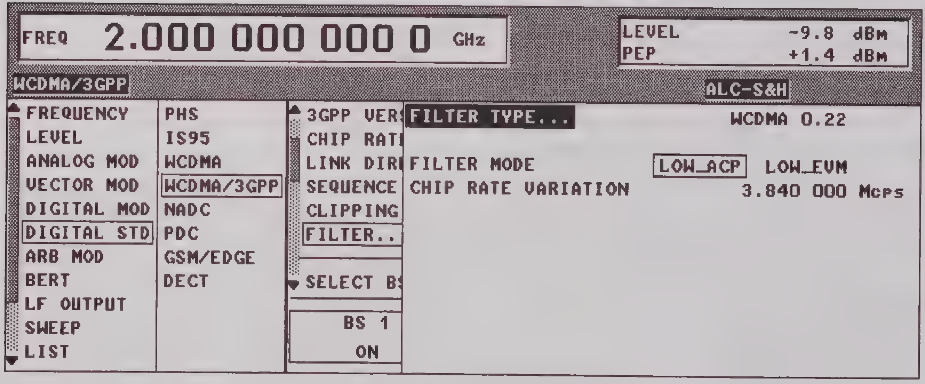
The maximum length is 13 frames.

IEC/IEEE-bus command `:SOUR:W3GP:SLEN 4`

CLIPPING LEVEL

Sets a clipping level relative to the highest peak in per cent. The value range is 1 to 100 %. A value of 100% means that the signal is not clipped. Clipping reduces the crest factor. For background information on how to use this parameter see section 2.14.2.10.
This menu item is only available with Option SMIQB48 installed. For details, see section 2.15.1.

IEC/IEEE-bus command `:SOUR:W3GP:CLIPP:LEV 50`


FREQ 2.000 000 000 0 GHz

LEVEL -9.8 dBm

PEP +1.4 dBm

FILTER TYPE... WCDMA 0.22

FILTER MODE LOW_ACP LOW_EVM

CHIP RATE VARIATION 3.840 000 Mcps

WCDMA/3GPP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
BERT
LF OUTPUT
SWEEP
LIST
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
3GPP VER
CHIP RATI
LINK DIRI
SEQUENCE
CLIPPING
FILTER..
SELECT BS
BS 1
ON

Fig. 2-106 DIGITAL STD - WCDMA/3GPP - FILTER... menu

FILTER... **FILTER**

Selects the baseband filter.
Either the W-CDMA standard filter WCDMA 0.22, the general Nyquist filters COS and SQRCOS or a user-defined filter USER (see section Digital Modulation) can be selected. The optimized square-root cosine filter WCDMA 0.22 is preset. Its roll-off factor is fixed at 0.22.

IEC/IEEE bus `:SOUR:W3GP:FILT:TYPE WCDM`

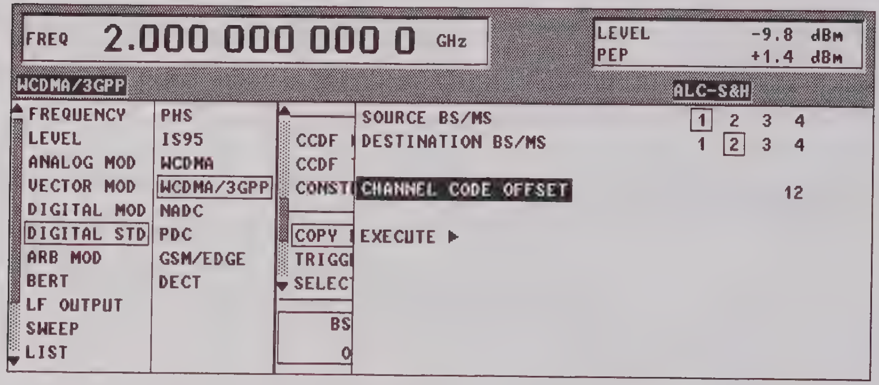
(FILTER...)	**ROLL OFF FACTOR**	Setting value for the roll-off factor. The setting is only possible if a COS or SQRCOS filter is selected under FILTER. IEC/IEEE bus `:SOUR:W3GP:FILT:PAR 0.22`
	FILTER MODE	Selection of filter mode. LOW_ACP Filter for lowest adjacent-channel power. IEC bus `:SOUR:W3GP:FILT:MODE LACP` LOW_EVM Filter for lowest error vector magnitude. IEC bus `:SOUR:W3GP:FILT:MODE LEVM`
	CHIP RATE VARIATION	Input value of chip clock frequency. The value for CHIP RATE from the main menu is preset. CHIP RATE VARIATION changes the output clock, the modulation bandwidth and the output sync signals. It has no effect on the calculated chip sequence. The value range is 100 cps to 18 Mcps. IEC bus `:SOUR:W3GP:CRAT:VAR 4096001`

----------------------**Assistant/Enhanced Functions**--

PARA. PREDEF.	Calling parameterized predefined settings. This menu item is only available in the downlink. For a description of this function see section 2.14.2.2.
ENHANCED CHANNELS BS1/MS1	Calling an operating menu for the configuration of extended functions of the digital standard 3GPP W-CDMA. The menu item is available only if option SMIQB48 is installed. For more detail on this menu see section 2.15.1.
OCNS CHANNELS	Simulation of Orthogonal Channel Noise. The menu item can be accessed only in the downlink an is available provided option SMIQB48 is installed. For more detail on this menu see section 2.15.1.
ADDITIONAL MS BASED ON MS4	Simulation of up to 50 further mobile stations. The menu item can be accessed only in the uplink and is available provided option SMIQB48 is installed. For more detail on this menu see section 2.15.1.

----------------------**Graphics**--

CCDF	A complementary cumulative distribution function is calculated and displayed using the signal in the waveform memory. For details see section 2.14.2.3.
CCDF TRACES	The number of simultaneously displayed CCDF traces can be set. For details see section 2.14.2.3.
CONSTELLATION	The constellation diagram for the signal in the waveform memory is calculated and displayed. For details see section 2.14.2.4.


WCDMA/3GPP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
BERT
LF OUTPUT
SWEEP
LIST
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
CCDF
CCDF
CONST
COPY
TRIGG
SELEC
BS
0
EXECUTE ▶

FREQ 2.000 000 000 0 GHz
LEVEL -9.8 dBm
PEP +1.4 dBm
SOURCE BS/MS ☐1 2 3 4
DESTINATION BS/MS 1 ☐2 3 4
CHANNEL CODE OFFSET 12

Fig. 2-107 DIGITAL STD - WCDMA/3GPP - Downlink - COPY BS(MS) menu

COPY BS(MS)...

Using COPY BS (MS) it is easy to create a new base/mobile station from an existing station. This feature is useful, say, when base stations with more than 128 data channels are to be simulated. For examples, see section 2.14.3.6.2.

SOURCE BS/MS For selecting the BS(MS) whose parameter set is to be copied. Possible values are 1, 2, 3 or 4.

IEC/IEEE-bus command `:SOUR:W3GP:COPY:SOUR 1`

DESTINATION BS/MS For selecting the destination BS(MS) whose parameter set is to be overwritten. Possible values are 1, 2, 3 or 4.

IEC/IEEE-bus command `:SOUR:W3GP:COPY:DEST 2`

CHANNELIZATION CODE OFFSET Offset used for the spreading codes during copying from the source BS to the channelization codes. The minimum value is 0 and the maximum value 511. This parameter is only available in the downlink.

IEC/IEEE-bus command `:SOUR:W3GP:COPY:COFF 123`

EXECUTE ▶ Starts copying.

IEC/IEEE-bus command `:SOUR:W3GP:COPY:EXEC`

TRIGGER MODE

Configuration of W-CDMA sequence control (see section 2.14.2.11).

AUTO The calculated W-CDMA chip sequence is immediately output and repeated cyclically. Trigger events are ignored.

IEC/IEEE-bus command `:SOUR:W3GP:SEQ AUTO`

RETRIG The calculated W-CDMA chip sequence is immediately output and repeated cyclically. A trigger event causes a restart from frame 1.

IEC/IEEE-bus command `:SOUR:W3GP:SEQ RETR`

ARMED_AUTO Only a trigger event results in the W-CDMA chip sequence being started from frame 1. Other trigger events are ignored.

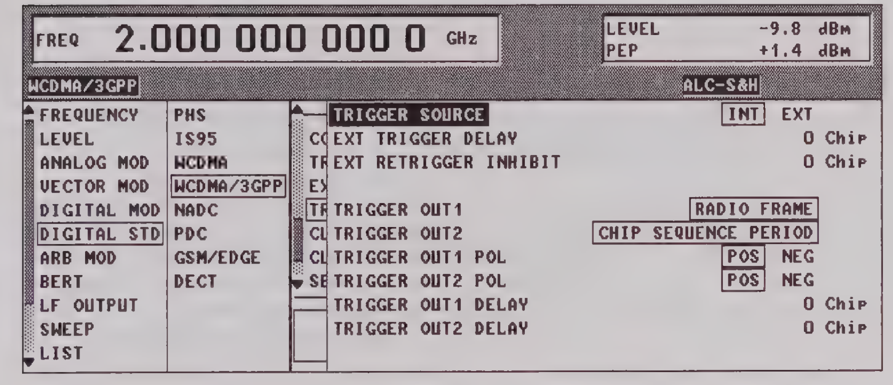
IEC/IEEE-bus command `:SOUR:W3GP:SEQ AAUT`

ARMED_RETRIG Only a trigger event results in the W-CDMA chip sequence being started from frame 1. Each further trigger event causes a restart.

IEC/IEEE-bus command `:SOUR:W3GP:SEQ ARET`

EXECUTE TRIGGER ▶ Executes a trigger event at the start of the W-CDMA chip sequence.

IEC/IEEE-bus command `:TRIG:DM:IMM`



FREQ 2.000 000 000 0 GHz
LEVEL -9.8 dBm
PEP +1.4 dBm

TRIGGER SOURCE INT EXT
EXT TRIGGER DELAY 0 Chip
EXT RETRIGGER INHIBIT 0 Chip
TRIGGER OUT1 RADIO FRAME
TRIGGER OUT2 CHIP SEQUENCE PERIOD
TRIGGER OUT1 POL POS NEG
TRIGGER OUT2 POL POS NEG
TRIGGER OUT1 DELAY 0 Chip
TRIGGER OUT2 DELAY 0 Chip

Fig. 2-108 DIGITAL STD – WCDMA/3GPP – TRIGGER... menu

TRIGGER... Opens a window to select the trigger source, to configure trigger output signals and to set the delay of an external trigger signal (see also section 2.14.2.11).

TRIGGER SOURCE For selecting the trigger source.

EXT The W-CDMA chip sequence is started from frame 1 on the active edge of an external trigger signal. The polarity, the trigger threshold and the input resistance of the TRIGIN input can be changed in the DIGITAL MOD - EXT INPUTS menu.

INT A trigger event is activated manually by EXECUTE TRIGGER.

IEC/IEEE-bus `:SOUR:W3GP:TRIG:SOUR EXT`

EXT TRIGGER DELAY For setting the number of chips by which an external trigger signal is delayed before it starts the W-CDMA chip sequence. This is used to set up synchronization with the DUT or other units.

IEC/IEEE-bus `:SOUR:W3GP:TRIG:DEL 3`

EXT RETRIGGER INHIBIT Sets the number of chips by which a restart is delayed after a trigger event. If the RETRIG TRIGGER MODE is selected, every further trigger signal restarts the W-CDMA chip sequence. This restart is inhibited for the number of chips that have been entered.

Example:
The effect of entering 82000 chips is that any further trigger signal is ignored for 82000 chips after a trigger event.

IEC/IEEE-bus `:SOUR:W3GP:TRIG:INH 82000`

(TRIGGER...)

TRIGGER OUT 1/2

For selecting signals for outputs TRIGOUT 1 and TRIGOUT 2 in the PARDATA connector.
The times only apply if the internal clock generation frequency was not modified with the CHIP RATE VARIATION parameter.

SLOT	0.667 ms slot clock IEC/IEEE-bus command: `:SOUR:W3GP:TRIG:OUTP1 SLOT`
RADIO FRAME	10 ms frame clock IEC/IEEE-bus command: `:SOUR:W3GP:TRIG:OUTP1 RFR`
CHIP SEQUENCE PERIOD	Marker signal for identifying the periodic repetition of the generated chip sequence IEC/IEEE-bus command: `:SOUR:W3GP:TRIG:OUTP1 CSP`
ENHANCED CHIP SEQUENCE PERIOD	Marker signal for marking the periodic repetition of generated enhanced chip sequence (only displayed with option SMIQB48 installed). IEC/IEEE-bus command: `:SOUR:W3GP:TRIG:OUTP1 ECSP`
P-CCPCH /BCH SFN RESTART	Marker signal for identifying the restart of the system frame number (SFN Restart) after 4096 frames (available only when option SMIQB48 is installed and a P-CCPCH/BCH is generated). IEC/IEEE-bus command: `:SOUR:W3GP:TRIG:OUTP1 SFNR`

TRIGGER OUT 1/2 POL

Selects the polarity of signals at the TRIGOUT 1 and TRIGOUT 2 outputs in the PARDATA connector.
IEC/IEEE-bus `:SOUR:W3GP:OUTP2:POL POS`

TRIGGER OUT 1/2 DELAY

Setting of the number of chips by which the selected trigger output signal is delayed.

IEC/IEEE-bus `:SOUR:W3GP:OUTP2:DEL 0`

CLOCK SOURCE

Allows selection of the clock source for the chip clock (see section 2.14.2.11).

INT	The SMIQ operates with internally generated clock signals.
EXT	A chip clock has to be fed in externally at the SYMBOL CLOCK connector. The CHIP RATE parameter must be set with an accuracy of ± 1 %. The polarity, the trigger threshold and the input resistance of the SYMBOL CLOCK input can be changed in DIGITAL MOD - EXT INPUTS menu.

IEC/IEEE-bus command

`:SOUR:W3GP:CLOC:SOUR INT`

EXT CLOCK MODE

Selection of type of external chip signal.

CHIP	chip clock
CHIPX4	Vierfacher Chiptakt

ATTENTION:
An external chip clock is fed in at connector SYMBOL CLOCK. Multi-folds chip clock has to be fed in at the connector BIT CLOCK!

IEC/IEEE bus command

`:SOUR:W3GP:CLOCk:MODE CHIP4`

CLOCK DELAY

Setting the delay of generated modulation signal to an external clock. This can be used, for example, for synchronization with a second unit to achieve time synchroneity between the modulation signals of the two units. The displayed setting resolution of 1/100 symbol is only attained for symbol-clock frequencies below 100 kHz. The resolution is reduced with increasing frequency. At a chip rate of 1.2288 Mcps, the resolution equals the chip duration × 0.1.

IEC/IEEE-bus command

`:SOUR:W3GP:CLOC:DEL 0.5`

TOTAL POWER

Displays the total power of activated code channels. The total power is calculated from the power ratio of the activated code channels with the modulation switched on (STATE=ON). If the value is not equal to 0 dB, the individual code channels are internally adjusted (while maintaining the power ratios) so that the TOTAL POWER is 0 dB to obtain the output level (LEVEL) set in the header of the display. In addition to the average power, the peak envelope power (PEP) is also displayed.

IEC/IEEE-bus command `:SOUR:W3GP:POW?`

ADJUST TOTAL POWER ▶

Changes the power of every activated code channel so that the TOTAL POWER returns to 0 dB again, while the ratio of the powers of the individual code channels remains the same.
IEC/IEEE-bus command
`:SOUR:W3GP:POW:ADJ`

SELECT BS/MS

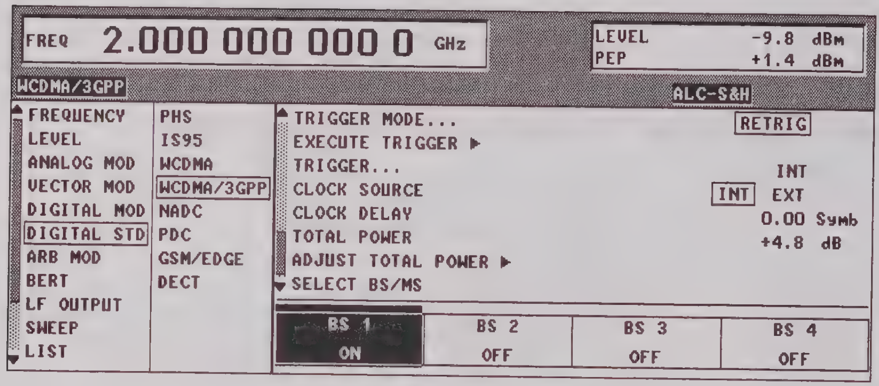

FREQ 2.000 000 000 0 GHz
LEVEL -9.8 dBm
PEP +1.4 dBm
TRIGGER MODE... RETRIG
CLOCK SOURCE INT EXT
CLOCK DELAY 0.00 Symb
TOTAL POWER +4.8 dB
BS 1 ON
BS 2 OFF
BS 3 OFF
BS 4 OFF
WCDMA/3GPP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
BERT
LF OUTPUT
SWEEP
LIST
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
EXECUTE TRIGGER ▶
TRIGGER...
INT
ADJUST TOTAL POWER ▶
SELECT BS/MS

Fig. 2-109 DIGITAL STD – WCDMA/3GPP – SELECT BS(MS) menu

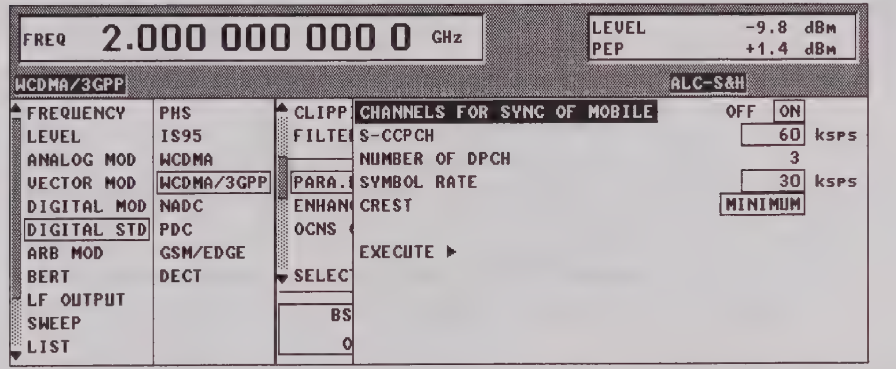
SELECT BS/MS has a special significance in the 3GPP W-CDMA menu: It is always displayed in the bottom line of the scrollable area, i.e. above the four station blocks. After selection of this menu item (**SELECT** hardkey), the cursor is positioned in the area below. The station whose parameters are to be edited can be selected by means of the spinwheel or the arrow keys.

Note: *The state of the BS/MS can also be toggled without selecting the station. Just move the cursor to the station and press the ENTER key.*

For editing the station parameters see section 2.14.2.5.

2.14.2.2 WCDMA/3GPP Menu - Para. Predef. Submenu

With the Para. Predef. function, it is possible to create highly complex scenarios with just a few keystrokes. This function is of use if, say, just the envelope of the signal is of interest. The menu is only available in the downlink.



FREQ 2.000 000 000 0 GHz
LEVEL -9.8 dBm
PEP +1.4 dBm
CHANNELS FOR SYNC OF MOBILE OFF ON
S-CCPCH 60 ksps
NUMBER OF DPCH 3
SYMBOL RATE 30 ksps
CREST MINIMUM
EXECUTE ▶

Fig. 2-110 DIGITAL STD - WCDMA/3GPP - PARA. PREDEF. menu (downlink only)

CHANNELS FOR SYNC OF MOBILE

When this parameter is set to ON, all "special channels" required by a mobile for synchronization are automatically activated. These are the following channels: P-CPICH, P-SCH, S-SCH, PCCPCH.
IEC/IEEE-bus command `:SOUR:W3GP:PPAR:SCH OFF`

SCCPCH

OFF — SCCPCH is not used in the scenario.
IEC/IEEE-bus command
`:SOUR:W3GP:PPAR:SCCPC:STAT OFF`

15k, 30k, to 960k — SCCPCH is used in the scenario at the stated symbol rate.
IEC/IEEE-bus commands
`:SOUR:W3GP:PPAR:SCCPC:STAT ON`
`:SOUR:W3GP:PPAR:SCCPC:SRAT D15K`

NUMBER OF DPCH

Number of DPCH in the scenario. The minimum number is 0. The maximum number is the ratio of the chip rate and the symbol rate (maximum 512 at the lowest symbol rate of 7.5 ksps).
IEC/IEEE-bus command `:SOUR:W3GP:PPAR:DPCH:COUN 123`

SYMBOL RATE

Symbol rate of all DPCH.
Possible values are 7,5k, 15k, 30k, 60k, 120k, 240k, 480k and 960k.
IEC/IEEE-bus command `:SOUR:W3GP:PPAR:DPCH:SRAT D30K`

CREST

The crest factor of the signal can be modified by setting appropriate channelization codes and timing offsets.

MINIMUM: The crest factor is minimized (the channelization codes are distributed uniformly over the code domain. The timing offsets are increased by 3 per channel).
IEC/IEEE-bus command `:SOUR:W3GP:PPAR:CRES MIN`

AVERAGE: An average crest factor is set (the channelization codes are distributed uniformly over the code domain. The timing offsets are all set to 0).
IEC/IEEE-bus command `:SOUR:W3GP:PPAR:CRES AVER`

WORST: The crest factor is set to an unfavourable value (i.e. maximum) (the channelization codes are assigned in ascending order. The timing offsets are all set to 0.)
IEC/IEEE-bus command `:SOUR:W3GP:PPAR:CRES WORS`

EXECUTE ▶

The channel table is automatically filled up with the set parameters.

Scrambling Code 0 is automatically selected (as defined in the 3GPP test models).

IEC/IEEE-bus command `:SOUR:W3GP:PPAR:EXEC`

2.14.2.3 WCDMA/3GPP Menu - Display of CCDF

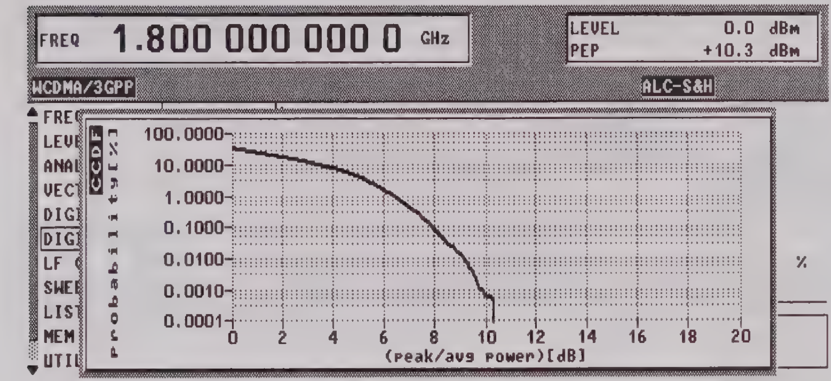

FREQ 1.800 000 000 0 GHz
LEVEL 0.0 dBm
PEP +10.3 dBm
WCDMA/3GPP
ALC-S&H
FREQ
LEVE
ANAL
VECT
DIGI
DIGI
LF
SWEE
LIST
MEM
UTIL
CCDF
probability[%]
100.0000
10.0000
1.0000
0.1000
0.0100
0.0010
0.0001
0
2
4
6
8
10
12
14
16
18
20
(peak/avg power)[dB]
%

Fig. 2-111 DIGITAL STD – WCDMA/3GPP – CCDF menu with a trace

A complementary cumulative distribution function" can be generated from the filtered I/Q samples. This function gives the probability of the magnitude of a complex sample exceeding a predefined threshold. The complete waveform, i.e. the sum for all (max. 4) stations is used to calculate the CCDF.

If this threshold is thought of as the radius of a circle centered on the origin, the radius is plotted along the horizontal axis and the probability of the samples lying outside this circle along the vertical axis. Only radii that correspond to values that are at least as great as the average power are considered.
The crest factor can be read off at the intersection of the trace with the x axis. The precise value is obtained from the displayed levels (PEP-LEVEL).

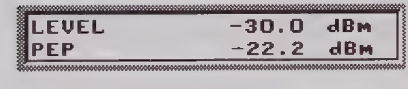

LEVEL -30.0 dBm
PEP -22.2 dBm

Fig. 2-112 Reading off the crest factor from LEVEL displays

In this example, the crest factor is 7.8 dB.

The number of simultaneously displayed traces can be set with the menu item NUMBER OF CCDF TRACES. Displaying several traces shows the effect of the parameters on the envelope. The baseband filter, the timing offsets and the channelization codes in particular have an effect on the CCDF.

The last three traces are distinguished in the following way:

Current trace: ━━━━━

Last trace: ─────

Last but one trace: – – – – –

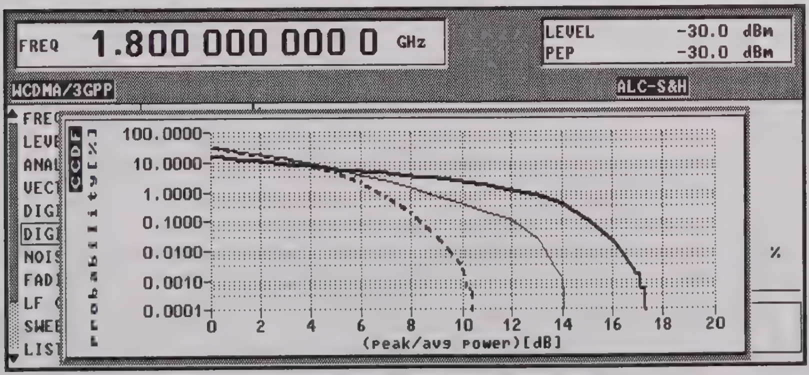
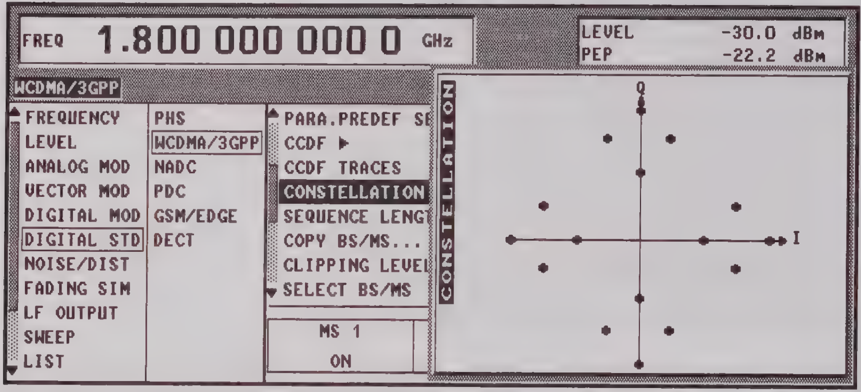

FREQ 1.800 000 000 0 GHz
LEVEL -30.0 dBm
PEP -30.0 dBm
WCDMA/3GPP
ALC-S&H
FREQ
LEVE
ANAL
VECT
DIGI
DIGI
NOIS
FADI
LF
SWEE
LIST
CCDF
probability[%]
100.0000
10.0000
1.0000
0.1000
0.0100
0.0010
0.0001
0
2
4
6
8
10
12
14
16
18
20
(peak/avg power)[dB]
%

Fig. 2-113 DIGITAL STD – WCDMA/3GPP – CCDF menu with three traces

2.14.2.4 WCDMA/3GPP Menu – Displaying Constellation Diagrams

A constellation diagram can be calculated and displayed from the unfiltered I/Q samples (menu item CONSTELLATION).


FREQ 1.800 000 000 0 GHz
LEVEL -30.0 dBm
PEP -22.2 dBm

WCDMA/3GPP
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
NOISE/DIST
FADING SIM
LF OUTPUT
SWEEP
LIST
PHS
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
PARA.PREDEF SE
CCDF ▶
CCDF TRACES
CONSTELLATION
SEQUENCE LENGT
COPY BS/MS...
CLIPPING LEVEL
SELECT BS/MS
MS 1
ON
CONSTELLATION
Q
I

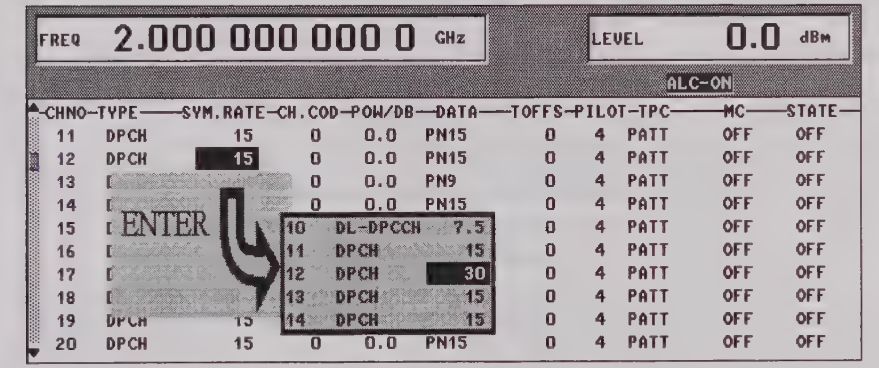
Fig. 2-114 DIGITAL STD - WCDMA/3GPP – CONSTELLATION menu

In addition to the code domain display (see section 2.14.2.9) and the CCDF display (see section 2.14.2.3) the constellation diagram is the most important display for a W-CDMA signal. In addition to channel powers, the effect of data sources etc and also the spreading scheme can be read off as a function of the type of channel. For further information on W-CDMA constellation diagrams see sections 2.14.3.1 and 2.14.3.3. The complete waveform, i.e. the sum for all (max. 4) stations is used to calculate the constellation diagram.

2.14.2.5 WCDMA/3GPP Menu - BS Configuration Submenu

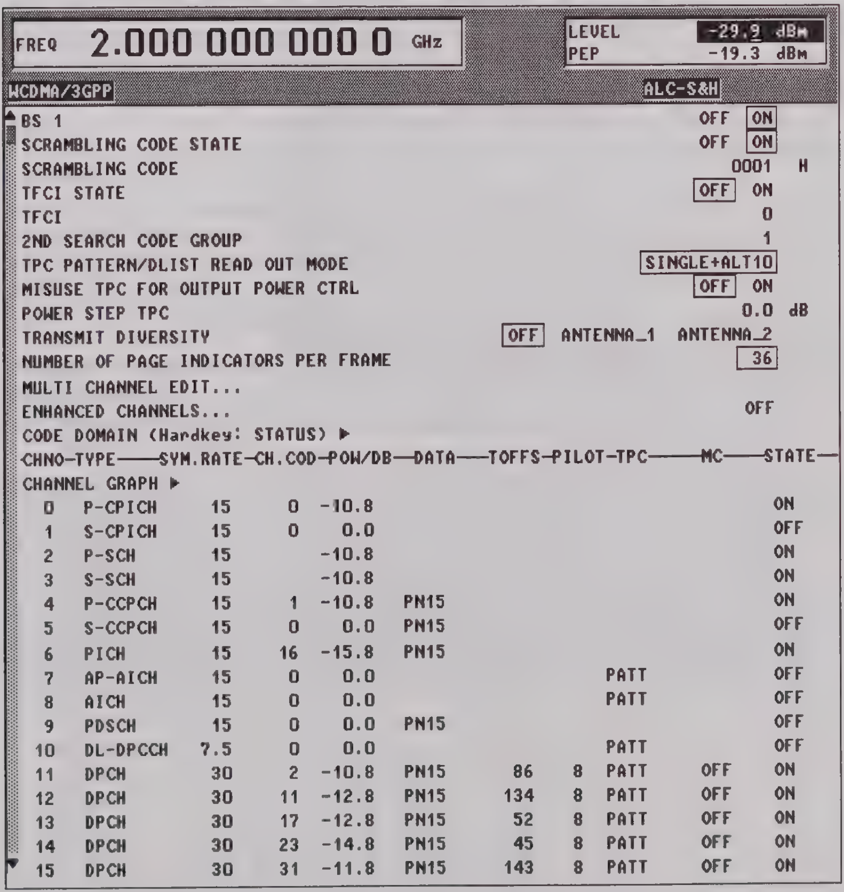
The channel configuration for each base station is performed in this window. In the upper part of the window, the parameters valid for the whole BS are set and the channel-specific parameters are set in the bottom part of the table.

Note: *To simplify handling of the large tables, state parameters like channel state or multi-code state can be toggled (ON/OFF) with the ENTER key.*
The ENTER key can also be used to switch from 1 value to the next when 1 of n values can be selected (e.g. symbol rate).



```
FREQ  2.000 000 000 0 GHz                  LEVEL        0.0 dBm
                                                     ALC-ON
CHNO-TYPE----SYM.RATE-CH.COD-POW/DB--DATA----TOFFS-PILOT-TPC------MC-----STATE-
  11   DPCH       15      0    0.0   PN15       0    4   PATT     OFF     OFF
  12   DPCH       15      0    0.0   PN15       0    4   PATT     OFF     OFF
  13   D                  0    0.0   PN9        0    4   PATT     OFF     OFF
  14   D                  0    0.0   PN15       0    4   PATT     OFF     OFF
  15   D                                        0    4   PATT     OFF     OFF
  16   D                                        0    4   PATT     OFF     OFF
  17   D                                        0    4   PATT     OFF     OFF
  18   D                                        0    4   PATT     OFF     OFF
  19   DPCH       15                            0    4   PATT     OFF     OFF
  20   DPCH       15      0    0.0   PN15       0    4   PATT     OFF     OFF

                 10  DL-DPCCH      7.5
                 11  DPCH           15
                 12  DPCH           30
                 13  DPCH           15
                 14  DPCH           15
```

Fig. 2-115 DIGITAL STD - WCDMA/3GPP menu – extended function of ENTER key



```
FREQ  2.000 000 000 0 GHz                  LEVEL      -29.9 dBm
                                           PEP        -19.3 dBm
WCDMA/3GPP                                           ALC-S&H
BS 1                                                      OFF  ON
SCRAMBLING CODE STATE                                     OFF  ON
SCRAMBLING CODE                                             0001   H
TFCI STATE                                                OFF  ON
TFCI                                                           0
2ND SEARCH CODE GROUP                                          1
TPC PATTERN/DLIST READ OUT MODE                      SINGLE+ALT10
MISUSE TPC FOR OUTPUT POWER CTRL                          OFF  ON
POWER STEP TPC                                               0.0  dB
TRANSMIT DIVERSITY                       OFF  ANTENNA_1  ANTENNA_2
NUMBER OF PAGE INDICATORS PER FRAME                           36
MULTI CHANNEL EDIT...
ENHANCED CHANNELS...                                         OFF
CODE DOMAIN (Hardkey: STATUS) ▶
CHNO-TYPE----SYM.RATE-CH.COD-POW/DB--DATA----TOFFS-PILOT-TPC------MC-----STATE-
CHANNEL GRAPH ▶
  0    P-CPICH     15      0   -10.8                                      ON
  1    S-CPICH     15      0     0.0                                      OFF
  2    P-SCH       15          -10.8                                      ON
  3    S-SCH       15          -10.8                                      ON
  4    P-CCPCH     15      1   -10.8   PN15                               ON
  5    S-CCPCH     15      0     0.0   PN15                               OFF
  6    PICH        15     16   -15.8   PN15                               ON
  7    AP-AICH     15      0     0.0                      PATT            OFF
  8    AICH        15      0     0.0                      PATT            OFF
  9    PDSCH       15      0     0.0   PN15                               OFF
 10    DL-DPCCH   7.5      0     0.0                      PATT            OFF
 11    DPCH        30      2   -10.8   PN15     86    8   PATT     OFF    ON
 12    DPCH        30     11   -12.8   PN15    134    8   PATT     OFF    ON
 13    DPCH        30     17   -12.8   PN15     52    8   PATT     OFF    ON
 14    DPCH        30     23   -14.8   PN15     45    8   PATT     OFF    ON
 15    DPCH        30     31   -11.8   PN15    143    8   PATT     OFF    ON
```

Fig. 2-116 DIGITAL STD - WCDMA/3GPP - BS CONFIGURATION menu

Parameters that are valid for the current base station:

BS 1(2,3,4) STATE

ON — The BS is active. All other parameters are activated.
IEC/IEEE-bus command `:SOUR:W3GP:BST1:STAT ON`

OFF — The BS is inactive. The other parameters are ignored.
IEC/IEEE-bus command `:SOUR:W3GP:BST2:STAT OFF`

SCRAMBLING CODE STATE

ON — The scrambling code is active.
IEC/IEEE-bus `:SOUR:W3GP:BST1:SCOD:STAT ON`

OFF — The scrambling code is inactive (test mode).
IEC/IEEE-bus `:SOUR:W3GP:BST2:SCOD:STAT OFF`

SCRAMBLING CODE

Code of base station. At the same time, the initial value for the scrambling code generator. Permissible values are in the range 0 to 0x5FFF.
IEC/IEEE-bus command `:SOUR:W3GP:BST1:SCOD #H1`

TFCI STATE

ON — TFCI field is used in the frame.
IEC/IEEE-bus command `:SOUR:W3GP:BST1:TFCI:STAT ON`

OFF — TFCI field is not used in the frame.
IEC/IEEE-bus command `:SOUR:W3GP:BST2:TFCI:STAT OFF`

TFCI (DPCH)

Setting the TFCI value in the range 0 to 1023. A combination of 30 bits is selected by this value. This combination is distributed over 15 successive slots in groups of two.

IEC/IEEE-bus command `:SOUR:W3GP:BST4:TFCI 21`

2ND SEARCH CODE GROUP

This parameter indexes the "Allocation of SSCs for secondary SCH" table described in the 3GPP standard, reference [2], Table 9. For each slot in the frame, this table assigns a certain spreading code to the synchronization code symbol. The parameter's range is 0 to 63. This parameter can only be read. The value is calculated from the SCRAMBLING CODE.

IEC/IEEE-bus command `:SOUR:W3GP:BST3:SSCG?`

TPC PATTERN READ OUT MODE

TPC bits are used for W-CDMA to inform the called station if the transmit power is being increased or decreased. The contents of the TPC field can be separately defined in this panel in the table below for the channels of the BS. The parameter TPC Pattern Read Out Mode is used to define how this bit pattern is to be used. The following modes are distinguished:

- Continuous: A bit is taken for each slot from the up to 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered in the bit stream several times (depending on the symbol rate). The specified bit pattern is used cyclically.
- Single + All 0: A bit is taken for each slot from the up to 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered the bit stream several times (depending on the symbol rate). The specified bit pattern is used once, then the TPC sequence is continued with 0 bits.
- Single + All 1: A bit is taken for each slot from the max. 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered in the bit stream several times (depending on the symbol rate. The specified bit pattern is used once, then the TPC sequence is continued with 1 bits.

(TPC PATTERN READ OUT MODE)

- Single + alt. 01: A bit is taken for each slot from the up to 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered the bit stream several times (depending on the symbol rate. The specified bit pattern is used once, then the TPC sequence is continued with 0 and 1 bits alternately (multiplied depending on the symbol rate, e.g. 00001111).
- Single + alt. 10: A bit is taken for each slot from the up to 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered in the bit stream several times (depending on the symbol rate. The specified bit pattern is used once, then the TPC sequence is continued with 1 and 0 bits alternately (multiplied depending on the symbol rate, e.g. 11110000).

The different modes can be used for example to set a mobile to a specific output power (e.g. with pattern 11111) and then let it oscillate about this power (using Single + alt. 01 or Single + alt. 10). Thus power measurements on the mobile can be performed (with a quasi-constant power). In conjunction with the option (Mis-)Use TPC for output power control (see below) the TPC Read Out Mode can also be used for generation of various output power profiles.

IEC/IEEE-bus command `:SOUR:W3GP:BST2:TPC:READ CONT`

MISUSE TPC FOR OUTPUT POWER CONTROL

TPC bits are used for W-CDMA to inform the called station if the transmit power is being increased or decreased. If this option is activated, the given pattern is being misused to vary one's own transmit power over time. For each slot, one bit of this pattern is taken to increase (bit = 1) or decrease (bit = 0) the channel power by the stated power step (**POWER STEP TPC**). The upper limit is 0 dB and the lower –60 dB. The following envelope occurs at a channel power of 0 dB, a power step of 1.0 dB, the pattern "001110100000011" and TPC Pattern ReadOut Mode **Continuous:**

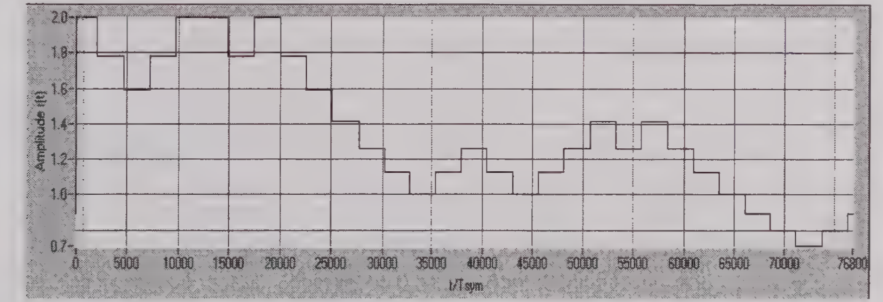


Fig. 2-117 Dynamic change of channel power (continuous)

Note: *Observe that the power change (as specified in the standard) is always performed at the beginning of the slot pilot field.*

IEC/IEEE-bus command `:SOUR:W3GP:BST1:TPC:MIS ON`

POWER STEP TPC

Size of power step in dB, if option **MISUSE TPC FOR OUTPUT POWER CONTROL** is activated. The value range is –10.0 dB to +10.0 dB.

IEC/IEEE-bus command `:SOUR:W3GP:BST1:POW:STEP 1.0`

TRANSMIT DIVERSITY

The 3GPP standard describes various forms of transmit diversity. The signal is distributed with different coding between two antennas. The SMIQ can simulate the signal for one of the two antennas. A fixed diversity scheme is assigned to each channel type:

DPCH, PCCPCH, SCCPH:	STTD	(Space time block coding transmit antenna diversity).
Primary SCH, secondary SCH:	TSTD	(Time switched transmit diversity for SCH).

Both schemes are described in detail in reference [1], section 5.3.1.
If transmit diversity is required, the antenna whose signal is to be simulated can be specified.

OFF: no transmit diversity
ANTENNA 1: calculate antenna signal for antenna 1 and display result
ANTENNA 2: calculate antenna signal for antenna 2 and display result

IEC/IEEE-bus command `:SOUR:W3GP:BST1:TRAN:DIV ANT1`

NUMBER OF PAGE INDICATORS PER FRAME

The number of page indicators per frame in the page indication channel (PICH) can be defined thanks to this menu item. The value range is 18, 36, 72, 144.

IEC/IEEE-bus command `:SOUR:W3GP:BST2:PIND:COUNT D36`

MULTI CHANNEL EDIT...

A certain number of DPCHs can be configured with just a few keystrokes using this menu item, see section 2.14.2.7.

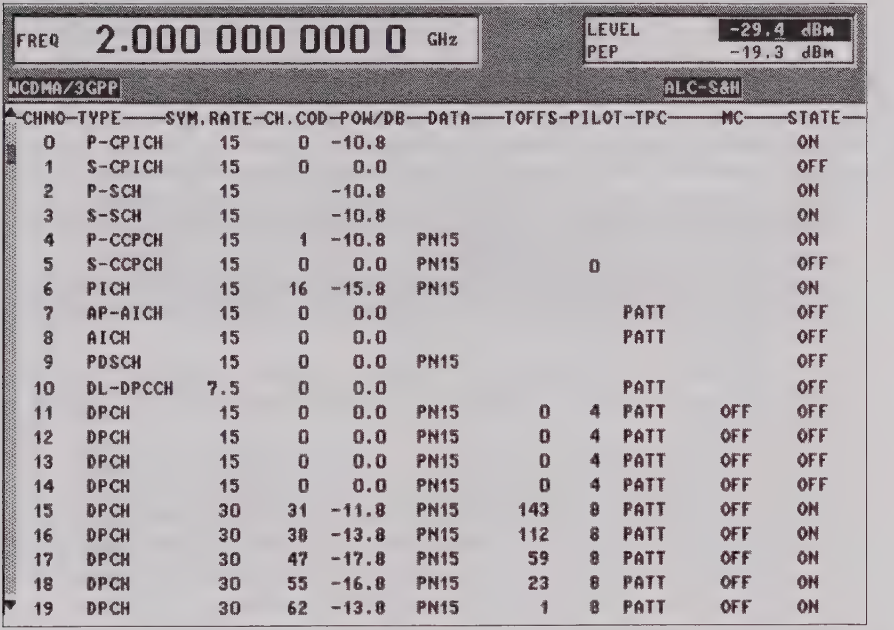
CHANNEL GRAPH

This menu item gives an overview of the assigned channels, see section 2.14.2.8.

CODE DOMAIN

This menu item indicates the assignment of code domain, see section 2.14.2.9.

Channel table:



FREQ 2.000 000 000 0 GHz

LEVEL -29.4 dBm

PEP -19.3 dBm

WCDMA/3GPP

ALC-S&H

CHNO	TYPE	SYM.RATE	CH.COD	POW/DB	DATA	TOFFS	PILOT	TPC	MC	STATE
0	P-CPICH	15	0	-10.8						ON
1	S-CPICH	15	0	0.0						OFF
2	P-SCH	15		-10.8						ON
3	S-SCH	15		-10.8						ON
4	P-CCPCH	15	1	-10.8	PN15					ON
5	S-CCPCH	15	0	0.0	PN15		0			OFF
6	PICH	15	16	-15.8	PN15					ON
7	AP-AICH	15	0	0.0				PATT		OFF
8	AICH	15	0	0.0				PATT		OFF
9	PDSCH	15	0	0.0	PN15					OFF
10	DL-DPCCH	7.5	0	0.0				PATT		OFF
11	DPCH	15	0	0.0	PN15	0	4	PATT	OFF	OFF
12	DPCH	15	0	0.0	PN15	0	4	PATT	OFF	OFF
13	DPCH	15	0	0.0	PN15	0	4	PATT	OFF	OFF
14	DPCH	15	0	0.0	PN15	0	4	PATT	OFF	OFF
15	DPCH	30	31	-11.8	PN15	143	8	PATT	OFF	ON
16	DPCH	30	38	-13.8	PN15	112	8	PATT	OFF	ON
17	DPCH	30	47	-17.8	PN15	59	8	PATT	OFF	ON
18	DPCH	30	55	-16.8	PN15	23	8	PATT	OFF	ON
19	DPCH	30	62	-13.8	PN15	1	8	PATT	OFF	ON

Fig. 2-118 DIGITAL STD – WCDMA/3GPP – BS CONFIGURATION / channel table menu

Below the general parameters of the current base station follows a table comprising all the parameters that can be edited on a channel-specific basis.

CH NO	Channel number, consecutive from 0 to 138. This value cannot be edited. All rows are displayed even if the channels are inactive. Activating/deactivating is via STATE at the end of the row.
TYPE	Channel type which is permanently linked to the channel number and which cannot be edited: The first 11 code channels in the table are occupied for the special channels P-CPICH to DL-DPCCH. All other code channels are DPCHs. The channel type is a pure display parameter and cannot be edited. Not all parameters in the table can be edited. This depends on the type of channel.
SYMBOL RATE	Symbol rate of channel in ksps. The value range of symbol rate is modified depending on the channel type. For example, the P-CPICH is fixed at a symbol rate of 15 ksps by the 3GPP standard, but all symbol rates between 7.5 ksps and 960 ksps are available for a DPCH. IEC/IEEE-bus command `:SOUR:W3GP:BST3:CHAN12:SRAT D30K`
CHANNELIZATION CODE	The channelization code (previously called spreading code number) is selected here. The code channel is then spread with the channelization code (spreading code) thus determined. The value range of the channelization code depends on the symbol rate of the channel. A fixed channelization code is allocated to a few channels as standard (the P-CPICH e.g. always uses the channelization code 0). The maximum value range extends from 0 to $\frac{chip\_rate(=3.84Mcps)}{symbol\_rate}-1$ IEC/IEEE-bus command `:SOUR:W3GP:BST3:CHAN12:CCOD 11`

POWER [DB]

Channel power in dB; range –60 to 0.0 dB.
The stated power is relative to the powers of other channels and initially does not refer to the LEVEL power display. After activating **ADJUST TOTAL POWER ▶** (top level of 3GPP W-CDMA menu) all power readings as referred to LEVEL.

IEC/IEEE-bus command `:SOUR:W3GP:BST3:CHAN3:POW -10.0`

DATA

Selects the data source for the DATA field.
Selection of PN9, PN11, PN15, PN16, ALL0, ALL1 and PATTern.
The PN generator function is explained in section 2.12.3, for example.
If the PATTern data type is used, the pattern can be modified with a special editor. The length is limited to 24 digits.

IEC/IEEE-bus command `:SOUR:W3GP:BST3:CHAN3:DATA PN15`
IEC/IEEE-bus command `:SOUR:W3GP:BST3:CHAN3:DATA:PATT #H1234,15`

TIMING OFFSET

A value greater than 0 causes the source symbols to be shifted prior to spreading. An offset of 1 for instance is recommended to obtain a low crest factor.
The absolute starting time of the frames (slot 0) is shifted relative to the beginning of the scrambling code sequence by T_{Offset} * 256 chips. This means that the resolution of the timing offset is always 256 chips, irrespective of the symbol rate. The effect of the timing offset is explained in the following. The value range of the timing offset is from 0 to 149. The parameter is accessible for DPCHs only.

IEC/IEEE-bus command `:SOUR:W3GP:BST3:CHAN3:TOFF 5`

PILOT LENGTH

Length of the pilot field in the slot structure of a channel. The value range of this parameter depends on the symbol rate. For DPCHs, the value range is 2 bits to max. 16 bits, for S_CCPCH 0, 8 and 16 bits. To achieve a constant length of the slot the data fields are lengthened or shortened depending on the pilot length as defined in the standard.

IEC/IEEE-bus command `:SOUR:W3GP:BST3:CHAN12:PLEN BIT2`

TPC

Selects the data source for the TPC field.

ALL0 All 0s are continuously entered in the TPC field.
IEC/IEEE-bus command `:SOUR:W3GP:BST1:CHAN2:TPC ZERO`

ALL1 All 1s are continuously entered in the TPC field.
IEC/IEEE-bus command `:SOUR:W3GP:BST2:CHAN129:TPC ONE`

PATT The TPC field is cyclically filled with a pattern that is up to 24 bits long. One bit is taken from this pattern per slot and, if necessary, reduplicated. A special pattern editor is displayed.
IEC/IEEE-bus commands

```
:SOUR:W3GP:BST2:CHAN24:TPC PATT
:SOUR:W3GP:BST2:CHAN24:TPC:PATT #H3F, 8
```

MC

ON This channel is to be simulated with multicode.
IEC/IEEE-bus command `:SOUR:W3GP:BST1:CHAN2:MCOD ON`

OFF This channel is to be simulated without multicode.
IEC/IEEE-bus command `:SOUR:W3GP:BST2:CHAN99:MCOD OFF`

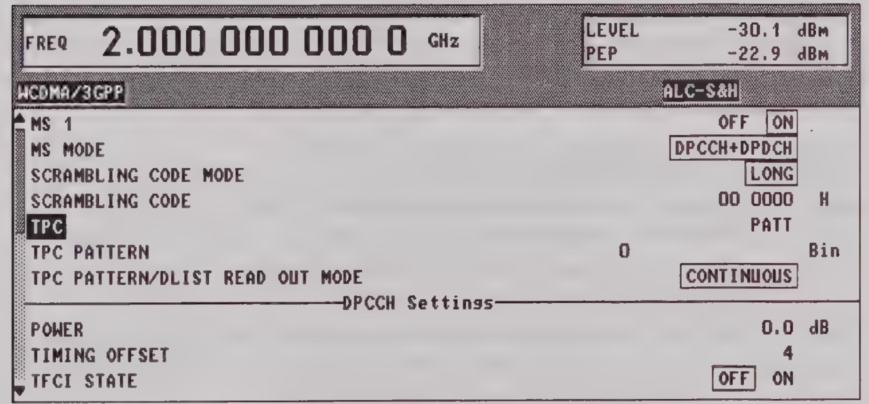
STATE

ON The channel is activated. All parameters in this row are active.
IEC/IEEE-bus command `:SOUR:W3GP:BST1:CHAN1:STATE ON`

OFF The channel is not active. All parameters in this row are ignored.
IEC/IEEE-bus command `:SOUR:W3GP:BST2:CHAN9:STATE OFF`

2.14.2.6 WCDMA/3GPP Menu - MS Configuration Submenu

The channels for each mobile station are configured in this window. The operation in the uplink was changed due to the extension to 3GPP version 3.1.1. In contrast to the 3GPP versions 2.0.0 and 2.1.0, there is no longer a large channel table with 128 DPDCHs. A mobile station has now 6 DPDCHs at maximum, the parameters of which are prescribed to a large extent by the standard. In order to obtain clear and simple operation, distinction is now made between 3 modes in the uplink (PRACH only, PCPCH only and DPCCH + DPDCH). In each mode, only the relevant parameters are displayed.



FREQ 2.000 000 000 0 GHz
LEVEL -30.1 dBm
PEP -22.9 dBm
WCDMA/3GPP
ALC-S&H

MS 1 OFF ON
MS MODE DPCCH+DPDCH
SCRAMBLING CODE MODE LONG
SCRAMBLING CODE 00 0000 H
TPC PATT
TPC PATTERN 0 Bin
TPC PATTERN/DLIST READ OUT MODE CONTINUOUS
DPCCH Settings
POWER 0.0 dB
TIMING OFFSET 4
TFCI STATE OFF ON

Fig. 2-119 DIGITAL STD – WCDMA/3GPP – MS CONFIGURATION menu

Parameters that are globally valid for current mobile station:

MS 1(2,3,4) STATE

ON The MS is active. All other parameters are active.
IEC/IEEE-bus command `:SOUR:W3GP:MST1:STAT ON`

OFF The MS is not active. The other parameters are ignored.
IEC/IEEE-bus command `:SOUR:W3GP:MST2:STAT OFF`

MS MODE

Mode in which the mobile station is to operate. Depending on the mode, the lower part of the panel changes. Only the parameters relevant to the current mode are displayed. The following modes can be selected:

PRACH only In this mode, the MS generates a single Physical Random Access Channel (PRACH). This channel is required when a call is set up from the mobile to the base station. The specific parameters of the PRACH can be set according to the section **PRACH Settings** (see further down in this section).
IEC/IEEE-bus command:
`:SOUR:W3GP:MST2:MODE PRACH`

PCPCH only In this mode, the MS generates a single Physical Common Packet Channel (PCPCH). This channel is used for the transmission of packet-oriented services (e.g. SMS). The specific parameters of the PCPCH can then be set according to the section **PCPCH Settings** (see further down in this chapter).
IEC/IEEE-bus command:
`:SOUR:W3GP:MST2:MODE PCPCH`

DPCCH + DPDCH This is the standard mode of the mobile for speech and data transmission. The MS generates a control channel (DPCCH) and up to 6 data channels (DPDCH). The channel-specific parameters can then be set in the sections **DPCCH Settings** and **DPDCH Settings** (see further down in this section).
IEC/IEEE-bus command:
`:SOUR:W3GP:MST2:MODE DPCDCH`

SCRAMBLING CODE MODE

A distinction is made between **Long** and **Short Scrambling Code**. The differences between the two codes can be found in section 2.14.1.1. The short SC can only be selected in the mode **DPCCH + DPDCH**. For PRACH and PCPCH, the long SC is always used.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:SCOD:MODE LONG`

For test purposes, it is also possible to deactivate the scrambling code (**OFF**).

IEC/IEEE-bus command `:SOUR:W3GP:MST2:SCOD:MODE OFF`

SCRAMBLING CODE

The scrambling code generator (previously called long code generator) is used to scramble the chip sequence depending on the transmitter. For more information of the structure of the generator and its initialization refer to section 2.14.1.1. The value range of the scrambling code is 0 to FF FFFF.

IEC/IEEE-bus command `:SOUR:W3GP:MST1:SCOD #H1`

TPC

Selects the data source for the TPC field.

ALL0 All 0s are continuously entered in the TPC field.
IEC/IEEE-bus command `:SOUR:W3GP:BST1:CHAN2:TPC ZERO`

ALL1 All 1s are continuously entered in the TPC field.
IEC/IEEE-bus command `:SOUR:W3GP:BST2:CHAN129:TPC ONE`

PATT The TPC field is cyclically filled with a pattern that is up to 24 bits long. One bit is taken from this pattern per slot and, if necessary, reduplicated. A special pattern editor is displayed.
IEC/IEEE-bus commands

```
:SOUR:W3GP:MST1:TPC:DATA PATT
:SOUR:W3GP:BST2:CHAN24:TPC:PATT #H3F, 8
```

DLIST The TPC field is cyclically filled with a data list. Per slot, one bit is taken from the list and duplicated (only offered with option SMIQB48 installed and the DPCCH is calculated as an enhanced channel.)
IEC/IEEE-bus command

```
:SOUR:W3GP:MST1:TPC:DATA DLIS "mylist"
```

TPC PATTERN/DLIST READ OUT MODE

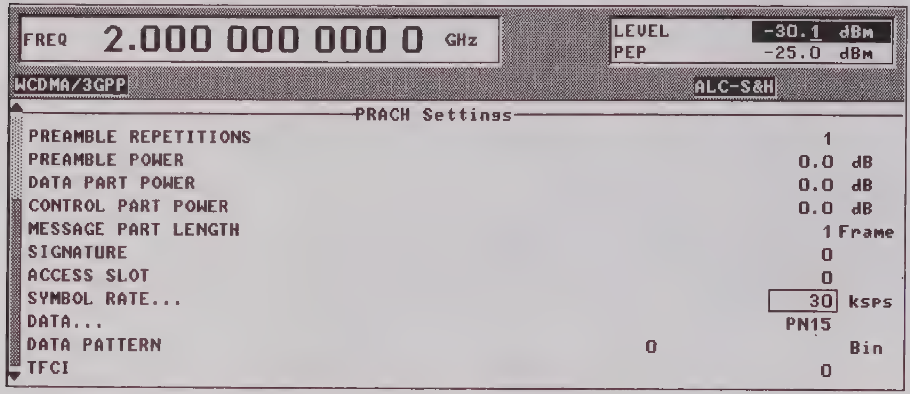
TPC bits are used for W-CDMA to inform the called station if the transmit power is being increased or decreased. The parameter TPC Pattern Read Out Mode is used to define how this bit pattern is to be used. The following modes are distinguished:

- Continuous: A bit is taken for each slot from the up to 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered in the bit stream several times (depending on the symbol rate). The specified bit pattern is used cyclically.
- Single + All 0: A bit is taken for each slot from the up to 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered the bit stream several times (depending on the symbol rate). The specified bit pattern is used once, then the TPC sequence is continued with 0 bits.
- Single + All 1: A bit is taken for each slot from the max. 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered in the bit stream several times (depending on the symbol rate. The specified bit pattern is used once, then the TPC sequence is continued with 1 bits.
- Single + alt. 01: A bit is taken for each slot from the up to 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered the bit stream several times (depending on the symbol rate. The specified bit pattern is used once, then the TPC sequence is continued with 0 and 1 bits alternately (multiplied depending on the symbol rate, e.g. 00001111).
- Single + alt. 10: A bit is taken for each slot from the up to 24-bit long bit pattern for the Transmitter Power Control field of the slots and entered in the bit stream several times (depending on the symbol rate. The specified bit pattern is used once, then the TPC sequence is continued with 1 and 0 bits alternately (multiplied depending on the symbol rate, e.g. 11110000).

The different modes can be used for example to set a BS to a specific output power (e.g. with pattern 11111) and then let it oscillate about this power (using Single + alt. 01 or Single + alt. 10). Thus power measurements on a channel of the BS can be performed (with a quasi-constant power). In conjunction with the option (Mis-)Use TPC for output power control (see below) the TPC Read Out Mode can also be used for generation of various output power profiles.
If option SMIQB48 is installed and if a data list is used as TPC data source, the READ OUT MODE is also valid.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:TPC:READ CONT`

Parameters in the PRACH only mode:



```
FREQ   2.000 000 000 0  GHz          LEVEL        -30.1  dBm
                                     PEP          -25.0  dBm
WCDMA/3GPP                                 ALC-S&H
                    PRACH Settings
PREAMBLE REPETITIONS                              1
PREAMBLE POWER                                  0.0  dB
DATA PART POWER                                 0.0  dB
CONTROL PART POWER                              0.0  dB
MESSAGE PART LENGTH                               1 Frame
SIGNATURE                                         0
ACCESS SLOT                                       0
SYMBOL RATE...                                   30  ksps
DATA...                                        PN15
DATA PATTERN                         0               Bin
TFCI                                              0
```

Fig. 2-120 DIGITAL STD – WCDMA/3GPP – MS CONFIGURATION: PRACH only Mode menu

PREAMBLE REPETITIONS

Number of repetitions of the preamble. Values between 1 and 10 can be selected.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:PREP 3`

PREAMBLE POWER

Power of the components of the PRACH. The value range of the power is from –60 dB to 0 dB.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:PPOW -5`

DATA PART POWER

Power of the data components of the PRACH. The value range of the power is from –60 dB to 0 dB.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:DPOW -3`

CONTROL PART POWER

Power of the control components of the PRACH. The value range of the power is from –60 dB to 0 dB.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:CPOW -7`

MESSAGE PART LENGTH

Length of message parts in Frames. Lengths 1 and 2 frames can be set.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:MLEN 2`

SIGNATURE

This field permits to determine the signature to be used for the PRACH. Values between 0 and 15 are possible.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:SIGN 2`

ACCESS SLOT#

Shifting of the starting time of the PRACH. The value range is 0 to 14.
(Shifting of the starting time in time slots can be calculated as follows:
2 * Access Slot #).

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:ASLO 2`

SYMBOL RATE

Symbol rate of the PRACH. It is possible to select between 15 ksps, 30 ksps, 60 ksps and 120 ksps.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:SRAT D30k`

DATA / DATA PATTERN

Selection of the data source for the DATA field. Selection from PN9, PN11, PN15, PN16, ALL0, ALL1 and PATTern. The PN generator function is explained in section 2.12.3, for example.
If PATTern data type is used, the pattern can be modified with a special editor. The length is limited to 24 digits.

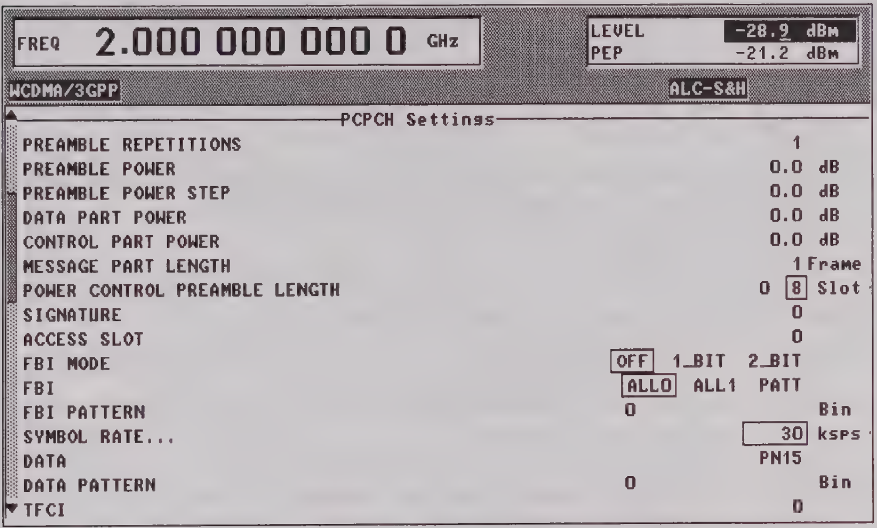
IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:DATA PN9`
IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:PATT #H1234, 15`

TFCI

Value of the TFCI (Transport Format Combination Indicator) field in the control part of the PRACH. The value range of the TFCI is 0 to 1023.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PRAC:TFCI 123`

Parameters in the PCPCH only mode:



FREQ 2.000 000 000 0 GHz

LEVEL -28.9 dBm

PEP -21.2 dBm

WCDMA/3GPP

ALC-S&H

PCPCH Settings

PREAMBLE REPETITIONS	1
PREAMBLE POWER	0.0 dB
PREAMBLE POWER STEP	0.0 dB
DATA PART POWER	0.0 dB
CONTROL PART POWER	0.0 dB
MESSAGE PART LENGTH	1 Frame
POWER CONTROL PREAMBLE LENGTH	0 8 Slot
SIGNATURE	0
ACCESS SLOT	0
FBI MODE	OFF 1_BIT 2_BIT
FBI	ALLO ALL1 PATT
FBI PATTERN	0 Bin
SYMBOL RATE...	30 ksps
DATA	PN15
DATA PATTERN	0 Bin
TFCI	0

Fig. 2-121 DIGITAL STD – WCDMA/3GPP – MS CONFIGURATION: PCPCH only Mode menu

PREAMBLE REPETITIONS Number of repetitions of the preamble. Values between 1 and 10 can be selected.
IEC/IEEE-bus command `:SOUR:W3GP:MST2:PCPC:PREP 3`

PREAMBLE POWER Power of the preamble components of the PCPCHs. The value range of the power is from –60 dB to 0 dB.
IEC/IEEE-bus command `:SOUR:W3GP:MST2: PCPC:PPOW -5`

PREAMBLE POWER STEP The power by which the preamble is increased from repetition to repetition can be set. The value range is 0 dB to 10 dB.

Note: *The preamble power that can be entered at the top of the panel is the destination power used in the last repetition of the preamble. If, for example, a preamble power of 0 dB, a repetition of 3 and a power step of 3 dB are set, the following power sequence will be generated:*

Preamble 1 -6 dB	→ + 3 dB	Preamble 2 -3 dB	→ + 3 dB	Preamble 3 0 dB

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PCPC:PPOW:STEP 3`

DATA PART POWER Power of the data components of the PCPCH. The value range of the power is from –60 dB to 0 dB.
IEC/IEEE-bus command `:SOUR:W3GP:MST2: PCPC:DPOW -3`

CONTROL PART POWER Power of the control components of the PCPCH. The value range of the power is from –60 dB to 0 dB.
IEC/IEEE-bus command `:SOUR:W3GP:MST2: PCPC:CPOW -7`

SHARED RESOURCE MODE The Shared Resource Mode influences the type of the scrambling code in the access preamble and in the message part of the PCPCH. For more details refer to [1] section 5.2.2.2.2 and [2] section 4.3.4.4.
IEC/IEEE-bus command `:SOUR:W3GP:MST2: PCPC:SHAR ON`

MESSAGE PART LENGTH Length of the message in frames. Lengths between 1 and 10 frames can be set.
IEC/IEEE-bus command `:SOUR:W3GP:MST2: PCPC:MLEN 3`

POWER CONTROL PREAMBLE LENGTH

Length of Power Control Preamble in slots. Lengths between 0 and 8 slots can be set.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PCPC:PLEN 8`

SIGNATURE

This field permits to determine the signature to be used for the PCPCH. Values between 0 and 15 can be set.

IEC/IEEE-bus command `:SOUR:W3GP:MST2: PCPC:SIGN 2`

ACCESS SLOT#

Shifting of the starting time of the PCPCH. The value range is 0 to 14. (Shifting of the starting time in time slots can be calculated as follows:
2 * Access Slot #).

IEC/IEEE-bus command `:SOUR:W3GP:MST2: PCPC:ASLO 2`

FBI MODE

OFF — The FBI field is not used in the frame.
IEC/IEEE-bus command
`:SOUR:W3GP:MST2:PCPC:FBI:MODE OFF`

1 BIT — The FBI field is used in the frame and is 1 bit long.
IEC/IEEE-bus command
`:SOUR:W3GP:MST2:PCPC:FBI:MODE D1B`

2 BIT — The FBI field is used in the frame and is 2 bits long.
IEC/IEEE-bus command
`:SOUR:W3GP:MST2:PCPC:FBI:MODE D2B`

FBI / FBI PATTERN

Selecting the data source for the FBI field.

ALL0 — All 0s are continuously entered in the FBI field.
IEC/IEEE-bus command
`:SOUR:W3GP:MST2:PCPC:FBI:DATA ZERO`

ALL1 — All 1s are continuously entered in the FBI field.
IEC/IEEE-bus command
`:SOUR:W3GP:MST2:PCPC:FBI:DATA ONE`

PATT — The FBI field is cyclically filled with a pattern that is up to 24 bits long. A special pattern editor is displayed.
IEC/IEEE-bus commands
`:SOUR:W3GP:MST2:PCPC:FBI:DATA PATT`
`:SOUR:W3GP:MST2:PCPC:FBI:PATT #H3F, 8`

SYMBOL RATE

Symbol rate of the PCPCH. All symbol rates between 15 ksps and 960 ksps are available.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PCPC:SRAT D30K`

DATA / DATA PATTERN

Selection of the data source for the DATA field. Selection from PN9, PN11, PN15, PN16, ALL0, ALL1 and PATTern. The PN generator function is explained in section 2.12.3, for example. If PATTern data type is used, the pattern can be modified with a special editor. The length is limited to 24 digits.

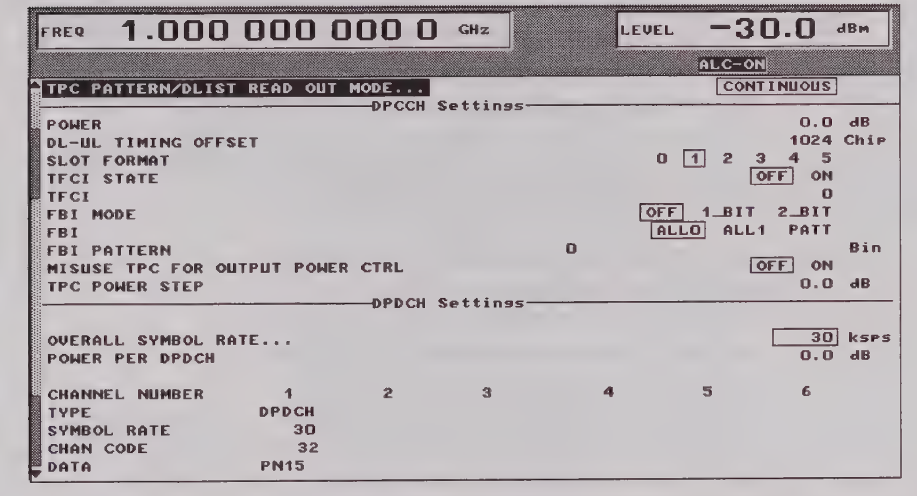
IEC/IEEE-bus command `:SOUR:W3GP:MST2:PCPC:DATA PN9`
IEC/IEEE-bus command `:SOUR:W3GP:MST2:PCPC:PATT #H1234, 15`

TFCI

Value of the TFCI (Transport Format Combination Indicator) field in the control part of the PCPC. The value range of the TFCI is 0 to 1023.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:PCPC:TFCI 123`

Parameters in the DPCCH + DPDCH mode:



```
FREQ  1.000 000 000 0 GHz                         LEVEL  -30.0 dBm
                                                         ALC-ON
TPC PATTERN/DLIST READ OUT MODE...                       CONTINUOUS
-------------------------DPCCH Settings-------------------------
POWER                                                      0.0 dB
DL-UL TIMING OFFSET                                       1024 Chip
SLOT FORMAT                                  0  1  2  3  4  5
TFCI STATE                                              OFF  ON
TFCI                                                         0
FBI MODE                                     OFF  1_BIT  2_BIT
FBI                                          ALLO  ALL1  PATT
FBI PATTERN                          0                         Bin
MISUSE TPC FOR OUTPUT POWER CTRL                        OFF  ON
TPC POWER STEP                                             0.0 dB
-------------------------DPDCH Settings-------------------------

OVERALL SYMBOL RATE...                                      30 ksps
POWER PER DPDCH                                            0.0 dB

CHANNEL NUMBER       1        2        3        4        5        6
TYPE               DPDCH
SYMBOL RATE          30
CHAN CODE            32
DATA               PN15
```

Fig. 2-122 DIGITAL STD – WCDMA/3GPP – MS CONFIGURATION: DPCCH + DPDCH Mode menu

------------------------**DPCCH Settings**--

POWER Channel power of the DPCCH, value range –60 to 0 dB

IEC/IEEE-bus command `:SOUR:W3GP:MST2:DPCC:POW -3`

TIMING OFFSET For the DPCCH (together with the DPDCHs), a fixed timing offset of 1024 chips (= 4 * 256 chips) is specified as standard. It is only displayed here and cannot be changed.

IEC/IEEE-bus command `:SOUR:W3GP:MST2:DPCC:TOFF?`

SLOT FORMAT Setting the TFCI STATE and FBI MODE parameters according to the following table :

SLOT FORMAT	TFCI STATE	FBI MODE
0	ON	OFF
1	OFF	OFF
2	ON	1bit
3	OFF	1bit
4	OFF	2bits
5	ON	2bits

TFCI STATE

ON The TFCI field is used in the frame.

IEC/IEEE-bus `:SOUR:W3GP:MST1:DPCC:TFCI:STAT ON`

OFF The TFCI field is not used in the frame.

IEC/IEEE-bus `:SOUR:W3GP:MST2:DPCC:TFCI:STAT OFF`

Note: *Any changes of this parameter will affect the SLOT FORMAT parameter (see table under SLOT FORMAT).*

TFCI Setting the TFCI value in the range 0 to 1023. A combination of 30 bits is selected by this value. This combination is distributed over 15 successive slots in groups of two.

IEC/IEEE-bus command `:SOUR:W3GP:MST4:DPCC:TFCI 21`

FBI MODE

OFF The FBI field is not used in the frame.

IEC/IEEE-bus `:SOUR:W3GP:MST2:DPCC:FBI:MODE OFF`

1 BIT The FBI field is used in the frame and is 1 bit long.

IEC/IEEE-bus `:SOUR:W3GP:MST2:DPCC:FBI:MODE D1B`

2 BIT The FBI field is used in the frame and is 2 bits long.

IEC/IEEE-bus `:SOUR:W3GP:MST2:DPCC:FBI:MODE D2B`

Note: *Any changes of this parameter will affect the SLOT FORMAT parameter (see table under SLOT FORMAT).*

FBI / FBI PATTERN

Selecting the data source for the FBI field.

ALL0 All 0s are continuously entered in the FBI field.

IEC/IEEE-bus command
`:SOUR:W3GP:MST2:PCPC:FBI:DATA ZERO`

ALL1 All 1s are continuously entered in the FBI field.

IEC/IEEE-bus command
`:SOUR:W3GP:MST2:PCPC:FBI:DATA ONE`

PATT The FBI field is cyclically filled with a pattern that is up to 24 bits long. A special pattern editor is displayed.

IEC/IEEE-bus commands:

```
:SOUR:W3GP:MST2:DPCC:FBI:DATA PATT
:SOUR:W3GP:MST2:DPCC:FBI:PATT #H3F, 8
```

MISUSE TPC FOR OUTPUT POWER CONTROL

TPC bits are used for W-CDMA to inform the called station if the transmit power is being increased or decreased. If this option is activated, the given pattern is being misused to vary one's own transmit power over time. For each slot, one bit of this pattern is taken to increase (bit = 1) or decrease (bit = 0) the channel power by the stated power step **(POWER STEP TPC**). The upper limit is 0 dB and the lower –60 dB.

The following envelope occurs at a channel power of 0 dB, a power step of 1.0 dB, the pattern "001110100000011" and TPC Pattern ReadOut Mode **Continuous:**

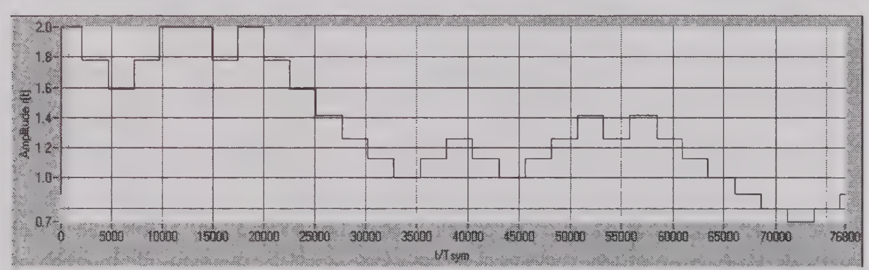


Fig. 2-123 Dynamic change of channel power (continuous)

Note: *Observe that the power change (as specified in the standard) is always performed at the beginning of the slot pilot field.*

IEC/IEEE-bus command `:SOUR:W3GP:MST1:DPCC:TPC:MIS ON`

TPC POWER STEP

Size of power step in dB, if option **MISUSE TPC FOR OUTPUT POWER CONTROL** is activated.

The range is –10.0 dB to +10.0 dB.

IEC/IEEE-bus command `:SOUR:W3GP:MST1:DPCC:TPC:PST 1.0`

-------------------------DPDCH Settings---

ENHANCED CHANNELS... Calling an operating menu for the configuration of extended functions of the digital standard 3GPP W-CDMA. The menu item is available only if option SMIQB48 is installed. For more details on this menu see section "Enhanced Functions for Digital Standard 3GPP W-CDMA".

OVERALL SYMBOL RATE Overall data rate of all uplink DPDCHs. The structure of the DPDCH table depends on this parameter. The overall symbol rate indicates the active DPDCHs, their symbol rate and the channelization codes used (see table further down).

Value range: off, 15 ksps, 30 ksps, 60 ksps, 120 ksps, 240 ksps, 480 ksps, 960 ksps, 2 x 960 ksps, 3 x 960 ksps, 4 x 960 ksps, 5 x 960 ksps and 6 x 960 ksps.

If **OFF** is selected, the signal only consists of one DPDCH. In this case, no channel table is displayed.

IEC/IEEE-bus command `:SOUR:W3GP:MST1:DPDC:ORAT D60K`

POWER PER DPCH Power of active DPDCHs. Each active DPDCH features this channel power. The power of the active channels cannot be configured independently (this also complies with the definition of 3GPP). Value range -60...0.0 dB.

IEC/IEEE-bus command `:SOUR:W3GP:MST1:DPDC:POW -23`

Table 2-23 Structure of the DPDCH channel table depending on the overall symbol rate

Overall symbol rate	DPDCH 1	DPDCH 2	DPDCH 3	DPDCH 4	DPDCH 5	DPDCH 6
15 ksps	State: ON S-Rate: 15 k Ch. Code: 64	State: OFF	State: OFF	State: OFF	State: OFF	State: OFF
30 ksps	State: ON S-Rate: 30 k Ch. Code: 32	State: OFF	State: OFF	State: OFF	State: OFF	State: OFF
60 ksps	State: ON S-Rate: 60 k Ch. Code: 16	State: OFF	State: OFF	State: OFF	State: OFF	State: OFF
120 ksps	State: ON S-Rate: 120 k Ch. Code: 8	State: OFF	State: OFF	State: OFF	State: OFF	State: OFF
240 ksps	State: ON S-Rate: 240 k Ch. Code: 4	State: OFF	State: OFF	State: OFF	State: OFF	State: OFF
480 ksps	State: ON S-Rate: 480 k Ch. Code: 2	State: OFF	State: OFF	State: OFF	State: OFF	State: OFF
960 ksps	State: ON S-Rate: 960 k Ch. Code: 1	State: OFF	State: OFF	State: OFF	State: OFF	State: OFF
2 x 960 ksps	State: ON S-Rate: 960 k Ch. Code: 1	State: ON S-Rate: 960 k Ch. Code: 1	State: OFF	State: OFF	State: OFF	State: OFF
3 x 960 ksps	State: ON S-Rate: 960 k Ch. Code: 1	State: ON S-Rate: 960 k Ch. Code: 1	State: ON S-Rate: 960 k Ch. Code: 3	State: OFF	State: OFF	State: OFF
4 x 960 ksps	State: ON S-Rate: 960 k Ch. Code: 1	State: ON S-Rate: 960 k Ch. Code: 1	State: ON S-Rate: 960 k Ch. Code: 3	State: ON S-Rate: 960 k Ch. Code: 3	State: OFF	State: OFF
5 x 960 ksps	State: ON S-Rate: 960 k Ch. Code: 1	State: ON S-Rate: 960 k Ch. Code: 1	State: ON S-Rate: 960 k Ch. Code: 3	State: ON S-Rate: 960 k Ch. Code: 3	State: ON S-Rate: 960 k Ch. Code: 2	State: OFF
6 x 960 ksps	State: ON S-Rate: 960 k Ch. Code: 1	State: ON S-Rate: 960 k Ch. Code: 1	State: ON S-Rate: 960 k Ch. Code: 3	State: ON S-Rate: 960 k Ch. Code: 3	State: ON S-Rate: 960 k Ch. Code: 2	State: ON S-Rate: 960 k Ch. Code: 2

Note: *With an overall rate smaller than 960 ksps only DPDCH1 is active, its rate is equal to the overall rate and the channelization code is equal to spreading factor/4 (spreading factor = chip rate / symbol rate). With an overall rate greater than 960 ksps, all active DPDCHs have the rate 960 ksps.*

Channel table of the DPDCHs:

SYMBOL RATE

The symbol rate of the DPDCH is displayed. This parameter cannot be changed. Its value depends on the overall symbol rate.
IEC/IEEE-bus command `:SOUR:W3GP:MST1:DPDC2:SRAT?`

CHANNELIZATION CODE

The channelization code of the DPDCH is displayed. This parameter cannot be changed. Its value depends on the overall symbol rate.
IEC/IEEE-bus command `:SOUR:W3GP:MST1:DPDC2:CCOD?`

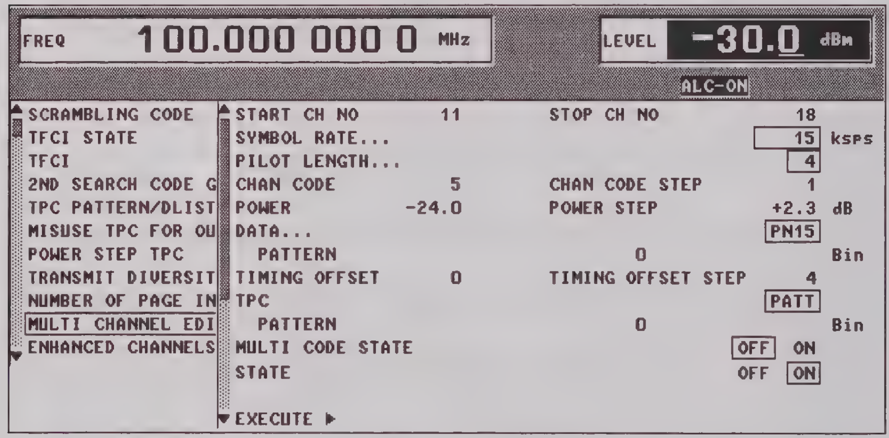
DATA

Selection of the data source for the DATA field. Selection from PN9, PN11, PN15, PN16, ALL0, ALL1 and PATTern. The PN generator function is explained in section 2.12.3, for example.
If PATTern data type is used, the pattern can be modified with a special editor. The length is limited to 24 digits.

IEC/IEEE-bus `:SOUR:W3GP:MST3:DPDC3:DATA PN15`
`:SOUR:W3GP:MST3:DPDC3:DATA:PATT #H1234, 15`

2.14.2.7 WCDMA/3GPP – Multi Channel Edit Menu

When MULTI CHANNEL EDIT is selected, a submenu in which several DPCHs can be configured is opened. This menu item is only available for base stations.



```
FREQ      100.000 000 0 MHz              LEVEL  -30.0 dBm
                                                ALC-ON
START CH NO          11       STOP CH NO              18
SYMBOL RATE...                                        15  ksps
PILOT LENGTH...                                        4
CHAN CODE             5       CHAN CODE STEP           1
POWER             -24.0       POWER STEP            +2.3  dB
DATA...                                             PN15
  PATTERN                            0                    Bin
TIMING OFFSET         0       TIMING OFFSET STEP       4
TPC                                                 PATT
  PATTERN                            0                    Bin
MULTI CODE STATE                                 OFF  ON
STATE                                            OFF  ON
```

Fig. 2-124 DIGITAL STD – WCDMA/3GPP – BS CONFIGURATION / MULTI CHANNEL EDIT menu

In this submenu, all parameters in the channel table can be set for a group of DPCHs. The parameter ranges are identical to those in the channel table.

MULTI CHANNEL EDIT...

START CH NO (DPCH)/ STOP CH NO (DPCH)

Range of DPCHs that are to be commonly configured. Range 11 to 138.

IEC/IEEE-bus command:
`:SOUR:W3GP:MCH:STAR 14`

IEC/IEEE-bus command:
`:SOUR:W3GP:MCH:STOP 54`

SYMBOL RATE

Symbol rate of DPCHs:
7.5 ksps, 15 ksps, 30 ksps, 60 ksps, 120 ksps, 240 ksps, 480 ksps or 960 ksps

IEC/IEEE-bus command:
`:SOUR:W3GP:MCH:SRAT D64K`

(MULTI CHANNEL EDIT...)

PILOT LENGTH

Length of the pilot field in the slot structure of a channel. The value range of this parameter depends on the symbol rate (2 bits to max. 16 bits). To achieve a constant length of the slot the data fields are lengthened or shortened depending on the pilot length as defined in the standard.

IEC/IEEE-bus `:SOUR:W3GP:MCH:PLEN BIT2`

CHANNELIZATION CODE / STEP

Channelization code for the first channel and step width for all other channels. The value ranges depend on the symbol rate.

IEC/IEEE-bus commands

```
:SOUR:W3GP:MCH:CCOD 4
:SOUR:W3GP:MCH:CCOD:STEP 1
```

POWER / dB
POWER STEP / dB

Channel power for the first channel (value range -60 dB to 0 dB) and stepwidth for all other channels (value range -60 dB to 60 dB).

IEC/IEEE-bus commands

```
:SOUR:W3GP:MCH:POW -20
:SOUR:W3GP:MCH:POW:STEP 1
```

DATA

Selection of data pattern for the DPCHs. For other patterns see channel table.

IEC/IEEE bus commands

```
:SOUR:W3GP:MCH:DATA PN9
:SOUR:W3GP:MCH:DATA:PATT #HFFFF, 16
```

TIMING OFFSET / TIMING OFFSET STEP

A value > 0 causes the source symbols to be shifted prior to spreading. An offset of 1 for instance is recommended to obtain a lower crest factor.
The absolute starting time of frames (slot 0) is shifted relative to the start of the scrambling code sequence by T_{Offset} * 256 chips. This means that the resolution of the timing offset is always 256 chips, irrespective of the symbol rate. The step width of the timing offset between two adjacent channels is defined with STEP.

IEC/IEEE bus commands

```
:SOUR:W3GP:MCH:TIM:OFFS 0
:SOUR:W3GP:MCH:TIM:STEP 3
```

TPC

Data source for the TPC field.

IEC/IEEE-bus `:SOUR:W3GP:MCH:TPC ZERO`

MULTI CODE STATE

ON The channels should be simulated with multicode.
IEC/IEEE bus `:SOUR:W3GP:MCH:MCOD ON`

OFF The channels should be simulated without multicode.
IEC/IEEE bus `:SOUR:W3GP:MCH:MCOD OFF`

STATE

ON The channels are active.
IEC/IEEE bus `:SOUR:W3GP:MCH:STAT ON`

OFF The channels are not active.
IEC/IEEE bus `:SOUR:W3GP:MCH:STAT OFF`

EXECUTE ▶

Configuration of DPCHs according to the set parameters.

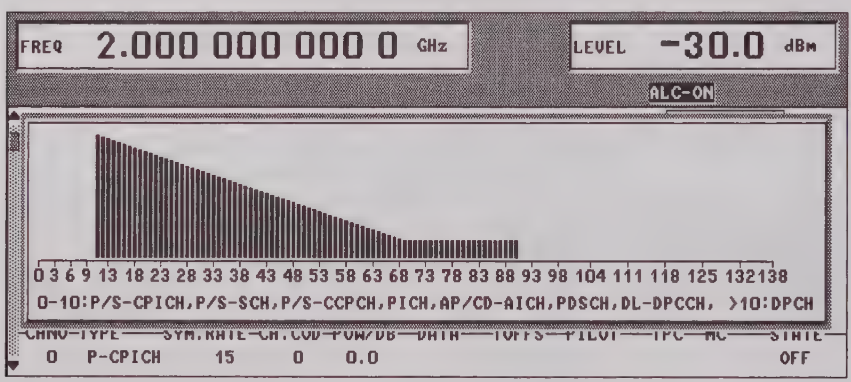
IEC/IEEE bus `:SOUR:W3GP:BST2:MCH:EXEC`

The channel parameter ranges correspond to the associated parameters in the channel table (see above).
Start Channel No (DPCH)/Stop Channel No (DPCH): The range of the DPCHs that are to be set jointly is defined.

For channel scenarios to be set up easily, start values and step widths can be entered for some parameters which are used to set the channel parameters. **Channelization Code** and **Channelization Code Step** parameters, for example, can be used to create simple scenarios in which spreading codes with a fixed step width are assigned to the various code channels. Power profiles (via the code axis) can be created in the same way with **power** and **power step**. The **timing offset** and **timing offset step** parameters can be used to change the relative slot timings of the channels, so changing the crest factor of the signal (see section 2.14.3.4).

2.14.2.8 WCDMA/3GPP – Display of Channel Graph Menu

The channel graph provides an overview of the contents of the channel table.



FREQ 2.000 000 000 0 GHz

LEVEL -30.0 dBm

ALC-ON

0-10:P/S-CPICH,P/S-SCH,P/S-CCPCH,PICH,AP/CD-AICH,PDSCH,DL-DPCCH, >10:DPCH

CHNO	TYPE	SYM.RATE	CH.COD	POW/DB	DATA	TOFFS	PILOT	TPC	MC	STATE
0	P-CPICH	15	0	0.0						OFF

Fig. 2-125 DIGITAL STD – WCDMA/3GPP – BS CONFIGURATION /CHANNEL GRAPH menu

All active channels and their channel powers are displayed. Unlike the code domain display (next chapter), all channels are shown with the same width. Multiple assignment of a code domain range cannot be recognized.

2.14.2.9 WCDMA/3GPP Menu – Display of Code Domain and Code Domain Conflicts

The channelization codes are taken from a code tree (see below).
The greater the spreading factor, the smaller the symbol rate and vice versa. The product of the spreading factor and the symbol rate is constant and always equals the current chip rate. The outer branches of the tree (right-most position in the figure) give the channelization codes for the lowest symbol rate (and so the highest spreading factor). The use of a channelization code from the level with a spreading factor N blocks the use of all other channelization codes from levels with spreading factor >N in the same branch of the code tree. Channelization codes with a smaller spreading factor are contained in the codes with larger spreading factor in the same code branch. If channelization codes of this type are used at the same time, the signals of associated code channels will be mixed up to such an extent that they could not be separated in the receiver and orthogonality would be lost.

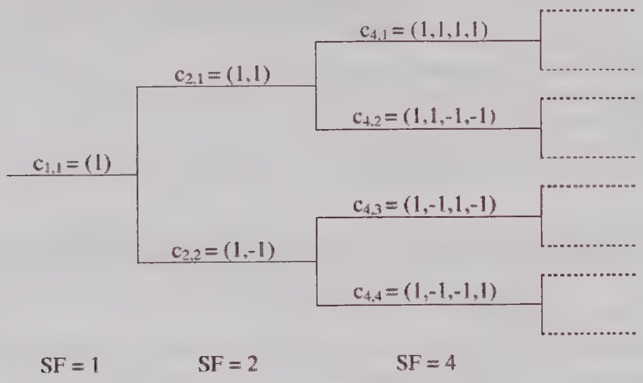


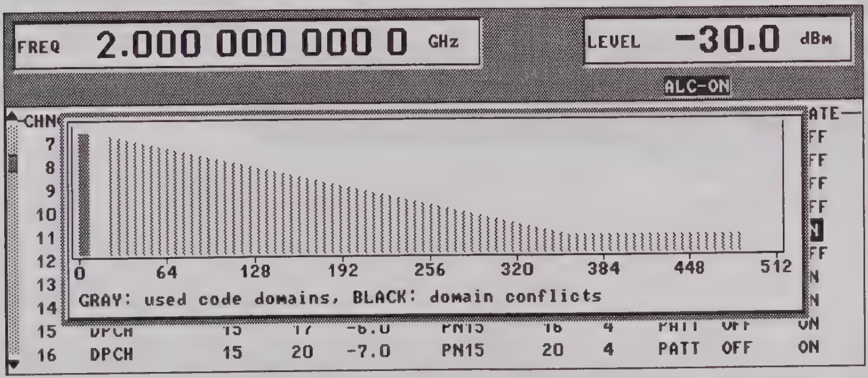
Fig. 2-126 Code tree of channelization codes

Example: If in Fig. 2-126 code $c_{2,1}$ is already used, the remaining branch is blocked with $c_{4,1}$ and $c_{4,2}$.

The outer branch region (with minimum symbol rate and max. spreading factor) which is based on the channelization code selected in the code tree is defined as the **domain** of a certain channelization code. Using a channelization code means that its entire domain is used. The whole domain ranges from 0 to 511 at the chip rate of 3.84 Mcps:

$$= \frac{Chip\_rate}{\min\_symbol\_rate} - 1 = \frac{3.84Mcps}{7.5ksps} - 1)$$

A graphic is displayed with the **CODE DOMAIN** menu item or the **STATUS** hardkey. This graphic shows the occupancy of the code domain by the active code channels.



GRAY: used code domains, BLACK: domain conflicts

```
15   DPCH   15   17   -6.0   PN15   16   4   PATT   OFF   ON
16   DPCH   15   20   -7.0   PN15   20   4   PATT   OFF   ON
```

Fig. 2-127 WCDMA/3GPP – BS CONFIGURATION / CODE DOMAIN menu (without conflict)

This display shows at a glance whether assigned code domains of various channels overlap, i.e. whether a domain conflict occurs. The symbol rates of code channels are indicated by the width of the associated bars. The height of the bars gives the power of the code channel. If a bar is grey, the code domain at this position is assigned once which means that no conflicts occur. If a bar is black (at least partly), the code domain is assigned at least twice and conflicts occur.

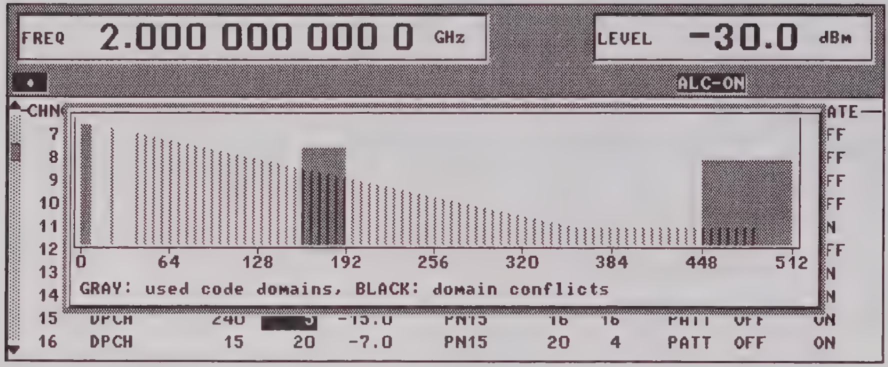

FREQ 2.000 000 000 0 GHz
LEVEL -30.0 dBm
GRAY: used code domains, BLACK: domain conflicts
16 DPCH 15 20 -7.0 PN15 20 4 PATT OFF ON

Fig. 2-128 WCDMA/3GPP – BS CONFIGURATION / CODE DOMAIN menu (with conflict)

Display of domain conflicts

If a domain conflict occurs in the base station/mobile station, this conflict is displayed at two locations (next to the code domain display).

1. Centrally for the whole station in the status line ●,
2. All lines in the channel table (processed in ascending channel numbers) showing a domain conflict in a row with a lower index are indicated with a warning symbol ● to the left of STATE.

A domain conflict occurs when the assigned domains of different channel rows overlap. The assigned code domain of a channel is calculated from the symbol rate of the channel, the minimum possible symbol rate (depending on the link direction), the chip rate and the channelization code number using the following equation:

$$Domain\_factor = \frac{current\_symbolrate}{\mathrm{min}imum\_symbolrate}$$ (the minimum symbol rate is 7.5 ksps in downlink),

where:

Lower domain limit = current channelization code number * domain factor
Upper domain limit = lower domain limit + domain_factor –1.

If the cursor is positioned on one of the warning symbols ● and if the SELECT button is clicked, a window is opened and shows more accurate information about the domain conflict that has occurred. The numbers of channel rows in the channel table and the assigned domains of the conflicting channels are displayed.

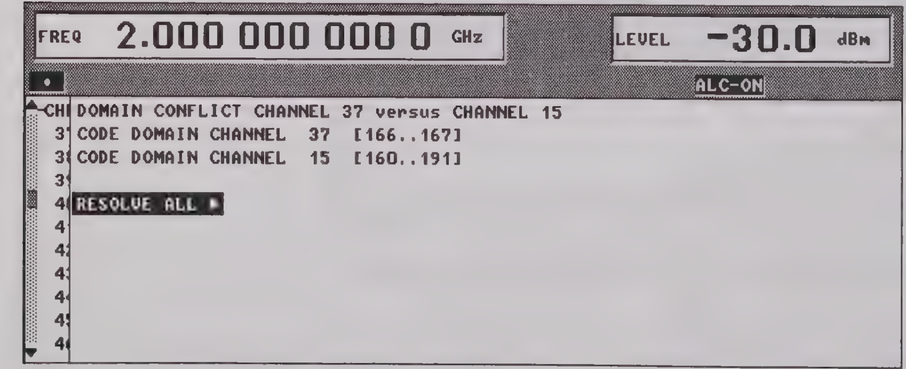
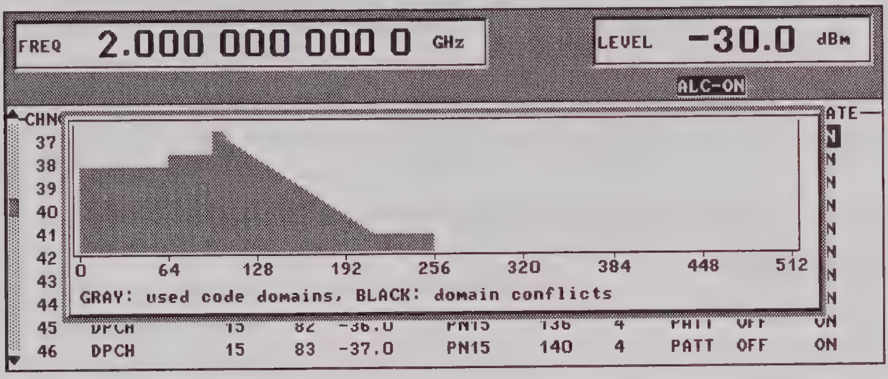

FREQ 2.000 000 000 0 GHz
LEVEL -30.0 dBm
DOMAIN CONFLICT CHANNEL 37 versus CHANNEL 15
CODE DOMAIN CHANNEL 37 [166..167]
CODE DOMAIN CHANNEL 15 [160..191]

Fig. 2-129 WCDMA/3GPP – BS CONFIGURATION / CODE DOMAIN CONFLICT menu

Resolve all Conflicts

If you select the **RESOLVE ALL** button in the domain conflict info window, the SMIQ tries to arrange the active code channels so that no overlapping occurs in the code domain. The channelization code number of the channels is varied to do this. The effect of a conflict resolution is shown by the code domain display.



FREQ 2.000 000 000 0 GHz

LEVEL -30.0 dBm

46 DPCH 15 83 -37.0 PN15 140 4 PATT OFF ON

Fig. 2-130 WCDMA/3GPP – BS CONFIGURATION / CODE DOMAIN menu (after conflict resolution)

If the assigned domain of all active channels is greater than the physically available domain (e.g. 5 channels with 960 ksps at a chip rate of 3.84 Mcps), conflicts can no longer be resolved. The message "no resolution of domain conflicts possible" is displayed.

2.14.2.10 Effect of CLIPPING LEVEL Parameter on Signal

W-CDMA signals may have very high crest factors - especially if the number of channels is high and the data offset is unfavourable (see sections 2.14.3.3 and 2.14.3.4). High crest factors entail two basic problems:

1. The nonlinearity of the power amplifier (compression) causes intermodulation which expands the spectrum (spectral regrowth).
2. Since the level in the D/A converter is relative to the maximum value, the average value is converted with a relatively low resolution. This results in a high quantization noise.

Both effects increase the adjacent-channel power.

From all the possibilities listed in section 2.14.3.4 for influencing the crest factor, changing the CLIPPING LEVEL is the simplest and most effective. In this case a limit value is defined which is a percentage of the highest peak value. All current values exceeding this limit will be clipped to this value. Since clipping is done prior to filtering, the procedure does not influence the spectrum. The EVM however increases. Since clipping the signal not only changes the peak value but also the average value, the effect on the crest factor is unpredictable. The following table shows the effect of the CLIPPING LEVEL on the crest factor for typical scenarios.

Table 2-24 Change of crest factor in the case of clipping

CLIPPING LEVEL	**Crest factor with scenario**			
	Downlink: 10 DPCHs "minimum crest" 30 ksps	**Downlink: 10 DPCHs "worst crest" 30 ksps**	**Downlink: 10 DPCHs "average crest" 30 ksps**	**Downlink: 128 DPCHs "average crest" 30 ksps**
100%	9.89 dB	14.7 dB	10.9 dB	21.7 dB
80%	8.86 dB	12.9 dB	9.39 dB	20.2 dB
50%	7.50 dB	10.1 dB	8.29 dB	16.9 dB
20%	5.50 dB	6.47 dB	6.23 dB	12.5 dB
10%	5.34 dB	6.06 dB	5.80 dB	9.57 dB
5%	5.34 dB	6.06 dB	5.80 dB	8.17 dB

Effect of clipping on the constellation shown by way of a 4-DPCH configuration:

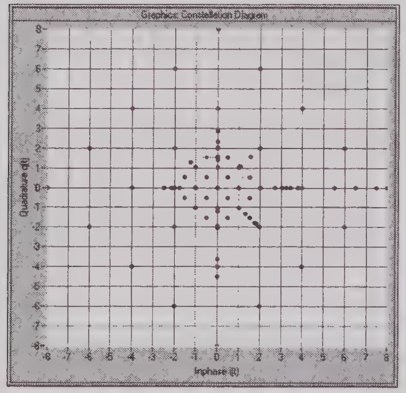


Fig. 2-131 Constellation at clipping level 100% (not clipped)

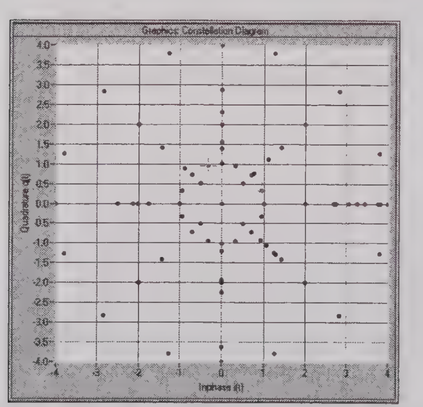


Fig. 2-132 Constellation at clipping level 50%

2.14.2.11 Synchronization and Trigger Signals

To generate the W-CDMA signals, a chip sequence is calculated and stored in the waveform memory of the modulation coder (option SMIQB20). This chip sequence is automatically repeated (TRIGGER MODE AUTO).
For measurements on receivers, trigger signals can also be used for synchronized sequences (TRIGGER MODE RETRIG, ARMED_AUTO or ARMED_RETRIG).

The trigger signal is applied to the TRIGIN input of the PAR DATA connector. The chip sequence either starts immediately after the active edge of this trigger signal or after a settable number of chips (EXT TRIGGER DELAY). A retrigger (RETRIG) can be inhibited for a settable number of chips (EXT RETRIGGER INHIBIT).

A trigger event can be triggered manually with EXECUTE TRIGGER or via the IEC/IEEE bus. A trigger signal is always output at TRIGOUT 3 output of the SMIQ at the same time as a trigger event.

The SMIQ generates the following sync signals:

- a 0.667 ms slot clock
- a 10 ms radio-frame clock
- a marker signal to identify the periodic repetition of the generated chip sequence
- a marker signal to identify the periodic repetition of the generated enhanced chip sequence (only available if Option SMIQB48 is installed)
- a marker signal to identify a restart of the system frame number (SFN Restart) after 4096 frames (only available if Option SMIQB48 is installed and a BCH is generated).

The SMIQ can output a selection of two or three signals via connectors TRIGOUT 1 and 2 of the PAR DATA connector.

The chip clock in the SMIQ is generated by a clock synthesizer in the modulation coder. All clock signals are in sync with the unit's 10 MHz reference. The chip clock is available at the SYMBOL CLOCK connector. If required, the clock synthesizer in the SMIQ can use an external chip clock which is fed in via the SYMBOL CLOCK connector.

To ensure reliable clock-synthesizer synchronization, the external clock must first be applied and the correct SMIQ chip rate set (MODULATION – CHIPRATE VARIATION). The CLOCK SOURCE can then be switched from INT to EXT.

Note: *The set chip rate may not differ by more than 1% from the chip rate of the external signal.*

2.14.2.12 Preset/Default Values

The following settings are made if the menu item SET DEFAULT is selected or if the PRESET key is pressed during switch-on.

General default settings

CHIP RATE	3.84 Mcps
LINK	DOWN/FORWARD
3GPP VERSION	3.2.0 (RELEASE 99)
MODULATION	WCDMA filter, roll-off 0.22, LOW ACP, chip rate of 3.84 Mcps
TRIGGER	
MODE	RETRIG
SOURCE	INT
DELAY	0
INHIBIT	0
OUT 1	RADIO FRAME
OUT 2	CHIP SEQUENCE PERIOD
OUT1/2 POL	POS
OUT ½ DELAY	0
CLOCK SOURCE	INT
CLOCK DELAY	0.00
SEQUENCE LENGTH	1

Default settings for DOWN/FORWARD Link

Para. Predef. settings

CHANNELS FOR SYNC...	ON
SCCPCH	60 ksps
NO OF DPCH	3
SYMBOL RATE	30 ksps
CREST	MINIMUM

Base station parameters

Table 2-25 Default values for base station parameters

Parameter	BS1	BS2	BS3	BS4
STATE	OFF	OFF	OFF	OFF
SCRAMBLING CODE STATE	ON	ON	ON	ON
SCRAMBLING CODE	0	0	0	0
USE TFCI	OFF	OFF	OFF	OFF
TFCI	0	0	0	0
2nd SEARCH CODE GR.	0	0	0	0
Misuse TPC...	OFF	OFF	OFF	OFF
TPC Read Out Mode	continuous	continuous	continuous	continuous
Power Step	0	0	0	0
Transmit Diversity	OFF	OFF	OFF	OFF
Page indicators per frame	18	18	18	18

Multi Channel Edit

START DPCH	11
STOP DPCH	11
SYMBOL RATE	30 ksym/s
PILOT LENGTH	4 bit
CHAN. CODE	0
CHAN. STEP	0
POWER	0dB
POWER STEP	0dB
DATA	PN15
TIMING OFFSET	0
TIMING OFFSET STEP	0
TPC	ALL 0 (0)
MULTI CODE STATE	OFF
STATE	ON

Channel table parameters

SYMBOL RATE	15 ksps (if possible, otherwise min. permissible rate)
CHAN. CODE	0 (if possible, otherwise min. permissible code)
POWER	0 dB
DATA	PN15
TIMING OFFSET	0
PILOT LENGTH	4 bit (if possible, otherwise min. permissible length)
TPC	ALL0 (0)
MC STATE	OFF
DATA PATTERN	"0000 0000 0000 0000"
TPC PATTERN	"0000 0000 0000 0000"

Default settings for UP/REVERSE link

Mobile station parameters

Table 2-26 Default values for mobile station parameters

Parameters	MS1	MS2	MS3	MS4
STATE	OFF	OFF	OFF	OFF
MODE	DPCCH + DPDCH	DPCCH + DPDCH	DPCCH + DPDCH	DPCCH + DPDCH
SCRAMBLING CODE	0	0	0	0
SC MODE	LONG	LONG	LONG	LONG
TPC READ OUT MODE	CONTINUOUS	CONTINUOUS	CONTINUOUS	CONTINUOUS
TPC	All 0	All 0	All 0	All 0

Parameter PRACH only mode:

PREAMBLE REPETITION	1
PREAMBLE POWER	0 dB
DATA PART POWER	0 dB
CONTROL PART POWER	0 dB
Message Part Length	1
SIGNATURE	0
ACCESS SLOT	0
SYMBOL RATE	30 ksps
DATA	PN15
DATA PATTERN	"0000 0000 0000 0000"
TFCI	0

Parameter PCPCH only mode:

PREAMBLE REPETITION	1
PREAMBLE POWER	0 dB
PREAMBLE POWER STEP	0 dB
DATA PART POWER	0 dB
CONTROL PART POWER	0 dB
MESSAGE PART LENGTH	1
Power CONTROL PREAMBLE LENGTH	0 slots
SIGNATURE	0
ACCESS SLOT	0
FBI MODE	OFF
FBI	ALL 0
FBI PATTERN	"0000 0000 0000 0000"
SYMBOL RATE	30 ksps
DATA	PN15
DATA PATTERN	"0000 0000 0000 0000"
TFCI	0

Parameter DPCCH + DPDCH mode:

DPCCH settings

POWER	0 dB
TIMING OFFSET	4 (fix)
TFCI STATE	OFF
TFCI	0
FBI MODE	OFF
FBI	ALL 0
FBI PATTERN	"0000 0000 0000 0000"
MISUSE TPC...	OFF
TPC POWER STEP	0 dB
PREAMBLE POWER	0 dB
PREAMBLE POWER STEP	0 dB
DATA PART POWER	0 dB
CONTROL PART POWER	0 dB
SHARED RESOURCE...	OFF
MESSAGE PART LENGTH	1

DPDCH settings

OVERALL SYMBOL RATE	30 ksps
POWER	0 dB
DATA	PN15 (for all DPDCHs)

2.14.3 Background Information for the Generation of 3GPP W-CDMA Signals

This section provides some background information on W-CDMA signals to support the user in generating signals with certain characteristics.

2.14.3.1 3GPP W-CDMA Signals in Time Domain

For information on 3GPP W-CDMA signals in the frequency domain refer to section 2.14.3.2.

Downlink

The two spreading methods for SCHs and the remaining (downlink) channel types are explained for the channels P-CCPCH, P-SCH and S-SCH. (These three channels were referred to as “Perch” in previous versions of the W-CDMA standard and option SMIQB45)

A (downlink) signal containing the following components is generated:

- P-CCPCH (-20 dB)
- P-SCH (-14 dB)
- S-SCH (-14 dB)

The SCH components are generated with double amplitude for better distinction against PCCPCH.

Sequence length = 1
Filtering: root cosine (roll-off = 0.22), impulse length = 32, oversampling = 8
Data source: PRBS23
No superimposed impairments active

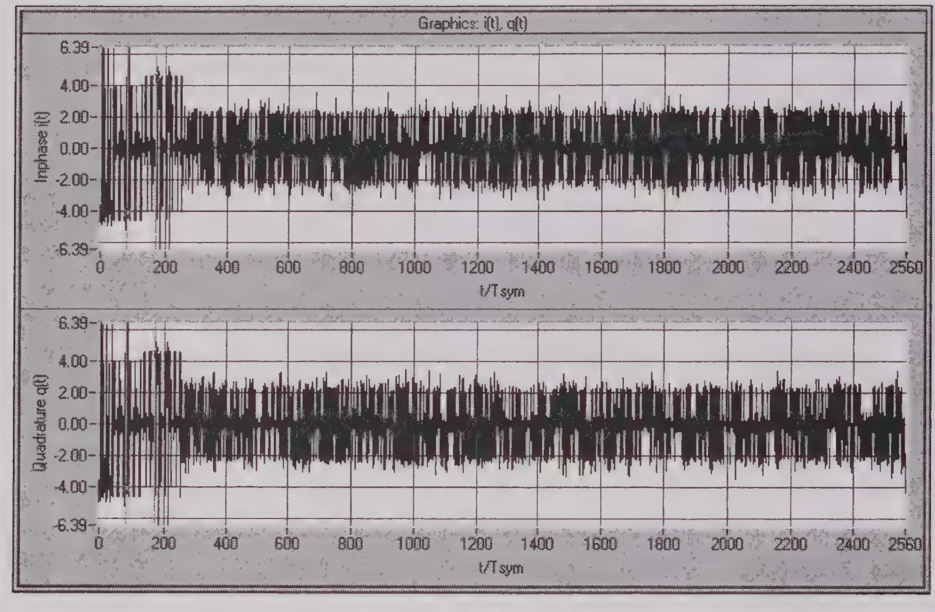


Fig. 2-133 Signal consisting of P-CCPCH, P-SCH and S-SCH in time domain

The above diagram reveals that a time slot consists of 2560 chips and the power is higher with the first 10% of the signal. This is the SCH. Zooming in on this transition point reveals the following:

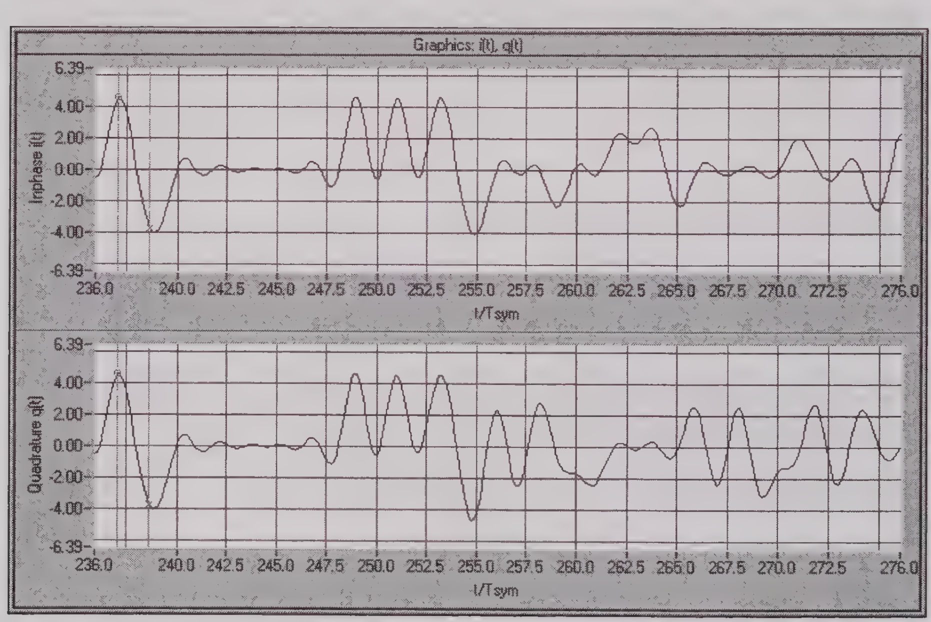


Fig. 2-134 Signal consisting of P-CCPCH, P-SCH and S-SCH in time domain (zoomed)

The right section (i.e. the 9 PCCPCH symbols) is spread as described in Fig. 2-90. You can see that for each symbol clock one of the two components is 0 and the other one ±2 (a certain inaccuracy results from the root cosine filter used).

This is the standard spreading scheme as it is also used for S-CCPCH, DPCH and all other downlink channel types.

The left section (the synchronization code symbol occupying the chip range 0 to 255) is obtained using special synchronization code spreading: there is no scrambling unit ($S_i = C_i$, $S_q = C_q$).

Since the synchronization code symbol is defined as 11, the following is always true:

$S_i = S_q$.

The result is a BPSK mapping turned by 45° relative to the QPSK constellation of the S-CCPCH component.

Since the synchronization code symbol is sent twice (in the primary and secondary SCH) with different spreading, the signal power is doubled in this range.

The constellation diagram of the perch channel, therefore, has the following appearance:

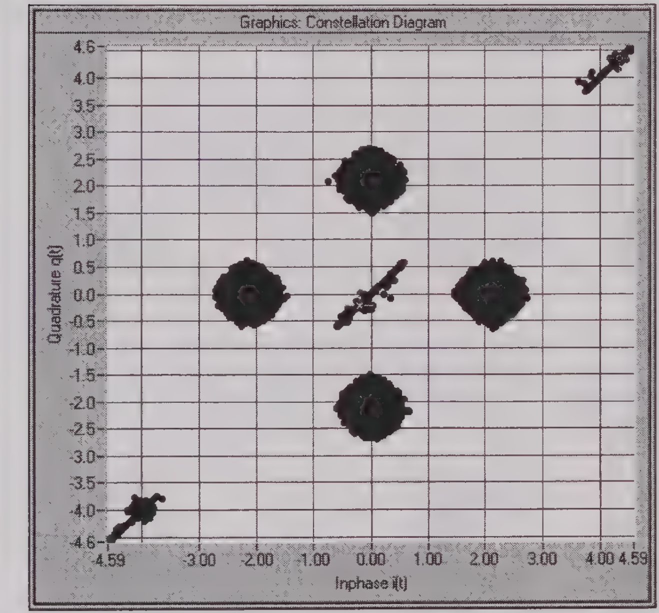


Fig. 2-135 Constellation diagram of a signal consisting of P-CCPCH, P-SCH and S-SCH

The four circular areas are due to P-CCPCH ("QPSK"), the linear areas (diagonals) to SCH ("BPSK"). The spreading/constellation diagram of P-CPICH, S-CPICH, S-CCPCH, PICH, AP-AICH, AICH, PDSCH, DL-DPCCH and DPCH are identical to the spreading/constellation diagram of P-CCPCH.

Envelopes
The following figures are based on a power of 0 dB.
To eliminate smearing of the amplitude due to the root cosine filter, all envelopes are calculated using a squarewave filter.

The channels P-CPICH, S-CPICH, S-CCPCH, PICH, PDSCH feature a constant envelope during the complete sequence.

Note: *Section 2.14.2.5, "Misuse TPC for output power control", describes how the envelope of e.g. DPCHs can be varied.*

The P-CCPCH is blanked during the first 256 chips of the slot.

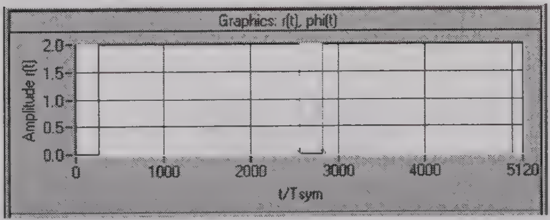


Fig. 2-136 Envelope of P-CCPCH

P-SCH and S-SCH are active during the first 256 chips only:

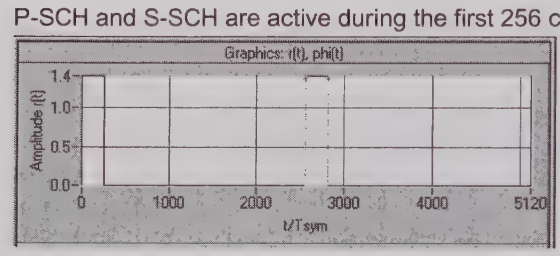


Fig. 2-137 Envelope of P-SCH or S-SCH

AP-AICH and AICH are transmitted only during the first 4096 chips of the signal:

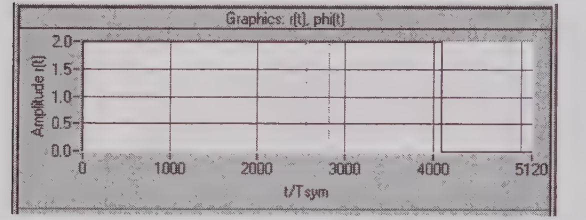


Fig. 2-138 Envelope of AICH (Subchannel)

AP-AICH and AICH consist of up to 16 superimposed single channels that are modulated with different orthogonal vectors. In some sections, cancellations or excessive levels may result. The following diagram shows the signal with the input pattern "1010":

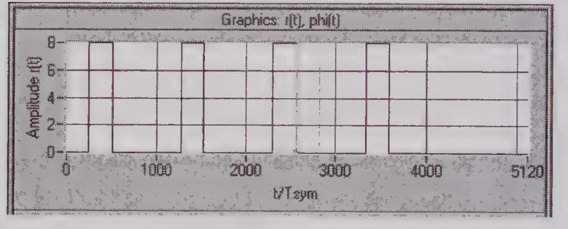


Fig. 2-139 Envelope of AICH (four subchannels)

The DL-DPCCH is used during transmission of the PCPCH to readjust the output power of the mobile. Since there is no data transfer to the mobile in this phase, the DL-DPCCH is based on a DPCH (7.5 ksps without TFCI), the data fields of which are blanked (“DTX“):

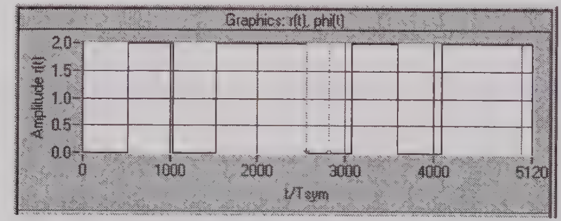


Fig. 2-140 Envelope of DL-DPCCH

At first, DPCHs are permanently active, however the TFCI fields are blanked at high data rates (>= 60 ksps) if no TFCI information is transferred ("DTX"). A 60-ksps channel, for example, then has the following appearance:

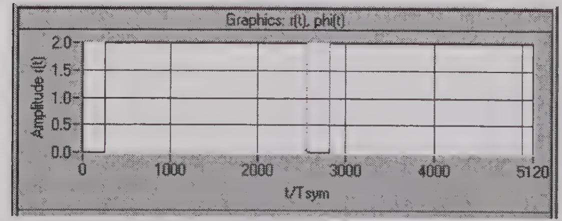


Fig. 2-141 Envelope of DPCH 60 ksps without TFCI

Uplink

In the "DPCCH+DPDCH" mode, the signal consists of a DPCCH and one or several (max. 6) DPDCHs. To derive the constellation of this signal, the constellation of a single channel (DPDCH or DPCCH) is considered first. Its data are exclusively supplied to the I or Q path. The second path is not used (second path is: $C_q = 0$).

with

$$S_i = C_i SC_i - C_q SC_q'$$
$$S_q = C_i SC_q' + C_q SC_i$$

the following equation is obtained:

$$S_i = C_i SC_i$$
$$S_q = C_i SC_q'$$

This corresponds to a QPSK constellation diagram. Compared to the DPCH constellation diagram, this diagram is shifted and the constellation points only have half the amplitude:

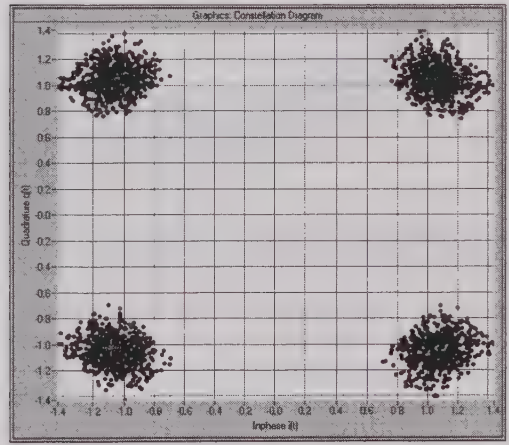


Fig. 2-142 Constellation of a DPDCH/DPCCH channel

Note: *A suitable modification of the PN sequences ("HPSK") in the uplink eliminates 50 % of all zero-crossings, which, in contrast to the downlink, causes a deformation of the constellation ranges.*

If DPCCH and DPDCH are combined, the constellation considerably depends on the ratio of the channel powers. If the two powers are equal, a constellation as with the P-CCPCH will be obtained. The following illustration shows the constellation with DPCCH power 0dB and DPDCH power –6 dB:

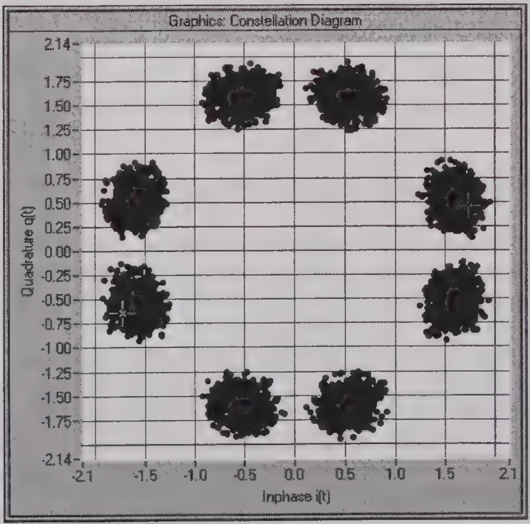


Fig. 2-143 Constellation of an uplink signal consisting of a DPDCH and a DPCCH

PRACH and PCPCH consist of preambles and the message part. Similar to a P-CCPCH, the preamble uses the 4 constellation points (2, 0), (0,2), (-2, 0) and (0, -2). In the case of the message part, separate powers can set for Data (on the I path) and Control (on the Q path) so that a component is obtained as described above for the DPCCH+DPDCH signal. As the third part of the constellation, points can be seen at the origin, which can be assigned to the interval between preamble and message part:

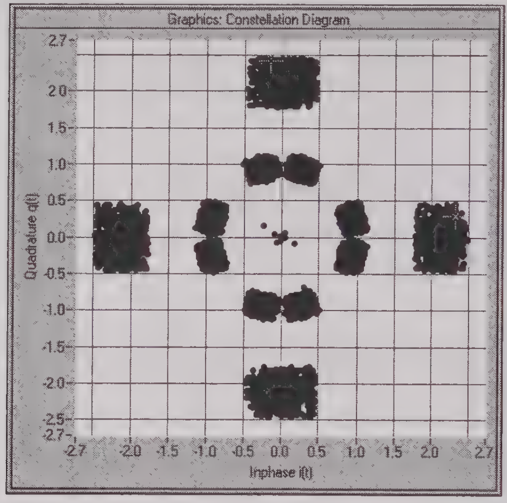


Fig. 2-144 Constellation of a PRACH

Envelopes

To eliminate smearing of the amplitude due to the root-cosine filter, all envelopes are calculated using a square-wave filter. The DPCCH and the DPDCHs are continuous channels that feature a constant envelope during the complete sequence.

Note: *Section 2.14.2.6 "Misuse TPC for output power control", describes how to vary also the envelope of DPCCH and DPDCHs.*

PRACH and PCPCH consist of one or several preambles and the message part.

Example of a PRACH with a preamble and reduced power in the message part:

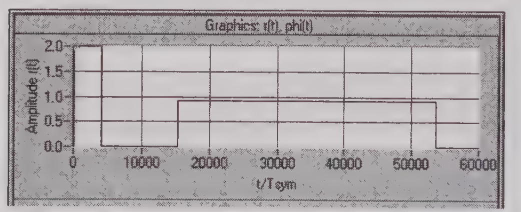


Fig. 2-145 Envelope of a PRACH

Example of a PCPCH with the parameters

- Access Slot # = 0
- Preamble Repetition = 4
- Preamble Power Step = 1 dB
- Message Part Length = 1 frame
- Preamble Power = 0 dB
- Control Part Power = 0 dB
- Data Part Power = 0 dB

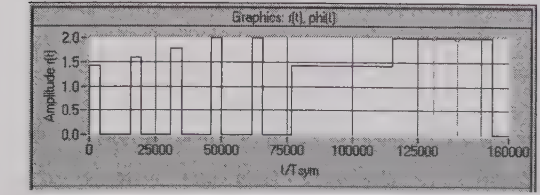


Fig. 2-146 Envelope of a PCPCH

2.14.3.2 3GPP W-CDMA Signals in the Frequency Range

For information on 3GPP W-CDMA signals in the time domain, refer to section 2.14.3.1.

The spectrum of a 3GPP W-CDMA signal (a DPCH channel) corresponds to that of a QPSK signal with identical filter parameters.

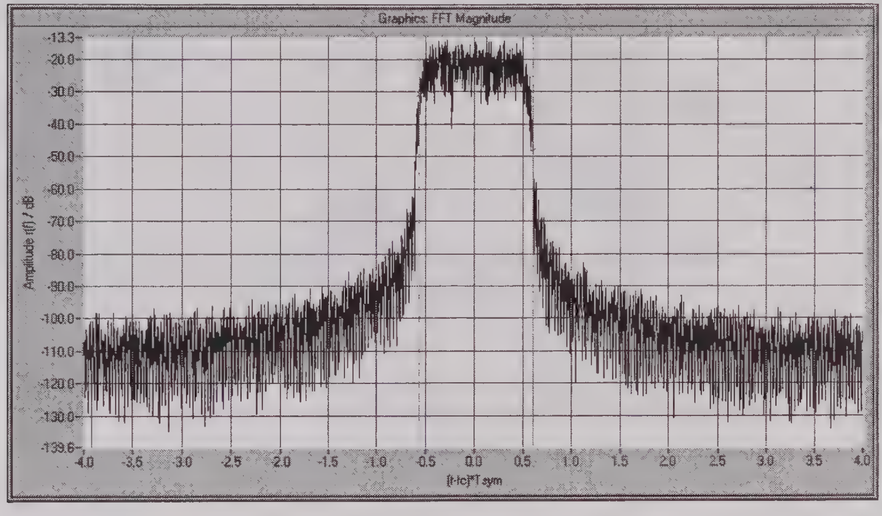


Fig. 2-147 Magnitude spectrum of a 3GPP W-CDMA signal

If signals with several channels are investigated, a certain ripple may occur in the range +/-0.5 of the chip rate.

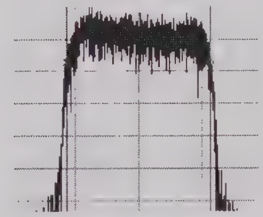


Fig. 2-148 Magnitude spectrum (section) of a 3GPP W-CDMA signal with several channels

This effect can be influenced by the choice of the channelization codes and is reduced with increasing period of observation, i.e. higher averaging for the FFT settings.

2.14.3.3 Effect of Data Source on the 3GPP W-CDMA Signal

The choice of the data sources is of vital importance for the signal characteristics. Especially the constellation diagram and the crest factor can be modelled to a large extent by an appropriate data selection.

Since the number of channels can be very high, particularly in the downlink, this section deals only with the simulation of downlink signals.
In the following it is assumed that all channels are generated with the same scrambling code sequences and the same channel powers of 0.0 dB.
A symbol rate of 60 ksps is selected.

2.14.3.3.1 Two DPCHs with Uncorrelated Data

If the two channels are uncorrelated (data source PRBS with active option ☑ Different start values for PRBS), the resulting constellation diagram is obtained from the superposition of the constellations of two separate signals (see Fig. 2-102).

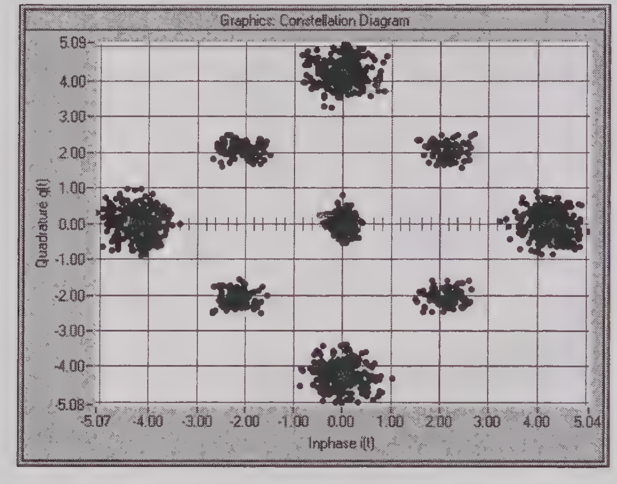


Fig. 2-149 Constellation of a signal with two DPCHs (uncorrelated data)

The resulting crest factor is 8.07 dB.

2.14.3.3.2 Two DPCHs with Same Data

If the two channels contain exactly the same data (PRBS data source with equal initial values), the individual components only differ in the channelization codes. Comparing any two lines of the channelization code matrix shows that 50% of the bits are identical and 50% are different (orthogonal matrix)). Where the bits are identical, the two channels furnish the same components; where the bits are different, the components cancel each other. The I/Q signal of this sum signal would then have the following characteristic:

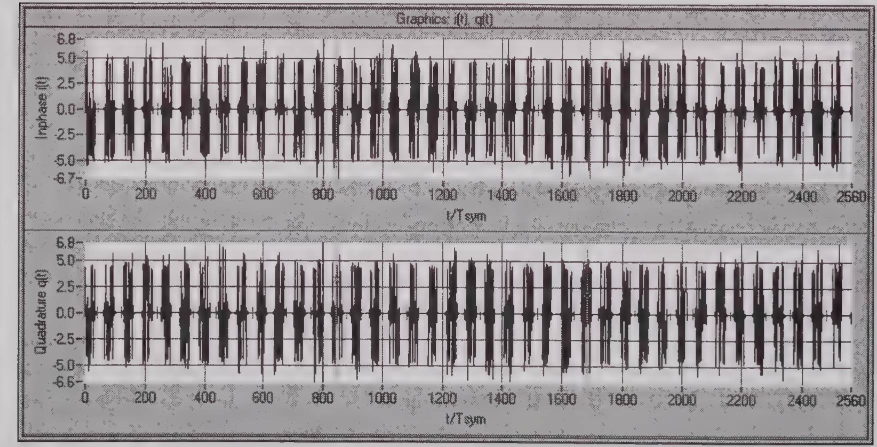


Fig. 2-150 Signal with two DPCHs (same data) in time domain

Note: *The bursts occurring at regular intervals are due to the use of the channelization code numbers 0 and 1. The two lines of the matrix are identical in the first half and different in the second half. Therefore, each of the 40 source symbols causes one of these bursts, the period of which is equivalent to half the symbol length. Other combinations of channelization code numbers often cause erratic results.*

The following associated constellation diagram is obtained:

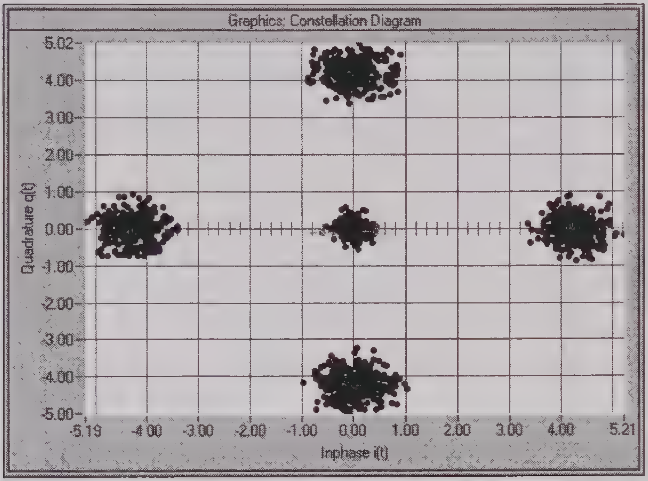


Fig. 2-151 Constellation of a signal with two DPCHs (uncorrelated data)

The new crest factor is 7.64 dB. Compared to section 2.14.3.1 the value has decreased since with constant peak value the average value has increased.

2.14.3.3.3 16 DPCHs with Uncorrelated Data

If the data of all channels are uncorrelated, the constellation diagram is obtained similarly to section 2.14.3.3 by multiple superposition of the basic constellation. Whether all possible points (channel number+1)^2 = 17*17 = 289 will be displayed depends on the data sequences and the sequence length.

Note: *In contrast to the last constellation diagrams, the following diagrams were recorded with active receiver filter (root cosine 0.22). This allows better differentiation of the individual points. Setting of the receiver filter is described in section 5.17.*

Example for 16 time slots:

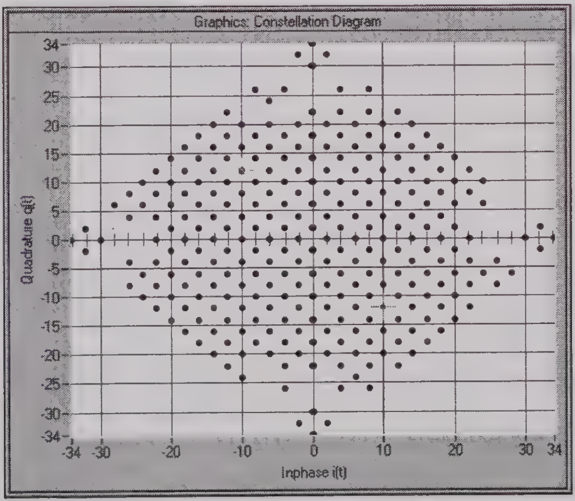


Fig. 2-152 Constellation with 16 uncorrelated channels (16 time slots)

This signal has a crest factor of 15.35 dB. The crest factor increases with the number of channels since the increase of the average value is slower than that of the peak value (cancelling effects).

The same diagram for a signal with one slot only is obtained:

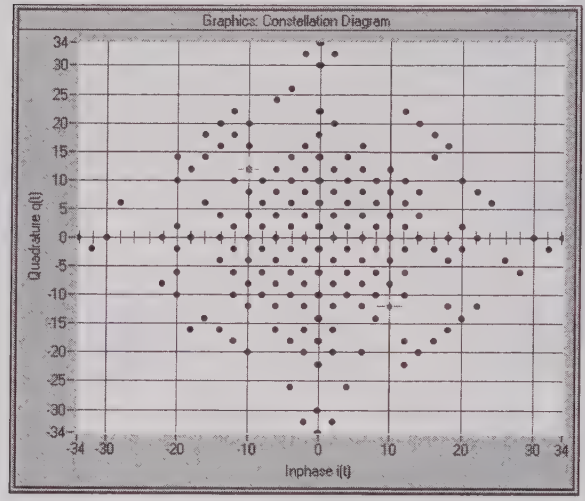


Fig. 2-153 Constellation with 16 uncorrelated channels (1 time slot)

The crest factor has the value of 15.35 dB even if the CCDF has a different characteristic.

2.14.3.3.4 16 DPCHs with same Data

Similar to section 2.14.3.3.2 there are strong cancellation effects and only points remain on the axes. The position of the points also depends on the choice of the channelization codes. The following diagram is an example for 16 channels, channelization code 0 to 15:

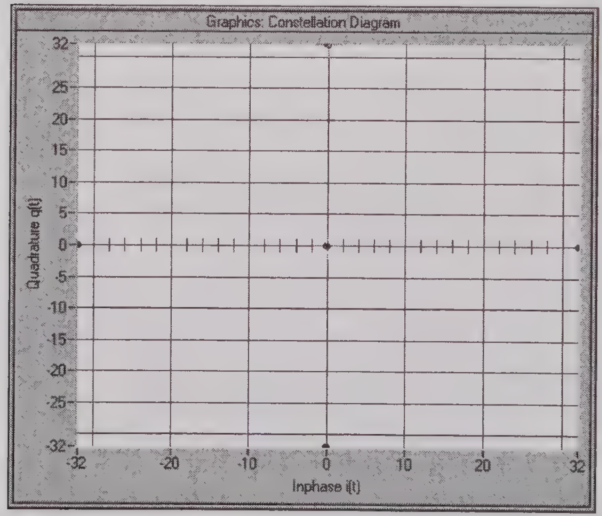


Fig. 2-154 Constellation diagram of 16 DPCHs with same data

In this case the crest factor is 16.17 dB. The high value arises because about 94 % of the points lie at the origin and only 6% of the points at the outer positions.

2.14.3.3.5 Use of Timing Offset

All previous scenarios did not use a timing offset. Therefore all channels contained the same bits at least in the pilot symbol part. The result of this special feature was that the 4 corner points (0, 2*number of channels), (0, -2*number of channels), (2*number of channels, 0), (-2*number of channels, 0) where always there (constructive superposition).

Through the use of a timing offset (in our example 1 unit (=256 chips) from channel to channel) the data bits (including pilot and TPC) are shifted against each other.

Note: *In contrast to earlier versions of the W-CDMA standard or WinIQSIM, the timing offset (previously "data offset") is no longer defined in symbols but in units of 256 chips. Thus shifting becomes independent of the spreading factor.*

The worst case is thus eliminated, the crest factor becomes smaller and the spread symbols are arranged as follows:

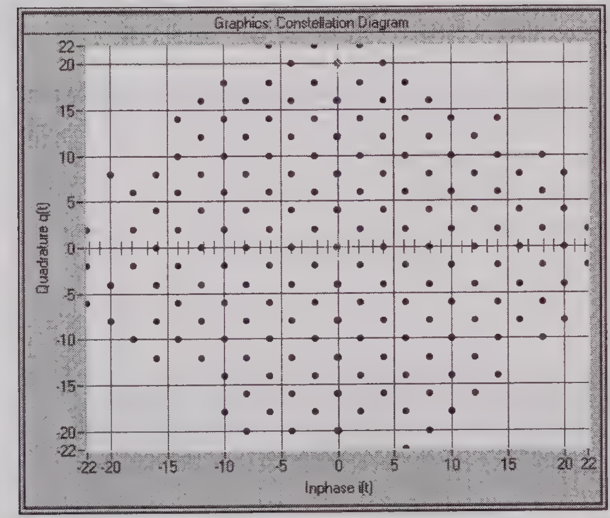


Fig. 2-155 Constellation diagram of 16 DPCHs with timing offset

Through the use of the timing offset the crest factor was reduced by approx. 4 dB to 12.26 dB.

The effect of the timing offset increases with the number of channels. If for instance 64 channels are used, the crest factor is reduced from 18.57 dB to 12.28 dB.

2.14.3.4 Effects on Crest Factor

The crest factor of the signal is mainly influenced by the following parameters:

- Link direction
  By modifying the Q component of the scrambling code in the uplink, it is possible to avoid every second zero crossing. In contrast to the downlink, the crest factor is reduced by approx. 1.8 dB. (Standard case DPCCH + 1DPDCH, the effect decreases with increasing number of channels.)
- Timing offset (see 2.14.3.3.5)
  In the case of more than 10 channels, an increment of 1 (i.e. the first channel is assigned 0, the second 1, etc.) causes a low crest factor. With a small number of channels, an increment of 3 or 5 permits to obtain even better values.
- Sequence length if timing offset is active
  Longer sequences cause higher peak values and hence higher crest factors since the probability of even higher peak values increases.
- Number of channels
  More channels cause higher crest factors.
- Selection of channelization codes
  Neighbouring channelization codes usually cause higher crest factors than numbers distributed over the full range of available values.
- **Example:** 16 channels with channelization codes 0 to 15: 16.7 dB
  16 channels with channelization codes 0, 4, 8, to 60: 12,45 dB
- Value of clipping level (see section 2.14.2.10)

2.14.3.5 Orthogonality of Channels

To enable a 3GPP W-CDMA demodulator to restore the information of the individual channels from the sum signal, only certain combinations of

- scrambling code sequences,
- channel power and
- symbol rate and channelization codes

are allowed.

2.14.3.5.1 Ideal Scenario

In an ideal scenario the measured channel power (with code domain power analyzers) is equal to the transmitted power (setting value in the channel configuration table), Even with great differences between the channel powers the information of the channels can be restored 100%. For this purpose however orthogonality of the channels is required. Orthogonality is given in all cases described in section 2.14.3.3 Orthogonality is given for instance if DPCH channels are used only and all of them have the same symbol rate but different channelization codes. In addition, identical scrambling code values have to be set for all channels, i.e. all channels have to belong to a base station.
In this case the bit streams SC_i and SC_q' are identical for all channels. Since the channelization code sequences of two channels are orthogonal, the chip sequences Si and Sq of two channels are also orthogonal.

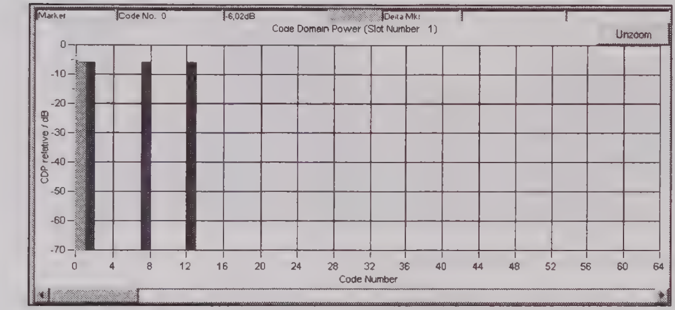
2.14.3.5.2 Real Scenario

A real scenario usually consist of signals from different base/mobile stations that are operated at different scrambling codes and have different delays. In this case there is no orthogonality, the channel powers overlap and the bit error rates increase with decreasing channel power (therefore power control is extremely important for all CDMA systems).

2.14.3.5.3 Effect of SCH

The SCH is not orthogonal to the DPCHs and all other downlink channels since the synchronization code symbol is spread according to a special scheme (see section 2.14.1.1, System Components).

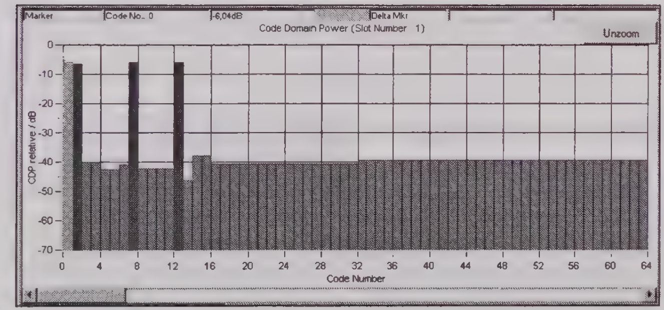
In the following example, the channels P-SCH, S-SCH, CPICH, P-CCPCH and two DPCHs (15 ksps to code 7 and 12) are included. If the SCH is compensated with the code domain power analysis, the channel powers can be determined ideally:



Marker
Code No. 0
-6,02dB
Delta Mkr
Code Domain Power (Slot Number 1)
Unzoom
CDP relative / dB
0
-10
-20
-30
-40
-50
-60
-70
0
4
8
12
16
20
24
28
32
36
40
44
48
52
56
60
64
Code Number

Fig. 2-156 CDPA of a signal with compensated SCH

If the SCH is not compensated, crosstalk to the other channels can be clearly seen:



Marker
Code No. 0
-6,04dB
Delta Mkr
Code Domain Power (Slot Number 1)
Unzoom
CDP relative / dB
0
-10
-20
-30
-40
-50
-60
-70
0
4
8
12
16
20
24
28
32
36
40
44
48
52
56
60
64
Code Number

Fig. 2-157 Effect of SCH on CDP analysis (without compensation)

The transmitted channels are measured with an error of <0.2 dB. For all other channels a power of approx. -40 dB is determined.
These conditions still allow error-free decoding of all transmitted channels.

2.14.3.5.4 Effect of S-CCPCH and the Other Downlink Channels

Like a DPCH, S-CCPCH, P-CPICH, S-CPICH, PDSCH, PICH, AICH and DL-DPCCH are spread with the same symbol rate. Therefore the same conditions as for DPCHs apply to the parameters of these channels (see section 2.14.3.5.7).

2.14.3.5.5 Effect of PRACH and PCPCH

The PRACH and the PCPCH are not orthogonal to DPCCH and DPDCHs since the scrambling code is formed differently. Mobile stations never use PRACH or PCPCH and DPCCH/DPDCH simultaneously – therefore mixed operation is only possible in WinIQSIM if these channel types are distributed to different stations (e.g. MS1 and MS2).

2.14.3.5.6 Effect of Scrambling Code

The scrambling code permits to distinguish between base stations (downlink) and mobile stations (uplink). If different scrambling codes are used within a signal (in this example, the scenario under section 2.14.3.5.3 is coded twice with different scrambling codes), the orthogonality between the channels will be lost.

The following example shows the level conditions measured after despreading:

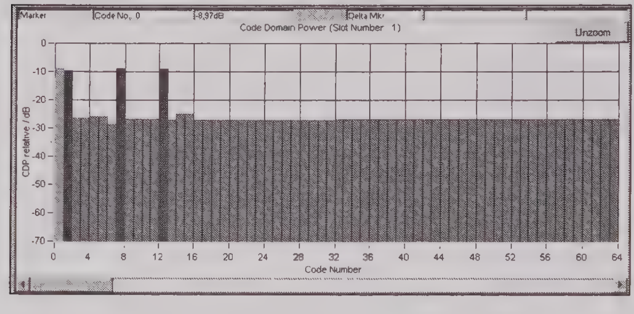

Marker
Code No. 0
-8,97dB
Delta Mkr
Code Domain Power (Slot Number 1)
Unzoom
CDP relative / dB
0
-10
-20
-30
-40
-50
-60
-70
0
4
8
12
16
20
24
28
32
36
40
44
48
52
56
60
64
Code Number

Fig. 2-158 Effect of different scrambling codes on the power distribution

These conditions still allow error-free reconstruction of the bit streams. A certain guard interval should however always be observed between the transmitted channel power and the measured power of the other (non-orthogonal) channels.

In this example a channel power of -10 dB is sufficient for error-free decoding, whereas with a power of -15 dB the bit error rate is about 10 % already. (The given values were evaluated at the physical layer using a CDPA. Even under worse conditions error-free reconstruction of the bit stream can be made through channel coding!)

2.14.3.5.7 Effect of Symbol Rates and Channelization Code Numbers

The channelization code generation method has the effect that with certain symbol rate/channelization code combinations the identical spreading sequences Si/Sq are generated. This is always the case with channelization code number 0 (all bits 0), but also with the combination 960 ksps/channelization code 1 (0011) <-> 480 ksps/channelization code 2 (00110011).

As a rule, the spreading sequences are identical if the products of symbol rate and channelization code are identical.

The despreader can therefore no longer differentiate between the two channels. Depending on the data contents, the channel powers are either added up or cancel each other. Restoring of all source data is not possible.

The following signal contains

- a DPCH with 960 ksps, channelization code number 1, 0 dB, data all 0
- a DPCH with 480 ksps, channelization code number 2, 0 dB, data all 1

An analysis at 960 ksps gives the following result:

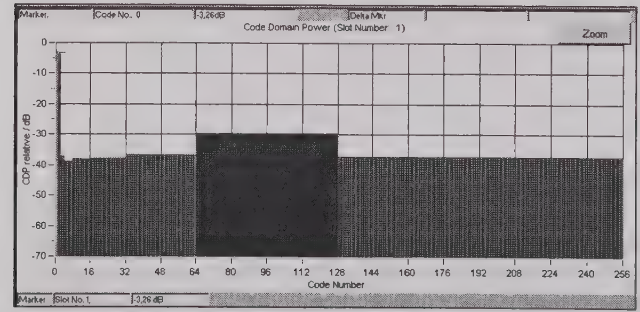

Marker
Code No. 0
-3,26dB
Delta Mkr
Code Domain Power (Slot Number 1)
Zoom
CDP relative / dB
0
-10
-20
-30
-40
-50
-60
-70
0
16
32
48
64
80
96
112
128
144
160
176
192
208
224
240
256
Code Number
Marker
Slot No. 1
-3,26 dB

Fig. 2-159 Cancellation possible in case of several channels with identical spreading sequences

Apart from this trivial case there are other cases in which problems may occur.
The above example is modified by changing the channelization code of the second DPCH from 2 to 3:

- a DPCH with 960 ksps, channelization code 1, 0 dB, data all 0
- a DPCH with 480 ksps, channelization code 3, 0 dB, data all 1

After despreading at symbol rate 480 ksps, the following power distribution is obtained:

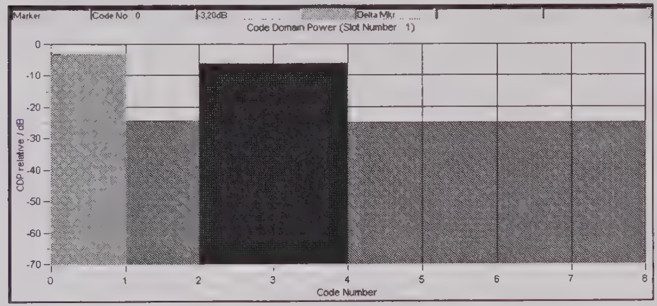


Fig. 2-160 Incorrect detection at various symbol rates

The 960-ksps channel (channelization code 1) is detected as channel 2 at 480 ksps. There is however no crosstalk to channel 3. Therefore the 480-ksps channel is detected with the original power and all bits can be restored.

Despreading of the 960-ksps channel however gives the following power distribution:

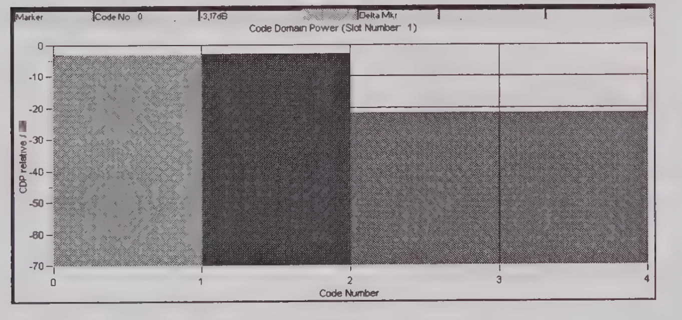


Fig. 2-161 Non-restorable DPCH channel

Both channels synchronize with channel 1, despreading is no longer possible with these level conditions.

For despreading all transmitted DPCHs, two channels of half the symbol rate (channelization codes 2n, 2n+1) should therefore be avoided for each channel used with a certain symbol rate (channelization code n). This is continued for four channels of one fourth of the symbol rate (channelization codes 4n, 4n+1, 4n+2, 4n+3), etc.

For further information refer to section 2.14.2.9.

2.14.3.6 Simulation of Special Scenarios

It is shown in the following how special scenarios can be simulated by means of WinIQSIM parameters. The downlink is used as an example, since a mobile station can always be completely implemented in an MS. Scenario for the simulation of several base stations 2.14.3.6.4 can also be applied to the simulation of mobile station signals.

Note: *Section 2.14.2.2 describes a particularly simple method for setting a base station.*

2.14.3.6.1 Standard Base Station

A standard base station is the easiest example of a base station.

- Activate the channels P-CPICH, P-SCH, S-SCH and P-CCPCH (State = ON). These channels are absolutely necessary for the synchronization of a mobile station with the signal.
- Activate between 0 and 128 DPCHs.
- Select channelization codes and symbol rates so that no code domain conflicts occur.
- Use timing offset in channels to model the offsets T_{DPCH}, T_{PICH}, $T_{S\text{-}CCPCH}$ ([1], section 7) as specified in the 3GPP W-CDMA standard.
- Set a valid combination of scrambling code and 2nd Search Code Group. (For details refer to [2] 5.2.2)

In [3], Appendix D, a few test models are specified that have been designed for certain tests. These scenarios are supplied as WinIQSIM setups.
They can be found in the 3GPP W-CDMA - TEST MODELS menu.

2.14.3.6.2 Base Station with More Than 128 DPCHs

To avoid the channel table becoming ambiguous, a BS in WinIQSIM may contain a maximum of 128 DPCHs. If a base station is to be simulated with more than 128 DPCHs, these channels have to be distributed to several BS.

- Make the same settings as in section 2.14.3.6.1 for the first BS.
- Assign the remaining DPCHs to the other BSs.
- Select the channelization codes and symbol rates so that no code domain conflicts occur, even beyond the limits of a BS.

Note: *The SMIQ does not support code domain power (conflict) display beyond the limits of a MS/BS. If several stations are available it is useful to divide the available range in subranges (e.g. BS1 : 0 to 256, BS2 : 257 to 467, BS3: 468 to 511).*

- Set the same value and type for the scrambling code for all BSs.

Note: *The function "Copy BS/MS", as described in section 2.14.3.6.1 facilitates the transfer of these values from one BS to another.*

2.14.3.6.3 Base Stations with Spreading Codes Used Several Times

3GPP W-CDMA envisages an increase in the number of channels using primary and secondary scrambling codes. The PCCPCH and the first part of DPCHs is then sent with the primary scrambling code, the remaining DPCHs with one of the assigned secondary scrambling codes. The following range of values is given in [2]:
Primary scrambling code : n = 16 * i with i=0 to 511
Secondary scrambling code : n = 16 * i + k with i=0 to 511 and k=1 to 15

Note: *The use of secondary scrambling codes principally yields the same results as the parameter T_{offset} specified in former standards.*

When the secondary scrambling codes are used, orthogonality is impaired and a crosstalk is produced (see section 2.14.3.5.6).
This scrambling code can be entered for a BS. If channels with different scrambling codes are to be simulated, a division into several BSs is required as described under 2.14.3.6.2:

- Set several BSs according to 2.14.3.6.1. The special channels (SCH, CPICH, ...) will be activated in one BS only.
- Set the desired values for the scrambling code for all BSs.

2.14.3.6.4 Several Base Stations

3GPP W-CDMA base stations can be at different RF frequencies. This case can be simulated in WinIQSIM using the multicarrier mixed signal system and option SMIQB60.
Since there are normally not enough frequencies available, the base stations within a transmit frequency are distinguished by the primary scrambling code.

- Set several BSs according to 2.14.3.6.1.
- Set the desired values for the scrambling code for all BSs.

2.15 Enhanced Functions For Digital Standard 3GPP W-CDMA (FDD)

Option SMIQB48 expands the scope of functions of option SMIQB45 and offers the following features:

- Sequences with a length of up to 2044 frames (SMIQB45: 13 frames)
- Data lists for data fields and TPC
- External control of channel power in real time
- Channel coding
- Insertion of bit errors (for testing a BER tester)
- Insertion of block errors (for testing a BLER tester
- Simulation of orthogonal channel noise (OCNS)
- Simulation of up to 64 additional mobile stations
- Simulation of a channel-coded P-CCPCH (BCP) with system frame number

2.15.1 Test Setup

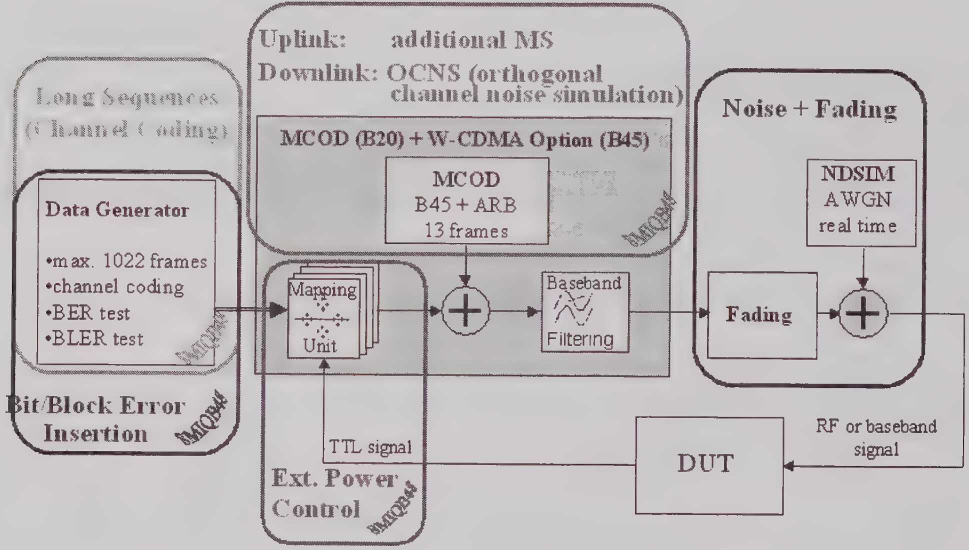

Uplink: additional MS
Downlink: OCNS (orthogonal channel noise simulation)
Long Sequences (Channel Coding)
MCOD (B20) + W-CDMA Option (B45)
MCOD B45 + ARB 13 frames
Noise + Fading
NDSIM AWGN real time
Data Generator
•max. 1022 frames
•channel coding
•BER test
•BLER test
Mapping Unit
Baseband Filtering
Fading
Bit/Block Error Insertion
TTL signal
Ext. Power Control
DUT
RF or baseband signal
SMIQB48

Fig. 2-163 Complete setup for testing a W-CDMA receiver with the SMIQ

This block shows the different functional blocks of the SMIQ with built-in option SMIQB48.
Blocks "Long Sequences/Channel Coding", "Bit/Block Error Insertion", "OCNS" and "External Power Control" are part of option SMIQB48 and will be described in detail.
"Noise" is available only if option SMIQB17 is built-in. For a description see section " Noise Generator and Distortion Simulator".
To simulate "Fading", option SMIQB14/SMIQB15 or SMIQB49 is required. This option is described in the section on "Fading Simulation".

References:

[1] 3GPP TS25.101 V4.1.0 UE Radio transmission and reception (FDD)
[2] 3GPP TS25.212 V4.1.0 Multiplexing and channel coding (FDD)
[3] 3GPP TS25.104 V4.1.0 BS Radio transmission and reception (FDD)
[4] 3GPP TS25.141 V4.1.0 Base Stations Conformance Testing (FDD)
[5] 3GPP TS25.944 V4.1.0 Channel Coding and multiplexing examples
[6] 3GPP TS25.331 V4.1.0 RRC Protocol Specification

2.15.2 Branching to Menus SMIQB48 of Digital Standard 3GPP WCDMA

The enhanced functions of option SMIQB48 are directly accessible from the menu of option SMIQB45, i.e. the digital standard WCDMA/3GPP must be selected. This section only describes the parameters and functions that are directly available with option SMIQB48 installed and which have not been described in the sections "Digital Standard 3GPP W-CDMA" and "Generation of 3GPP W-CDMA Signals".

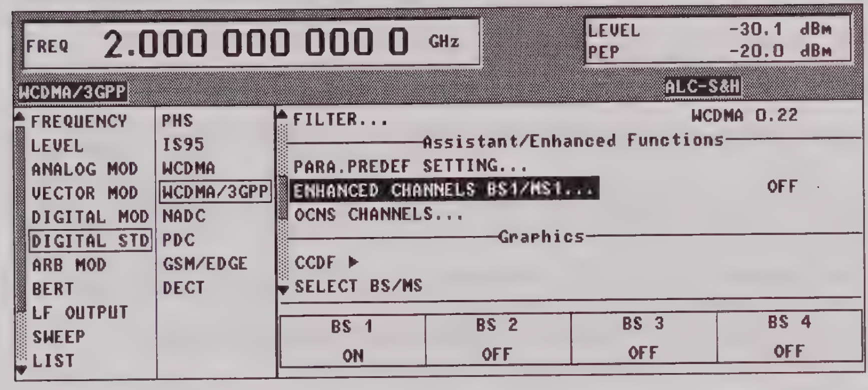

FREQ 2.000 000 000 0 GHz
LEVEL -30.1 dBm
PEP -20.0 dBm
WCDMA 0.22
ENHANCED CHANNELS BS1/MS1... OFF
BS 1 ON
BS 2 OFF
BS 3 OFF
BS 4 OFF
WCDMA/3GPP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
BERT
LF OUTPUT
SWEEP
LIST
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
FILTER...
Assistant/Enhanced Functions
PARA.PREDEF SETTING...
OCNS CHANNELS...
Graphics
CCDF ▶
SELECT BS/MS

Fig. 2-164 Menu DIGITAL STD – WCDMA/3GPP – Section Assistant/Enhanced Functions (downlink)

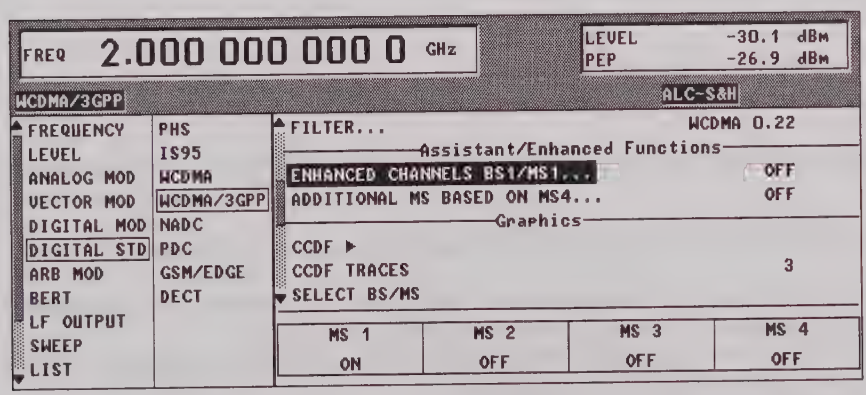

FREQ 2.000 000 000 0 GHz
LEVEL -30.1 dBm
PEP -26.9 dBm
WCDMA 0.22
ENHANCED CHANNELS BS1/MS1... OFF
ADDITIONAL MS BASED ON MS4... OFF
CCDF TRACES 3
MS 1 ON
MS 2 OFF
MS 3 OFF
MS 4 OFF
WCDMA/3GPP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
BERT
LF OUTPUT
SWEEP
LIST
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
FILTER...
Assistant/Enhanced Functions
Graphics
CCDF ▶
SELECT BS/MS

Fig. 2-165 Menu DIGITAL STD – WCDMA/3GPP – Section Assistant/Enhanced Functions (uplink)

--------------------Assistant/Enhanced Functions--

ENHANCED CHANNELS BS1/MS1	Call-up of user menu for configuring the enhanced channels. The menu item is only available with option SMIQB48 installed. For more detailed information regarding this menu see section 2.15.3
OCNS CHANNELS	Simulation of orthogonal channel noise. The menu item is only available in the downlink with option SMIQB48 installed. For more detailed information regarding this menu see section 2.15.4.
ADDITIONAL MS BASED ON MS4	Simulation of up to 50 additional mobile stations. The menu item is only available in the uplink and with option SMIQB48 installed. For more detailed information regarding this menu see section 2.15.5.

2.15.3 Enhanced Channels BS1/MS1

Enhanced Channels are channels which – in contrast to option SMIQB45 – are neither calculated nor generated on the modulation coder but on the data generator. Up to four enhanced channels (or seven with uplink mode) are possible where the maximum sequence length is calculated from the free list memory and the number of channels:

Downlink:

$$Sequence\_Length/Frames = \frac{Available\_Data\_List\_Memory/Bits}{Number\_Of\_Enhanced\_Channels * 2 * 38400}$$

With maximum memory capacity, there are thus between 255 (4 DPCH) and 1022 (1 DPCH) frames.

In the downlink (possibly depending on memory capacity), a P-CCPCH/BCH with a continuous system frame number (period 4096 frames) can also be generated at the same time as a DPCH (period 2044 frames) with a max physical rate of 30 ksps.
The simulation of higher rate channels (>30 ksps) together with the P-CCPCH/BCH is also supported however with a reduced maximum sequence length of 500 frames.
(For a systematic list of all possible sequence lengths see description of SEQUENCE LENGTH parameter, section **Fehler! Verweisquelle konnte nicht gefunden werden.**).

Note: *This sequence length is sufficient for all performance tests in [1], section 8, since in these tests the block error rate is measured.*

Uplink:

$$Sequence\_Length/Frames = \frac{Available\_Data\_List\_Memory/Bits}{(Number\_Of\_Enhanced\_Channels + 1) * 38400}$$

With maximum memory capacity, there are thus between 255 (DPCCH + 6 DPDCH) and 1022 (1 DPCCH or 1 DPDCH) frames.

Note: *The higher number of channels in the uplink is possible through the BPSK data modulation.*

These long sequences allow a physical **BER measurement (PN9) without a restart** of the BER tester being required. An enhanced channel with a length of 511 frames is to be set.
If the maximum length of 1022 frames is used, BER measurements with channel coding and without restart as required for receiver tests to [1], [3], [4] are possible.
Compared to the clearly enhanced sequence length range (max. 13 frames for SMIQB45), option SMIQB48 offers the following advantages:
After generation of the sequence in the data generator **Data lists are available as data sources** for the data fields and the TPC fields. Externally precoded data can be loaded into the SMIQ by the user, for example to implement channel-coding schemes that are not supported by option SMIQB48. Analogously, the long TPC lists are necessary to generate long non-repetitive power profiles.

Moreover, a base station can be simulated, which comprises a channel-coded P-CCPCH (BCH) and a 12.2 kbps reference measurement channel in addition to background and synchronization channels. The system frame number (SFN) in BCH is automatically incremented in steps of two from 0 to 4094 (period 4096). The remaining system information of BCH can be filled from data lists, for example.

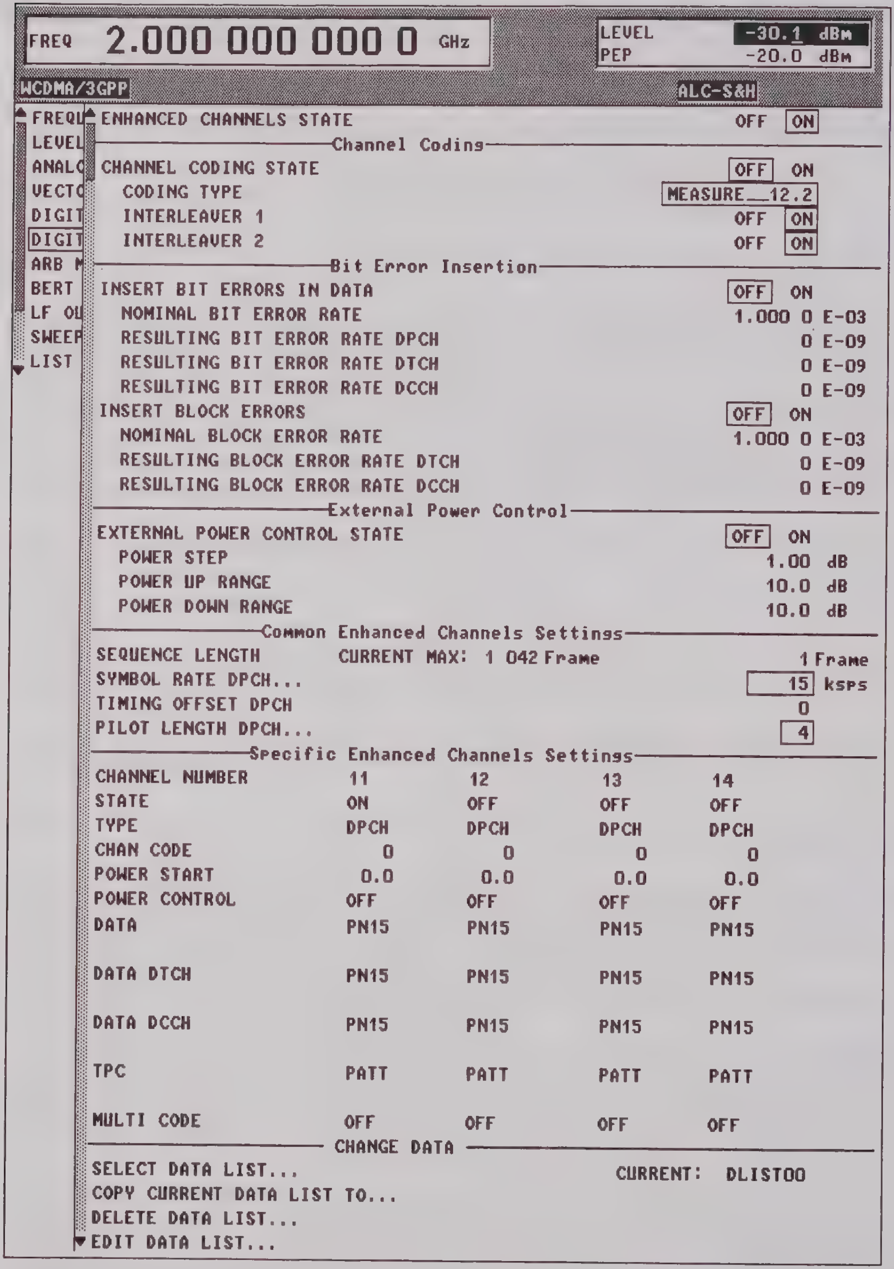
Another feature of enhanced channels is the possibility to generate the **reference measurement channels** described in [1], [3] and [4] and the AMR channels described in [5]. For a description see 2.15.3.1.1.

The chip sequences of the enhanced channels are transmitted in binary form to the modulation coder and are converted into (initially digital) I/Q levels in a mapper. This mapping can be switched by an external input line (TTL, connected to LEVATT (PARDATA pin 9 or SMIQ-Z5)). The **channel powers of the enhanced channels can be controlled in real time**, allowing the test of "SIR based closed loop power control", for example. For further information see 2.15.3.1.3.

For testing BER/BLER testers (e.g. integrated into base station), artificial **bit errors (or block errors into the CRC checksum) can be integrated** in all data sources. The required parameters can be found in section 2.15.3.1.5

The menu structure depends on the link direction. Since there are only slight differences, first the downlink and all common parameters are described in section 2.15.3.2 and then all uplink parameters.

2.15.3.1 Downlink



```
FREQ   2.000 000 000 0 GHz                    LEVEL   -30.1 dBm
                                              PEP     -20.0 dBm
WCDMA/3GPP                                    ALC-S&H
FREQU  ENHANCED CHANNELS STATE                          OFF [ON]
LEVEL  ----------------------Channel Coding----------------------
ANALO  CHANNEL CODING STATE                            [OFF] ON
VECTO    CODING TYPE                             [MEASURE__12.2]
DIGIT    INTERLEAVER 1                                  OFF [ON]
DIGIT    INTERLEAVER 2                                  OFF [ON]
ARB M  ---------------------Bit Error Insertion------------------
BERT   INSERT BIT ERRORS IN DATA                       [OFF] ON
LF OU    NOMINAL BIT ERROR RATE                      1.000 0 E-03
SWEEP    RESULTING BIT ERROR RATE DPCH                     0 E-09
LIST     RESULTING BIT ERROR RATE DTCH                     0 E-09
         RESULTING BIT ERROR RATE DCCH                     0 E-09
       INSERT BLOCK ERRORS                             [OFF] ON
         NOMINAL BLOCK ERROR RATE                    1.000 0 E-03
         RESULTING BLOCK ERROR RATE DTCH                   0 E-09
         RESULTING BLOCK ERROR RATE DCCH                   0 E-09
       ---------------------External Power Control---------------
       EXTERNAL POWER CONTROL STATE                    [OFF] ON
         POWER STEP                                     1.00 dB
         POWER UP RANGE                                 10.0 dB
         POWER DOWN RANGE                               10.0 dB
       -----------------Common Enhanced Channels Settings---------
       SEQUENCE LENGTH      CURRENT MAX: 1 042 Frame      1 Frame
       SYMBOL RATE DPCH...                               [15] ksps
       TIMING OFFSET DPCH                                   0
       PILOT LENGTH DPCH...                                [4]
       ---------------Specific Enhanced Channels Settings---------
       CHANNEL NUMBER       11        12        13        14
       STATE                ON        OFF       OFF       OFF
       TYPE                 DPCH      DPCH      DPCH      DPCH
       CHAN CODE               0         0         0         0
       POWER START           0.0       0.0       0.0       0.0
       POWER CONTROL        OFF       OFF       OFF       OFF
       DATA                 PN15      PN15      PN15      PN15

       DATA DTCH            PN15      PN15      PN15      PN15

       DATA DCCH            PN15      PN15      PN15      PN15

       TPC                  PATT      PATT      PATT      PATT

       MULTI CODE           OFF       OFF       OFF       OFF
       ------------------ CHANGE DATA ----------------------------
       SELECT DATA LIST...                    CURRENT:  DLIST00
       COPY CURRENT DATA LIST TO...
       DELETE DATA LIST...
       EDIT DATA LIST...
```

Fig. 2-166 Menu DIGITAL STD-WCDMA/3GPP-ENHANCED CHANNEL (downlink)

ENHANCED CHANNELS STATE	Switch for activating or deactivating the calculation of up to 4 enhanced channels on DGEN. In the OFF state, all the following menu parameters no longer influence the 3GPP W-CDMA signal which is only calculated in MCOD ARB. IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:STAT ON`

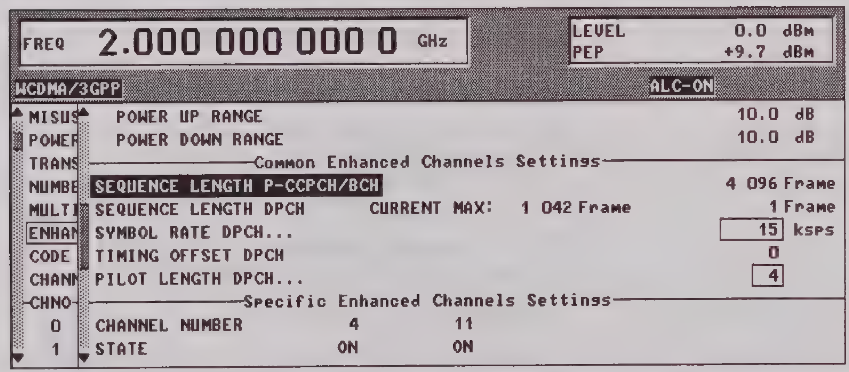
2.15.3.1.1 P-CCPCH/BCH with System Frame Number

In addition to channel-coded reference measurement channels, test specifications for mobile or UE tests (see [1]) often stipulate a channel-coded P-CCPCH (BCH). With the Enhanced P-CCHCP/BCH State activated, the SMIQ can generate this test scenario.
The SMIQ then offers one P-CCPCH and one DPCH instead of the 4 enhanced DPCHs in the Standard Enhanced mode (Enhanced P-CCHCP/BCH State Off).

ENHANCED P-CCPCH/BCH (INCL. SFN) STATE	This switch enables one P-CCPCH/BCH and one DPCH to be generated as enhanced channels (OFF) instead of the 4 DPCHs. IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:PCCP:STAT ON`

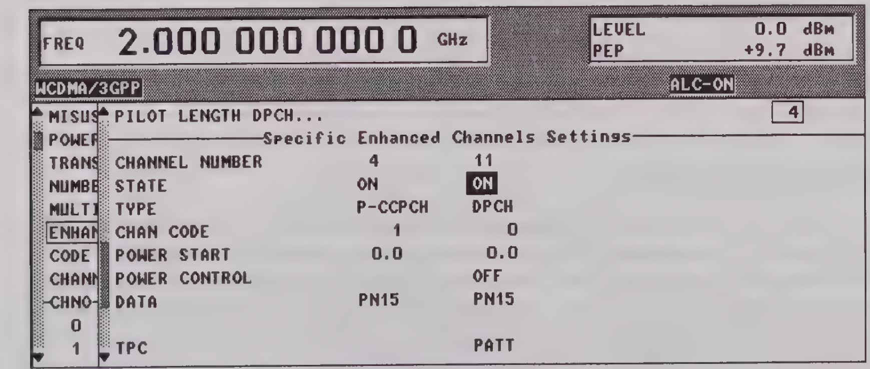
With Enhanced P-CCHCP/BCH State On, this may affect the structure of the other operating menus:

- The channel table under **Specific Enhanced Channel Settings** contains the P-CCPCH and a DPCH.
- If the P-CCPCH is activated under **Specific Enhanced Channel Settings**, it will automatically be generated with the sequence length 4096 frames (SFN period).
- The maximum sequence length (SEQUENCE LENGTH DPCH) changes:
  - for symbol rates ≤ 30 ksps: 2044 frames
  - for symbol rates >30 ksps: 500 frames



```
FREQ  2.000 000 000 0 GHz                     LEVEL          0.0 dBm
                                              PEP           +9.7 dBm
WCDMA/3GPP                                    ALC-ON
MISUS  POWER UP RANGE                                       10.0 dB
POWER  POWER DOWN RANGE                                     10.0 dB
TRANS ─────────────Common Enhanced Channels Settings──────────────
NUMBE SEQUENCE LENGTH P-CCPCH/BCH                     4 096 Frame
MULTI SEQUENCE LENGTH DPCH     CURRENT MAX:  1 042 Frame   1 Frame
ENHAN SYMBOL RATE DPCH...                                15 ksps
CODE  TIMING OFFSET DPCH                                  0
CHANN PILOT LENGTH DPCH...                                4
CHNO ──────────────Specific Enhanced Channels Settings────────────
  0   CHANNEL NUMBER          4          11
  1   STATE                   ON         ON
```

Fig. 2-167 Common Enhanced Channel Settings with Enhanced P-CCHCP/BCH State On



```
FREQ  2.000 000 000 0 GHz                     LEVEL          0.0 dBm
                                              PEP           +9.7 dBm
WCDMA/3GPP                                    ALC-ON
MISUS PILOT LENGTH DPCH...                                4
POWER ─────────────Specific Enhanced Channels Settings────────────
TRANS CHANNEL NUMBER          4          11
NUMBE STATE                   ON         ON
MULTI TYPE                    P-CCPCH    DPCH
ENHAN CHAN CODE                  1          0
CODE  POWER START             0.0        0.0
CHANN POWER CONTROL                      OFF
CHNO  DATA                    PN15       PN15
  0
  1   TPC                                PATT
```

Fig. 2-168 Specific Enhanced Channel Settings with Enhanced P-CCHCP/BCH State On

Generation principle of channel-coded P-CCPCH with system frame number
With Enhanced P-CCHCP/BCH State On, channel coding activated (Channel Coding State On) and with P-CCPCH State under Specific Enhanced Channel Settings On, the SMIQ generates a channel-coded P-CCPCH (i.e. a broadcast channel BCH) according to the following principle:

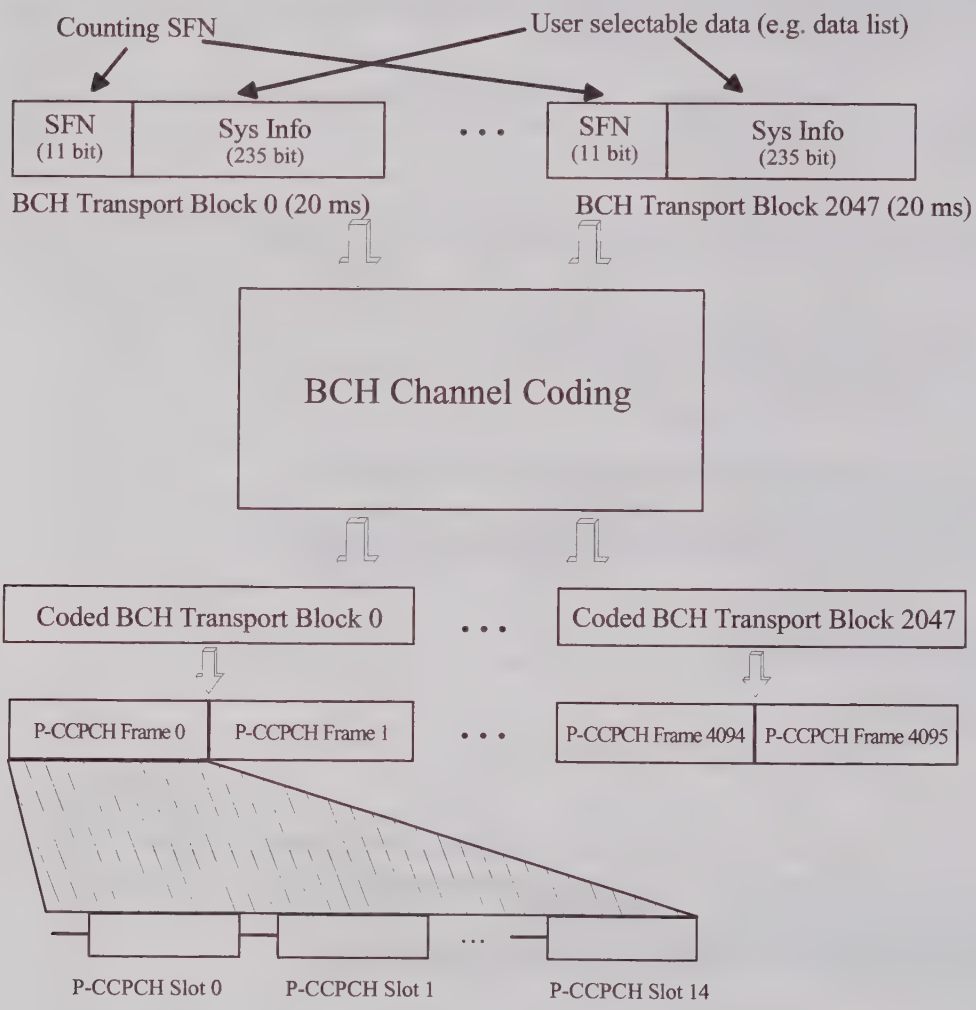

Counting SFN
User selectable data (e.g. data list)
SFN (11 bit)
Sys Info (235 bit)
. . .
SFN (11 bit)
Sys Info (235 bit)
BCH Transport Block 0 (20 ms)
BCH Transport Block 2047 (20 ms)
BCH Channel Coding
Coded BCH Transport Block 0
. . .
Coded BCH Transport Block 2047
P-CCPCH Frame 0
P-CCPCH Frame 1
. . .
P-CCPCH Frame 4094
P-CCPCH Frame 4095
. . .
P-CCPCH Slot 0
P-CCPCH Slot 1
P-CCPCH Slot 14

Fig. 2-169 Generation principle P-CCPCH/BCH

The data blocks of BCH at transport-channel level comprise data determined for 20 ms of the P-CCPCH (i.e. 2 frames) after channel coding. The first field of such a data block is an 11-bit long field for the system frame number (SFN). The SFN is automatically incremented by 1 (as stipulated in the standard) from transport block to transport block (equivalent to a step width of 2 frames due to the transport time interval length of 20 ms). After 2048 transport blocks (equivalent to 4096 frames) SFN is reset and starts again at 0 (SFN restart). An output trigger indicating the SFN restart can be generated (see 2.14.2.1).
The SFN format is defined in [6], sections 10.2.48, 11.2 (page 578) and 11.3 (page 691). It is MSB-first coded.
The remaining system information (a 235-bit long field per block) is filled from the data source selected for the P-CCPCH under Specific Enhanced Channel Settings.
A data list can be used to transmit further specific system information in addition to the SFN. If only the SFN is required, **ALL 0** is recommended as data source for P-CCPCH.
The BCH transport blocks are then channel-coded. A coded transport block comprises the data sequence for two P-CCPCH frames.

2.15.3.1.2 Channel Coding

Many tests can be found in [1] and [3] all of which require channel-coded measurement channels, so-called Reference Measurement Channels. Types 12.2 kbps, 64 kbps, 144 kbps and 384 kbps are indicated for each link direction.
In addition, the SMIQ can simulate an AMR voice channel according to [5]. The detailed structure of these channels is described in the following documents:

Table 2-27 References to measurement channels

CODING TYPE	Reference downlink	Reference uplink
MEASURE__12.2	[1], A.3.1	[3], A.2 and [4], A.2
MEASURE__64	[1], A.3.2	[3], A.3 and [4], A.3
MEASURE_144	[1], A.3.3	[3], A.4 and [4], A.4
MEASURE_384	[1], A.3.5	[3], A.5 and [4], A.5
AMR_CODER	[5], 4.1.1.3.1.2	[5], 4.1.2.2.1.2

To simplify the implementation of channel decoding in the test receiver the two interleaver stages in the measurement channels can separately be switched on and off.

-------------------**Channel Coding**--

CHANNEL CODING STATE

Switch for activating or deactivating the channel coding of all enhanced channels.
In the OFF state, the following menu parameters are not effective:
CODING TYPE,
INTERLEAVER 1,
INTERLEAVER 2,
CODING (enhanced channel specific).

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:CCOD ON`

CODING TYPE

Setting the channel coding type of all enhanced channels.
The 3GPP specification currently stipulates 4 predefined MEASUREMENT channel coding types which differ in the input data bit rate to be processed (12.2, 64, 144 and 384 KBPS). The additional AMR_CODER coding scheme generates the coding of a speech channel.
For channel coding, the input data bits are taken from the data source specified in menu parameter DATA. The bits with a higher data rate are available at the output of channel coding. There are fixed assignments between the measurement input data bit rate and the output symbol rate, i.e. the menu parameters SYMBOL RATE DPCH (Downlink) or OVERALL SYMBOL RATE are adapted automatically.

MEASURE_12.2	Measurement channel 12.2 kbps
MEASURE_64	Measurement channel 64 kbps
MEASURE_144	Measurement channel 144 kbps
MEASURE_384	Measurement channel 384 kbps
AMR_CODER	Channel coding for AMR Coder

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:CCOD:TYP M12K`

INTERLEAVER 1,2

Switch for separately activating or deactivating channel coding interleaver stages 1 and 2 of all enhanced channels. (The interleaver stages do not change the symbol rate.)

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:CCOD:INT1 ON`

2.15.3.1.3 Bit Error Insertion

Errors can be inserted in the data source or the CRC checksum for testing the bit/block error rate tester. If channel coding is inactive (CHANNEL CODING STATE = OFF), the data bits are fed into the slots at the physical level. If channel coding is active (CHANNEL CODING STATE = ON), the data bits are fed at the transport layer level. The bit errors are always generated at the data source level.

INSERT BIT ERRORS IN DATA

Switch used to activate or deactivate the insertion of bit errors in the data fields of all active enhanced channels at the frequency indicated under the NOMINAL BIT ERROR RATE parameter.
In the OFF state, the following menu parameters NOMINAL BIT ERROR RATE and RESULTING BIT ERROR RATE are not effective.

IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:DERR:BIT:STAT ON`

NOMINAL BIT ERROR RATE

Sets a user-defined bit error rate in the range of 10^{-1} to 10^{-7} with which the bits can be inserted into the data fields of the enhanced channel timeslots. Upon readout of the data source individual bits are inverted at random at a given error rate to simulate an erroneous signal.
Since the bit error rate set by the user may not be adhered to as a function of the frame SEQUENCE LENGTH and the selected SYMBOL RATE DPCH or OVERALL SYMBOL RATE, the actual bit error rate is displayed in the next line, i.e. RESULTING BIT ERROR RATE.

IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:DERR:BIT:RATE 2E-3`

RESULTING BIT ERROR RATE

Parameter indicating the resulting bit error rate used to insert the bits into the data fields of the enhanced channel timeslots (see NOMINAL BIT ERROR RATE parameter).

CHANNEL CODING STATE = OFF:

The BER of DPCH is displayed here.

IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:DERR:BIT:DPCH?`

CHANNEL CODING STATE = ON:

The BER of DTCH and DCCH is displayed here.

IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:DERR:BIT:DTCH?`
IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:DERR:BIT:DCCH?`

2.15.3.1.4 External Power Control

Two test constellations have to be distinguished in the test of Closed (Inner) Loop Power Control:

1. Test whether the DUT responds with the correct output power to received TPC bits (e.g. for testing according to [1], 6.4.2.1). This can be carried out by using a data list adapted to the test condition as TPC data source. The TPC pattern can be defined in the channel table (see previous section, "Menu WCDMA/3GPP - Submenu BS Configuration" or "Menu WCDMA/3GPP - Submenu MS Configuration", and also section 2.15.3.1.5).
2. Test whether the DUT correctly performs the SIR (Signal to Interference Ratio) measurement and inserts the corresponding bits into the TPC field of its transmit signal. Since the SMIQ has no receive channel, the TPC control information has to be taken to the generator via another channel. This is possible via "External Power Control".

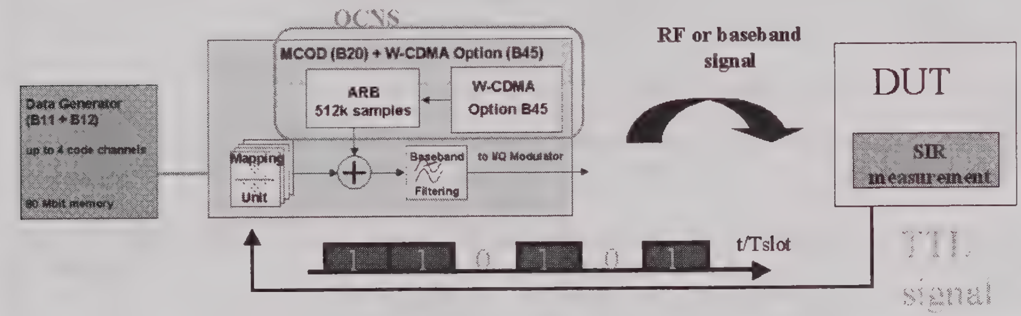


Fig. 2-170 Setup for testing Closed Loop Power Control

The power of all activated enhanced channels can be increased or decreased in a predefined dynamic range (= POWER UP RANGE + POWER DOWN RANGE) and in the defined step width (= POWER STEP) with the TTL signal "External Power Control".
The power change of the channels is performed by a switchover of the mapping table, controlled by the external power control signal which is queried at the beginning of the pilot field. Since the number of mappings is limited, the maximum dynamic range is restricted to 30 dB and the step width to min. 0.25 dB. The output power of each channel is thus limited to the [POWER_START-POWER_DOWN_RANGE...POWER_START+POWER_UP_RANGE] range.

Note : *To obtain optimum signal quality, the POWER_UP_RANGE should not be set higher than necessary since the mapping of the I/Q level in this range must be maintained as a level margin.*

POWER_START and POWER_CONTROL = UP/DOWN can be set channel-specifically (see section 2.15.3.1.5).

In the following example it is assumed that

- POWER_UP_RANGE = POWER_DOWN_RANGE
- POWER_CONTROL = UP for channels 0, 2 and 3, POWER_CONTROL = DOWN for channel 1

Available mappings are shown at the right with MAP_M being the starting point. In this point, all channels have the power which was set in the menu as POWER_START.
At the beginning of the pilot field the LEVATT line is queried in each timeslot. If this line is set to logical "0" switchover is made to the left mapping MAP_{M-1}. This means a reduction of the output power by POWER_STEP for all channels with POWER_CONTROL = UP . The power of channel 1 is increased by the same value.
If the LEVATT line is set to logical "0" switchover is made to the right mapping MAP_{M+1}. This means an increase of the output power by POWER_STEP for all channels with POWER_CONTROL = UP. The power of channel 1 is decreased by the same value.

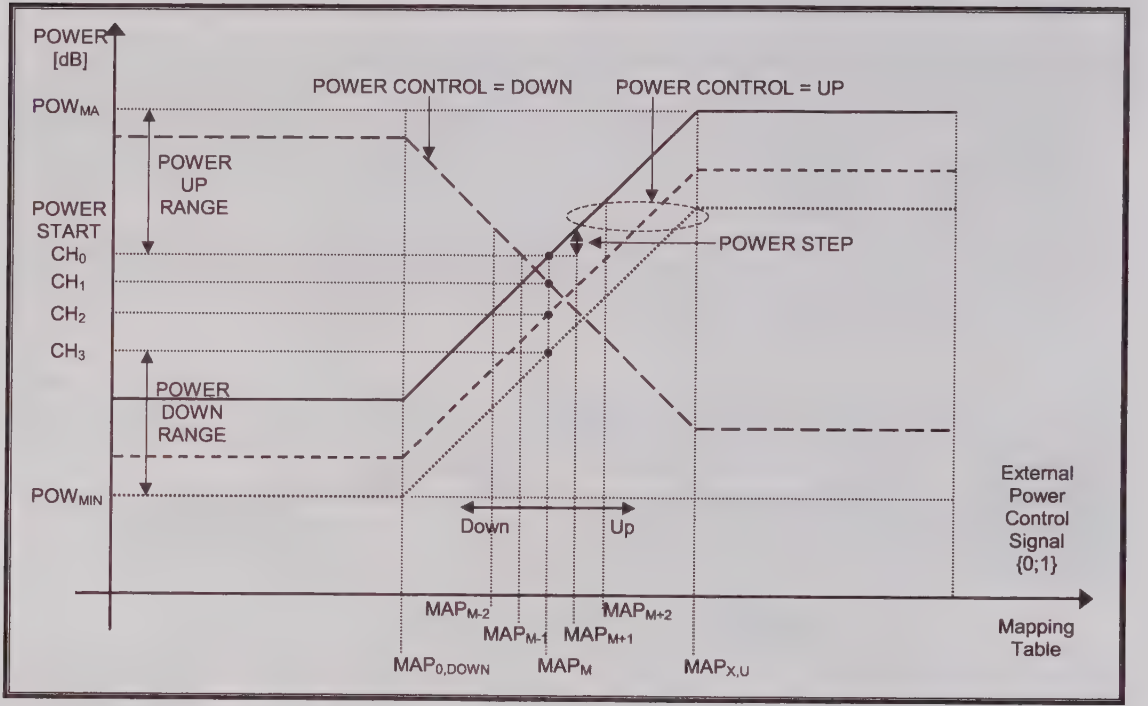

POWER [dB]
POW_{MA}
POWER CONTROL = DOWN
POWER CONTROL = UP
POWER UP RANGE
POWER START
CH_0
CH_1
CH_2
CH_3
POWER STEP
POWER DOWN RANGE
POW_{MIN}
Down
Up
External Power Control Signal {0;1}
MAP_{M-2}
MAP_{M-1}
MAP_{M+1}
MAP_{M+2}
$MAP_{0,DOWN}$
MAP_M
$MAP_{X,U}$
Mapping Table

Fig. 2-171 Change of channel power of 4 enhanced channels

--------------------**External Power Control**--------------------

EXTERNAL POWER CONTROL STATE

Switch for activating or deactivating the external power control mechanism of all enhanced channels.

In the OFF state, the following menu parameters are not effective:

POWER UP RANGE,
POWER DOWN RANGE,
POWER STEP,
POWER CONTROL GRAPH ?,
POWER START DPCCH,
POWER START DPDCH,
POWER START (enhanced channel-specific).
POWER CONTROL (enhanced channel-specific).

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:EPOW:STAT ON`

POWER STEP

Step width (0.25 to 6.0 dB) by which – with the external power control mechanism being switched on - the channel powers of the activated enhanced channels in the timeslot grid are increased or decreased within the set dynamic range.

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:EPOW:STEP 1.0`

POWER UP RANGE

Dynamic range (0 to 30 dB) by which – with external power control mechanism switched on – the channel powers of all activated enhanced channels can be increased. The value POWER UP RANGE can be changed by POWER STEP. If the dynamic range is exceeded, the warning MAX. POWER UP REACHED is displayed.

The resulting external power control dynamic range (= POWER UP RANGE – POWER DOWN RANGE) may be 30 dB at max. which is why the POWER UP RANGE is limited as a function of the POWER DOWN RANGE.

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:EPOW:RANG:UP 10.0`

POWER DOWN RANGE

Dynamic range (0 to 30 dB) by which – with external power control mechanism switched on – the channel powers of all activated enhanced channels can be decreased. The value POWER UP RANGE can be changed by POWER STEP. If the dynamic range is underranged, the warning MAX. POWER UP REACHED is displayed.

The resulting external power control dynamic range (= POWER UP RANGE – POWER DOWN RANGE) may be 30 dB at max. which is why the POWER DOWN RANGE is limited as a function of the POWER UP RANGE.

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:EPOW:RANG:DOWN 10.0`

POWER CONTROL GRAPH ►

With this menu parameter selected, the display window is opened again and - with external power control mechanism switched on - the "current" deviation of the channel power from the set power start value of the corresponding enhanced channels is displayed graphically(see section 2.15.3.3).

The display window is only shown if the EXTERNAL POWER CONTROL switch is set to ON and if the specific POWER CONTROL switch is positioned to UP or DOWN in the downlink for at least one enhanced channel.

Note : *This window is opened automatically after calculation of the signals triggered by STATE = ON or MOD ON but can be closed again manually by the user. This menu parameter allows the insertion of the window again without recalculating the signal.*

Note: *With Enhanced P-CCPCH/BCH State On in the downlink, only the DPCH is power-controlled. The P-CCPCH always has the power set in the enhanced channel table.*

2.15.3.1.5 Further Setting of Enhanced Channels Menu

---------------------Common Enhanced Channels Settings---

SEQUENCE LENGTH P-CCPCH/BCH With Enhanced P-CCPCH/BCH State On in the downlink, the sequence length of the P-CCPCH/BCH is displayed. The length is always 4096 frames and cannot be edited.

IEC/IEEE-bus command (query) `:SOUR:W3GP:BST:ENH:PCCP:SLEN?`

SEQUENCE LENGTH Sets the sequence length in radio frames used to calculate the 3 GPP W-CDMA enhanced channel signal component on DGEN.
Depending on the currently available free DGEN list memory, the currently available maximum frame sequence length is displayed in the middle of the menu parameter line.

Note : *After triggering a new calculation, the 3GPP W-CDMA signal component calculated on DGEN is stored in a hidden data list. The maximum number of radio frames depends on the currently available free DGEN list memory for the hidden data list max. 80 Mbit).*

In addition to the free DGEN list memory the maximum sequence length crucially depends on the ENHANCED P-CCPCH/BCH state and the number of DPCHs:

ENHANCED P-CCPCH/BCH STATE = OFF

1 DPCH:	1022 frames
2 DPCHs:	511 frames
3 DPCHs:	341 frames
4 DPCHs:	255 frames

ENCHANCED P-CCPCH/BCH STATE = ON

SYMBOL RATE DPCH ≤30 ksps:	2044 frames
SYMBOL RATE DPCH >30 ksps:	500 frames

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:SLEN 123`

SYMBOL RATE DPCH Sets symbol rate of all downlink enhanced channels in ksps.
If the CHANNEL CODING STATE switch is set to ON, there is a close relation between the settings CODING TYPE and SYMBOL RATE DPCH:

MEASURE_12.2KBPS :	SYMBOL RATE DPCH = 30 ksps
MEASURE_64KBPS :	SYMBOL RATE DPCH = 120 ksps
MEASURE_144KBPS :	SYMBOL RATE DPCH = 240 ksps
MEASURE_384KBPS :	SYMBOL RATE DPCH = 480 ksps
AMR_CODEC :	SYMBOL RATE DPCH = 30 ksps;

When a parameter is changed (CODING TYPE or SYMBOL RATE DPCH) the associated parameter is adapted automatically.
In the higher BS1 menu the SYMBOL RATE settings of all activated enhanced channels are automatically updated in the channel table.

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:SRAT D30K`

TIMING OFFSET	Sets a time offset (0 to 149) with which the corresponding downlink enhanced channels are sent. (The resulting crest factor of the overall signal can be influenced). The timing offset can be set in an interval of 256 chips. IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:TOFF 1`
PILOT LENGTH	Sets the pilot field size in the timeslots of the corresponding downlink enhanced channels (2, 4, 8 or 16 depending on the symbol rate). IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:PLEN BIT4`

----------------------**Specific Enhanced Channel Settings**---

CHANNEL NUMBER Display parameter for the assigned serial number of a downlink enhanced DPCH or an uplink enhanced DPDCH from the BS1 or MS1 channel table. The enhanced channels are assigned to the first 4 dedicated channels in the BS1 channel table or to the DPCCH/DPDCH of the MS1 channel table.
With Enhanced P-CCPCH/BCH State On in the downlink, also the P-CCPCH with channel number 4 is available.

STATE Switch for activating or deactivating the corresponding downlink enhanced channel. In the ON state the channel is marked as enhanced channel by an additional E (in addition to CH. NO) in the channel table line determined by the CHANNEL NUMBER in the higher BS1 menu – under the precondition that the ENHANCED CHANNELS STATE switch is set to ON.

In the OFF state, the following channel-specific menu parameters are not effective:
CHAN CODE
POWER START
POWER CONTROL
DATA
TPC
MULTI CODE

In the higher BS1 menu, the STATE parameter has to be updated in the channel table line determined by the ENHANCED CHANNEL NUMBER.

Note: *The enhanced channels cannot be activated individually in the uplink; the required enhanced channels are automatically activated via the OVERALL SYMBOL RATE setting.*

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:CHAN11:STAT ON`

TYPE Display parameter for the channel type of the corresponding enhanced channel.
The type is taken from the TYPE parameter of the channel table line or column determined by the ENHANCED CHANNEL NUMBER with the channel table belonging to the higher BS1/MS1 menu.
In the downlink, enhanced channels have the DPCH type and in the uplink, the DPCCH type (1st enhanced channel) or DPDCH (2nd to 4th enhanced channel).

With Enhanced P-CCPCH/BCH State On in the downlink, channel type P-CCPCH is additionally available in the table.

CHAN CODE

Sets the corresponding enhanced channel.

Downlink:

The channelization codes of the downlink enhanced channels are variable from 0 to -1 (3.840 MCPS/enhanced channel symbol rate). In the higher BS1 menu, the CH. CODE parameter is updated in the channel-table line determined by the ENHANCED CHANNEL NUMBER.

With Enhanced P-CCPCH/BCH State On in the downlink, the P-CCPCH automatically has the data rate of 15 ksps and the channelization code 1 stipulated in the standard.

Uplink:

The channelization codes of the uplink enhanced channels are constant and described in [2].

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:CHAN11:CHNC 0`

POWER START

Sets the channel (start) power of the corresponding downlink enhanced channel. If the EXTERNAL POWER CONTROL STATE switch is set to ON and the channel-specific POWER CONTROL switch to UP or DOWN, this value is the channel start power for the external power control mechanism.
In the higher BS1 menu, the POWER DB parameter has to be updated in the channel table line determined by the ENHANCED CHANNEL NUMBER.

Note : *By starting the ADJUST TOTAL POWER menu parameter in the upper menu level of 3GPP W-CDMA all channel powers (POWER DB parameter) of the BS1 channel table and thus also the POWER START values of the set enhanced channel are adapted so that the average overall power of all activated channels is 0 dB with reference to the LEVEL indicated in the SMIQ display.*

In the uplink, the channel start powers are determined via the parameters POWER START DPCCH and POWER START DPDCH.

IEC/IEEE-bus command `:SOUR:W3GP:BST1:ENH:CHAN11:POW:STAR -30.0`

POWER CONTROL

Switch indicating the direction (UP or DOWN) with external power control mechanism activated – with which the channel power of the corresponding downlink enhanced channel is changed in the POWER STEP. The link is with reference to the high level of the external power control TTL signal at the beginning of the pilot field of a timeslot.

In the OFF state, the corresponding downlink enhanced channel of the external power control mechanism is not taken into account.

IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:CHAN1:POW:CONT UP`

Note: *The Power Control parameter is not provided with P-CCPCH since it is not power-controlled.*

DATA
DATA DTCH
DATA DCCH

If channel coding is inactive, the data source for the data fields of the timeslots of the corresponding enhanced channel can be selected here (DATA DTCH and DATA DCCH are nor displayed in this case.).
If channel coding is active, data sources can be selected for the two components of the channel (DTCH and DCCH).

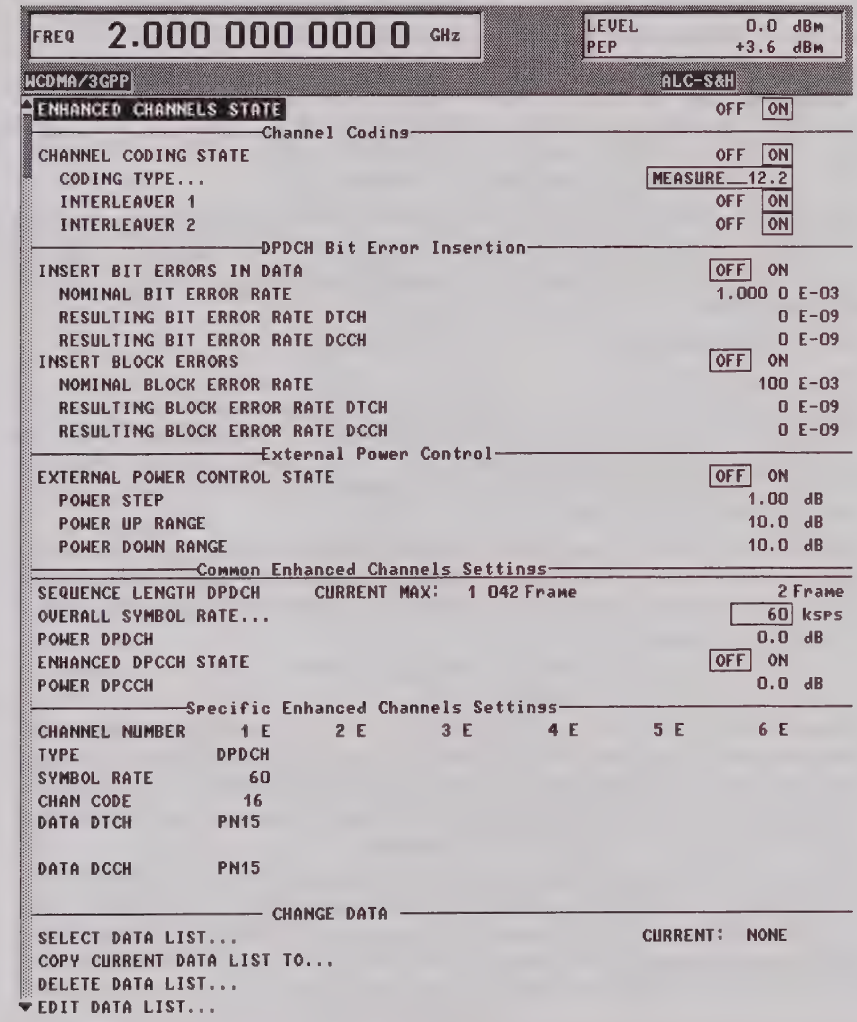
In a submenu the data sources PN9, PN11, PN15, PN16, ALL0, ALL1 and a 24-bit PATTERN as well as any data lists DLISTxx can be selected. In the first uplink enhanced channel (DPCCH) a data source cannot be indicated since no data field is available for this channel in the timeslots.

IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:CHAN1:DATA PN9`
IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:CHAN1:DATA:PATT #H3F, 8`
IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:CHAN1:DATA:DSEL 'name'`

TPC	Selects the data source for the TPC fields of the timeslots of the corresponding enhanced channel. In a submenu the data sources ALL0, ALL1 and a 24-bit PATTERN as well as any data lists DLISTxx can be selected. Only one bit is read out from the data source per TPC field. This bit is then multiplied in the whole TPC field. IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:CHAN1:TPC ONE` IEC/IEEE-bus `:SOUR:W3GP:BST:ENH:CHAN1:TPC:PATT #H3F, 8`
MULTI CODE	Switch with which the multicode channel trunking of the corresponding downlink enhanced channel can be activated or deactivated with other dedicated channels. All dedicated channels of a base station whose multicode switch is set to ON are trunked to form one virtual channel – irrespective of whether they are declared as enhanced dedicated channels or as normal dedicated channels. IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:CHAN1:MCOD OFF`
SELECT DATA LIST...	Selects an available data list or generation of a new data list (in the submenu). The currently selected data list is displayed on the right.
CURRENT: DLIST01	IEC/IEEE-bus command `:SOUR:W3GP:BST:ENH:DSEL 'ABC'`
COPY CURRENT DATA LIST TO...	Copies the contents of the current data list into an existing or new data list. IEC/IEEE-bus command `:SOUR:DM:DLIS:COPY 'ABC'`
DELETE DATA LIST...	Deletes an existing data list. IEC/IEEE-bus command `:SOUR:DM:DLIS:DEL 'ABC'`
EDIT DATA LIST...	Edits an existing data list.

2.15.3.2 Uplink

Only the uplink parameters are described here. If the parameters of section 2.15.3.1 are also available in the uplink, :BST has to be replaced by :MST for the corresponding remote-control command.



```
FREQ  2.000 000 000 0 GHz                    LEVEL          0.0 dBm
                                             PEP           +3.6 dBm
WCDMA/3GPP                                   ALC-S&H
ENHANCED CHANNELS STATE                                  OFF [ON]
----------------Channel Coding-------------------------------------
CHANNEL CODING STATE                                     OFF [ON]
  CODING TYPE...                               [MEASURE__12.2]
  INTERLEAVER 1                                          OFF [ON]
  INTERLEAVER 2                                          OFF [ON]
----------------DPDCH Bit Error Insertion--------------------------
INSERT BIT ERRORS IN DATA                               [OFF] ON
  NOMINAL BIT ERROR RATE                              1.000 0 E-03
  RESULTING BIT ERROR RATE DTCH                             0 E-09
  RESULTING BIT ERROR RATE DCCH                             0 E-09
INSERT BLOCK ERRORS                                     [OFF] ON
  NOMINAL BLOCK ERROR RATE                                100 E-03
  RESULTING BLOCK ERROR RATE DTCH                           0 E-09
  RESULTING BLOCK ERROR RATE DCCH                           0 E-09
----------------External Power Control-----------------------------
EXTERNAL POWER CONTROL STATE                            [OFF] ON
  POWER STEP                                              1.00 dB
  POWER UP RANGE                                          10.0 dB
  POWER DOWN RANGE                                        10.0 dB
-----------Common Enhanced Channels Settings-----------------------
SEQUENCE LENGTH DPDCH     CURRENT MAX:  1 042 Frame        2 Frame
OVERALL SYMBOL RATE...                                   [60] ksps
POWER DPDCH                                                0.0 dB
ENHANCED DPCCH STATE                                    [OFF] ON
POWER DPCCH                                                0.0 dB
-----------Specific Enhanced Channels Settings---------------------
CHANNEL NUMBER      1 E      2 E      3 E      4 E      5 E      6 E
TYPE              DPDCH
SYMBOL RATE          60
CHAN CODE            16
DATA DTCH          PN15

DATA DCCH          PN15

---------------- CHANGE DATA --------------------------------------
SELECT DATA LIST...                              CURRENT:  NONE
COPY CURRENT DATA LIST TO...
DELETE DATA LIST...
EDIT DATA LIST...
```

Fig. 2-172 DIGITAL STD - WCDMA/3GPP - ENHANCED CHANNELS STATE (uplink) menu

----------------------External Power Control----------------------

EXTERNAL POWER CONTROL STATE

Switch for activating or deactivating the external power control mechanism of all enhanced channels.

In the OFF state, the following menu parameters are not effective:
POWER UP RANGE,
POWER DOWN RANGE,
POWER STEP,

IEC/IEEE-bus command `:SOUR:W3GP:MST:ENH:EPOW:STAT ON`

POWER STEP

Step width (0.25 to 6.0 dB) by which – with the external power control mechanism switched on – the channel powers of the activated enhanced channels in the timeslot grid are increased or decreased within the set dynamic range.

IEC/IEEE-bus command `:SOUR:W3GP:MST:ENH:EPOW:STEP 1.0`

POWER UP RANGE

Dynamic range (0 to 30 dB) in which – with the external power control mechanism switched on – the channel powers of all the activated enhanced channels can be increased. The value POWER UP RANGE can be changed by POWER STEP. If the dynamic range is exceeded, the warning "MAX. POWER UP REACHED" is displayed.

The resulting external power control dynamic range (= POWER UP RANGE – POWER DOWN RANGE) must not exceed 30 dB. For this reason, the POWER UP RANGE is limited as a function of the POWER DOWN RANGE.

IEC/IEEE-bus command
`:SOUR:W3GP:MST:ENH:EPOW:RANG:UP 10.0`

POWER DOWN RANGE

Dynamic range (0 to 30 dB) in which – with the external power control mechanism switched on – the channel powers of all the activated enhanced channels can be decreased. The value POWER DOWN RANGE can be changed by POWER STEP. If the dynamic range is underranged, the warning "MIN. POWER DOWN REACHED" is displayed.

The resulting external power control dynamic range (= POWER UP RANGE – POWER DOWN RANGE) must not exceed 30 dB. For this reason, the POWER DOWN RANGE is limited as a function of the POWER UP RANGE.

IEC/IEEE-bus command
`:SOUR:W3GP:MST:ENH:EPOW:RANG:DOWN 10.0`

----------------------Common Enhanced Channel Settings--

OVERALL SYMBOL RATE

Sets the overall symbol rate in ksps and the required uplink DPDCHs.
For an overall uplink symbol rate of up to 960 ksps only one DPDCH is required in addition to the normally available DPCCH. Further DPDCHs with 960 ksps each are added to the signal to obtain a higher overall symbol rate (maximum 6 DPDCHs).

The enhanced channel table is adapted depending on the setting for OVERALL SYMBOL RATE (e.g. number of active enhanced channels). In the channel table of the higher MS1 menu, the enhanced channels are marked by an additional E (in addition to CHANNEL NUMBER).

If the CHANNEL CODING STATE switch is set to ON, there is a close relation between the settings CODING TYPE and OVERALL SYMBOL RATE:

MEASURE__12.2: OVERALL SYMBOL RATE = 60 ksps
MEASURE__64: OVERALL SYMBOL RATE = 240 ksps
MEASURE_144: OVERALL SYMBOL RATE = 480 ksps
MEASURE_384: OVERALL SYMBOL RATE = 1 * 960 ksps
AMR_CODER: SYMBOL RATE DPDCH = 60 ksps;

When a parameter is changed (CODING TYPE or OVERALL SYMBOL RATE) the associated parameter is adapted automatically.

IEC/IEEE-bus command `:SOUR:W3GP:MST:ENH:ORAT X2`

DPDCH POWER

Sets the channel power (-60 dB to 0 dB) of the DPDCHs. Each active DPDCH is sent with this power. If the EXTERNAL POWER CONTROL STATE switch is set to ON, this power is also the start power for the power control.

Note: *Starting the ADJUST TOTAL POWER menu parameter at the top menu level of 3GPP W-CDMA adapts all the channel powers (POWER DB parameter) of the mobile stations (and, thus, also the POWER START values of the set enhanced channels) so that the average overall power of all the activated channels is 0 dB with reference to the LEVEL in the SMIQ display.*

IEC/IEEE-bus command `:SOUR:W3GP:MST:ENH: POW:DPDC -30.0`

ENHANCED DPCCH STATE

This parameter is used to determine whether the DPCCH is calculated as enhanced channel (on the data generator) or as "normal" channel (on the modulation coder).

STATE = ON:

The DPCCH is calculated as enhanced channel. This setting is useful when External Power Control is active.

STATE = OFF:

The DPCCH is calculated as a normal channel. This setting is useful for all measurements which require no External Power Control.

IEC/IEEE-bus `:SOUR:W3GP:MST:ENH:DPCC:STAT OFF`

DPCCH POWER

Sets the channel power (-60 dB to 0 dB) of the DPCCH. If the EXTERNAL POWER CONTROL STATE switch is set to ON, this power is also the start power for power control.

Note : *Starting the ADJUST TOTAL POWER menu parameter at the top menu level of 3GPP W-CDMA adapts all the channel powers (POWER DB parameter) of the mobile stations (and, thus, also the POWER START values of the set enhanced channel) so that the average overall power of all the activated channels is 0 dB with reference to the LEVEL indicated in the SMIQ display.*

IEC/IEEE-bus command `:SOUR:W3GP:MST:ENH:POW:DPCC -30.0`

----------------------**Specific Enhanced Channel Settings**--

SYMBOL RATE

Display parameter indicating the symbol rates of the individual uplink enhanced channels.

2.15.3.3 Display of External Power Control Mode of Four Enhanced Channels

The current External Power Control mode of the four enhanced channels is displayed here. The deviation of the channel power (= ΔPOW) of the set POWER START caused by the external power control mechanism is displayed in graphical form.
The window is shown in the SMIQ display after calculating the 3GPP W-CDMA signal triggered by STATE = ON or MOD ON. The window is blanked out after pressing the RETURN key to be able to operate the SMIQ during the signal output.

Since a realtime update of the window in the timeslot (= 0.667 ms) is not possible for reasons of speed, an update can be performed in a more coarse time interval. Fast channel power changes are not displayed but the settled state of the control loop can be recognized very easily by the user.

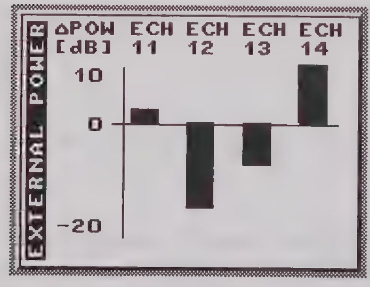
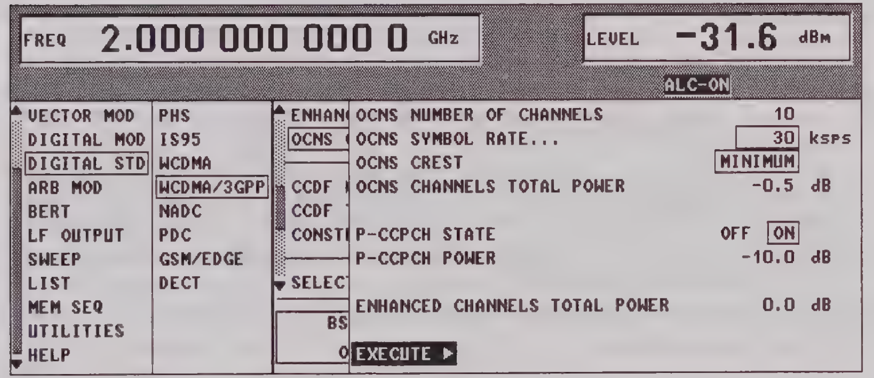


Fig. 2-173 Display of external power control mode

2.15.4 OCNS Channels

A so-called Orthogonal Channel Noise (OCNS) has to be simulated for testing the maximum input level ([1], 7.4) of mobile stations (user equipment).
In addition to the 12.2 kbps measurement channel the base station signal comprises further high-power orthogonal channels and a PCCPCH.
The OCNS setting function has a similar effect as Para. Predef. (see sect. Digital Standard 3GPP W-CDMA), i.e. all parameters of the WCDMA/3GPP system are set to useful values.

2.15.4.1 OCNS Menu



```
FREQ  2.000 000 000 0 GHz        LEVEL  -31.6 dBm
                                        ALC-ON
OCNS NUMBER OF CHANNELS                 10
OCNS SYMBOL RATE...                     30  ksps
OCNS CREST                              MINIMUM
OCNS CHANNELS TOTAL POWER               -0.5  dB

P-CCPCH STATE                           OFF  ON
P-CCPCH POWER                           -10.0  dB

ENHANCED CHANNELS TOTAL POWER           0.0  dB
EXECUTE ►
```

Fig. 2-174 DIGITAL STD - WCDMA/3GPP - OCNS CHANNELS menu

OCNS NUMBER OF CHANNELS

Number of OCNS channels additionally added to the 3GPP W-CDMA signal (in addition to the enhanced channels). The maximum number of OCNS channels depends on the OCNS symbol rate:

MAX_NOF_OCNS_Channels =
(Chip rate / OCNS SYMBOL RATE)
– 4 enhanced channels

This menu parameter is only effective after pressing EXECUTE ►.

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:COUN 10`

OCNS SYMBOL RATE

Sets the symbol rate of all OCNS channels in ksps.
This menu parameter is only effective after pressing EXECUTE ►.

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:SRAT D240K`

OCNS CREST

The crest factor of the OCNS signal can be influenced by setting the channelization codes and timing offsets.

MINIMUM: The crest factor is minimized (the channelization codes are distributed uniformly over the code domain, the timing offsets are increased by 3 from channel to channel).

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:CRES MIN`

AVERAGE: An average crest factor is set (the channelization codes are distributed uniformly over the code domain, the timing offsets are all set to 0).

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:CRES AVER`

WORST: The crest factor is set to an unfavourable value (i.e. maximum) (the channelization codes are assigned in ascending order. The timing offsets are all set to 0).

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:CRES WORS`

OCNS CHANNELS TOTAL POWER

Fraction of power of OCNS channel signal component at the overall signal normalized to 0 dB. This menu parameter is only effective after pressing EXECUTE ►.

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:POW -10.0`

P-CCPCH STATE

Switch with which the P-CCPCH can be added to the OCNS dedicated channels. This menu parameter is only effective after pressing EXECUTE ►.

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:PCCP:STAT ON`

P-CCPCH POWER

Fraction of power of P-CCPCH signal component at the overall signal normalized to 0 dB. This menu parameter is only effective after pressing EXECUTE ►.

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:PCCP:POW -10.0`

ENHANCED CH. TOTAL POWER

Fraction of power of enhanced channel signal component at the overall signal normalized to 0 dB. The value for ENHANCED CH. TOTAL POWER is determined from the POWER START values of all activated enhanced channels and offered to the user as default value. If the user changes this value, also the values for POWER START of the activated enhanced channels are adapted. If all enhanced channels are inactive, ENHANCED CH. TOTAL POWER are set to –60.0 dB.

This menu parameter is only effective after pressing EXECUTE ►.

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:EPOW -10.0`

EXECUTE ►

The EXECUTE command is used to validate the settings described above and to fill in the channel tables of the base stations.

The rules for filling in the channel tables correspond to those of the PARA.PREDEF SETTING... menu parameter with the following exceptions:

The first 4 DPCHs are reserved for the enhanced channels. The OCNS channel settings are entered from the 5th DPCH in the channel table. The maximum number of DPCHs (= OCNS channels) is reduced by 4 (= number of enhanced channels).

S-CCPCH is switched off (STATE = OFF).

The channel power values of OCNS channels must be determined from menu parameters NUMBER OF OCNS CHANNELS and OCNS CHANNELS TOTAL POWER. The P-CCPCH channel power is taken over from P-CCPCH POWER menu parameter (if P-CCPCH is switched on) .

IEC/IEEE-bus command `:SOUR:W3GP:BST:OCNS:EXEC`

2.15.4.2 Test of Maximum Input Level with SMIQ

To test the maximum input level of a mobile station (UE) (according to [1], 7.4) proceed as follows:

1. In the DIGITAL-STD menu select menu item SET DEFAULT to set all parameters to defined values.
2. Set sequence length to 13. (see section 2.15.4.3)
3. Enter the DIGITAL STD-WCDMA/3GPP-ENHANCED CHANNELS menu.
4. Set STATE to ON.
5. Set CHANNEL CODING STATE to ON.
6. Select CODING TYPE = MEASURE__12.2 and switch on both interleavers.
7. Switch off EXTERNAL POWER CONTROL.
8. Select a sequence length of 1022 (see section 2.15.4.3).
9. Activate channel 11 (STATE to ON), all other channels are inactive.
10. Set the following parameters for channel 11:
    CHAN CODE = 25 (not defined in [1], other code also possible)
    DATA DTCH = PN9
    DATA DCCH = PN15 (not defined in [1], other data also possible)
11. Change to the DIGITAL STD-WCDMA/3GPP-OCNS CHANNELS menu.
12. Set number of channels (NUMBER OF CHANNELS) to 60. (A value for [1] is not given but a utilization of 50% for the code domain is recommended.)
13. Set SYMBOL RATE to 30 ksps. (A value is not given in [1] but normal voice channels are recommended.)
14. Select CREST=MINIMUM.
15. Set OCNS CHANNELS TOTAL POWER to –1 dB.
16. Activate P-CCPCH (STATE=ON).
17. Select –12 dB as P-CCPCH POWER.
18. Set ENHANCED CHANNELS TOTAL POWER to –19 dB.
19. Activate EXECUTE.
20. Change to the DIGITAL STD-WCDMA/3GPP - BS1 menu.
21. Set TFCI STATE = ON.
22. Activate P-CPICH with –10 dB.
23. Activate P-SCH and S-SCH with –15 dB each.
24. Activate PICH with –15 dB. Select its channelization code so that no domain conflict occurs.

Overview of all active channels in the OCNS measurement:

Table 2-28 OCNS channels

Physical channel	**Power (log.)**	**Power (lin.)**	
DPCH (measurement)	-19 dB	1.26 %	
CPICH	-10 dB	10.0 %	
PCCPCH	-12 dB	6.30 %	
SCH	-12 dB	6.30 %	-15 dB for P-SCH and -15 dB for S-SCH
PICH	-15 dB	3.16 %	
OCNS	-1 dB	79.3 %	60 channels, -18.8 dB each

2.15.4.3 Favourable Sequence Length for OCNS Measurement

To perform a realistic OCNS measurement, certain sequence length combinations are to be preferred. Both signal components (measurement channel and OCNS channels) are cyclically output. The cycle length of the measurement channel can be entered in the ENHANCED CHANNEL (SEQUENCE LENGTH) menu, the cycle of the OCNS channel in the WCDMA/3GPP menu.
If these lengths are identical, also the overall signal is repeated with this cycle length. If the two lengths are different, the overall signal is repeated with a cycle length that corresponds to the least common multiple of the individual lengths.
To simulate a real base-station signal optimally, the overall cycle length should be selected as large as possible. Therefore, both sequence lengths should be relatively prime. This is the case if two prime numbers are used, for example. The overall cycle length then becomes the product of the two sequence lengths.
The values in section 2.15.4.2 are selected so that the overall cycle length becomes a maximum (1022*13 frames = 133 seconds).

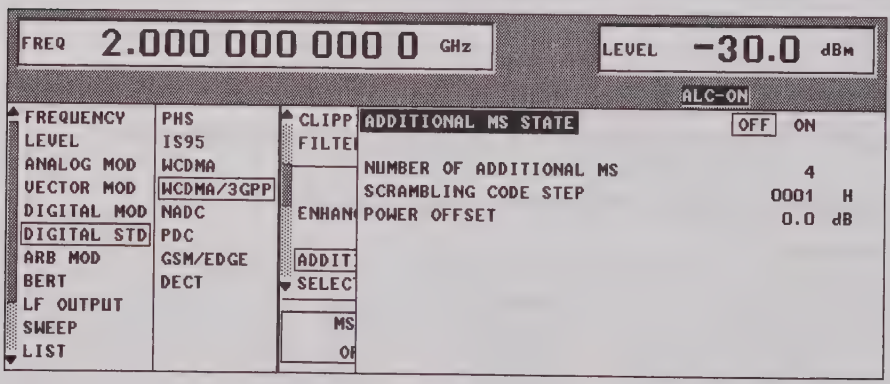
***Note**: To reduce the computation time for the signal it can be useful to use smaller sequence lengths.*

2.15.5 Additional MS Based On MS4

Similar to simulating a realistic downlink signal for testing a mobile station receiver with OCNS (section 2.15.4), there is a realistic uplink signal (for testing a base station receiver) consisting of the individual signals of many mobile stations.
Up to three mobile stations are configured, see section "Digital Standard 3GPP W-CDMA" and section "WCDMA/3GPP menu - Submenu MS Configuration".

The fourth mobile station MS4 is used as a reference for all other stations. Based on the settings of this mobile station, all Additional Mobile Stations are calculated and the following parameters are modified:

- Scrambling code (all stations are different)
- Power (different from MS4, but same power)



FREQ 2.000 000 000 0 GHz
LEVEL -30.0 dBm

ADDITIONAL MS STATE OFF ON
NUMBER OF ADDITIONAL MS 4
SCRAMBLING CODE STEP 0001 H
POWER OFFSET 0.0 dB

Fig. 2-175 DIGITAL STD - WCDMA/3GPP ADDITIONAL MS STATE menu

ADDITIONAL MS STATE

Switch with which up to 50 additional mobile stations can be added (ON) to a 3GPP W-CDMA signal (defined in MS1 to MS4) otherwise all additional MS are deactivated (OFF).
The additional MS are also calculated on the MCOD and added to the ARB memory with the signal component of the primary MS.
The basic settings of the additional mobile stations (e.g. Signature, Access Slot, TFCI, FBI and TPC settings as well as the channel scenario from PRACH, PCPCH, DPCCH + DPDCHs) are taken over from the primary reference mobile station 4.

In the OFF state, the following menu parameters have no influence on the 3GPP W-CDMA signal:

NUMBER OF ADDITIONAL MS,
SCRAMBLING CODE STEP,
POWER OFFSET [RELATIVE TO MS4].

IEC/IEEE-bus command `:SOUR:W3GP:MST:ADD:STAT ON`

NUMBER OF ADDITIONAL MS

Number of additional mobile stations 1 to 64.

IEC/IEEE-bus command `:SOUR:W3GP:MST:ADD:COUNT 33`

SCRAMBLING CODE STEP

Initialization increment of uplink scrambling code generator for additional mobile stations. The scrambling code value of the primary reference MS4 is used as the basic value of the scrambling code initialization of an additional MS.

IEC/IEEE-bus command `:SOUR:W3GP:MST:ADD:SCOD:STEP 5`

POWER OFFSET

With this menu parameter it is possible to set a power offset of the active channels of the additional MS relative to the powers of the active channels of the primary reference MS4.
The resulting power value of the channel must be in the range from 0 to –60 dB (limitation in case of underrange or overrange).

IEC/IEEE-bus command `:SOUR:W3GP:MST:ADD:POW:OFFS -3.0`

2.16 Digital Standard NADC

With the options Modulation Coder (SMIQB20) and Data Generator (SMIQB11) provided, modulation signals according to the American NADC standard (IS-54[1] or IS-136) can be generated. NADC is a TDMA standard for cellular mobile radio networks.

SMIQ can generate both the transmit signal of a base station (BS) and the transmit signal of a mobile station (MS). Transmission from BS to MS is called "downlink", "uplink" being used for transmission in the opposite direction.

Each TDMA frame consists of 6 slots. The 6 slots can be configured for both full rate and half rate mode. For half rate, the data content for each of the 6 slots can be defined separately by means of a slot editor whereas in the full rate mode 2 slots for example slot 1 and 4 are combined in a frame. The settings for the first slot are then automatically used for the assigned second slot. Each slot can be switched on or off. Moreover, a defined intermediate level can be set for uplink slots.

A burst type has to be defined to configure a slot. The following burst types can be selected:

- TCH a traffic channel burst with a different structure for uplink and downlink,
- SHORT the so-called "shortened burst" only available during uplink and,
- ALL_DATA burst type for test purposes with freely programmable data contents in the selected slot.

The following internal modulation sources are available:

- different PRBS generators with a sequence length between 2^9-1 and 2^{23}-1 and
- data lists, ie freely programmable data sequences from the data generator memory.

For generating the NADC signals, SMIQ inserts the modulation data continuously (in real time) into the selected slots. Using a digital signal processor the data generator generates a data sequence with modulation data and control signals for envelope control.

The data generator in SMIQ generates a data stream which is converted into IQ signals in the modulation coder. According to the NADC standard, the modulation type is π/4 DQPSK at a symbol rate of 24.3 ksymbol/s and √COS filtering. Symbol rate and filtering can be changed in SMIQ.

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[1] Personal Handy Phone System ARIB Standard (RCR STD-28)

2.16.1 Sync and Trigger Signals

The data generator generates a data sequence with modulation data, control signals for envelope control, and synchronization signals.

When TRIGGER MODE AUTO is selected, the NADC signal generation is started automatically.

This start can also be activated by an external trigger signal (TRIGGER MODE ARMED_AUTO) which allows a synchronous sequence for BER measurements to be carried out on receivers.

A trigger signal can be fed via the TRIGIN input at connector PAR DATA. The active slope of a trigger signal applied there executes a trigger event.
NADC signal generation at a frame limit is started after a trigger event. Data from data lists are inserted into the selected slots starting from the first bit. PRBS generators start with the set initialization status.

Signal generation either starts immediately after the active slope of the trigger signal or after a settable number of symbols (EXT TRIGGER DELAY). Retriggering (RETRIG) can be inhibited for a settable number of symbols (EXT RETRIGGER INHIBIT).

A trigger event can be executed manually or via the IEC/IEEE bus using EXECUTE TRIGGER.

When a trigger event is executed, a trigger signal is output at the TRIGOUT 3 output of SMIQ.

SMIQ also generates the following sync signals:

- a frame clock at TRIGOUT 1 output,
- a frame or multiframe clock at TRIGOUT 2 output with settable position in the frame,
- the symbol clock and the bit clock.

A clock synthesizer on the modulation coder generates the symbol clock and the bit clock in SMIQ. All the clock signals are synchronized to the 10-MHz reference of SMIQ. The symbol clock is available at connector SYMBOL CLOCK and the bit clock at connector BIT CLOCK. If required, the clock synthesizer in SMIQ can be synchronized to an external symbol or bit clock.

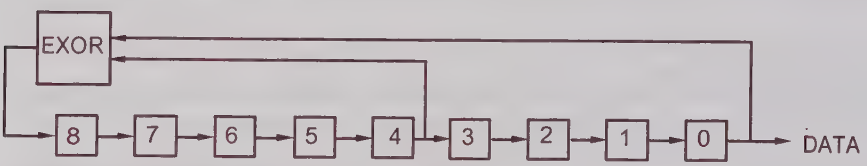
The clock signal is selected in the menu via CLOCK-CLOCK SOURCE EXT.

To allow for a trouble-free synchronization of the clock synthesizer first apply the external clock and set the correct symbol rate at SMIQ. Then switch CLOCK SOURCE from INT to EXT.

Note: *The set symbol rate should not differ by more than 1% from the symbol rate of the external signal.*

2.16.2 PN Generators as Internal Data Source

Independent PN generators (Pseudo Noise) can be selected for each slot as data source for data fields DATA and SACCH. These PN generators provide pseudo-random bit sequences of different length or period. That is why they are also called PRBS generators (Pseudo Random Binary Sequence).
Data sequences are sequences of maximum length which are generated by means of feedback shift registers.
The following figure gives an example of a 9 bit generator with feedbacks after register 4 and 0 (output).



The pseudo-random sequence of a PRBS generator is clearly defined by the number of registers and the feedback. The following table describes all PRBS generators available:

Table 2-29 PRBS generators for NADC

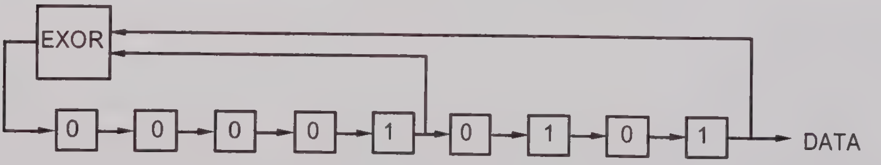
PRBS generator	Length in bits	Feedback after
9 bit	$2^9 -1 = 511$	Register 4, 0
11 bit	$2^{11} -1 =2047$	Register 2, 0
15 bit	$2^{15} -1 = 32767$	Register 1, 0
16 bit	$2^{16} -1 = 65535$	Register 5, 3, 2, 0
20 bit	$2^{20} -1 = 1048575$	Register 3, 0
21 bit	$2^{21} -1 = 2097151$	Register 2, 0
23 bit	$2^{23} -1 = 8388607$	Register 5, 0

PN generators PN9,11,15,20 and PN23 are configured according to CCITT Rec. 0.151/152/153. The output sequence is inverted for generators PN15 and PN23.

The start value of the PN generators is different in the slots and equals

$$\text{start value} = 1 + 14\,\text{hex} \times \text{slot number}$$

Example: PN9 generator in slot 1 with start value 15hex = 10101 binary.



The resulting output sequence is `1010100000010100101011110010` etc.

2.16.3 Lists as Internal Data Source

A freely programmable memory on the data generator serves as internal data source for the data fields of the slots. The data are managed in so-called lists. A list editor allows to select, copy, modify and delete data lists (DATA LIST).
The list editor is available via menu DIGITAL-MOD - SOURCE....

2.16.4 External Modulation Data

External data can (only) be applied via the SERDATA interface. A selection of SERDATA as data source is only possible for a single data field of a slot. For further information on the characteristics of the SERDATA interface see Annex A.

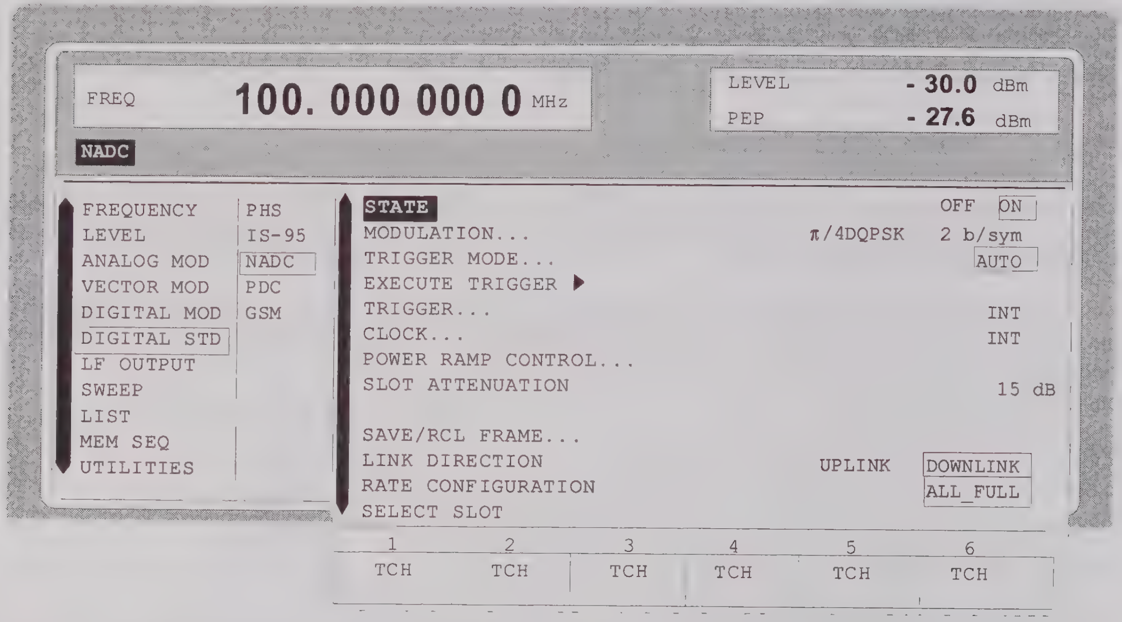
To ensure that the external data bits are assigned to specific positions in the data field of the selected slot and that they are reproducible, the buffer of the RS-232 transmitter and receiver has to be deleted. A triggered start has to follow.
The following setting sequence is required in the DIGITAL STD - NADC menu:

1. Carry out desired settings in menu.
2. Select data source SERDATA for the data field of the slot using SELECT SLOT - ....
3. Make connection to external data source, but do not yet start external data source.
4. Switch off digital standard using STATE - OFF.
5. Set TRIGGER MODE ARMED_AUTO.
   In this state, SMIQ is ready for reception, but discards data that are read in via SERDATA.
6. Switch on digital standard with STATE ON.
7. Start external data source.
   The read-in data are written into the receiving buffer. Only if this buffer is filled can SMIQ react to a trigger event.
8. Activate trigger event. Signal generation is thus started at a frame limit. The first bit received via SERDATA is put to the first bit position in the selected data field.

2.16.5 Menu DIGITAL STANDARD - NADC

Menu DIGITAL STD - NADC provides access to settings for generating NADC signals.

Menu selection: DIGITAL STD - NADC



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

STATE OFF ON

MODULATION... π/4DQPSK 2 b/sym

TRIGGER MODE... AUTO

TRIGGER... INT

CLOCK... INT

SLOT ATTENUATION 15 dB

LINK DIRECTION UPLINK DOWNLINK

RATE CONFIGURATION ALL_FULL

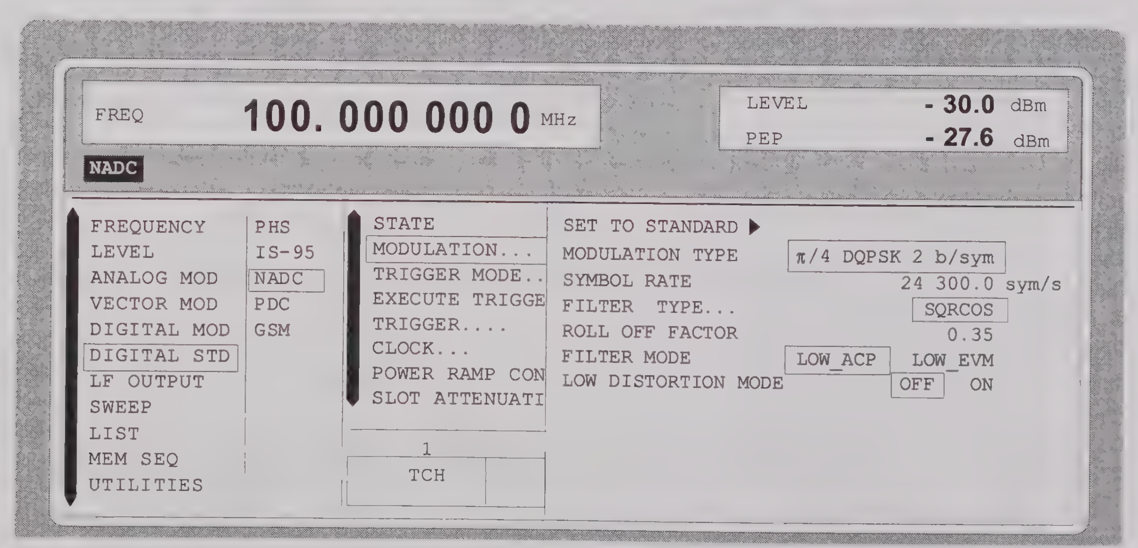
NADC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
EXECUTE TRIGGER
POWER RAMP CONTROL...
SAVE/RCL FRAME...
SELECT SLOT
1
2
3
4
5
6
TCH
TCH
TCH
TCH
TCH
TCH

Fig. 2-176 Menu DIGITAL STD - NADC, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

STATE Switch on/off of Digital Standard NADC modulation. Vector Modulation or Digital Modulation will be switched off automatically.

IEC/IEEE-bus command `:SOUR:NADC:STAT ON`

MODULATION... Opens a window for setting the modulation parameters.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

MODULATION TYPE π/4 DQPSK 2 b/sym

SYMBOL RATE 24 300.0 sym/s

FILTER TYPE... SQRCOS

ROLL OFF FACTOR 0.35

FILTER MODE LOW_ACP LOW_EVM

LOW DISTORTION MODE OFF ON

NADC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
STATE
MODULATION...
TRIGGER MODE..
EXECUTE TRIGGE
TRIGGER....
CLOCK...
POWER RAMP CON
SLOT ATTENUATI
SET TO STANDARD
1
TCH

Fig. 2-177 Menu DIGITAL STD - NADC - MODULATION..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(MODULATION...)

SET TO STANDARD ► Sets the subsequent modulation parameters to the values predefined by the standard.

MODULATION TYPE Displays the modulation type.

SYMBOL RATE Input value for the symbol clock. 24.3 ksymbol/s are preset.

IEC/IEEE-bus `:SOUR:NADC:SRAT 24.31KHZ`

FILTER Selection of baseband filter. A selection between Nyquist filters COS, SQRCOS or a user-defined filter USER (cf. Section Digital Modulation) is possible.

IEC/IEEE-bus `:SOUR:NADC:FILT:TYPE COS`

ROLL OFF FACTOR Input value for the roll-off factor.

IEC/IEEE-bus `:SOUR:NADC:FILT:PAR 0.35`

FILTER MODE Selection of filter mode.

LOW_ACP Filter for minimum Adjacent Channel Power.

IEEE `:SOUR:NADC:FILT:MODE LACP`

LOW_EVM Filter for minimum vector error.

IEEE `:SOUR:NADC:FILT:MODE LEVM`

LOW DISTORTION Switch on/off of low-distortion mode.. After switch-on, the level of the IQ baseband signals is reduced by 3 dB. In some cases, this might reduce undesired intermodulation products. OFF is normally the more favourable setting.

IEC/IEEE-bus command `:SOUR:NADC:LDIS OFF`

TRIGGER MODE... Opens a window for selecting the trigger mode.

AUTO The NADC signals are continuously transmitted in the activated slots.

IEC/IEEE-bus `:SOUR:NADC:SEQ AUTO`

RETRIG The NADC signals are continuously transmitted in the activated slots. A trigger event causes a restart.

IEC/IEEE-bus `:SOUR:NADC:SEQ RETR`

ARMED_AUTO The NADC signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the AUTO mode and can no longer be triggered.

IEC/IEEE-bus `:SOUR:NADC:SEQ AAUT`

ARMED_RETRIG The NADC signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the RETRIG mode. Each new trigger event causes a restart.

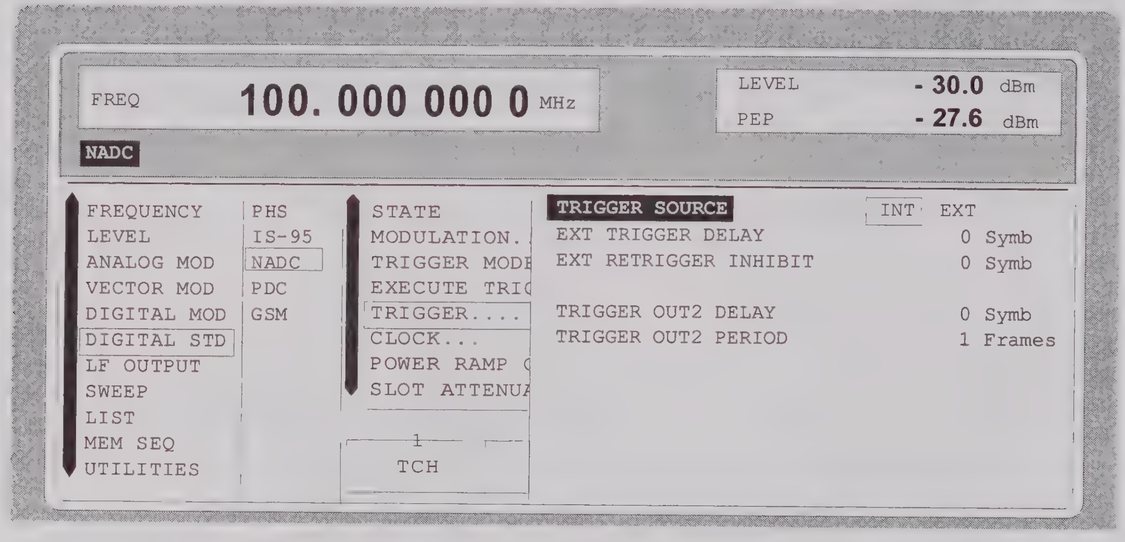
IEC/IEEE-bus `:SOUR:NADC:SEQ ARET`

EXECUTE TRIGGER ▶

Executes a trigger even to start the NADC signal generation.
IEC/IEEE-bus command `:TRIG:DM:IMM`

TRIGGER...

Opens a window for selecting the trigger source, for configuring the trigger output signals and for setting the time delay of an external trigger signal.


NADC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
STATE
MODULATION.
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
1
TCH

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.6 dBm

TRIGGER SOURCE INT EXT
EXT TRIGGER DELAY 0 Symb
EXT RETRIGGER INHIBIT 0 Symb
TRIGGER OUT2 DELAY 0 Symb
TRIGGER OUT2 PERIOD 1 Frames

Fig. 2-178 Menu DIGITAL STD - NADC_TRIGGER..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(TRIGGER...) **TRIGGER SOURCE**

Selection of trigger source.

EXT	The NADC signal generation is started by the active slope of an external trigger signal. The polarity, the trigger threshold and the input resistance of the TRIGIN input can be modified in menu DIGITAL MOD - EXT INPUTS.
INT	A trigger event can be executed by EXECUTE TRIGGER ▶.
IEC/IEEE-bus	`:SOUR:NADC:TRIG:SOUR EXT`

EXT TRIGGER DELAY

Setting the number of symbols by which an external trigger signal is delayed before it starts the NADC signal generation.
This is used for setting the time synchronization between the the SMIQ and the DUT.

IEC/IEEE-bus `:SOUR:NADC:TRIG:DEL 3`

(TRIGGER...)

EXT RETRIGGER INHIBIT

Setting the number of symbols for which a restart is inhibited after a trigger event.
With TRIGGER MODE RETRIG selected, each new trigger signal restarts the NADC signal generation. This restart can be inhibited for the entered number of symbols.

Example:
The entry of 1000 symbols causes new trigger signals to be ignored for the duration of 1000 symbols after a trigger event

IEC/IEEE-bus `:SOUR:NADC:TRIG:INH 1000`

TRIGGER OUT 2 DELAY

Input value of delay of trigger signal at TRIGOUT 2 output compared with beginning of frame.

IEC/IEEE-bus `:SOUR:NADC:TRIG:OUTP:DEL 2`

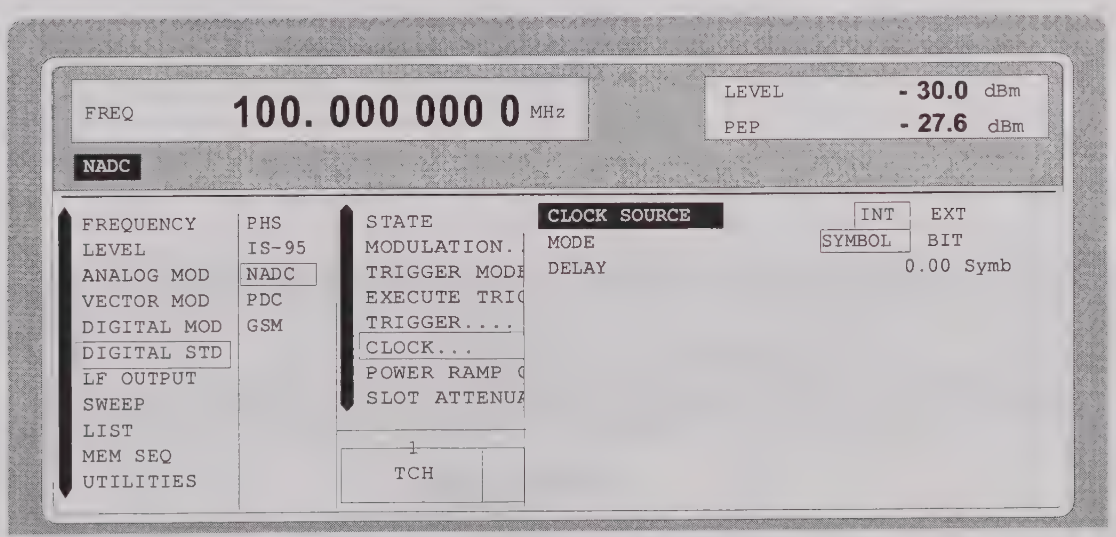
TRIGGER OUT2 PERIOD

Input value of output signal period at TRIGOUT 2 output given in frames.

IEC/IEEE-bus `:SOUR:NADC:TRIG:OUTP:PER 1`

CLOCK...

Opens a window for selecting the clock source and for setting a delay.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

CLOCK SOURCE INT EXT

MODE SYMBOL BIT

DELAY 0.00 Symb

NADC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
STATE
MODULATION.
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
1
TCH

Fig. 2-179 Menu DIGITAL STD - NADC - CLOCK..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(CLOCK...)

CLOCK SOURCE Selection of clock source.

INT	SMIQ uses internally generated clock signals.
EXT	An external symbol clock or bit clock is fed in at connectors SYMBOL CLOCK or BIT CLOCK. The clock synthesizer on the modulation coder is synchronized to this clock. The symbol rate has to be set with an accuracy of ± 1 %. The polarity, the trigger threshold and the input resistance of the clock inputs can be modified in menu DIGITAL MOD - EXT INPUTS.

IEC/IEEE-bus command `:SOUR:NADC:CLOC:SOUR INT`

MODE Selection of clock for external clock signal.

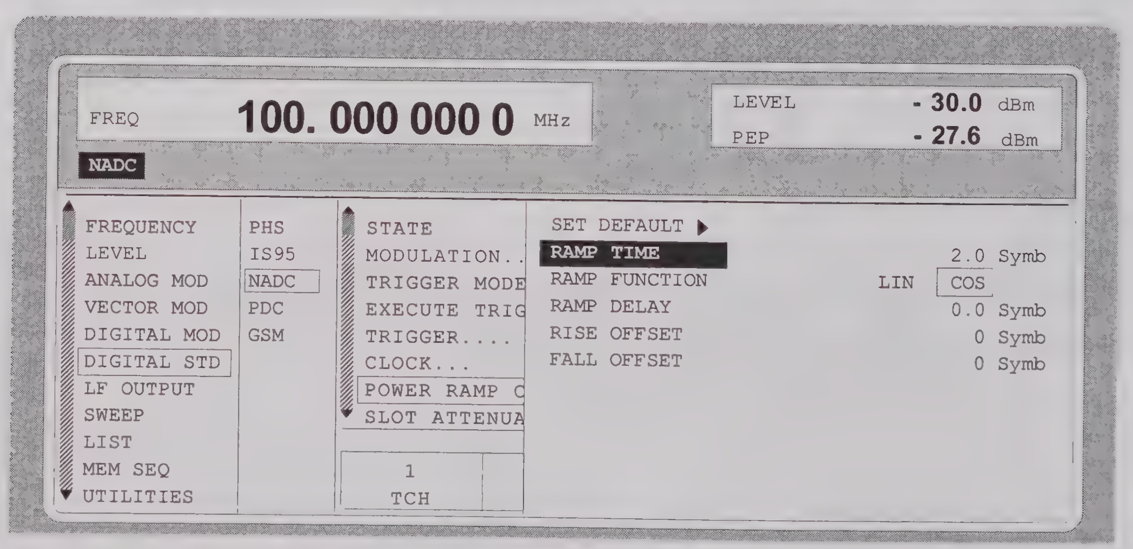
SYMBOL	The external clock has to be a symbol clock.
BIT	The external clock has to be a bit clock.

IEC/IEEE-bus command `:SOUR:NADC:CLOC:MODE SYMB`

DELAY Setting the delay of generated modulation signal to an external clock.
This can be used, for example, for synchronization with a second unit to achieve time synchronization between the modulation signals of the two units.

IEC/IEEE-bus command `:SOUR:NADC:CLOC:DEL 0.5`

POWER RAMP CONTROL... Opens a window for setting the envelope control, especially for the rising and falling ramp at the beginning and end of a slot.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

RAMP TIME 2.0 Symb

RAMP FUNCTION LIN COS

RAMP DELAY 0.0 Symb

RISE OFFSET 0 Symb

FALL OFFSET 0 Symb

NADC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
STATE
MODULATION..
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
1
TCH
SET DEFAULT

Fig. 2-180 Menu DIGITAL STD - NADC - POWER RAMP CONTROL... , SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(POWER RAMP CONTROL...)

SET DEFAULT ▶

Resets the subsequent parameters to the factory-set values.

IEC/IEEE-bus `:SOUR:NADC:PRAM:PRES`

RAMP TIME

Input value for the rise and fall time of the envelope at the beginning or end of a slot. The time is set in units of symbol period.

IEC/IEEE-bus `:SOUR:NADC:PRAM:TIME 0.25`

RAMP FUNCTION

Selection of shape of rising and falling ramp for envelope control.

LIN	Linear ramp function.
COS	Cosine function. A more favourable spectrum than that of the LIN function is obtained.

IEC/IEEE-bus `:SOUR:NADC:PRAM:SHAP LIN`

RAMP DELAY

Input value for a shift of the envelope characteristic to the modulated signal. A positive value causes a delay of the envelope. The values are set in the units of the symbol length.

IEC/IEEE-bus `SOUR:NADC:PRAM:DEL 0.1`

RISE OFFSET

Input value for a positive or negative offset of the rising ramp of the envelope at the beginning of a slot.

IEC/IEEE-bus `:SOUR:NADC:PRAM:ROFF -1`

FALL OFFSET

Input value for a positive or negative offset of the falling ramp of the envelope at the end of a slot.

IEC/IEEE-bus `:SOUR:NADC:PRAM:LOFF 1`

SLOT ATTENUATION

Input value for the level reduction in dB of all active slots whose SLOT LEVEL was set to ATTEN. Menu SELECT SLOT allows the slots to be determined whose level is to be reduced.

IEC/IEEE-bus command `:SOUR:NADC:SLOT:ATT 40 DB`

SAVE/RCL FRAME... Opens a window for saving and loading a frame configuration. Loading a frame affects all parameters that can be set under SELECT SLOT.

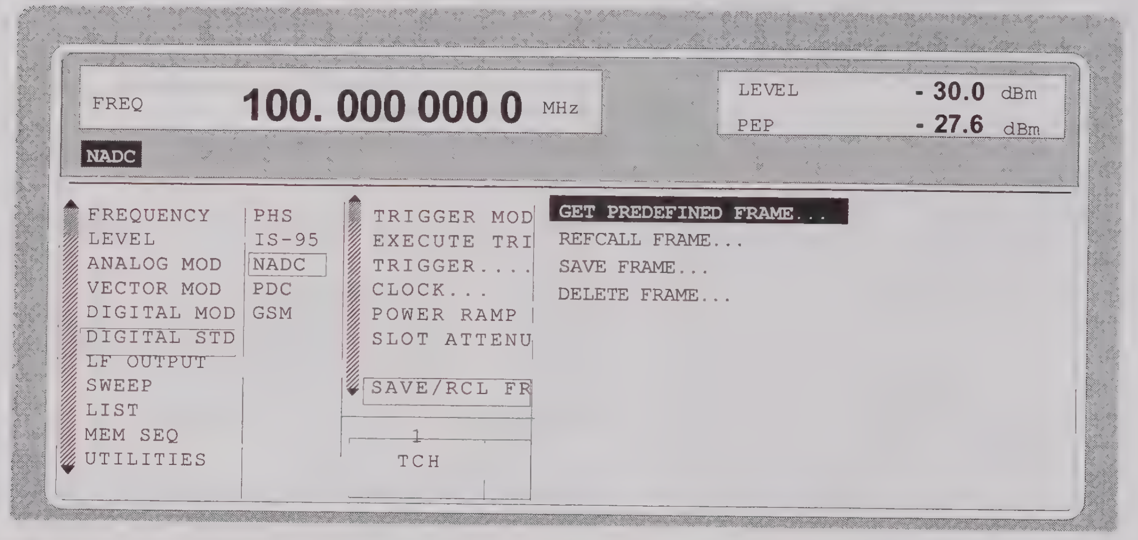

FREQ
100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.6 dBm
NADC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
TRIGGER MOD
EXECUTE TRI
TRIGGER....
CLOCK...
POWER RAMP
SLOT ATTENU
SAVE/RCL FR
1
TCH
GET PREDEFINED FRAME...
REFCALL FRAME...
SAVE FRAME...
DELETE FRAME...

Fig. 2-181 Menu DIGITAL STD - NADC - SAVE/RCL FRAME, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SAVE/RCL FRAME...) **GET PREDEFINED FRAME...**

Loads a factory-set frame configuration.

DN_TCH	Downlink traffic channels in all slots.
UP1TCH	Uplink traffic channels in slot 1

IEEE `:SOUR:NADC:FLIS:PRED:LOAD "dn_tch"`

RECALL FRAME

Loads a frame configuration saved by the user.

IEC/IEEE-bus `:SOUR:NADC:FLIS:LOAD "name"`

SAVE FRAME

Saves a user-defined frame configuration.

IEC/IEEE-bus `:SOUR:NADC:FLIS:STOR "name"`

DELETE FRAME

Deletes a frame configuration saved by the user.

IEC/IEEE-bus `:SOUR:NADC:FLIS:DEL "name"`

LINK DIRECTION Selection between UPLINK and DOWNLINK. Depending on this selection, various burst types are offered under SELECT SLOT.

IEC/IEEE-bus command `:SOUR:NADC:LINK UP`

RATE CONFIGURATION...

Opens a window for determining which slots in the frame are to be configured as half-rate and full-rate channels. Full-rate channels occupy two slots in a frame, the second slot is fully determined by the configuration of the first slot. The following eight combinations can be selected:

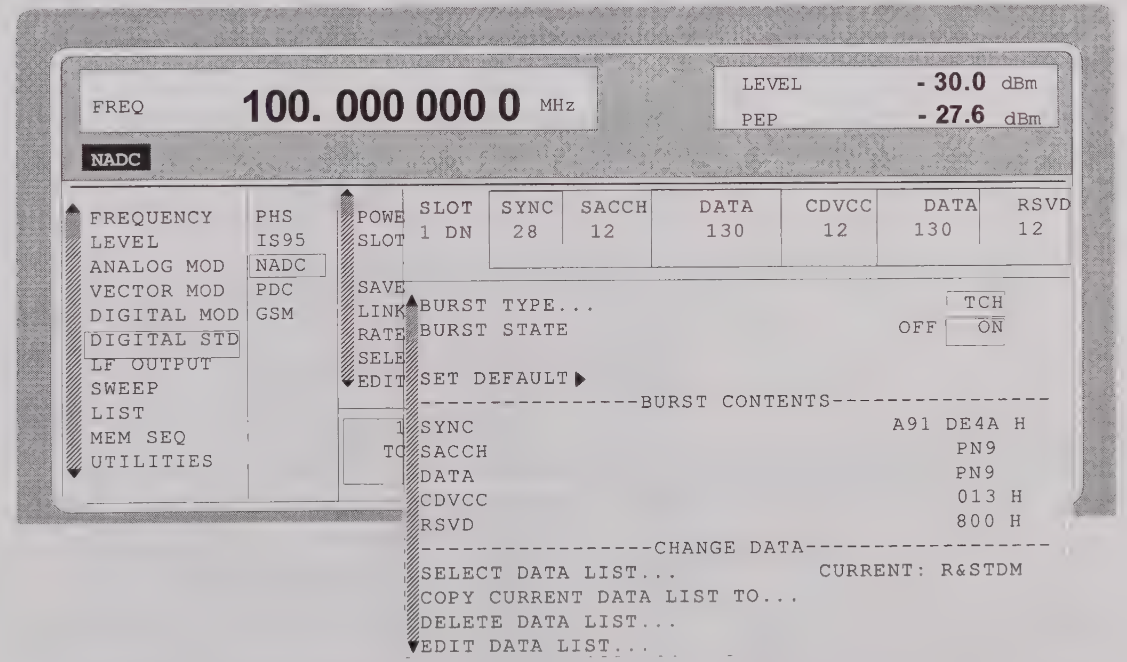
Selection	Full-rate slots	Half-rate slots
ALL HALF	-	1, 2, 3, 4, 5, 6
FULL 1	1 + 4	2, 3, 5, 6
FULL 2	2 + 5	1, 3, 4, 6
FULL 3	3 + 6	1, 2, 4, 5
FULL 1+2	1 + 4 and 2 + 5	3, 6
FULL 1+3	1 + 4 and 3 + 6	2, 5
FULL 2+3	2 + 5 and 3 + 6	1, 4
ALL FULL	1 + 4 and 2 + 5 and 3 + 6	-

IEC/IEEE-bus command `:SOUR:NADC:RCON AHAL`

SELECT SLOT...

Selection of one of 6 possible slots. The number depends on the setting under RATE CONFIGURATION. When selecting the slot, a window is opened in which the data contents belonging to this slot can be defined. The content of the window depends on whether an UPLINK or DOWNLINK was selected under LINK DIRECTION.
If the cursor is placed onto a slot in the diagram, it may be switched on and off by pressing one of the unit keys (toggle function).

Menu selection: BURST TYPE = TCH


FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.6 dBm
NADC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
POWE
SLOT
SAVE
LINK
RATE
SELE
EDIT
1
TC

```
SLOT   SYNC  SACCH   DATA   CDVCC   DATA   RSVD
1 DN    28    12     130     12     130     12

BURST TYPE...                              TCH
BURST STATE                          OFF    ON
SET DEFAULT ▶
----------------BURST CONTENTS----------------
SYNC                                 A91 DE4A H
SACCH                                       PN9
DATA                                        PN9
CDVCC                                     013 H
RSVD                                      800 H
-----------------CHANGE DATA------------------
SELECT DATA LIST...            CURRENT: R&STDM
COPY CURRENT DATA LIST TO...
DELETE DATA LIST...
EDIT DATA LIST...
```

Fig. 2-182 Menu DIGITAL STD - NADC - SELECT SLOT, LINK DIRECTION = DOWNLINK, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) TCH

BURST TYPE

Selection of burst type used to configure the selected slot.

TCH Traffic channel burst

IEC/IEEE-bus `:SOUR:NADC:SLOT2:TYPE TCH`

SLOT LEVEL (UPLINK only)

Selection of level for selected slot.

OFF Maximum attenuation

IEC/IEEE `:SOUR:NADC:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.

IEC/IEEE `:SOUR:NADC:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.

IEC/IEEE `:SOUR:NADC:SLOT2:LEV ATT`

BURST STATE (DOWNLINK only)

Substitute for parameter SLOT LEVEL for DOWNLINK.

ON The burst contents defined in the data fields are sent in the selected slot.

IEC/IEEE-bus `:SOUR:NADC:SLOT2:LEV FULL`

OFF Only 1s will be sent.

IEC/IEEE-bus `:SOUR:NADC:SLOT2:LEV OFF`

Parameter SLOT LEVEL is omitted since the NADC base station in downlink always sends with the same power in each slot.

SET DEFAULT ▶

Resets the subsequent parameters to the factory-set values.

IEC/IEEE-bus command `:SOUR:NADC:SLOT8:PRES`

G (UPLINK only)

Display of data contents in the 6 bit data field "Guard" in hexadecimal form.

R (UPLINK only)

Display of data contents in the 6 bit data field "Ramp".

DATA

Selection of data source for DATA fields. These fields are regarded as a continuous field, ie a pseudo-random sequence is fully continued from one DATA field to the next. The following data sources are available:

PN.. PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1.

IEEE `:SOUR:NADC:SLOT3:DATA PN15`

DLIST Data from a programmable data list.

IEEE `:SOUR:NADC:SLOT3:DATA DLIS`

SERDATA Data from data input SER DATA.

IEEE `:SOUR:NADC:SLOT3:DATA SDAT`

(SELECT SLOT...) TCH

SYNC

Input value for the 28 bit sync word in hexadecimal form. Depending on the selected slot, the following sync words are predefined according to the NADC standard:

SLOT 1	`A91DE4A`
SLOT 2	`A9D127A`
SLOT 3	`C7E3C0C`
SLOT 4	`342C3F3`
SLOT 5	`13E23D1`
SLOT 6	`DC2EC1D`

IEC/IEEE-bus `:SOUR:NADC:SLOT2:SYNC A91EE4A`

SACCH

Selection of data source for SACCH field (Slow Associated Control Channel).

PN..	PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1. IEEE `:SOUR:NADC:SLOT3:SACC PN15`
DLIST	Data from a programmable data list. IEEE `:SOUR:NADC:SLOT3:SACC DLIS`
SERDATA	Data from data input SER DATA. IEEE `:SOUR:NADC:SLOT3:SACC SDAT`

CDVCC

Input value for the 12 bit "Coded digital verification color code" field in hexadecimal form. SMIQ does not perform any coding but directly accepts the input data. 013 hex. is preset. This corresponds to a value of 1 for the uncoded colour code DVCC.

IEC/IEEE-bus `:SOUR:NADC:SLOT2:CDVC #HFFF`

RSVD (DOWNLINK only)

Input value for the 12 bit "Reserved" field in hexadecimal form.

IEC/IEEE-bus `:SOUR:NADC:SLOT2:CDVC #HFFF`

SELECT DATA LIST...

Opens a window for selecting a stored data list or for generating a new list.

COPY CURRENT DATA LIST TO...

Stores the current data list under a different name.

DELETE DATA LIST...

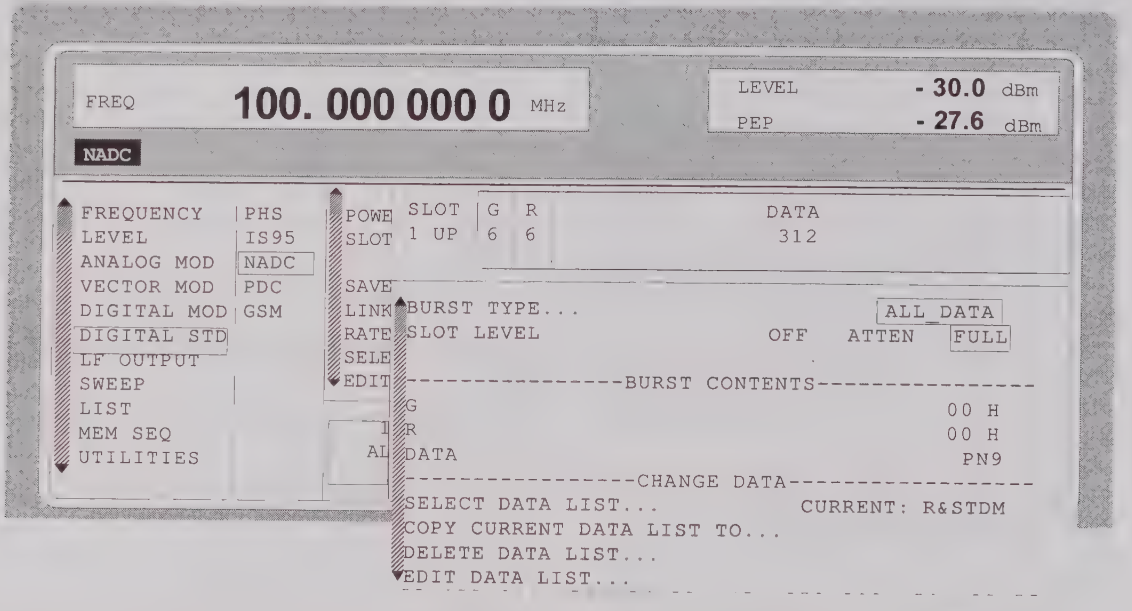
Deletes a data list.

EDIT DATA LIST...

Opens a window for editing a data list bit-by-bit. The available storage capacity and the length of the current list is displayed in parameters FREE and LEN (see also Section List Editor).

COPY	Copies a list range
FILL	Fills the range with filler pattern
INSERT	Inserts a list range at a different position of the list
DELETE	Deletes a list range
EDIT/VIEW	Edits or views the list

Menu selection: BURST TYPE = ALL_DATA


FREQ
100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.6 dBm
NADC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
POWE
SLOT
SAVE
LINK
RATE
SELE
EDIT

SLOT G R DATA
1 UP 6 6 312

BURST TYPE... ALL_DATA
SLOT LEVEL OFF ATTEN FULL
----------------BURST CONTENTS----------------
G 00 H
R 00 H
DATA PN9
-----------------CHANGE DATA-----------------
SELECT DATA LIST... CURRENT: R&STDM
COPY CURRENT DATA LIST TO...
DELETE DATA LIST...
EDIT DATA LIST...

Fig. 2-183 Menu DIGITAL STD - NADC - SELECT SLOT, LINK DIRECTION = UPLINK,SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) ALL_DATA

BURST TYPE Selection of burst type used to configure the selected slot.

ALL DATA Burst type for testing with freely programmable data contents

IEC/IEEE-bus `:SOUR:NADC:SLOT2:TYPE ADAT`

SLOT LEVEL (UPLINK only) Selection of level for selected slot.

OFF Maximum attenuation

IEC/IEEE `:SOUR:NADC:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.

IEC/IEEE `:SOUR:NADC:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.

IEC/IEEE `:SOUR:NADC:SLOT2:LEV ATT`

BURST STATE (DOWNLINK only) Substitute for parameter SLOT LEVEL for LINK DIRECTION = DOWNLINK.

ON The burst contents defined in the data fields are sent in the selected slot.

OFF Only 1s will be sent.

Parameter SLOT LEVEL is omitted since the NADC base station in downlink always sends with the same power in each slot.

IEC/IEEE-bus command `:SOUR:NADC:SLOT2:LEV FULL`

G (UPLINK only) Display of data contents in the 6 bit data field "Guard" in hexadecimal form.

(SELECT SLOT...) ALL_DATA — **R** (UPLINK only)

Display of data contents in the 6 bit data field "Ramp".

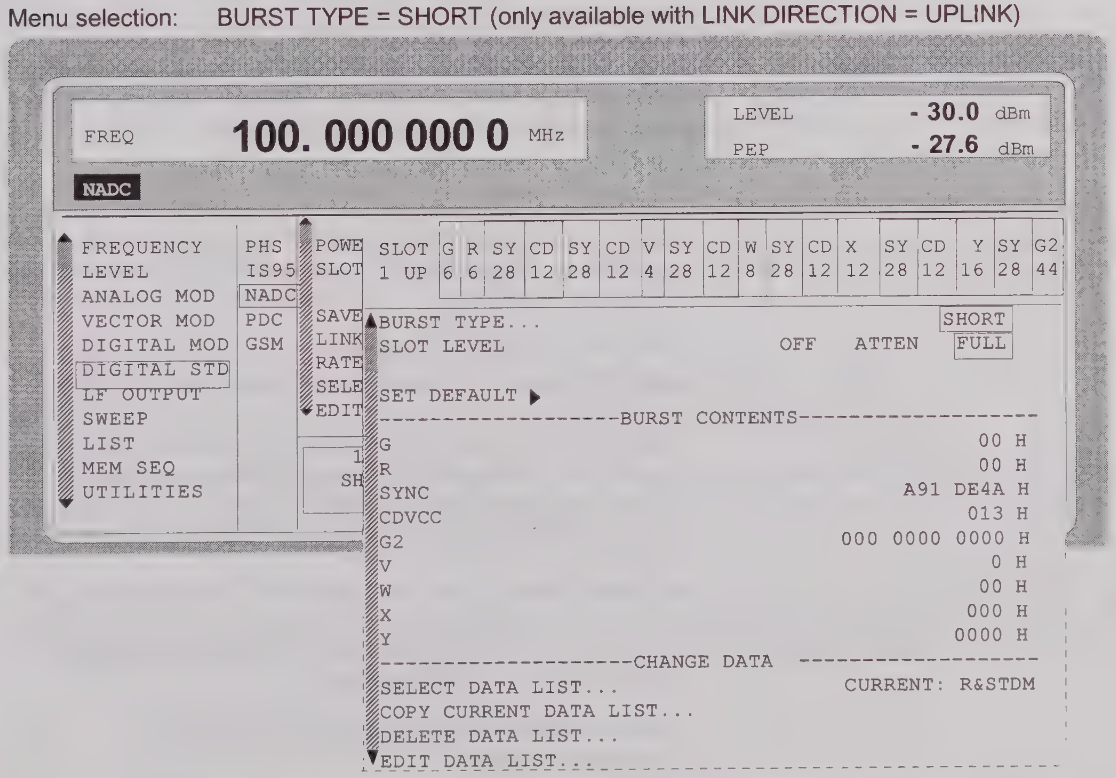
DATA

Selection of data source for DATA fields. These fields are regarded as a continuous field, ie a pseudo-random sequence is fully continued from one DATA field to the next. The following data sources are available:

PN..	PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1. IEEE `:SOUR:NADC:SLOT3:DATA PN15`
DLIST	Data from a programmable data list. IEEE `:SOUR:NADC:SLOT3:DATA DLIS`
SERDATA	Data from data input SER DATA. IEEE `:SOUR:NADC:SLOT3:DATA SDAT`

Parameter **SELECT DATA LIST...** to **EDIT DATA LIST...** see menu selection BURST TYPE = TCH

Menu selection: BURST TYPE = SHORT (only available with LINK DIRECTION = UPLINK)


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
POWE
SLOT
SAVE
LINK
RATE
SELE
EDIT
1
SH
NADC

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.6 dBm

SLOT	G	R	SY	CD	SY	CD	V	SY	CD	W	SY	CD	X	SY	CD	Y	SY	G2
1 UP	6	6	28	12	28	12	4	28	12	8	28	12	12	28	12	16	28	44

```
BURST TYPE...                                  SHORT
SLOT LEVEL                  OFF     ATTEN      FULL

SET DEFAULT ▶
--------------------BURST CONTENTS--------------------
G                                               00 H
R                                               00 H
SYNC                                      A91 DE4A H
CDVCC                                          013 H
G2                                   000 0000 0000 H
V                                                0 H
W                                               00 H
X                                              000 H
Y                                             0000 H
--------------------CHANGE DATA ---------------------
SELECT DATA LIST...                   CURRENT: R&STDM
COPY CURRENT DATA LIST...
DELETE DATA LIST...
EDIT DATA LIST...
```

Fig. 2-184 Menu DIGITAL STD - NADC - SELECT SLOT, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) SHORT (UPLINK ONLY) — **BURST TYPE**

Selection of burst type used to configure the selected slot.

SHORT Shortened Burst

IEC/IEEE-bus `:SOUR:NADC:SLOT2:TYPE SHOR`

(SELECT SLOT...) SHORT
(UPLINK ONLY)

SLOT LEVEL Selection of level for selected slot.

OFF Maximum attenuation
IEC/IEEE `:SOUR:NADC:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.
IEC/IEEE `:SOUR:NADC:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.
IEC/IEEE `:SOUR:NADC:SLOT2:LEV ATT`

SET DEFAULT ▶ Resets the subsequent parameters to the factory-set values.
IEC/IEEE-bus command `:SOUR:NADC:SLOT8 PRES`

G Display of data contents in the 6 bit data field "Guard" in hexadecimal form.

R Display of data contents in the 6 bit data field "Ramp" in hexadecimal form..

SYNC Input value for the sync word in hexadecimal form. Depending on the selected slot, the following sync words are predefined according to the NADC standard:

SLOT 1	`A91DE4A`
SLOT 2	`A9D127A`
SLOT 3	`C7E3C0C`
SLOT 4	`342C3F3`
SLOT 5	`13E23D1`
SLOT 6	`DC2EC1D`

IEC/IEEE-bus `:SOUR:NADC:SLOT2:SYNC A91EE4A`

CDVCC Input value for the 12 bit "Coded digital verification color code" field in hexadecimal form. SMIQ does not perform any coding but directly accepts the input data. 013 hex. is preset. This corresponds to a value of 1 for the uncoded colour code DVCC.
IEC/IEEE-bus `:SOUR:NADC:SLOT2:CDVC #HFFF`

V, W, X, Y Display of data contens of other data fields that are predefined by the NADC standard.

G2 Display of data contents in the second "Guard" in hexadecimal form.

Parameter **SELECT DATA LIST...** to **EDIT DATA LIST...** see menu selection BURST TYPE = TCH

2.17 Digital Standard PDC

With the options Modulation Coder (SMIQB20) and Data Generator (SMIQB11) provided, modulation signals according to the Japanese PDC standard[1] can be generated. PDC is a TDMA standard for cellular mobile radio networks.

SMIQ can generate both the transmit signal of a base station (BS) and the transmit signal of a mobile station (MS). Transmission from BS to MS is called "downlink", "uplink" being used for transmission in the opposite direction.

Each TDMA frame consists of 6 slots. The 6 slots can be configured for both full rate and half rate mode. For half rate, the data content for each of the 6 slots can be defined separately by means of a slot editor whereas in the full rate mode 2 slots for example slot 1 and 4 are combined in a frame. The settings for the first slot are then automatically used for the assigned second slot. Each slot can be switched on or off. Moreover, a defined intermediate level can be set for uplink slots.

A burst type has to be defined to configure a slot. The following burst types can be selected:

- TCH a traffic channel burst with a different structure for uplink and downlink,
- SYNC a burst type for simulation of a sync channel,
- VOX a burst type which is only available in uplink for simulating a communication channel in speech intervals,
- ALL_DATA burst type for test purposes with freely programmable data contents in the selected slot.

For TCH and VOX bursts, SMIQ supports the generation of superframes with a length of 18 frames. The frame position of the so-called "housekeeping channel" RCH can be set in the superframe. According to the standard, the beginning of the superframe is marked by a special sync word in the SYNC data field of the first slot.

The following internal modulation sources are available:

- different PRBS generators with a sequence length between 2^9-1 and 2^{23}-1 and
- data lists, ie freely programmable data sequences from the data generator memory.

For generating the PDC signals, SMIQ inserts the modulation data continuously (in real time) into the selected slots. Using a digital signal processor the data generator generates a data sequence with modulation data and control signals for envelope control.

The data generator in SMIQ generates a data stream which is converted into IQ signals in the modulation coder. According to the PDC standard, the modulation type is $\pi/4$ DQPSK at a symbol rate of 21 ksymbol/s and √COS filtering. Symbol rate and filtering can be changed in SMIQ.

---

[1] Personal Digital Cellular Telecommunication System, RCR STD-27 D

2.17.1 Sync and Trigger Signals

The data generator generates a data sequence with modulation data, control signals for envelope control, and synchronization signals.

When TRIGGER MODE AUTO is selected, the PDC signal generation is started automatically.

This start can also be activated by an external trigger signal (TRIGGER MODE ARMED_AUTO) which allows a synchronous sequence for BER measurements to be carried out on receivers.

A trigger signal can be fed via the TRIGIN input at connector PAR DATA. The active slope of a trigger signal applied there executes a trigger event.
PDC signal generation at a frame or a super frame limit is started after a trigger event. Data from data lists are inserted into the selected slots starting from the first bit. PRBS generators start with the set initialization status.

Signal generation either starts immediately after the active slope of the trigger signal or after a settable number of symbols (EXT TRIGGER DELAY). Retriggering (RETRIG) can be inhibited for a settable number of symbols (EXT RETRIGGER INHIBIT).

A trigger event can be executed manually or via the IEC/IEEE bus using EXECUTE TRIGGER.

When a trigger event is executed, a trigger signal is output at the TRIGOUT 3 output of SMIQ.

SMIQ also generates the following sync signals:

- a frame clock at TRIGOUT 1 output,
- a frame or multiframe clock at TRIGOUT 2 output with settable position in the frame,
- the symbol clock and the bit clock.

A clock synthesizer on the modulation coder generates the symbol clock and the bit clock in SMIQ. All the clock signals are synchronized to the 10-MHz reference of SMIQ. The symbol clock is available at connector SYMBOL CLOCK and the bit clock at connector BIT CLOCK. If required, the clock synthesizer in SMIQ can be synchronized to an external symbol or bit clock.

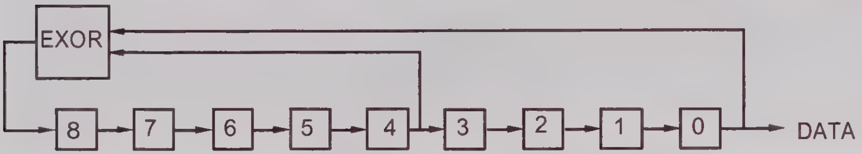
The clock signal is selected in the menu via CLOCK-CLOCK SOURCE EXT.

To allow for a trouble-free synchronization of the clock synthesizer first apply the external clock and set the correct symbol rate at SMIQ. Then switch CLOCK SOURCE from INT to EXT.

Note: *The set symbol rate should not differ by more than 1% from the symbol rate of the external signal.*

2.17.2 PN Generators as Internal Data Source

Independent PN generators (Pseudo Noise) can be selected for each slot as data source for data fields DATA, SACCH, RCH and SI. These PN generators provide pseudo-random bit sequences of different length or period. That is why they are also called PRBS generators (Pseudo Random Binary Sequence). Data sequences are sequences of maximum length which are generated by means of feedback shift registers.
The following figure gives an example of a 9 bit generator with feedbacks after register 4 and 0 (output).



The pseudo-random sequence of a PRBS generator is clearly defined by the number of registers and the feedback. The following table describes all PRBS generators available:

Table 2-30 PRBS generators for PDC

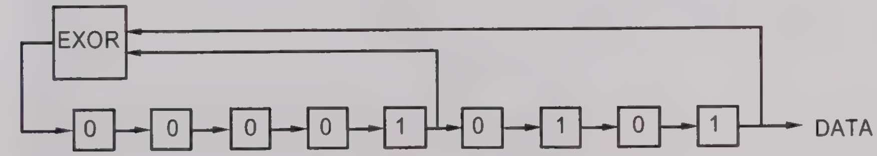
PRBS generator	Length in bits	Feedback after
9 bit	$2^9 -1 = 511$	Register 4, 0
11 bit	$2^{11} -1 =2047$	Register 2, 0
15 bit	$2^{15} -1 = 32767$	Register 1, 0
16 bit	$2^{16} -1 = 65535$	Register 5, 3, 2, 0
20 bit	$2^{20} -1 = 1048575$	Register 3, 0
21 bit	$2^{21} -1 = 2097151$	Register 2, 0
23 bit	$2^{23} -1 = 8388607$	Register 5, 0

PN generators PN9,11,15,20 and PN23 are configured according to CCITT Rec. 0.151/152/153. The output sequence is inverted for generators PN15 and PN23.

The start value of the PN generators is different in the slots and equals

$$\text{start value} = 1 + 14\,\text{hex} \times \text{slot number}$$

Example: PN9 generator in slot 1 with start value 15hex = 10101 binary.



The resulting output sequence is `1010100000010100101011110010` etc.

2.17.3 Lists as Internal Data Source

A freely programmable memory on the data generator serves as internal data source for the data fields of the slots. The data are managed in so-called lists. A list editor allows to select, copy, modify and delete data lists (DATA LIST).
The list editor is available via menu DIGITAL-MOD - SOURCE....

2.17.4 External Modulation Data

External data can (only) be applied via the SERDATA interface. A selection of SERDATA as data source is only possible for a single data field of a slot. For further information on the characteristics of the SERDATA interface see Annex A.

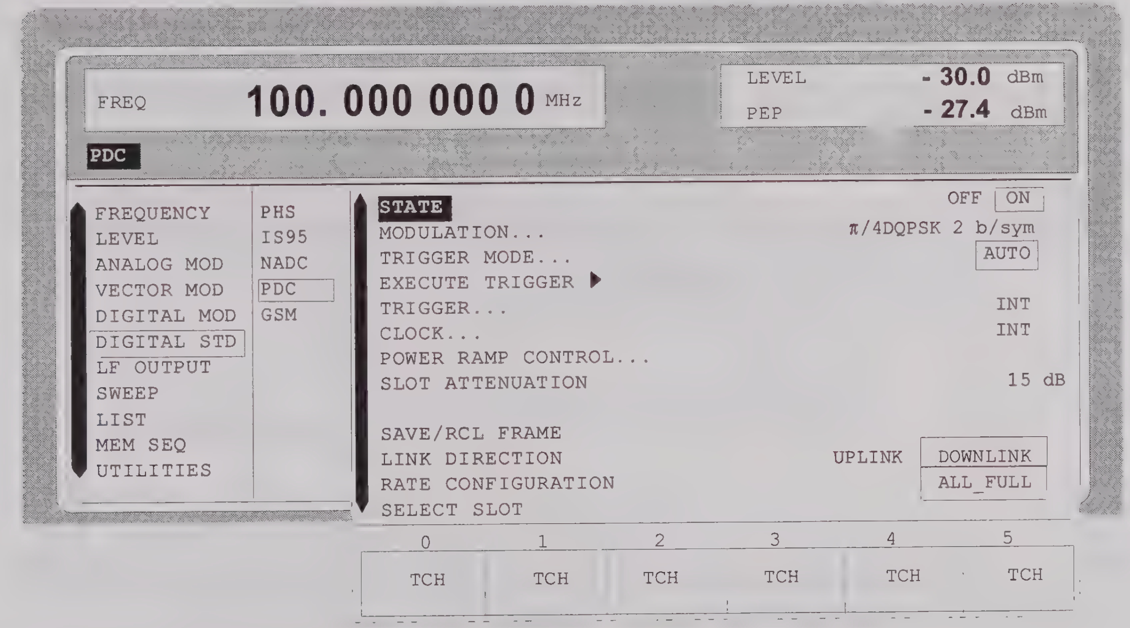
To ensure that the external data bits are assigned to specific positions in the data field of the selected slot and that they are reproducible, the buffer of the RS-232 transmitter and receiver has to be deleted. A triggered start has to follow.
The following setting sequence is required in the DIGITAL STD - PDC menu:

1. Carry out desired settings in menu.
2. Select data source SERDATA for the data field of the slot using SELECT SLOT - ....
3. Make connection to external data source, but do not yet start external data source.
4. Switch off digital standard using STATE - OFF.
5. Set TRIGGER MODE ARMED_AUTO.
   In this state, SMIQ is ready for reception, but discards data that are read in via SERDATA.
6. Switch on digital standard with STATE ON.
7. Start external data source.
   The read-in data are written into the receiving buffer. Only if this buffer is filled can SMIQ react to a trigger event.
8. Activate trigger event. Signal generation is thus started at a frame limit. The first bit received via SERDATA is put to the first bit position in the selected data field.

2.17.5 Menu DIGITAL STANDARD - PDC

Menu DIGITAL STD - PDC provides access to settings for generating PDC signals.

Menu selection: DIGITAL STD - PDC


FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.4 dBm
PDC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM

STATE OFF ON
MODULATION... π/4DQPSK 2 b/sym
TRIGGER MODE... AUTO
EXECUTE TRIGGER ▶
TRIGGER... INT
CLOCK... INT
POWER RAMP CONTROL...
SLOT ATTENUATION 15 dB

SAVE/RCL FRAME
LINK DIRECTION UPLINK DOWNLINK
RATE CONFIGURATION ALL_FULL
SELECT SLOT

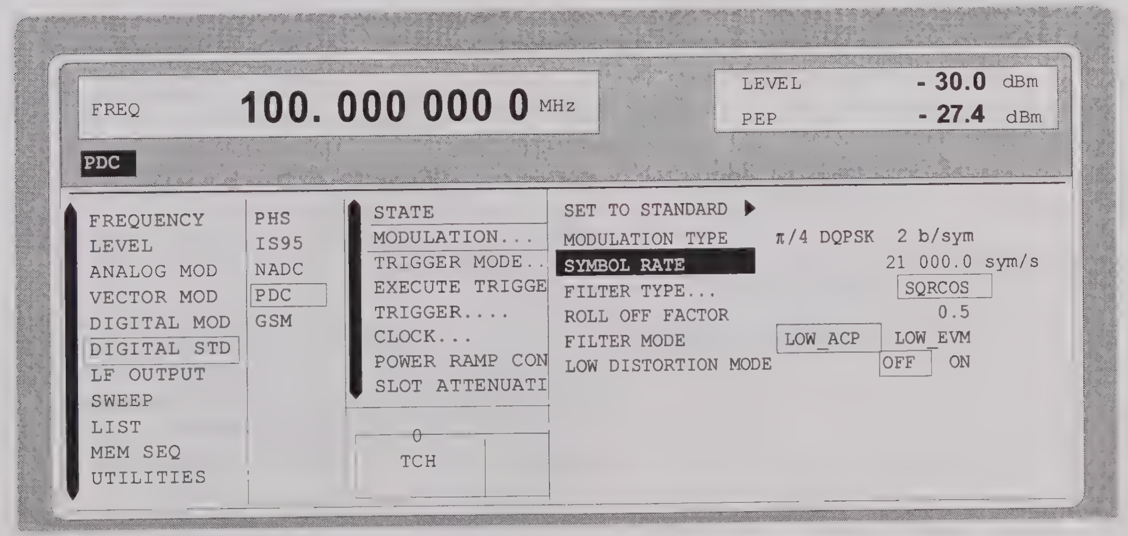
0	1	2	3	4	5
TCH	TCH	TCH	TCH	TCH	TCH

Fig. 2-185 Menu DIGITAL STD - PDC, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

STATE Switch on/off of Digital Standard PDC modulation. Vector Modulation or Digital Modulation will be switched off automatically.

IEC/IEEE-bus command `:SOUR:PDC:STAT ON`

MODULATION... Opens a window for setting the modulation parameters.


FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.4 dBm
PDC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
STATE
MODULATION...
TRIGGER MODE..
EXECUTE TRIGGE
TRIGGER....
CLOCK...
POWER RAMP CON
SLOT ATTENUATI
0
TCH

SET TO STANDARD ▶
MODULATION TYPE π/4 DQPSK 2 b/sym
SYMBOL RATE 21 000.0 sym/s
FILTER TYPE... SQRCOS
ROLL OFF FACTOR 0.5
FILTER MODE LOW_ACP LOW_EVM
LOW DISTORTION MODE OFF ON

Fig. 2-186 Menu DIGITAL STD - PDC - MODULATION..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(MODULATION...)	**SET TO STANDARD ►**	Sets the subsequent modulation parameters to the values predefined by the standard.
	MODULATION TYPE	Displays the modulation type.
	SYMBOL RATE	Input value for the symbol clock. 21 ksymbol/s are preset. IEC/IEEE-bus `:SOUR:PDC:SRAT 21.1KHZ`
	FILTER	Selection of baseband filter. A selection between Nyquist filters COS, SQRCOS or a user-defined filter USER (cf. Section Digital Modulation) is possible. IEC/IEEE-bus `:SOUR:PDC:FILT:TYPE COS`
	ROLL OFF FACTOR	Input value for the roll-off factor. IEC/IEEE-bus `:SOUR:PDC:FILT:PAR 0.51`
	FILTER MODE	Selection of filter mode. LOW_ACP Filter for minimum Adjacent Channel Power. IEC `:SOUR:PDC:FILT:MODE LACP` LOW_EVM Filter for minimum vector error. IEC `:SOUR:PDC:FILT:MODE LEVM`
	LOW DISTORTION	Switch on/off of low-distortion mode.. After switch-on, the level of the IQ baseband signals is reduced by 3 dB. In some cases, this might reduce undesired intermodulation products. OFF is normally the more favourable setting. IEC/IEEE-bus command `:SOUR:PDC:LDIS OFF`
TRIGGER MODE...	Opens a window for selecting the trigger mode.	
	AUTO	The PDC signals are continuously transmitted in the activated slots. IEC/IEEE-bus `:SOUR:PDC:SEQ AUTO`
	RETRIG	The PDC signals are continuously transmitted in the activated slots. A trigger event causes a restart. IEC/IEEE-bus `:SOUR:PDC:SEQ RETR`
	ARMED_AUTO	The PDC signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the AUTO mode and can no longer be triggered. IEC/IEEE-bus `:SOUR:PDC:SEQ AAUT`
	ARMED_RETRIG	The PDC signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the RETRIG mode. Each new trigger event causes a restart. IEC/IEEE-bus `:SOUR:PDC:SEQ ARET`

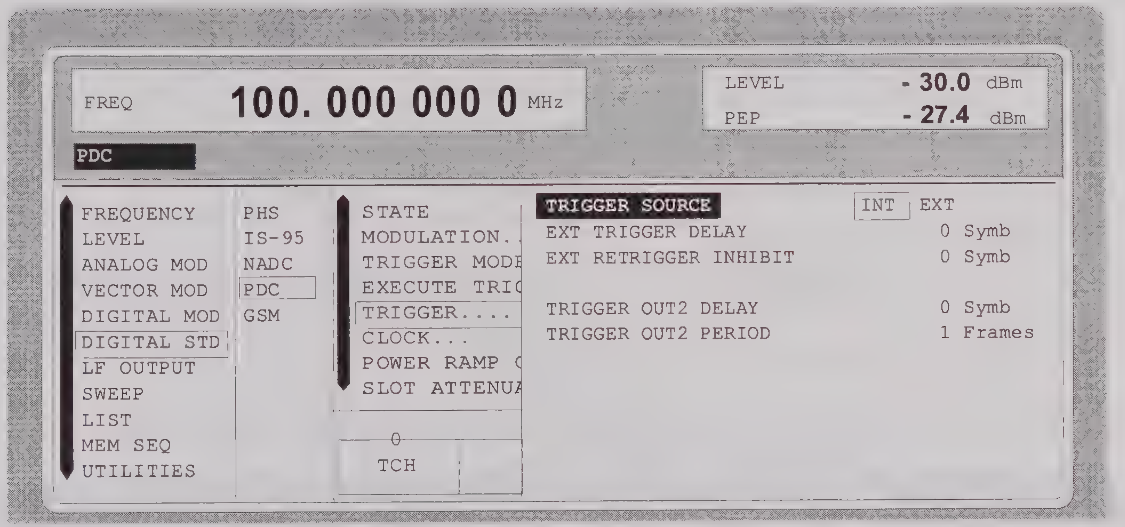
EXECUTE TRIGGER ▶

Executes a trigger even to start the PDC signal generation.

IEC/IEEE-bus command `:TRIG:DM:IMM`

TRIGGER...

Opens a window for selecting the trigger source, for configuring the trigger output signals and for setting the time delay of an external trigger signal.


FREQ
MHz
LEVEL
PEP
PDC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
STATE
MODULATION.
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
0
TCH

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.4 dBm

TRIGGER SOURCE INT EXT
EXT TRIGGER DELAY 0 Symb
EXT RETRIGGER INHIBIT 0 Symb
TRIGGER OUT2 DELAY 0 Symb
TRIGGER OUT2 PERIOD 1 Frames

Fig. 2-187 Menu DIGITAL STD - PDC_TRIGGER..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(TRIGGER...)

TRIGGER SOURCE

Selection of trigger source.

EXT The PDC signal generation is started by the active slope of an external trigger signal. The polarity, the trigger threshold and the input resistance of the TRIGIN input can be modified in menu DIGITAL MOD - EXT INPUTS.

INT A trigger event is executed by EXECUTE TRIGGER ▶.

IEC/IEEE-bus `:SOUR:PDC:TRIG:SOUR EXT`

EXT TRIGGER DELAY

Setting the number of symbols by which an external trigger signal is delayed before it starts the PDC signal generation.
This is used for setting the time synchronization between the SMIQ and the DUT.

IEC/IEEE-bus `:SOUR:PDC:TRIG:DEL 3`

(TRIGGER...)

EXT RETRIGGER INHIBIT

Setting the number of symbols for which a restart is inhibited after a trigger event.
With TRIGGER MODE RETRIG selected, each new trigger signal restarts the PDC signal generation. This restart can be inhibited for the entered number of symbols.

Example:
The entry of 1000 symbols causes new trigger signals to be ignored for the duration of 1000 symbols after a trigger event

IEC/IEEE-bus `:SOUR:PDC:TRIG:INH 1000`

TRIGGER OUT 2 DELAY

Input value of delay of trigger signal at TRIGOUT 2 output compared with beginning of frame.

IEC/IEEE-bus `:SOUR:PDC:TRIG:OUTP:DEL 2`

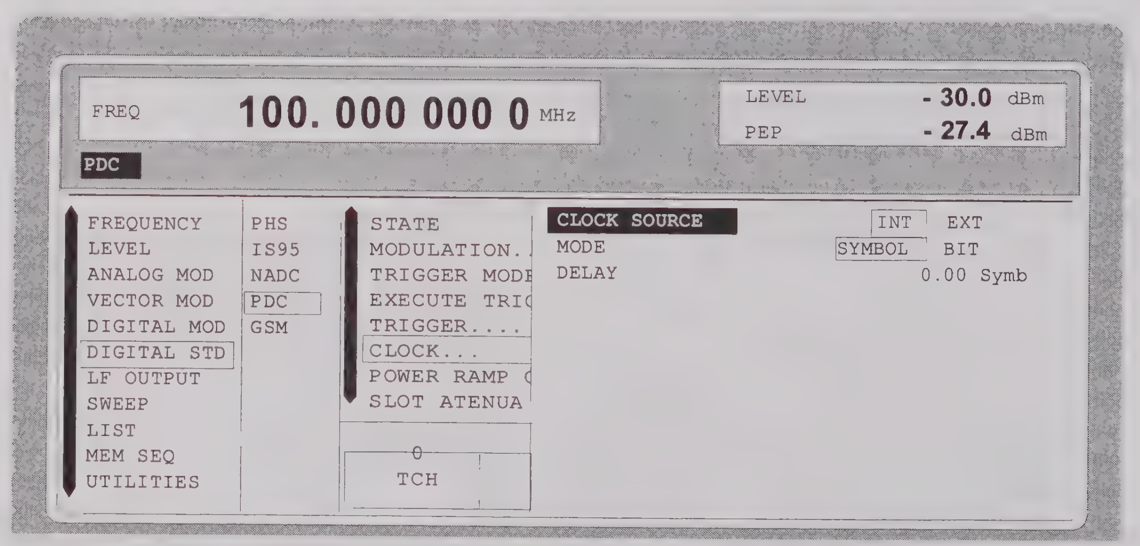
TRIGGER OUT2 PERIOD

Input value of output signal period at TRIGOUT 2 output given in frames.

IEC/IEEE-bus `:SOUR:PDC:TRIG:OUTP:PER 1`

CLOCK...

Opens a window for selecting the clock source and for setting a delay.



FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.4 dBm

CLOCK SOURCE [✓ INT] EXT
MODE [✓ SYMBOL] BIT
DELAY 0.00 Symb

PDC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
STATE
MODULATION.
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATENUA
0
TCH

Fig. 2-188 Menu DIGITAL STD - PDC - CLOCK..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(CLOCK...)

CLOCK SOURCE Selection of clock source.

INT SMIQ uses internally generated clock signals.

EXT An external symbol clock or bit clock is fed in at connectors SYMBOL CLOCK or BIT CLOCK. The clock synthesizer on the modulation coder is synchronized to this clock.
The symbol rate has to be set with an accuracy of ± 1 %.
The polarity, the trigger threshold and the input resistance of the clock inputs can be modified in menu DIGITAL MOD - EXT INPUTS.

IEC/IEEE-bus command `:SOUR:PDC:CLOC:SOUR INT`

MODE Selection of clock for external clock signal.

SYMBOL The external clock has to be a symbol clock.

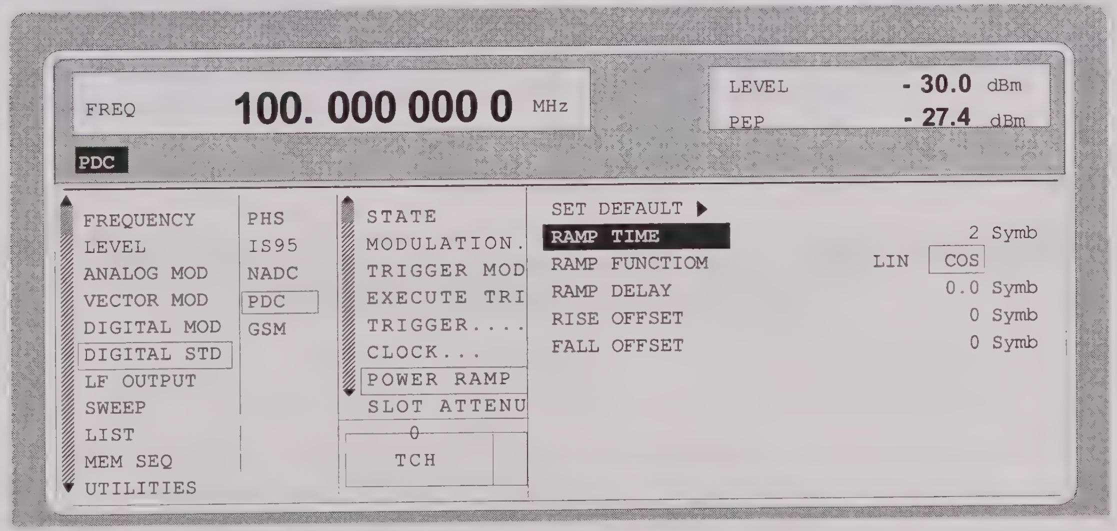
BIT The external clock has to be a bit clock.

IEC/IEEE-bus command `:SOUR:PDC:CLOC:MODE SYMB`

DELAY Setting the delay of generated modulation signal to an external clock.
This can be used, for example, for synchronization with a second unit to achieve time synchronization between the modulation signals of the two units.

IEC/IEEE-bus command `:SOUR:PDC:CLOC:DEL 0.5`

POWER RAMP CONTROL... Opens a window for setting the envelope control, especially for the rising and falling ramp at the beginning and end of a slot.



FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.4 dBm

SET DEFAULT ▶
RAMP TIME 2 Symb
RAMP FUNCTIOM LIN COS
RAMP DELAY 0.0 Symb
RISE OFFSET 0 Symb
FALL OFFSET 0 Symb

PDC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
STATE
MODULATION.
TRIGGER MOD
EXECUTE TRI
TRIGGER....
CLOCK...
POWER RAMP
SLOT ATTENU
0
TCH

Fig. 2-189 Menu DIGITAL STD - PDC - POWER RAMP CONTROL... , SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(POWER RAMP CONTROL...)

SET DEFAULT ▶
Resets the subsequent parameters to the factory-set values.
IEC/IEEE-bus `:SOUR:PDC:PRAM:PRES`

RAMP TIME
Input value for the rise and fall time of the envelope at the beginning or end of a slot. The time is set in units of symbol period.
IEC/IEEE-bus `:SOUR:PDC:PRAM:TIME 0.25`

RAMP FUNCTION
Selection of shape of rising and falling ramp for envelope control.

LIN	Linear ramp function.
COS	Cosine function. A more favourable spectrum than that of the LIN function is obtained.

IEC/IEEE-bus `:SOUR:PDC:PRAM:SHAP LIN`

RAMP DELAY
Input value for a shift of the envelope characteristic to the modulated signal. A positive value causes a delay of the envelope. The values are set in the units of the symbol length.
IEC/IEEE-bus `:SOUR:PDC:PRAM:DEL 0.1`

RISE OFFSET
Input value for a positive or negative offset of the rising ramp of the envelope at the beginning of a slot.
IEC/IEEE-bus `:SOUR:PDC:PRAM:ROFF -1`

FALL OFFSET
Input value for a positive or negative offset of the falling ramp of the envelope at the end of a slot.
IEC/IEEE-bus `:SOUR:PDC:PRAM:LOFF 1`

SLOT ATTENUATION
Input value for the level reduction in dB of all active slots whose SLOT LEVEL was set to ATTEN. Menu SELECT SLOT allows the slots to be determined whose level is to be reduced.
IEC/IEEE-bus command `:SOUR:PDC:SLOT:ATT 15 DB`

SAVE/RCL FRAME...
Opens a window for saving and loading a frame configuration. Loading a frame affects all parameters that can be set under SELECT SLOT.

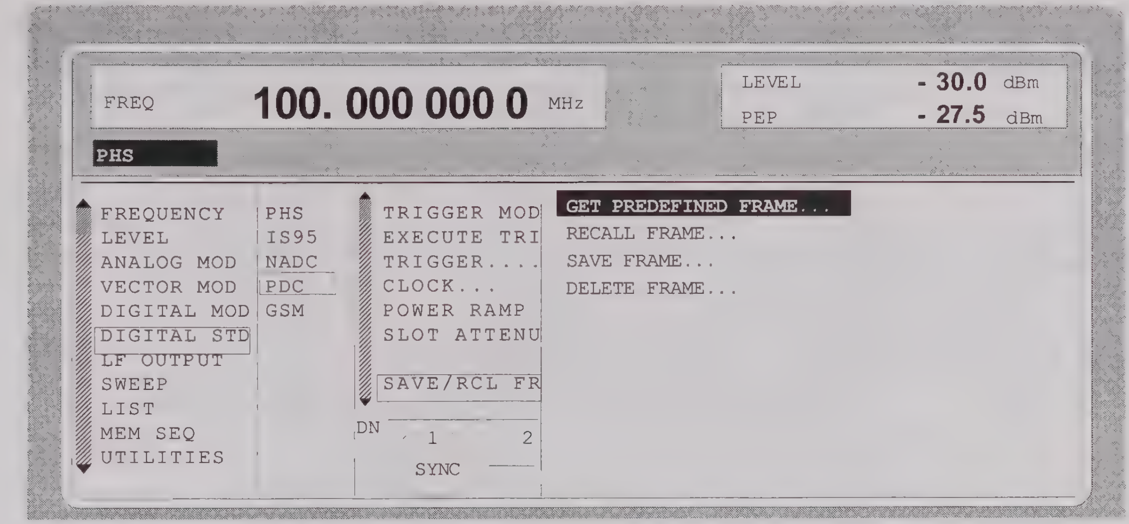

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.5 dBm
PHS
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
TRIGGER MOD
EXECUTE TRI
TRIGGER....
CLOCK...
POWER RAMP
SLOT ATTENU
SAVE/RCL FR
DN
1
2
SYNC
GET PREDEFINED FRAME...
RECALL FRAME...
SAVE FRAME...
DELETE FRAME...

Fig. 2-190 Menu DIGITAL STD - PDC - SAVE/RCL FRAME, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SAVE/RCL FRAME...)

GET PREDEFINED FRAME... Loads a factory-set frame configuration.

DN_TCH	Downlink traffic channels in all slots.
UP0_TCH	Uplink traffic channels in slot 0
IEEE	`:SOUR:PDC:FLIS:PRED:LOAD "dn_tch"`

RECALL FRAME... Loads a frame configuration saved by the user.

IEC/IEEE-bus `:SOUR:PDC:FLIS:LOAD "name"`

SAVE FRAME... Saves a user-defined frame configuration.

IEC/IEEE-bus `:SOUR:PDC:FLIS:STOR "name"`

DELETE FRAME... Deletes a frame configuration saved by the user.

IEC/IEEE-bus `:SOUR:PDC:FLIS:DEL "name"`

LINK DIRECTION Selection between UPLINK and DOWNLINK. Depending on the selection, different burst types are offered under SELECT SLOT.

IEC/IEEE-bus command `:SOUR:PDC:LINK UP`

RATE CONFIGURATION...

Opens a window for determining which slots in the frame are to be configured as half-rate and full-rate channels. Full-rate channels occupy two slots in a frame, the second slot is fully determined by the configuration of the first slot. The following eight combinations can be selected:

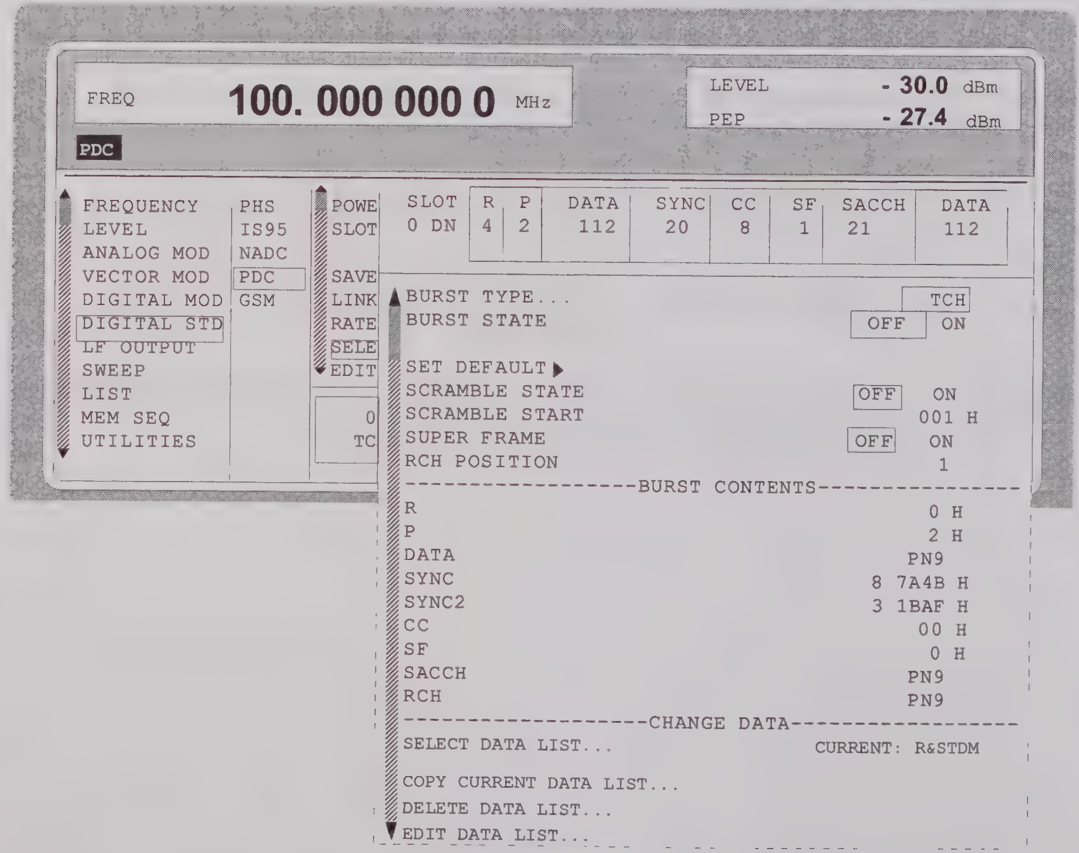
Selection	Full-rate slots	Half-rate slots
ALL HALF	keine	0, 1, 2, 3, 4, 5
FULL 1	0 + 3	1, 2, 4, 5
FULL 2	1 + 4	0, 2, 3, 5
FULL 3	2 + 5	0, 1, 3, 4
FULL 1+2	0 + 3 and 1 + 4	2, 5
FULL 1+3	0 + 3 and 2 + 5	1, 4
FULL 2+3	1 + 4 and 2 + 5	0, 3
ALL FULL	0 + 3 and 1 + 4 and 2 + 5	none

IEC/IEEE-bus command `SOUR:PDC:RCON AHAL`

SELECT SLOT...

Selection of one of 6 possible slots. The number depends on the setting under RATE CONFIGURATION. When selecting the slot, a window is opened in which the data contents belonging to this slot can be defined. The content of the window depends on whether an UPLINK or DOWNLINK was selected under LINK DIRECTION. If the cursor is placed onto a slot in the diagram, it may be switched on and off by pressing one of the unit keys (toggle function).

Menu selection: BURST TYPE = TCH


FREQ
100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.4 dBm
PDC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
POWE
SLOT
SAVE
LINK
RATE
SELE
EDIT
0
TC

SLOT	R	P	DATA	SYNC	CC	SF	SACCH	DATA
0 DN	4	2	112	20	8	1	21	112

```
BURST TYPE...                         TCH
BURST STATE                       OFF  ON

SET DEFAULT ▶
SCRAMBLE STATE                    OFF  ON
SCRAMBLE START                      001 H
SUPER FRAME                       OFF  ON
RCH POSITION                          1
-----------------BURST CONTENTS---------------
R                                     0 H
P                                     2 H
DATA                                PN9
SYNC                           8 7A4B H
SYNC2                          3 1BAF H
CC                                   00 H
SF                                    0 H
SACCH                               PN9
RCH                                 PN9
------------------CHANGE DATA-----------------
SELECT DATA LIST...         CURRENT: R&STDM

COPY CURRENT DATA LIST...
DELETE DATA LIST...
EDIT DATA LIST...
```

Fig. 2-191 Menu DIGITAL STD - PDC - SELECT SLOT, LINK DIRECTION DOWNLINK, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) TCH

BURST TYPE

Selection of burst type used to configure the selected slot.

TCH Traffic channel burst

IEC/IEEE-bus `:SOUR:PDC:SLOT2:TYPE TCH`

SLOT LEVEL (UPLINK only)

Selection of level for selected slot.

OFF Maximum attenuation

IEC/IEEE `:SOUR:PDC:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.

IEC/IEEE `:SOUR:PDC:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.

IEC/IEEE `:SOUR:PDC:SLOT2:LEV ATT`

BURST STATE (DOWNLINK only)

Substitute for parameter SLOT LEVEL for downlink.

ON The burst contents defined in the data fields are sent in the selected slot.

IEC/IEEE-bus `:SOUR:PDC:SLOT2:LEV FULL`

OFF Only 1s will be sent.

IEC/IEEE-bus `:SOUR:PDC:SLOT2:LEV OFF`

Parameter SLOT LEVEL is omitted since the NADC base station in downlink always sends with the same power in each slot.

SET DEFAULT ▶

Resets the subsequent parameters to the factory-set values.

IEC/IEEE-bus command `SOUR:PDC:SLOT8:PRES`

SCRAMBLE

Switch on/off of scrambling function. Scrambling with the continuous scramble sequence from a PN9 generator is applied to the data fields DATA, SF, and SACCH.

IEC/IEEE-bus `:SOUR:PDC:SLOT2:SCR:STAT ON`

SCRAMBLE START

9 bit input value for initializing the scramble PN9 generator in hexadecimal form. The value 0 is not permissible, 1 is preset. The input field is only displayed if SCRAMBLE is switched on.

IEC/IEEE-bus `:SOUR:PDC:SLOT2:SCR:STAR #H12`

SUPERFRAME

Switch on/off of the superframe generation. 18 frames form a superframe. If SUPERFRAME is switched on, RCH data instead of SACCH data will be sent in 2 slots. The first of the two RCH blocks is fixed in the first slot of the superframe, the position of the second can be set.

IEC/IEEE-bus `:SOUR:PDC:SLOT2:SFR:STAT ON`

(SELECT SLOT...) TCH **RCH POSITION**

Input value for the position of the second RCH block. The input field is only displayed if SUPERFRAME is switched on.

IEC/IEEE-bus `:SOUR:PDC:SLOT2:SFR:RCHP 10`

R

Display of data contents in the 4 bit data field "RAMP" in hexadecimal form.

P

Input value of the 2 bit data field "Preamble".

IEC/IEEE-bus `:SOUR:PDC:SLOT2:PRE #H1`

DATA

Selection of data source for DATA fields. These fields are regarded as a continuous field, ie a pseudo-random sequence is fully continued from one DATA field to the next. The following data sources are available:

PN.. PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1.

IEC/IEEE `:SOUR:PDC:SLOT3:DATA PN15`

DLIST Data from a programmable data list.

IEC/IEEE `:SOUR:PDC:SLOT3:DATA DLIS`

SERDATA Data from data input SER DATA.

IEC/IEEE `:SOUR:PDC:SLOT3:DATA SDAT`

SYNC

Input value for the 20 bit sync word in hexadecimal form. Depending on the selected slot the LINK DIRECTION, the following sync words are predefined according to the PDC standard:

Slot No.	Downlink Sync Word	Uplink Sync Word
0	87A4B	785B4
1	9D236	62DC9
2	81D75	7E28A
3	A94EA	56B15
4	5164C	AE9B3
5	4D9DE	B2621

According to the standard, the sync words of the uplink are the inverted sync words of the downlink.

IEC/IEEE-bus `:SOUR:PDC:SLOT2:SYNC #H62DC8`

(SELECT SLOT...) TCH

SYNC2

Input value for the 20 bit sync word in the first slot of a superframe in hexadecimal form. This sync word marks the beginning of the superframe. The input field is only displayed if SUPERFRAME is switched on. Depending on the selected slot and the LINK DIRECTION, the following sync words are predefined according to the PDC standard:

Slot No.	Downlink Sync2 Word	Uplink Sync2 Word
0	31BAF	CE450
1	1E56F	E1A90
2	E712C	18ED3
3	FBC1F	043E0
4	8279E	7D861
5	98908	676F7

According to the standard, the sync words of the uplink are the inverted sync words of the downlink.

IEC/IEEE-bus `:SOUR:PDC:SLOT2:SYNC #HE2A90`

CC

Input value of the 8 bit data field " Color code ".

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:CCOD #HFF`

SF

Input value of the data field " Steal Flag ".

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:SF #H0`

SACCH

Selection of data source for SACCH field (Slow Associated Control Channel).

PN.. PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1.

IEC/IEEE `:SOUR:PDC:SLOT3:SACC PN15`

DLIST Data from a programmable data list.

IEC/IEEE `:SOUR:PDC:SLOT3:SACC DLIS`

SERDATA Data from data input SER DATA.

IEC/IEEE `:SOUR:PDC:SLOT3:SACC SDAT`

RCH

Selection of data source for the RCH data field. RCH data replace SACCH data in certain slots of a superframe. The input field is only displayed if SUPERFRAME is switched on.

PN.. PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1.

IEC/IEEE `:SOUR:PDC:SLOT3:RCH PN15`

DLIST Data from a programmable data list.

IEC/IEEE `:SOUR:PDC:SLOT3:RCH DLIS`

SERDATA Data from data input SER DATA.

IEC/IEEE `:SOUR:PDC:SLOT3:RCH SDAT`

(SELECT SLOT...) TCH	**G** (UPLINK only)	Display of data content in the 6 bit "Guard" field in hexadecimal form.
	SELECT DATA LIST...	Opens a window for selecting a stored data list or for generating a new list.
	COPY CURRENT DATA LIST TO...	Stores the current data list under a different name.
	DELETE DATA LIST...	Deletes a data list.
	EDIT DATA LIST...	Opens a window for editing a data list bit-by-bit. The available storage capacity and the length of the current list is displayed in parameters FREE and LEN (see also Section List Editor).

COPY	Copies a list range
FILL	Fills the range with filler pattern
INSERT	Inserts a list range at a different position of the list
DELETE	Deletes a list range
EDIT/VIEW	Edits or views the list

Menu selection: BURST TYPE = ALL_DATA

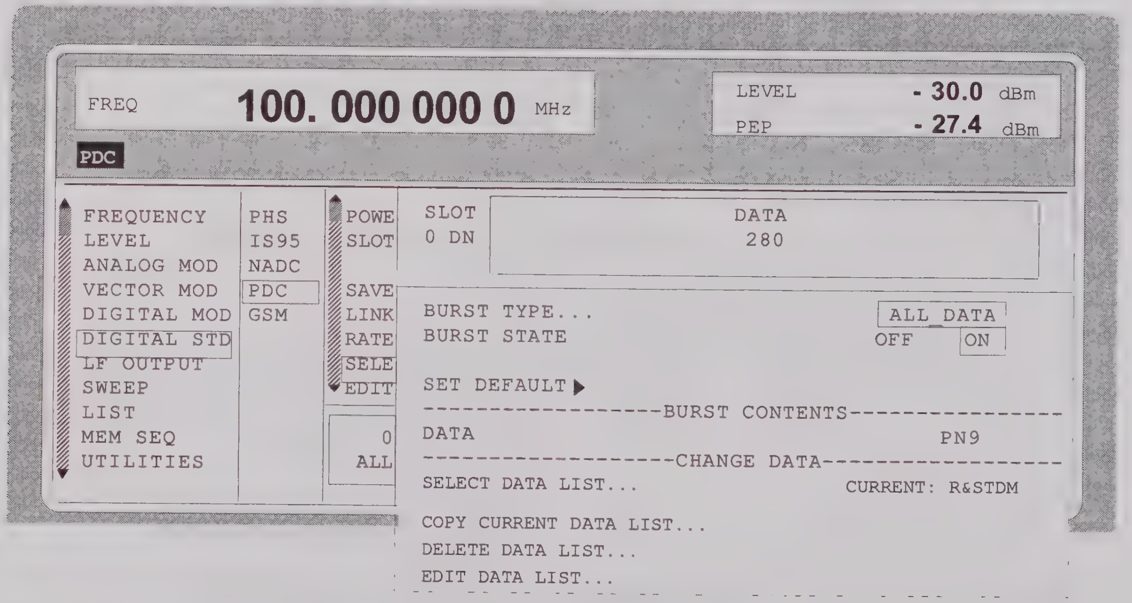

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 27.4 dBm
SLOT 0 DN
DATA 280
BURST TYPE... ALL_DATA
BURST STATE OFF ON
DATA PN9
SELECT DATA LIST... CURRENT: R&STDM
PDC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
POWE
SLOT
SAVE
LINK
RATE
SELE
EDIT
0
ALL
SET DEFAULT
BURST CONTENTS
CHANGE DATA
COPY CURRENT DATA LIST...
DELETE DATA LIST...
EDIT DATA LIST...

Fig. 2-192 Menu DIGITAL STD - PDC - SELECT SLOT, LINK DIRECTION = DOWNLINK, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) ALL_DATA

BURST TYPE

Selection of burst type used to configure the selected slot.

ALL DATA	Burst type for testing with freely programmable data contents

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:TYPE ADAT`

SLOT LEVEL (UPLINK only)

Selection of level for selected slot.

OFF	Maximum attenuation IEC/IEEE `:SOUR:PDC:SLOT2:LEV OFF`
FULL	The level corresponds to the value indicated on the SMIQ LEVEL display. IEC/IEEE `:SOUR:PDC:SLOT2:LEV FULL`
ATTEN	The level is reduced by the value set under SLOT ATTENUATION. IEC/IEEE `:SOUR:PDC:SLOT2:LEV ATT`

BURST STATE (DOWNLINK only)

Substitute for parameter SLOT LEVEL for downlink.

ON	The burst contents defined in the data fields are sent in the selected slot. IEC/IEEE-bus `:SOUR:PDC:SLOT2:LEV FULL`
OFF	Only 1s will be sent. IEC/IEEE-bus `:SOUR:PDC:SLOT2:LEV OFF`

Parameter SLOT LEVEL is omitted since the NADC base station in downlink always sends with the same power in each slot.

R (UPLINK only)

Display of data contents in the 6 bit data field "Ramp" in hexadecimal form..

G (UPLINK only)

Display of data contents in the 6 bit data field "Guard" in hexadecimal form.

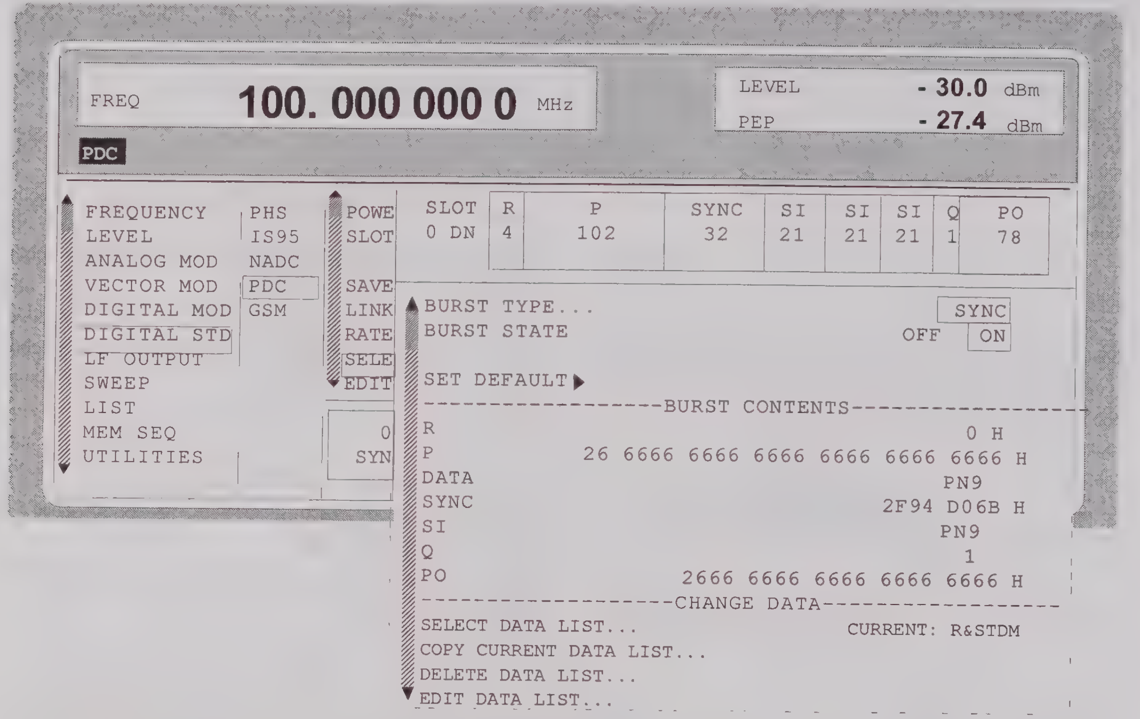
DATA

Selection of data source for DATA fields. These fields are regarded as a continuous field, ie a pseudo-random sequence is fully continued from one DATA field to the next. The following data sources are available:

PN..	PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1. IEC/IEEE `:SOUR:PDC:SLOT3:DATA PN15`
DLIST	Data from a programmable data list. IEC/IEEE `:SOUR:PDC:SLOT3:DATA DLIS`
SERDATA	Data from data input SER DATA. IEC/IEEE `:SOUR:PDC:SLOT3:DATA SDAT`

Parameter **SELECT DATA LIST...** to **EDIT DATA LIST...** see menu selection BURST TYPE = TCH

Menu selection: BURST TYPE = SYNC


PDC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
POWE
SLOT
SAVE
LINK
RATE
SELE
EDIT
0
SYN

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.4 dBm

SLOT	R	P	SYNC	SI	SI	SI	Q	PO
0 DN	4	102	32	21	21	21	1	78

BURST TYPE... SYNC

BURST STATE OFF ON

SET DEFAULT ▶

-------------------BURST CONTENTS-------------------

R 0 H

P 26 6666 6666 6666 6666 6666 6666 H

DATA PN9

SYNC 2F94 D06B H

SI PN9

Q 1

PO 2666 6666 6666 6666 6666 H

-------------------CHANGE DATA-------------------

SELECT DATA LIST... CURRENT: R&STDM

COPY CURRENT DATA LIST...

DELETE DATA LIST...

EDIT DATA LIST...

Fig. 2-193 Menu DIGITAL STD - PDC - SELECT SLOT, LINK DIRECTION = DOWNLINK, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) SYNC

BURST TYPE

Selection of burst type used to configure the selected slot.

SHORT Shortened Burst

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:TYPE SYNC`

SLOT LEVEL (UPLINK only)

Selection of level for selected slot.

OFF Maximum attenuation

IEC/IEEE `:SOUR:PDC:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.

IEC/IEEE `:SOUR:PDC:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.

IEC/IEEE `:SOUR:PDC:SLOT2:LEV ATT`

BURST STATE (DOWNLINK only)

Substitute for parameter SLOT LEVEL for downlink.

ON The burst contents defined in the data fields are sent in the selected slot.

OFF Only 1s will be sent.

Parameter SLOT LEVEL is omitted since the PDC base station in downlink always sends with the same power in each slot.

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:LEV FULL`

(SELECT SLOT...) SYNC

SET DEFAULT ▶ Resets the subsequent parameters to the factory-set values.

IEC/IEEE-bus command `:SOUR:PDC:SLOT8 PRES`

G
(UPLINK only)

Display of data contents in the 54 bit data field "Guard" in hexadecimal form.
The field is only displayed with link direction UPLINK selected.

R

Display of data contents in the 6 bit data field "Ramp".

P

Input value of the data field "Preamble" in hexadecimal form.The length of the field is 48 bit with link direction = UPLINK, and 102 bit with DOWNLINK

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:PRE #HFF`

SYNC

Input value for the 32 bit sync word in hexadecimal form. Depending on the selected slot and the link direction, the following sync words are predefined according to the PDC standard:

Slot No.	Downlink Sync Word	Uplink Sync Word
0	2F94D06B	D06B2F94
1	1D4EE2B1	E2B11D4E
2	70168FE9	8FE97016
3	83527CAD	7CAD8352
4	3678C987	C9873678
5	48D8B727	B72748D8

According to the standard, the sync words of the uplink are the inverted sync words of the downlink.

IEC/IEEE-bus `:SOUR:PDC:SLOT2:SYNC #HE2B11D4F`

SI

Selection of data source for "Sync Information" field. The data in each of the three SI fields are identical

PN.. PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1.
IEC/IEEE `:SOUR:PDC:SLOT2:SOUR:SI PN15`

DLIST Data from a programmable data list.
IEC/IEEE `:SOUR:PDC:SLOT2:SOUR:SI DLIS`

SERDATA Data from data input SER DATA.
IEC/IEEE `:SOUR:PDC:SLOT2:SOUR:SI SDAT`

Q

Display of the tail bits.

PO
(DOWNLINK only)

Input value for the 78 bit "Postamble" field in hexadecimal form.

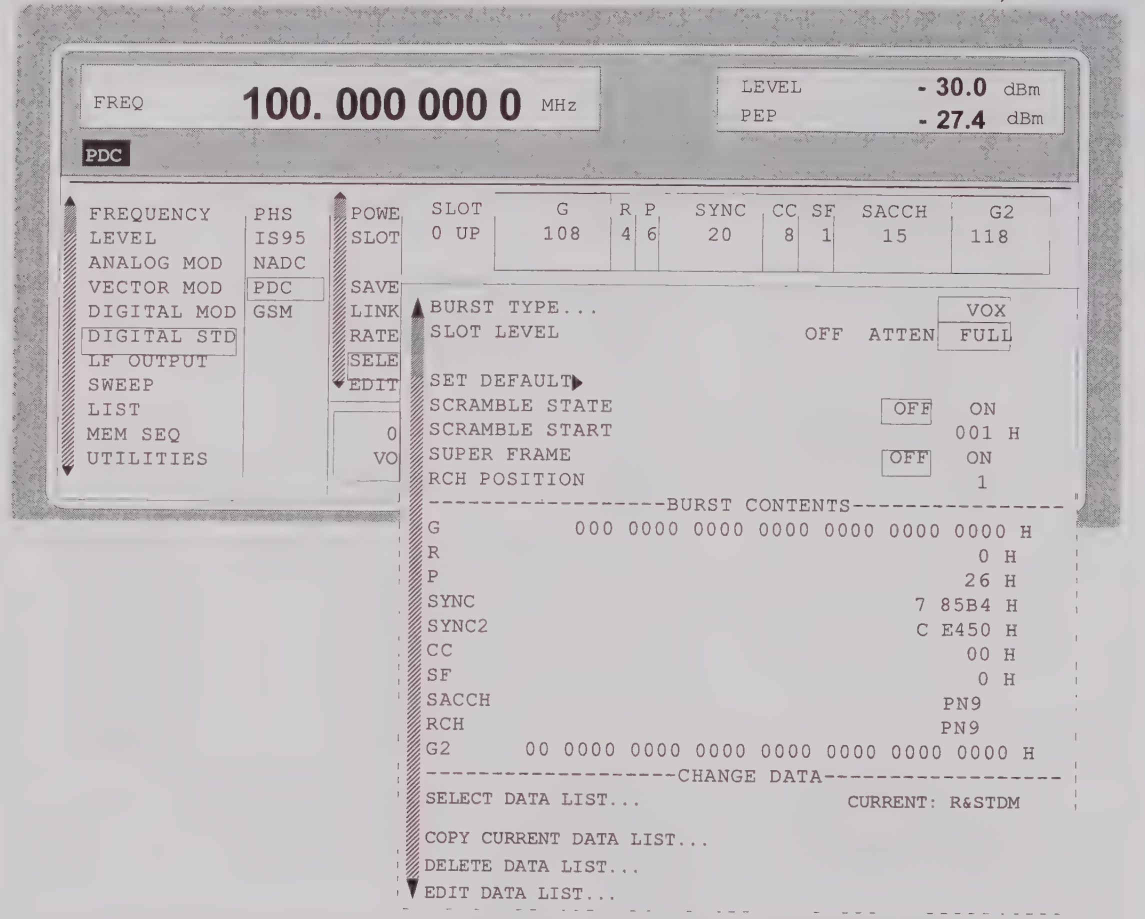
IEC/IEEE-bus command `:SOUR:PDC:SLOT2:POST #HFF`

G2
(UPLINK only)

Display of data contents in the 78 bit "Guard" field in hexadecimal form at the end of the slot.

Parameter **SELECT DATA LIST...** to **EDIT DATA LIST...** see menu selection BURST TYPE = TCH

Menu selection: BURST TYPE = VOX (only available with LINK DIRECTION = UPLINK)


PDC
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
POWE
SLOT
SAVE
LINK
RATE
SELE
EDIT
0
VO
SET DEFAULT
SELECT DATA LIST...
COPY CURRENT DATA LIST...
DELETE DATA LIST...
EDIT DATA LIST...

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 27.4 dBm

SLOT	G	R	P	SYNC	CC	SF	SACCH	G2
0 UP	108	4	6	20	8	1	15	118

BURST TYPE... VOX

SLOT LEVEL OFF ATTEN FULL

SCRAMBLE STATE OFF ON

SCRAMBLE START 001 H

SUPER FRAME OFF ON

RCH POSITION 1

----------------BURST CONTENTS----------------

```
G          000 0000 0000 0000 0000 0000 0000 H
R                                          0 H
P                                         26 H
SYNC                                 7 85B4 H
SYNC2                                C E450 H
CC                                        00 H
SF                                         0 H
SACCH                                    PN9
RCH                                      PN9
G2      00 0000 0000 0000 0000 0000 0000 0000 H
```

----------------CHANGE DATA----------------

SELECT DATA LIST... CURRENT: R&STDM

Fig. 2-194 Menu DIGITAL STD - PDC - SELECT SLOT, LINK DIRECTION = UPLINK, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) VOX (UPLINK ONLY) **BURST TYPE**

Selection of burst type used to configure the selected slot. VOX is only available with LINK DIRECTION = UPLINK selected.

VOX VOX burst

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:TYPE VOX`

SLOT LEVEL

Selection of level for selected slot.

OFF Maximum attenuation

IEC/IEEE `:SOUR:PDC:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.

IEC/IEEE `:SOUR:PDC:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.

IEC/IEEE `:SOUR:PDC:SLOT2:LEV ATT`

(SELECT SLOT...) VOX (UPLINK ONLY)	**SET DEFAULT ▶**	Resets the subsequent parameters to the factory-set values. IEC/IEEE-bus command `SOUR:PDC:SLOT8 PRES`
	SCRAMBLE	Switch on/off of scrambling function. Scrambling with the continuous scramble sequence from a PN9 generator is applied to the data fields SF and SACCH. IEC/IEEE-bus `:SOUR:PDC:SLOT2:SCR:STAT ON`
	SCRAMBLE START	9 bit input value for initializing the scramble PN9 generator in hexadecimal form. The value 0 is not permissible, 1 is preset. The input field is only displayed if SCRAMBLE is switched on. IEC/IEEE-bus `:SOUR:PDC:SLOT2:SCR:STAR #H2`
	SUPERFRAME	Switch on/off of the superframe generation. 18 frames form a superframe. If SUPERFRAME is switched on, RCH data instead of SACCH data will be sent in 2 slots. The first of the two RCH blocks is fixed in the first slot of the superframe, the position of the second can be set. IEC/IEEE-bus `:SOUR:PDC:SLOT2:SFR:STAT ON`
	RCH POSITION	Input value for the position of the second RCH block. The input field is only displayed if SUPERFRAME is switched on. IEC/IEEE-bus `:SOUR:PDC:SLOT2:SFR:RCHP 10`
	G	Display of data contents in the 108 bit "Guard" field at the beginning of the slot in hexadecimal form.
	R	Display of data contents in the 4 bit data field "RAMP" in hexadecimal form.
	P	Input value of the 26 bit data field "Preamble". IEC/IEEE-bus command `:SOUR:PDC:SLOT2:PRE #HFF`
	SYNC	Input value for the 20 bit sync word in hexadecimal form. Depending on the selected slot, the following sync words are predefined according to the PDC standard:

Slot No.	VOX Sync Word
0	785B4
1	62DC9
2	7E28A
3	56B15
4	AE9B3
5	B2621

IEC/IEEE-bus `:SOUR:PDC:SLOT2:SYNC #H72DC9`

(SELECT SLOT...) VOX
(UPLINK ONLY)

SYNC2

Input value for the 20 bit sync word in the first slot of a superframe in hexadecimal form. This sync word marks the beginning of a superframe. The input field is only displayed if SUPERFRAME is switched on. Depending on the selected slot, the following sync words are predefined according to the PDC standard:

Slot No.	VOX Sync2 Word
0	CE450
1	E1A90
2	18ED3
3	043E0
4	7D861

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:SYNC E1A91`

CC

Input value of the 8 bit data field " Color code ".

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:CCOD #HFF`

SF

Input value of the data field " Steal Flag ".

IEC/IEEE-bus command `:SOUR:PDC:SLOT2:SF #H0`

SACCH

Selection of data source for SACCH field (Slow Associated Control Channel).

PN..	PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1. IEC/IEEE `:SOUR:PDC:SLOT3:SACC PN15`
DLIST	Data from a programmable data list. IEC/IEEE `:SOUR:PDC:SLOT3:SACC DLIS`
SERDATA	Data from data input SER DATA. IEC/IEEE `:SOUR:PDC:SLOT3:SACC SDAT`

RCH

Selection of data source for the RCH data field. RCH data replace SACCH data in certain slots of a superframe. The input field is only displayed if SUPERFRAME is switched on.

PN..	PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1. IEC/IEEE `:SOUR:PDC:SLOT3:RCH PN9`
DLIST	Data from a programmable data list. IEC/IEEE `:SOUR:PDC:SLOT3:RCH DLIS`
SERDATA	Data from data input SER DATA. IEC/IEEE `:SOUR:PDC:SLOT3:RCH SDAT`

G2

Display of data contents in the 118 bit "Guard" field at the end of the slot in hexadecimal form. The field is only displayed with LINK DIRECTION = UPLINK selected.

Parameter **SELECT DATA LIST...** to **EDIT DATA LIST...** see menu selection BURST TYPE = TCH

2.18 Digital Standard GSM/EDGE

With the options Modulation Coder (SMIQB20) and Data Generator (SMIQB11) provided, modulation signals according to the GSM standard can be generated. GSM is a TDMA standard for cellular mobile radio networks used worldwide. SMIQ is suitable for use as a signal generator for all GSM types.
With option SMIQB20, the use of GSM and EDGE slots is not limited. GSM/EDGE may also be used with the older modulation coder option (SMIQB10), however in this case GSM and EDGE cannot be mixed.

SMIQ can generate both the transmit signal of a base station (BS) and the transmit signal of a mobile station (MS). Transmission from BS to MS is called "downlink", "uplink" being used for transmission in the opposite direction.

Each TDMA frame consists of 8 slots. Each slot can be switched on or off separately. Moreover, a defined intermediate level can be set.

A burst type has to be defined to configure a slot. The following burst types can be selected:

- NORM the so-called "normal burst",
- DUMMY burst type with a defined data pattern according to GSM standard and
- ALL_DATA burst type for test purposes with freely programmable data contents.
- EDGE the so-called EDGE Normal Burst

The following internal modulation sources are available:

- different PRBS generators with a sequence length between 2^9-1 and 2^{23}-1 and
- data lists, i.e. freely programmable data sequences from the data generator memory.

For generating the GSM signals, SMIQ inserts the modulation data continuously (in real time) into the selected slots. Using a digital signal processor the data generator generates a data sequence with modulation data and control signals for envelope control.

The data generator in SMIQ generates a data stream which is converted into IQ signals in the modulation coder. According to the GSM standard, the modulation type is GMSK at a symbol rate of 270.833 ksymbol/s and Gauss filtering. The symbol rate can be changed in SMIQ. GFSK with adjustable frequency deviation can be used as a modulation type, too.
The modulation type for EDGE slots is 8PSK, with 3/8π rotation at a symbol rate of 270.833 ksymbols/s and linear Gauss filter.

2.18.1 Sync and Trigger Signals

The data generator generates a data sequence with modulation data, control signals for envelope control, and synchronization signals.

When TRIGGER MODE AUTO is selected, the GSM signal generation is started automatically.

This start can also be activated by an external trigger signal (TRIGGER MODE ARMED_AUTO) which allows a synchronous sequence for BER measurements to be carried out on receivers.

A trigger signal can be fed via the TRIGIN input at connector PAR DATA. The active slope of a trigger signal applied there executes a trigger event.
GSM signal generation at a frame limit is started after a trigger event. Data from data lists are inserted into the selected slots starting from the first bit. PRBS generators start with the set initialization status.

Signal generation either starts immediately after the active slope of the trigger signal or after a settable number of symbols (EXT TRIGGER DELAY). Retriggering (EXT RETRIG) can be inhibited for a settable number of symbols (EXT RETRIGGER INHIBIT).

A trigger event can be executed manually using EXECUTE TRIGGER or via the IEC/IEEE bus.

When a trigger event is executed, a trigger signal is output at the TRIGOUT 3 output of SMIQ.

SMIQ also generates the following sync signals:

- a slot or frame clock at TRIGOUT 1 output with settable position in the frame,
- a frame or multiframe clock at TRIGOUT 2 output with settable position in the frame,
- the symbol clock and the bit clock.

A clock synthesizer on the modulation coder generates the symbol clock and the bit clock in SMIQ. The two clocks are identical for GSM. All the clock signals are synchronized to the 10-MHz reference of SMIQ. The bit clock is available at connector BIT CLOCK. If required, the clock synthesizer in SMIQ can be synchronized to an external bit clock.

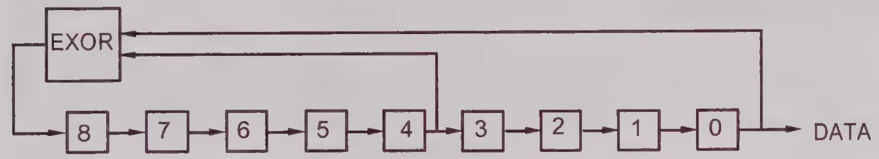
The clock signal is selected in the menu via CLOCK-CLOCK SOURCE EXT.

To allow for a trouble-free synchronization of the clock synthesizer first apply the external clock and set the correct symbol rate at SMIQ. Then switch CLOCK SOURCE from INT to EXT.

Note: *The set symbol rate should not differ by more than 1% from the symbol rate of the external signal.*

2.18.2 PN Generators as Internal Data Source

Independent PN generators (Pseudo Noise) can be selected for each slot as data source for data field DATA. These PN generators provide pseudo-random bit sequences of different length or period. That is why they are also called PRBS generators (Pseudo Random Binary Sequence).
Data sequences are sequences of maximum length which are generated by means of feedback shift registers.
The following figure gives an example of a 9 bit generator with feedbacks after register 4 and 0 (output).



The pseudo-random sequence of a PRBS generator is clearly defined by the number of registers and the feedback. The following table describes all PRBS generators available:

Table 2-31 PRBS generators for GSM

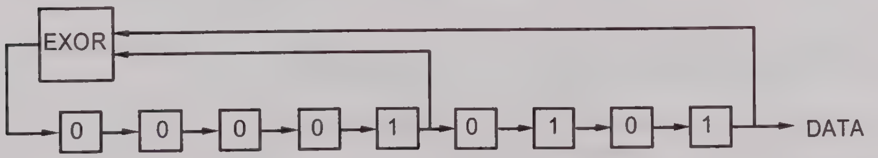
PRBS generator	**Length in bits**	**Feedback after**
9 bit	$2^9 -1 = 511$	Register 4, 0
11 bit	$2^{11} -1 =2047$	Register 2, 0
15 bit	$2^{15} -1 = 32767$	Register 1, 0
16 bit	$2^{16} -1 = 65535$	Register 5, 3, 2, 0
20 bit	$2^{20} -1 = 1048575$	Register 3, 0
21 bit	$2^{21} -1 = 2097151$	Register 2, 0
23 bit	$2^{23} -1 = 8388607$	Register 5, 0

PN generators PN9,11,15, 20 and PN23 are configured according to CCITT Rec. 0.151/152/153. The output sequence is inverted for generators PN15 and PN23.

The start value of the PN generators is different in the slots and equals

$$\text{start value} = 1 + 14\,\text{hex} \times \text{slot number}$$

Example: PN9 generator in slot 1 with start value 15hex = 10101 binary.



The resulting output sequence is `10101000000101001010111110010` etc.

2.18.3 Lists as Internal Data Source

A freely programmable memory on the data generator serves as internal data source for the data fields of the slots. The data are managed in so-called lists. A list editor allows data lists (DATA LIST) to be selected, copied, modified, and deleted.
The list editor is available via menu DIGITAL-MOD - SOURCE....

2.18.4 External Modulation Data

External data can (only) be applied via the SERDATA interface. A selection of SERDATA as data source is only possible for a single data field of a slot. For further information on the characteristics of the SERDATA interface see Annex A.

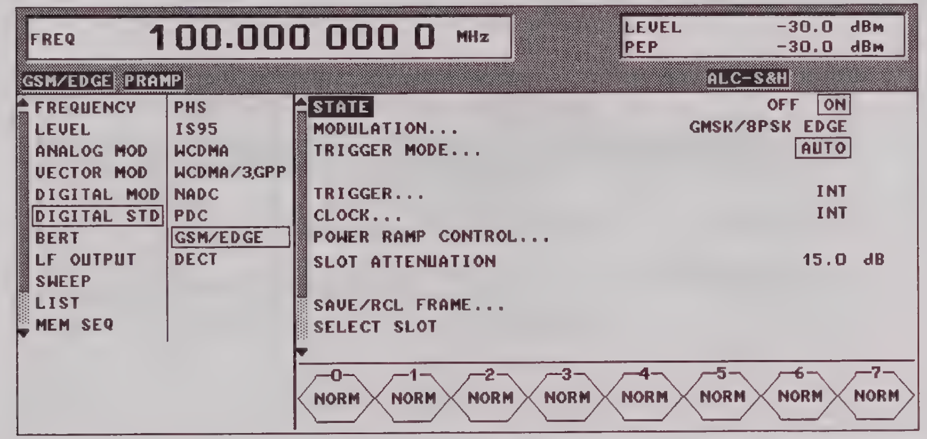
To ensure that the external data bits are assigned to specific positions in the data field of the selected slot and that they are reproducible, the buffer of the RS-232 transmitter and receiver has to be deleted. A triggered start has to follow.
The following setting sequence is required in the DIGITAL STD - GSM/EDGE menu:

1. Carry out desired settings in menu.
2. Select data source SERDATA for the data field of the slot using SELECT SLOT - ....
3. Make connection to external data source, but do not yet start external data source.
4. Switch off digital standard using STATE - OFF.
5. Set TRIGGER MODE ARMED_AUTO.
   In this state, SMIQ is ready for reception, but discards data that are read in via SERDATA.
6. Switch on digital standard with STATE ON.
7. Start external data source.
   The read-in data are written into the receiving buffer. Only if this buffer is filled can SMIQ react to a trigger event.
8. Activate trigger event. Signal generation is thus started at a frame limit. The first bit received via SERDATA is put to the first bit position in the selected data field.

2.18.5 Menu DIGITAL STANDARD - GSM/EDGE

Menu DIGITAL STD - GSM/EDGE provides access to settings for generating GSM signals.

Menu selection: DIGITAL STD - GSM/EDGE


GSM/EDGE
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
0 NORM
1 NORM
2 NORM
3 NORM
4 NORM
5 NORM
6 NORM
7 NORM

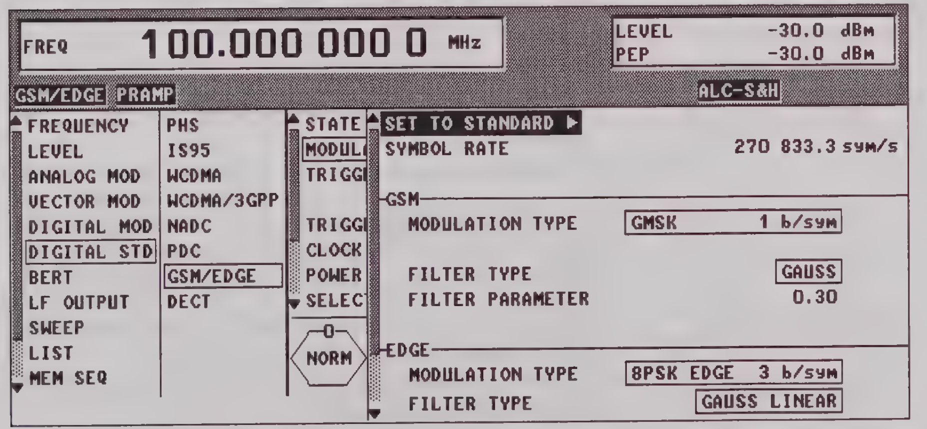
FREQ 100.000 000 0 MHz
LEVEL -30.0 dBm
PEP -30.0 dBm
STATE OFF ON
MODULATION... GMSK/8PSK EDGE
TRIGGER MODE... AUTO
TRIGGER... INT
CLOCK... INT
POWER RAMP CONTROL...
SLOT ATTENUATION 15.0 dB
SAVE/RCL FRAME...
SELECT SLOT

Fig. 2-195 Menu DIGITAL STD - GSM/EDGE, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

STATE Switch on/off of Digital Standard GSM/EDGE modulation. Vector Modulation or Digital Modulation will be switched off automatically.

IEC/IEEE-bus command `SOUR:GSM:STAT ON`

MODULATION... Opens a window for setting the modulation parameters.


GSM/EDGE
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
STATE
MODUL
TRIGG
TRIGG
CLOCK
POWER
SELEC
0 NORM
SET TO STANDARD ▶

FREQ 100.000 000 0 MHz
LEVEL -30.0 dBm
PEP -30.0 dBm
SYMBOL RATE 270 833.3 sym/s
GSM
MODULATION TYPE GMSK 1 b/sym
FILTER TYPE GAUSS
FILTER PARAMETER 0.30
EDGE
MODULATION TYPE 8PSK EDGE 3 b/sym
FILTER TYPE GAUSS LINEAR

Fig. 2-196 Menu DIGITAL STD - GSM/EDGE - MODULATION..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

MODULATION...

SET TO STANDARD ▶ Sets the subsequent modulation parameters to the values predefined by the standard.

SYMBOL RATE Input value for the symbol clock. 270.833 ksymbol/s are preset.

IEC/IEEE-bus `:SOUR:GSM:SRAT 270KHZ`

(MODULATION...) **The following parameters are used with GSM**

MODULATION TYPE Opens a window for selection of the modulation type.

GMSK	Gaussian Minimum Shift Keying
GFSK	Gaussian filtered Frequency Shift Keying

IEC/IEEE-bus `SOUR:GSM:FORMat GMSK`

FSK DEVIATION Input value of deviation with GFSK selected. With GMSK selected, the deviation is fixed to the fourth part of the symbol rate.

IEC/IEEE-bus `SOUR:GSM:FSK:DEV 67 KHZ`

FILTER TYPE Display of the baseband filter for GSM.

FILTER PARAMETER Input value for the B×T value (Gaussian filter).

IEC/IEEE-bus `SOUR:GSM:FILT:PAR 0.31`

The following parameters are used with EDGE

MODULATION TYPE The modulation type for EDGE is displayed. It is permanently set to 8PSK EDGE. In contrast to the modulation types for GSM, the one for EDGE has three bits per symbol.

FILTER TYPE The filter for EDGE is displayed. It is permanently set to GAUSS LINEAR.

TRIGGER MODE... Opens a window for selecting the trigger mode.

AUTO The GSM signals are continuously transmitted in the activated slots.

IEC/IEEE-bus command `SOUR:GSM:SEQ AUTO`

RETRIG The GSM signals are continuously transmitted in the activated slots. A trigger event causes a restart.

IEC/IEEE-bus command `SOUR:GSM:SEQ RETR`

ARMED_AUTO The GSM signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the AUTO mode and can no longer be triggered.

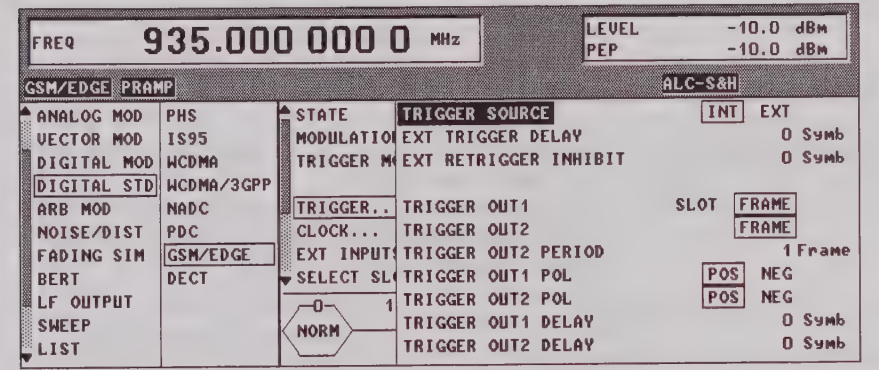
IEC/IEEE-bus command `SOUR:GSM:SEQ AAUT`

ARMED_RETRIG The GSM signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the RETRIG mode. Each new trigger event causes a restart.

IEC/IEEE-bus command `SOUR:GSM:SEQ ARET`

EXECUTE TRIGGER ▶ Executes a trigger even to start the GSM signal generation.
IEC/IEEE-bus command `:TRIG:DM:IMM`

TRIGGER... Opens a window for selecting the trigger source, for configuring the trigger output signals and for setting the time delay of an external trigger signal.



FREQ 935.000 000 0 MHz
LEVEL -10.0 dBm
PEP -10.0 dBm

TRIGGER SOURCE INT EXT
EXT TRIGGER DELAY 0 Symb
EXT RETRIGGER INHIBIT 0 Symb
TRIGGER OUT1 SLOT FRAME
TRIGGER OUT2 FRAME
TRIGGER OUT2 PERIOD 1 Frame
TRIGGER OUT1 POL POS NEG
TRIGGER OUT2 POL POS NEG
TRIGGER OUT1 DELAY 0 Symb
TRIGGER OUT2 DELAY 0 Symb

GSM/EDGE
PRAMP
ALC-S&H
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
NOISE/DIST
FADING SIM
BERT
LF OUTPUT
SWEEP
LIST
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
STATE
MODULATIO
TRIGGER M
TRIGGER..
CLOCK...
EXT INPUT
SELECT SL
0
1
NORM

Fig. 2-197 Menu DIGITAL STD - GSM/EDGE_TRIGGER..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(TRIGGER...) **TRIGGER SOURCE** Selection of trigger source.

EXT The GSM signal generation is started by the active slope of an external trigger signal.
The polarity, the trigger threshold and the input resistance of the TRIGIN input can be modified in menu DIGITAL MOD - EXT INPUTS.

INT A trigger event can be executed by EXECUTE TRIGGER ▶.

IEC/IEEE-bus `SOUR:GSM:TRIG:SOUR EXT`

EXT TRIGGER DELAY Setting the number of symbols by which an external trigger signal is delayed before it starts the GSM signal generation.
This is used for setting the time synchronization between the SMIQ and the DUT.

IEC/IEEE-bus `SOUR:GSM:TRIG:DEL 3`

(TRIGGER...) **EXT RETRIGGER INHIBIT** Setting the number of symbols for which a restart is inhibited after a trigger event.
With TRIGGER MODE RETRIG selected, each new trigger signal restarts the GSM signal generation. This restart can be inhibited for the entered number of symbols.

Example:
The entry of 1000 symbols causes new trigger signals to be ignored for the duration of 1000 symbols after a trigger event

IEC/IEEE-bus `SOUR:GSM:TRIG:INH 1000`

TRIGGER OUT 1

Selecting the signal for TRIGOUT 1 output in PARDATA connector.
The times only apply if the default value of 270.833 ksps for GSM/EDGE is used as the symbol rate in the Modulation menu.

SLOT	0.577 ms e.g. clock IEC/IEEE-bus command: `:SOUR:GSM:TRIG:OUTP1 SLOT`
FRAME	4.615 ms frame clock IEC/IEEE-bus command: `:SOUR:GSM:TRIG:OUTP1 FRAM`

TRIGGER OUT 2

Selecting the signal for TRIGOUT 2 output in PARDATA connector.
The times only apply if the default value of 270.833 ksps for GSM/EDGE is used as the symbol rate in the Modulation menu.

FRAME	4.615 ms frame clock IEC/IEEE-bus command: `:SOUR:GSM:TRIG:OUTP2 FRAM`

TRIGGER OUT 1/2 DELAY

Input value of delay of trigger signal at TRIGOUT 2 output compared with beginning of slot or frame.

IEC/IEEE-bus `SOUR:GSM:TRIG:OUTP:DEL 2`

TRIGGER OUT2 PERIOD

Input value of output signal period at TRIGOUT 2 output given in frames.

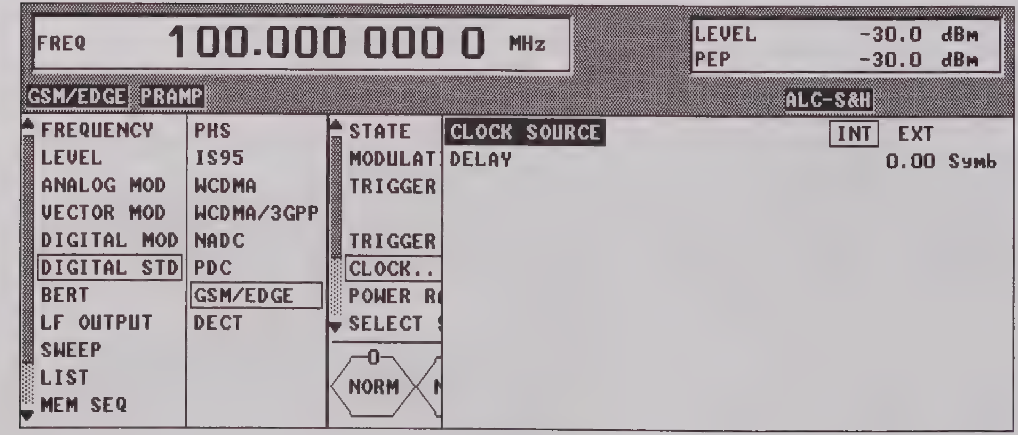
IEC/IEEE-bus `SOUR:GSM:TRIG:OUTP:PER 1`

TRIGGER OUT 1/2 POL

Selecting the polarity of signals at the TRIGOUT 1 and TRIGOUT 2 outputs in the PARDATA connector.

IEC/IEEE-bus command
`:SOUR:GSM:TRIG:OUTP2:POL POS`

CLOCK... Opens a window for selecting the clock source and for setting a delay.



FREQ 100.000 000 0 MHz

LEVEL -30.0 dBm

PEP -30.0 dBm

CLOCK SOURCE [✓ INT] EXT

DELAY 0.00 Symb

GSM/EDGE
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
STATE
MODULAT
TRIGGER
TRIGGER
CLOCK..
POWER R
SELECT
0
NORM

Fig. 2-198 Menu DIGITAL STD - GSM/EDGE - CLOCK..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(CLOCK...)

CLOCK SOURCE Selection of clock source.

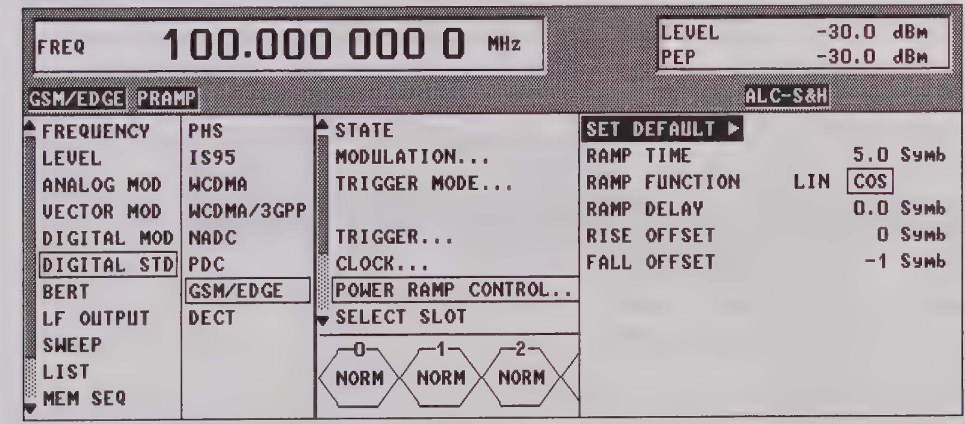
INT	SMIQ uses internally generated clock signals.
EXT	An external bit clock is fed in at connector BIT CLOCK. The clock synthesizer on the modulation coder is synchronized to this clock. The symbol rate has to be set with an accuracy of ± 1 %. The polarity, the trigger threshold and the input resistance of the clock inputs can be modified in menu DIGITAL MOD - EXT INPUTS.

IEC/IEEE-bus `SOUR:GSM:CLOC:SOUR INT`

DELAY Setting the delay of generated modulation signal to an external clock.
This can be used, for example, for synchronization with a second unit to achieve time synchronization between the modulation signals of the two units.

IEC/IEEE-bus `SOUR:GSM:CLOC:DEL 0.5`

POWER RAMP CONTROL... Opens a window for setting the envelope control, especially for the rising and falling ramp at the beginning and end of a slot.


FREQ 100.000 000 0 MHz
LEVEL -30.0 dBm
PEP -30.0 dBm
RAMP TIME 5.0 Symb
RAMP FUNCTION LIN COS
RAMP DELAY 0.0 Symb
RISE OFFSET 0 Symb
FALL OFFSET -1 Symb

GSM/EDGE
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
STATE
MODULATION...
TRIGGER MODE...
TRIGGER...
CLOCK...
POWER RAMP CONTROL..
SELECT SLOT
0
1
2
NORM
NORM
NORM
SET DEFAULT ▶

Fig. 2-199 Menu DIGITAL STD - GSM/EDGE - POWER RAMP CONTROL... , SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(POWER RAMP CONTROL...)

SET DEFAULT ▶ Resets the subsequent parameters to the factory-set values.

IEC/IEEE-bus `:SOUR:GSM:PRAM:PRES`

RAMP TIME Input value for the rise and fall time of the envelope at the beginning or end of a slot. The time is set in units of symbol period.

IEC/IEEE-bus `SOUR:GSM:PRAM:TIME 2.5`

(POWER RAMP CONTROL...)

RAMP FUNCTION

Selection of shape of rising and falling ramp for envelope control.

LIN	Linear ramp function.
COS	Cosine function. A more favourable spectrum than that of the LIN function is obtained.

IEC/IEEE-bus `SOUR:GSM:PRAM:SHAP LIN`

RAMP DELAY

Input value for a shift of the envelope characteristic to the modulated signal. A positive value causes a delay of the envelope. The values are set in the units of the symbol length.

IEC/IEEE-bus `SOUR:GSM:PRAM:DEL 0.1`

RISE OFFSET

Input value for a positive or negative offset of the rising ramp of the envelope at the beginning of a slot.

IEC/IEEE-bus `SOUR:GSM:PRAM:ROFF -1`

FALL OFFSET

Input value for a positive or negative offset of the falling ramp of the envelope at the end of a slot.

IEC/IEEE-bus `SOUR:GSM:PRAM:FOFF 1`

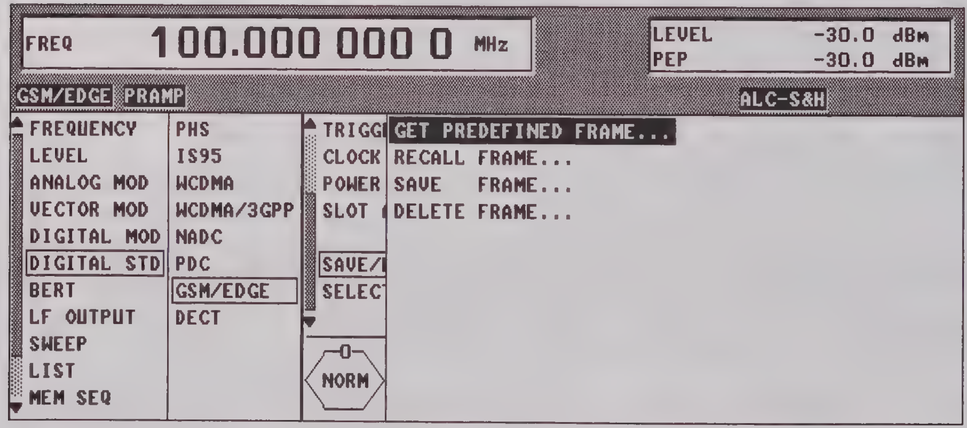
SLOT ATTENUATION

Input value in dB for the level reduction of all active slots whose SLOT LEVEL was set to ATTEN. Menu SELECT SLOT allows the slots to be determined whose level is to be reduced.

IEC/IEEE-bus command `SOUR:GSM:SLOT:ATT 40 DB`

SAVE/RCL FRAME...

Opens a window for saving and loading a frame configuration. Loading a frame affects all parameters that can be set under SELECT SLOT.



FREQ 100.000 000 0 MHz

LEVEL -30.0 dBm

PEP -30.0 dBm

GSM/EDGE
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
TRIGG
CLOCK
POWER
SLOT
SAVE/
SELEC
NORM
GET PREDEFINED FRAME...
RECALL FRAME...
SAVE FRAME...
DELETE FRAME...

Fig. 2-200 Menu DIGITAL STD - GSM/EDGE - SAVE/RCL FRAME, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SAVE/RCL FRAME...) **GET PREDEFINED FRAME...**

Loads a factory-set frame configuration.

NB0 Normal Burst in slot 0

IEC/IEEE-bus `:SOUR:GSM:FLIS:PRED:LOAD "nb0"`

RECALL FRAME...

Loads a frame configuration saved by the user.

IEC/IEEE-bus `:SOUR:GSM:FLIS:LOAD "name"`

SAVE FRAME...

Saves a user-defined frame configuration.

IEC/IEEE-bus `:SOUR:GSM:FLIS:STOR "name"`

DELETE FRAME...

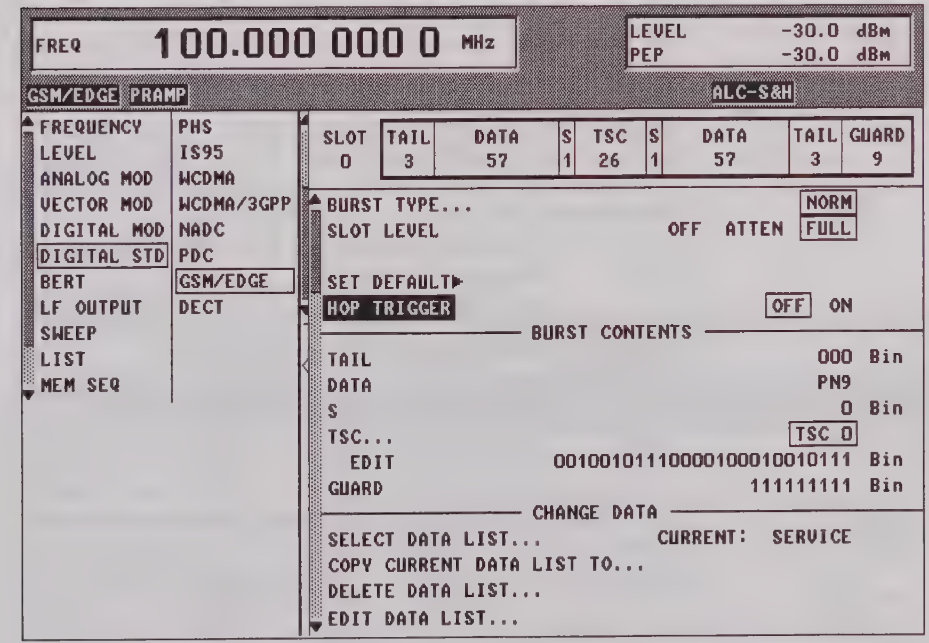
Deletes a frame configuration saved by the user.

IEC/IEEE-bus `:SOUR:GSM:FLIS:DEL "name"`

SELECT SLOT...

Selection of one of 8 possible slots. When selecting the slot, a window is opened in which the data contents belonging to this slot can be defined.
If the cursor is placed onto a slot in the diagram, it may be switched on and off by pressing one of the unit keys (toggle function).

Menu selection: BURST TYPE = NORM


GSM/EDGE
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
SET DEFAULT▸
BURST CONTENTS
CHANGE DATA
COPY CURRENT DATA LIST TO...
DELETE DATA LIST...
EDIT DATA LIST...

FREQ 100.000 000 0 MHz
LEVEL -30.0 dBm
PEP -30.0 dBm

SLOT	TAIL	DATA	S	TSC	S	DATA	TAIL	GUARD
0	3	57	1	26	1	57	3	9

BURST TYPE... NORM
SLOT LEVEL OFF ATTEN FULL
HOP TRIGGER OFF ON
TAIL 000 Bin
DATA PN9
S 0 Bin
TSC... TSC 0
EDIT 00100101110000100010010111 Bin
GUARD 111111111 Bin
SELECT DATA LIST... CURRENT: SERVICE

Fig. 2-201 Menu DIGITAL STD - GSM/EDGE - SELECT SLOT - NORM, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) NORM

BURST TYPE

Opens a window for the selection of the burst type used to configure the selected slot.

NORM Normal Burst

IEC/IEEE `:SOUR:GSM:SLOT2:TYPE NORM`

SLOT LEVEL

Selection of level for selected slot.

OFF Maximum attenuation

IEC/IEEE `:SOUR:GSM:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.

IEC/IEEE `:SOUR:GSM:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.

IEC/IEEE `:SOUR:GSM:SLOT2:LEV ATT`

SET DEFAULT ▶

Resets the subsequent parameters to the factory-set values.

IEC/IEEE-bus command `:SOUR:GSM:SLOT8:PRES`

HOP TRIGGER

Switches the HOP trigger signal on or off. With ON selected, a HOP signal will be generated at the end of the selected slot. The signal is available at the HOP output of the PARDATA connector. It can be used to perform a frequency hop mode in the LIST MODE (see section 'List Mode').

IIEEE-bus command `SOUR:GSM:SLOT1:HOPP:TRIG`

TAIL

Display of data contents in the 3 bit data field "Tail". The tail bits are set to 000 according to the GSM standard.

DATA

Selection of data source for DATA fields. These fields are regarded as a continuous field, i.e. a pseudo-random sequence is fully continued from one DATA field to the next. The following data sources are available:

PN.. PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1.

IEC/IEEE `:SOUR:GSM:SLOT3:DATA PN15`

DLIST Data from a programmable data list.

IEC/IEEE `:SOUR:GSM:SLOT3:DATA DLIS`

SERDATA Data from data input SER DATA.

IEC/IEEE `:SOUR:GSM:SLOT3:DATA SDAT`

S

Setting for the so-called "Stealing Flag". The selected setting is valid for the two S fields.

IEC/IEEE-bus command `SOUR:GSM:SLOT2:SF 1`

TSC...

Opens a window for selecting the so-called "Training Sequence Code". A selection between 8 different training sequences is possible.

IEC/IEEE-bus command `SOUR:GSM:SLOT2:TSC 0`

(SELECT SLOT...) NORM

EDIT

Input field for binary modifications of the training sequence. A modified training sequence is stored as a USER sequence after a SAVE FRAME has been called up.

IEC/IEEE-bus command
`SOUR:GSM:SLOT2:USER #B011011...`

GUARD

Display of data content in the "Guard" field in binary form. The length of the field is 8 bit in slots 1,2,3,5,6,7 and 9 bit in slots 0 and 4. It is thus ensured that a frame has exactly 1250 bit as stipulated in the GSM standard.

SELECT DATA LIST...

Opens a window for selecting a stored data list or for generating a new list.

COPY CURRENT DATA LIST TO...

Stores the current data list under a different name.

DELETE DATA LIST...

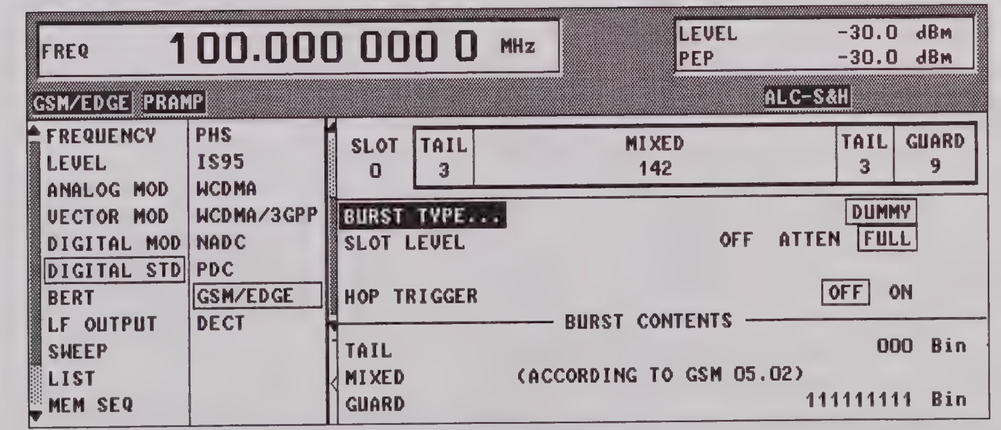
Deletes a data list.

EDIT DATA LIST...

Opens a window for editing a data list bit-by-bit. The available storage capacity and the length of the current list is displayed in parameters FREE and LEN (see also Section List Editor).

COPY	Copies a list range
FILL	Fills the range with filler pattern
INSERT	Inserts a list range at a different position of the list
DELETE	Deletes a list range
EDIT/VIEW	Edits or views the list

Menu selection: BURST TYPE = DUMMY


GSM/EDGE
PRAMP
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
PHS
IS95
WCDMA
WCDMA/3GPP
NADC
PDC
GSM/EDGE
DECT
SLOT 0
TAIL 3
MIXED 142
TAIL 3
GUARD 9
BURST CONTENTS

FREQ 100.000 000 0 MHz
LEVEL -30.0 dBm
PEP -30.0 dBm
BURST TYPE... DUMMY
SLOT LEVEL OFF ATTEN FULL
HOP TRIGGER OFF ON
TAIL 000 Bin
MIXED (ACCORDING TO GSM 05.02)
GUARD 111111111 Bin

Fig. 2-202 Menu DIGITAL STD - GSM/EDGE - SELECT SLOT - DUMMY, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) DUMMY

BURST TYPE

Opens a window for the selection of the burst type used to configure the selected slot.

DUMMY Synchronization Burst

IEC/IEEE-bus command `SOUR:GSM:SLOT2:TYPE DUMM`

SLOT LEVEL

Selection of level for selected slot.

OFF	Maximum attenuation IEC/IEEE `:SOUR:GSM:SLOT2:LEV OFF`
FULL	The level corresponds to the value indicated on the SMIQ LEVEL display. IEC/IEEE `:SOUR:GSM:SLOT2:LEV FULL`
ATTEN	The level is reduced by the value set under SLOT ATTENUATION. IEC/IEEE .....`:SOUR:GSM:SLOT2:LEV ATT`

TAIL

Display of data content in the 3 bit data field "Tail ". The tail bits are set to 000 according to the GSM standard.

MIXED

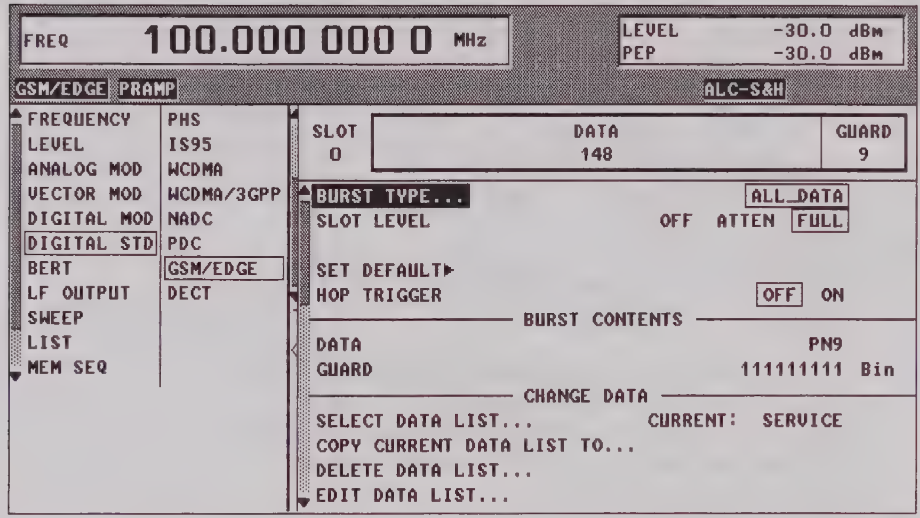
Display of data content of the mixed-bit field. It has a data content stipulated by GSM 05.02, the so-called mixed bits":

11111011011101100000101001001110000010010001000000
01111100011100010111000101110001010111010010100011
001100111001111010011111000100101111101010

GUARD

Display of data content in the Guard field in binary form. The length of the field is 8 bit in slots 1,2,3,5,6,7 and 9 bit in slots 0 and 4. It is thus ensured that a frame has exactly 1250 bit as stipulated in the GSM standard.

Menu selection: BURST TYPE = ALL_DATA



FREQ 100.000 000 0 MHz

LEVEL -30.0 dBm

PEP -30.0 dBm

BURST TYPE... ALL_DATA

SLOT LEVEL OFF ATTEN FULL

SET DEFAULT▶

HOP TRIGGER OFF ON

BURST CONTENTS

DATA PN9

GUARD 111111111 Bin

CHANGE DATA

SELECT DATA LIST... CURRENT: SERVICE

COPY CURRENT DATA LIST TO...

DELETE DATA LIST...

EDIT DATA LIST...

Fig. 2-203 Menu DIGITAL STD - GSM/EDGE - SELECT SLOT – ALL_DATA, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) ALL_DATA

BURST TYPE Opens a window for the selection of burst type used to configure the selected slot.

ALL_DATA Burst type for testing with freely programmable data contents.
IEC/IEEE `:SOUR:GSM:SLOT2:TYPE ADAT`

SLOT LEVEL Selection of level for selected slot.

OFF Maximum attenuation
IEC/IEEE `:SOUR:GSM:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.
IEC/IEEE `:SOUR:GSM:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.
IEC/IEEE `:SOUR:GSM:SLOT2:LEV ATT`

DATA Selection of data source for DATA fields. These fields are regarded as a continuous field, i.e. a pseudo-random sequence is fully continued from one DATA field to the next. The following data sources are available:

PN.. PRBS data according to CCITT with periods between 2^9-1 and 2^{23}-1.
IEC/IEEE `:SOUR:GSM:SLOT3:DATA PN15`

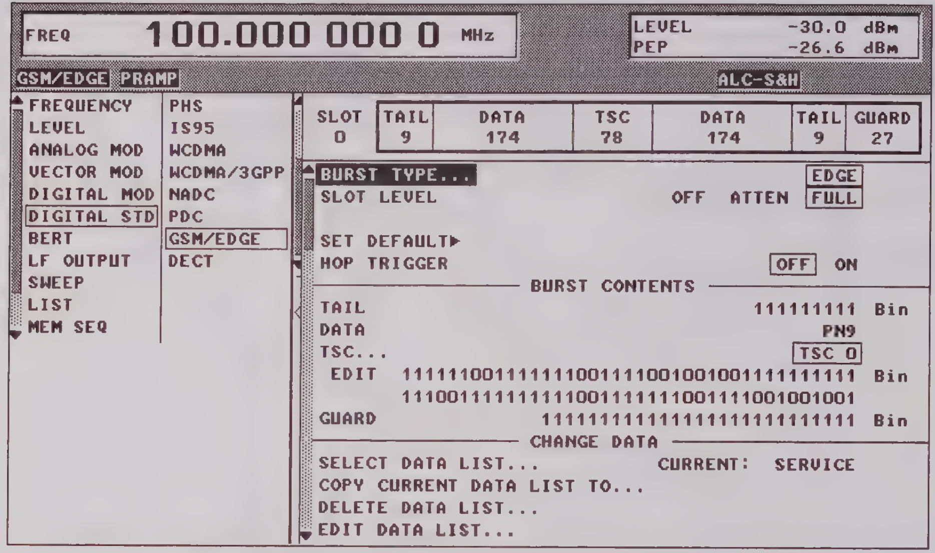
DLIST Data from a programmable data list.
IEC/IEEE `:SOUR:GSM:SLOT3:DATA DLIS`

SERDATA Data from data input SER DATA.
IEC/IEEE `:SOUR:GSM:SLOT3:DATA SDAT`

GUARD Display of data content in the Guard field in binary form. The length of the field is 8 bit in slots 1,2,3,5,6,7 and 9 bit in slots 0 and 4. It is thus ensured that a frame exactly has 1250 bit as stipulated in the GSM standard.

Parameter **SELECT DATA LIST...** to **EDIT DATA LIST...** see menu selection BURST TYPE = NORM

Selection: BURST TYPE = EDGE



FREQ 100.000 000 0 MHz

LEVEL -30.0 dBm

PEP -26.6 dBm

GSM/EDGE PRAMP ALC-S&H

SLOT 0 | TAIL 9 | DATA 174 | TSC 78 | DATA 174 | TAIL 9 | GUARD 27

BURST TYPE... EDGE

SLOT LEVEL OFF ATTEN FULL

SET DEFAULT▶

HOP TRIGGER OFF ON

BURST CONTENTS

TAIL 111111111 Bin

DATA PN9

TSC... TSC 0

EDIT 111111001111111001111001001001111111111 Bin
111001111111111001111111001111001001001

GUARD 111111111111111111111111111 Bin

CHANGE DATA

SELECT DATA LIST... CURRENT: SERVICE

COPY CURRENT DATA LIST TO...

DELETE DATA LIST...

EDIT DATA LIST...

Fig. 2-204 Menu DIGITAL STD - GSM/EDGE - SELECT SLOT – EDGE, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT...) EDGE

BURST TYPE

Opens a window for selecting the burst type to be used for configuring the selected slot.

EDGE EDGE Normal Burst

IEC/IEEE bus `:SOUR:GSM:SLOT2:TYPE EDGE`

SLOT LEVEL

Selection of the level for the selected slot.

OFF Maximum attenuation

IEC/IEEE bus `:SOUR:GSM:SLOT2:LEV OFF`

FULL The level is the same as that indicated in the LEVEL display of SMIQ.

IEC/IEEE bus `:SOUR:GSM:SLOT2:LEV FULL`

ATTEN The level is reduced by the value set under SLOT ATTENUATION.

IEC/IEEE bus `:SOUR:GSM:SLOT2:LEV ATT`

SET DEFAULT ▶

Resets the following parameters to the factory-selected values.

IEC/IEEE bus command `:SOUR:GSM:SLOT1:PRES`

HOP TRIGGER

Switches the HOP trigger signal on or off. When set to ON, a HOP signal is generated at the end of the selected slot. The signal is available at the HOP output of the PARDATA connector. It may be used for implementing frequency hopping in the LIST MODE (see section "List Mode").

IEC/IEEE-bus `:SOUR:GSM:SLOT1:HOPP:TRIG`

TAIL

Display of data contents in the 9-bit data field "Tail". The tail bits are permanently set to 111111111 in line with the GSM standard.

(SELECT SLOT...) EDGE

DATA

Selection of the data source for the DATA fields. These fields are considered as one continuous field, i.e. a pseudo random sequence continues seamlessly from one DATA field to the next. The following data sources can be chosen:

PN..	PRBS data according to CCITT with period lengths between 2^9-1 and 2^{23}-1. IEC/IEEE `:SOUR:GSM:SLOT3:DATA PN15`
DLIST	Data from a programmable data list. IEC/IEEE `:SOUR:GSM:SLOT3:DATA DLIS`
SERDATA	Data via the data input SER DATA. IEC/IEEE `:SOUR:GSM:SLOT3:DATA SDAT`

TSC...

Opens a window for selecting the so-called "training sequence code". One of eight different training sequences can be chosen.

IEC/IEEE-bus command `:SOUR:GSM:SLOT2:TSC 0`

EDIT Field for entering changes to the training sequence in binary form. A modified training sequence is saved as USER sequence after SAVE FRAME was called.

IEC/IEEE bus command
`:SOUR:GSM:SLOT2:USER #B011011...`

GUARD

Display of data contents in the GUARD field in binary form. The length of the field is 24 bits in slots 1, 2, 3, 5, 6, 7, and 27 bits in slots 0 and 4.

SELECT DATA LIST...

Opens a window for selecting a stored data list or generating a new list.

COPY CURRENT DATA LIST TO...

Saves the current data list under a different name.

DELETE DATA LIST...

Deletes a data list.

EDIT DATA LIST...

Opens a window for editing a data list bit by bit. The parameters FREE and LEN indicate the free memory capacity and the length of the current list (see also chapter 2, section "List Editor").

COPY	Copying part of a list
FILL	Filling a part of the list with a pattern
INSERT	Inserting a part of the list at a different position within the list
DELETE	Deleting a part of the list
EDIT/VIEW	Editing or viewing the list

2.19 Digital Standard DECT

With the options Modulation Coder (SMIQB20) and Data Generator (SMIQB11) provided, modulation signals according to the ETSI DECT standard can be generated. DECT is a TDMA standard for private and public cordless phones.

SMIQ can generate both the transmit signal of a cell station (FP, Fixed Part) and the transmit signal of a personal station (PP, Portable Part). Transmission from FP to PP is called "downlink", "uplink" being used for transmission in the opposite direction.

Uplink and downlink are transmitted in the separate time slots of a frame using the time duplex method. Each frame consists of 24 slots. The data contents of each slot can be defined individually by SMIQ by means of a slot editor. Each slot can be switched on or off. A defined intermediate level can also be set. A maximum of 12 slots within a frame can be switched on simultaneously.

A slot type has to be defined to configure a slot. The following slot types can be selected:

• FULL	full slot; simulation of a basic R32 physical channel,
• DOUBLE	double slot, simulation of a high capacity R80 physical channel and
• ALL_DATA	slot type for test purposes with arbitrarily programmable data contents in full slot format.

The following internal modulation sources are available:

- different PRBS generators with a sequence length between 2^9-1 and 2^{23}-1 and
- data lists, i.e. freely programmable data sequences from the data generator memory.

For generating DECT signals, SMIQ inserts the modulation data continuously (in real time) into the selected slots. Using a digital signal processor the data generator generates a data sequence with modulation data and control signals for envelope control.

The data generator in SMIQ generates a data stream which is converted into IQ signals in the modulation coder. According to the DECT standard, the default modulation type is GFSK with a symbol rate of 1152 ksymbols/s and Gauss filtering. Symbol rate and filtering can be changed in SMIQ. Alternatively, π/4 DQPSK with √cos filtering may be selected.

2.19.1 Sync and Trigger Signals

The data generator generates a data sequence with modulation data, control signals for envelope control, and synchronization signals.

When TRIGGER MODE AUTO is selected, the DECT signal generation starts automatically.

This start can also be activated by an external trigger signal (TRIGGER MODE ARMED_AUTO) which allows a synchronous sequence for BER measurements to be carried out on receivers.

Trigger signals for synchronized sequences can be used for measuring the bit error rate of receivers. A trigger signal can be fed via the TRIGIN input at connector PAR DATA. The active slope of a trigger signal applied there executes a trigger event.
DECT signal generation at a frame limit is started after a trigger event. Data from data lists are inserted into the selected slots starting from the first bit. PRBS generators start with the set initialization status.

Signal generation either starts immediately after the active slope of the trigger signal or after a settable number of symbols (EXT TRIGGER DELAY). Retriggering (RETRIG) can be inhibited for a settable number of symbols (EXT RETRIGGER INHIBIT).

A trigger event can be executed manually or via the IEC/IEEE bus using EXECUTE TRIGGER.

When a trigger event is executed, a trigger signal is output at the TRIGOUT 3 output of SMIQ.

SMIQ also generates the following sync signals:

- a frame clock at TRIGOUT 1 output,
- a frame or multiframe clock at TRIGOUT 2 output with settable position in the frame,
- the symbol clock and the bit clock.

A clock synthesizer on the modulation coder generates the symbol clock and the bit clock in SMIQ. All clock signals are synchronized to the 10-MHz reference of SMIQ. The bit clock is available at connector BIT CLOCK. If desired, the clock synthesizer in SMIQ can be synchronized to an external bit clock.

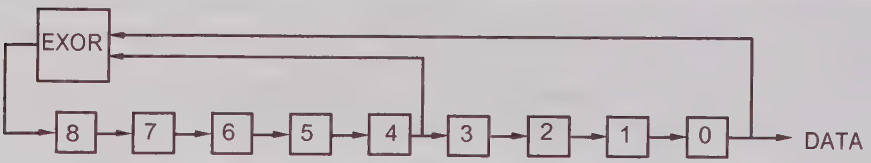
The clock source is selected in the CLOCK-CLOCK SOURCE EXT menu.

To allow for a trouble-free synchronization of the clock synthesizer first apply the external clock and set the correct symbol rate at SMIQ. Then switch CLOCK SOURCE from INT to EXT.

Note: *The set symbol rate should not differ by more than 1% from the symbol rate of the external signal.*

2.19.2 PN Generators as Internal Data Source

Independent PN generators (Pseudo Noise) can be selected for each slot as data sources for the data fields A-FIELD, B-FIELD and DATA. These PN generators provide pseudo-random bit sequences of different length or period. That is why they are also called PRBS generators (Pseudo Random Binary Sequence).
Data sequences are sequences of maximum length which are generated by means of feedback shift registers.
The following figure gives an example of a 9-bit generator with feedbacks after register 4 and 0 (output).



The pseudo-random sequence of a PRBS generator is clearly defined by the number of registers and the feedback. The following table describes all PRBS generators available:

Table 2-32 PRBS generators for DECT

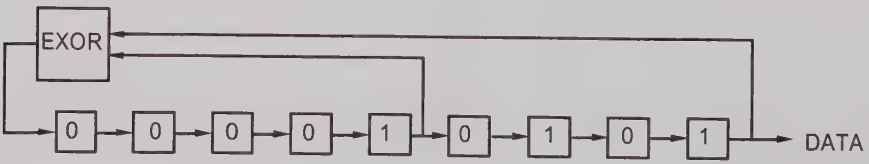
PRBS generator	Length in bits	Feedback after
9 bit	$2^9 -1 = 511$	Register 4, 0
11 bit	$2^{11} -1 =2047$	Register 2, 0
15 bit	$2^{15} -1 = 32767$	Register 1, 0
16 bit	$2^{16} -1 = 65535$	Register 5, 3, 2, 0
20 bit	$2^{20} -1 = 1048575$	Register 3, 0
21 bit	$2^{21} -1 = 2097151$	Register 2, 0
23 bit	$2^{23} -1 = 8388607$	Register 5, 0

PN generators PN9,11,15,20 and PN23 are configured according to CCITT Rec. 0.151/152/153. The output sequence is inverted for generators PN15 and PN23.

The start value of the PN generators is different in the slots and equals

$$\text{start value} = 1 + 14\,\text{hex} \times \text{slot number}$$

Example: PN9 generator in slot 1 with start value 15hex = 10101 binary.



The resulting output sequence is `1010100000010100101011110010` etc.

2.19.3 Lists as Internal Data Source

A freely programmable memory on the data generator serves as an additional internal data source for the data fields of the slots. The data are managed in so-called lists. A list editor allows to select, copy, modify and delete data lists (DATA LIST).
The list editor is available via menu DIGITAL-MOD - SOURCE....

2.19.4 External Modulation Data

External data can (only) be applied via the SERDATA interface. A selection of SERDATA as data source is only possible for a single data field of a slot. For further information on the characteristics of the SERDATA interface see Annex A.

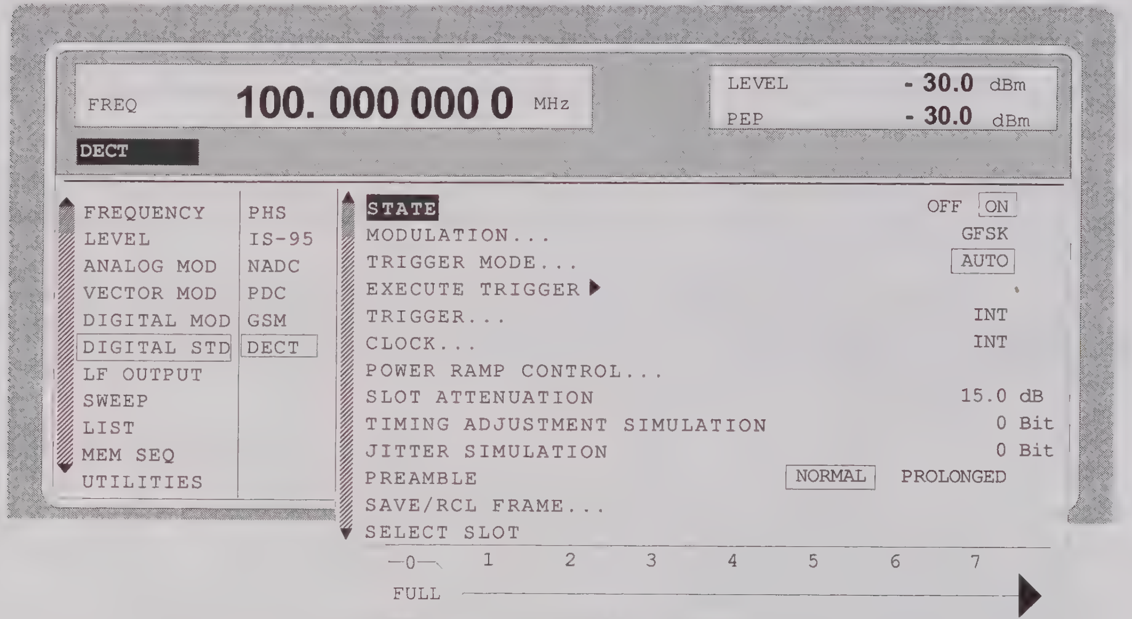
To ensure that the external data bits are assigned to specific positions in the data field of the selected slot and that they are reproducible, the buffer of the RS-232 transmitter and receiver has to be deleted. A triggered start has to follow.
The following setting sequence is required in the DIGITAL STD - DECT menu:

1. Carry out desired settings in menu.
2. Select data source SERDATA for the data field of the slot using SELECT SLOT - ....
3. Make connection to external data source, but do not yet start external data source.
4. Switch off digital standard using STATE - OFF.
5. Set TRIGGER MODE ARMED_AUTO.
   In this state, SMIQ is ready for reception, but discards data that are read in via SERDATA.
6. Switch on digital standard with STATE ON.
7. Start external data source.
   The read-in data are written into the receiving buffer. Only if this buffer is filled can SMIQ react to a trigger event.
8. Activate trigger event. Signal generation is thus started at a frame limit. The first bit received via SERDATA is put to the first bit position in the selected data field.

2.19.5 Menu DIGITAL STANDARD - DECT

Menu DIGITAL STD - DECT provides access to settings for generating DECT signals.

Menu selection: DIGITAL STD - DECT


FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 30.0 dBm
DECT
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
DECT

STATE OFF ON
MODULATION... GFSK
TRIGGER MODE... AUTO
EXECUTE TRIGGER ▶
TRIGGER... INT
CLOCK... INT
POWER RAMP CONTROL...
SLOT ATTENUATION 15.0 dB
TIMING ADJUSTMENT SIMULATION 0 Bit
JITTER SIMULATION 0 Bit
PREAMBLE NORMAL PROLONGED
SAVE/RCL FRAME...
SELECT SLOT

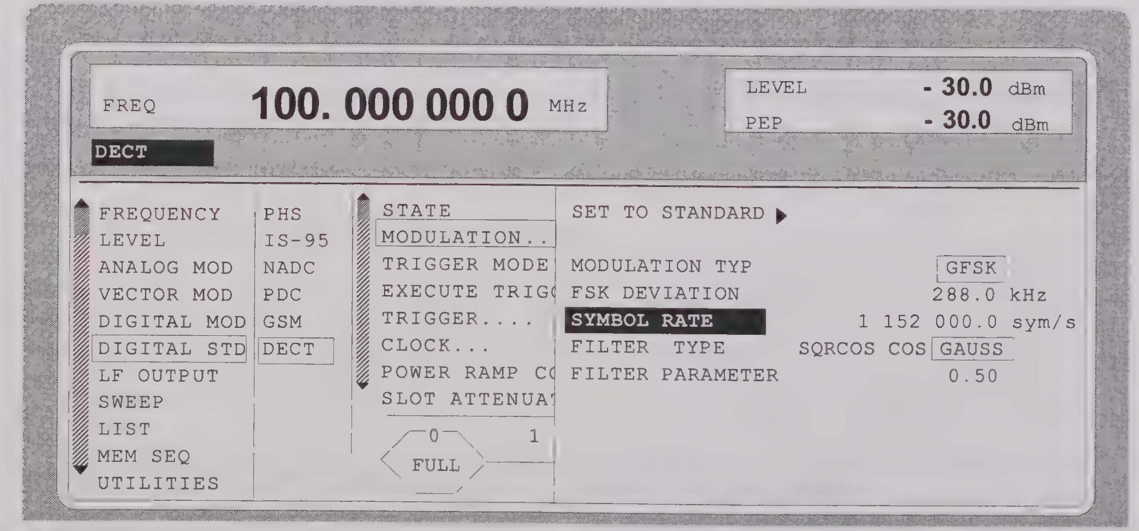
0 1 2 3 4 5 6 7
FULL

Fig. 2-205 Menu DIGITAL STD - DECT, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

STATE Switch on/off of Digital Standard DECT modulation. Vector Modulation or Digital Modulation will be switched off automatically.

IEC/IEEE-bus command `:SOUR:DECT:STAT ON`

MODULATION... Opens a window for setting some of the modulation parameters.


FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
PEP - 30.0 dBm
DECT
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
DECT
STATE
MODULATION..
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
0 1
FULL

SET TO STANDARD ▶
MODULATION TYP GFSK
FSK DEVIATION 288.0 kHz
SYMBOL RATE 1 152 000.0 sym/s
FILTER TYPE SQRCOS COS GAUSS
FILTER PARAMETER 0.50

Fig. 2-206 Menu DIGITAL STD - DECT - MODULATION...

(MODULATION...)

SET TO STANDARD ▶
Sets the subsequent modulation parameters to the values predefined by the standard.
IEC/IEEE-bus `:SOUR:DECT:STAN`

MODULATION TYPE
Selection of the modulation type. GFSK is preset.
IEC/IEEE-bus `:SOUR:DECT:FORM GFSK`

FSK DEVIATION
Input value for the frequency deviation for GFSK modulation
IEC/IEEE-bus `:SOUR:DECT:FSK 280 KHZ`

SYMBOL RATE
Input value for the symbol rate. 1152 ksymbols/s are preset.
IEC/IEEE-bus `:SOUR:DECT:SRAT 1151 KHZ`

FILTER TYPE
Selection of baseband filter. For π/4 DQPSK modulation, a selection between Nyquist filters COS and SQRCOS or a user-defined filter USER (cf. Section Digital Modulation) is possible.
IEC/IEEE-bus `:SOUR:DECT:FILT:TYPE COS`

FILTER PARAMETER
Input value for the BT value for Gauss filtering or for the roll-off factor for Nyquist filtering.
IEC/IEEE-bus `SOUR:DECT:FILT:PAR 0.51`

TRIGGER MODE...
Opens a window for selecting the trigger mode.

AUTO
The DECT signals are continuously transmitted in the activated slots.
IEC/IEEE-bus command `SOUR:DECT:SEQ AUTO`

RETRIG
The DECT signals are continuously transmitted in the activated slots. A trigger event causes a restart. This mode is not available if values different from zero are set for TIMING ADJUSTMENT or JITTER SIMULATION.
IEC/IEEE-bus command `:SOUR:DECT:SEQ RETR`

ARMED_AUTO
DECT signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the AUTO mode and can no longer be triggered.
IEC/IEEE-bus command `:SOUR:DECT:SEQ AAUT`

ARMED_RETRIG
DECT signal generation does not start until a trigger event has occurred. The unit then automatically switches over to the RETRIG mode. Each new trigger event causes a restart. This mode is not available if values different from zero are set for TIMING ADJUSTMENT or JITTER SIMULATION.
IEC/IEEE-bus command `:SOUR:DECT:SEQ ARET`

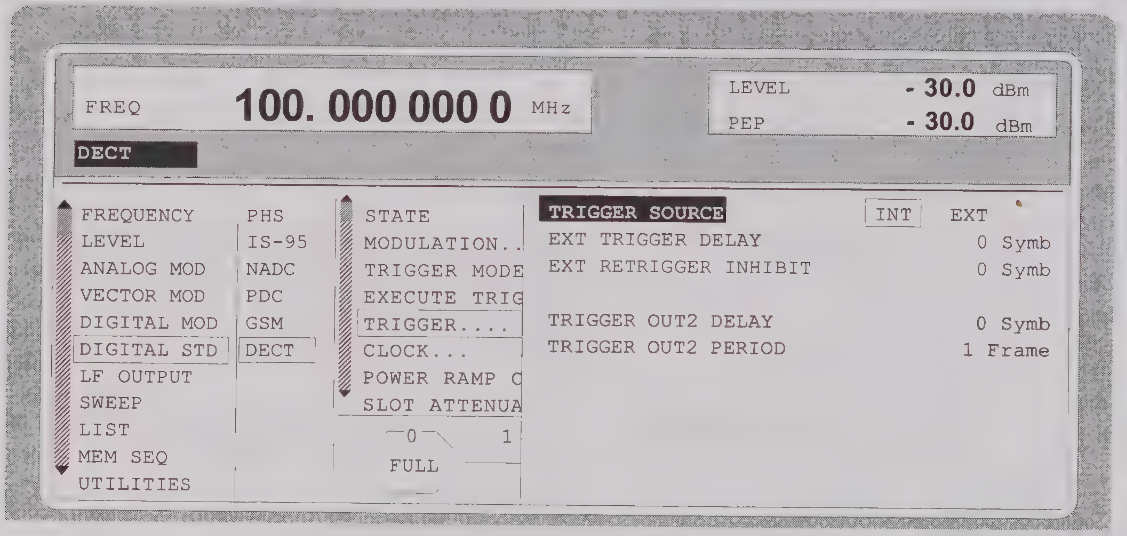
EXECUTE TRIGGER ▶

Executes a trigger even to start the DECT signal generation. This menu item is not available if TRIGGER MODE - AUTO is set.

IEC/IEEE-bus command `:TRIG:DM:IMM`

TRIGGER...

Opens a window for selecting the trigger source, for configuring the trigger output signals and for setting the time delay of an external trigger signal.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 30.0 dBm

TRIGGER SOURCE INT EXT

EXT TRIGGER DELAY 0 Symb

EXT RETRIGGER INHIBIT 0 Symb

TRIGGER OUT2 DELAY 0 Symb

TRIGGER OUT2 PERIOD 1 Frame

DECT
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
DECT
STATE
MODULATION..
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
0
1
FULL

Fig. 2-207 Menu DIGITAL STD - DECT_TRIGGER..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(TRIGGER...) **TRIGGER SOURCE**

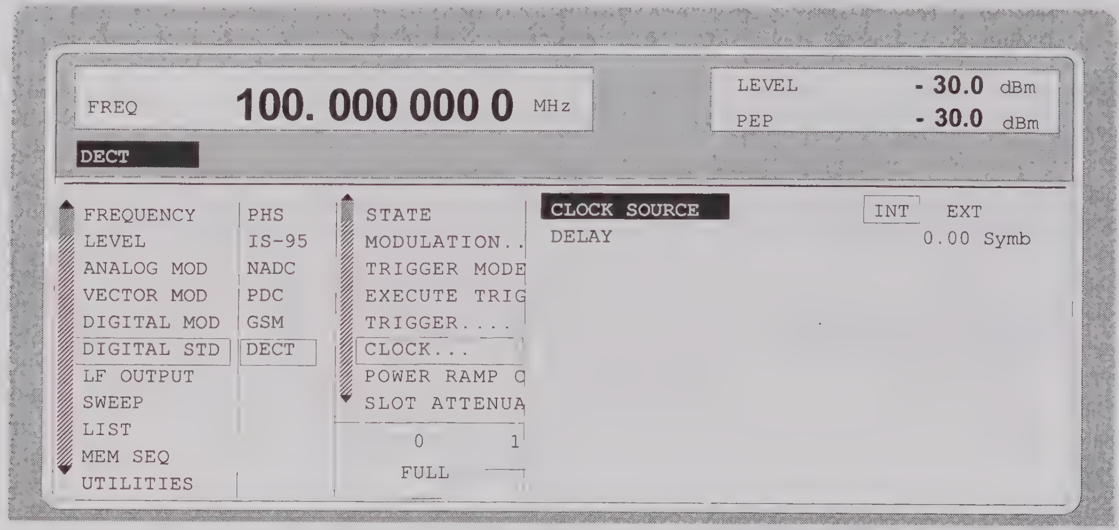
Selection of trigger source.

EXT	The DECT signal generation is started by the active slope of an external trigger signal. The polarity, the trigger threshold and the input resistance of the TRIGIN input can be modified in menu DIGITAL MOD - EXT INPUTS.
INT	A trigger event can be executed by EXECUTE TRIGGER ▶.

IEC/IEEE-bus `:SOUR:DECT:TRIG:SOUR EXT`

(TRIGGER...)	**EXT TRIGGER DELAY**	Setting the number of symbols by which an external trigger signal is delayed before it starts the DECT signal generation. This is used for setting the time synchronization between the SMIQ and the DUT. IEC/IEEE-bus `:SOUR:DECT:TRIG:DEL 3`
	EXT RETRIGGER INHIBIT	Setting the number of symbols for which a restart is inhibited after a trigger event. With TRIGGER MODE RETRIG selected, each new trigger signal restarts the DECT signal generation. This restart can be inhibited for the entered number of symbols. Example: The entry of 1000 symbols causes new trigger signals to be ignored for the duration of 1000 symbols after a trigger event IEC/IEEE-bus `:SOUR:DECT:TRIG:INH 1000`
	TRIGGER OUT2 DELAY	Input value of delay of trigger signal at TRIGOUT 2 output compared with beginning of frame. IEC/IEEE-bus `:SOUR:DECT:TRIG:OUTP:DEL 2`
	TRIGGER OUT2 PERIOD	Input value of output signal period at TRIGOUT 2 output given in frames. IEC/IEEE-bus `:SOUR:DECT:TRIG:OUTP:PER 1`

CLOCK... Opens a window for selecting the clock source and for setting a delay.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 30.0 dBm

CLOCK SOURCE INT EXT

DELAY 0.00 Symb

DECT
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
DECT
STATE
MODULATION..
TRIGGER MODE
EXECUTE TRIG
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
0
1
FULL

Fig. 2-208 Menu DIGITAL STD - DECT - CLOCK..., SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(CLOCK...) **CLOCK SOURCE** Selection of clock source.

INT SMIQ uses internally generated clock signals.

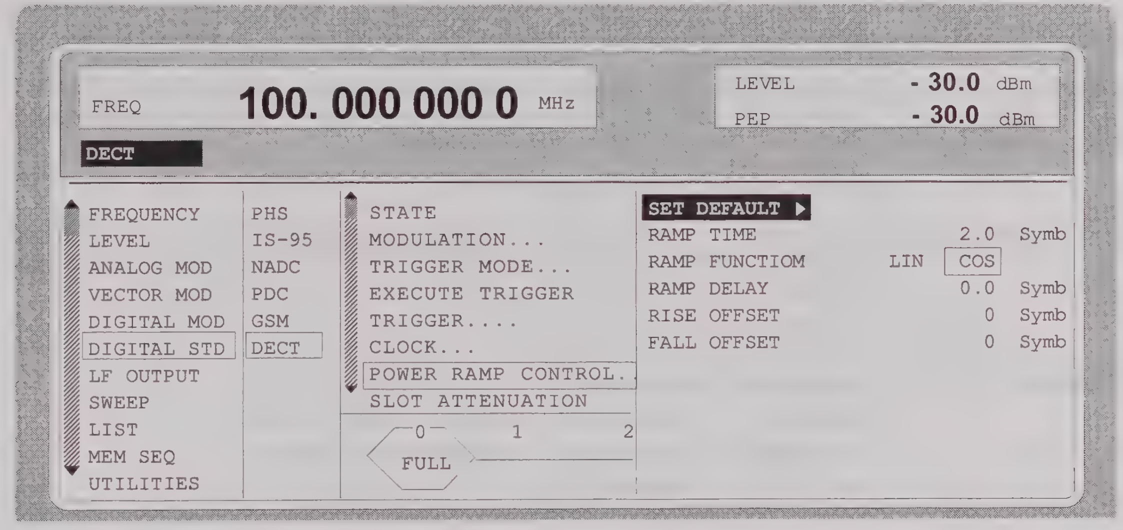
EXT An external symbol clock or bit clock is fed in at connectors SYMBOL CLOCK or BIT CLOCK. The clock synthesizer on the modulation coder is synchronized to this clock.
The symbol rate has to be set with an accuracy of ± 1 %.
The polarity, the trigger threshold and the input resistance of the clock inputs can be modified in menu DIGITAL MOD - EXT INPUTS.

IEC/IEEE-bus command `:SOUR:DECT:CLOC:SOUR INT`

DELAY Setting the delay of generated modulation signal to an external clock.
This can be used, for example, for synchronization with a second unit to achieve time synchronization between the modulation signals of the two units.

IEC/IEEE-bus command `:SOUR:DECT:CLOC:DEL 0.5`

POWER RAMP CONTROL... Opens a window for setting the envelope control, especially for the rising and falling ramp at the beginning and end of a slot.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 30.0 dBm

RAMP TIME 2.0 Symb

RAMP FUNCTIOM LIN COS

RAMP DELAY 0.0 Symb

RISE OFFSET 0 Symb

FALL OFFSET 0 Symb

DECT
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS-95
NADC
PDC
GSM
DECT
STATE
MODULATION...
TRIGGER MODE...
EXECUTE TRIGGER
TRIGGER....
CLOCK...
POWER RAMP CONTROL.
SLOT ATTENUATION
0
1
2
FULL
SET DEFAULT

Fig. 2-209 Menu DIGITAL STD - DECT - POWER RAMP CONTROL... , SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(POWER RAMP CONTROL...)

SET DEFAULT Sets the subsequent parameters to factory-set default values.

IEC/IEEE-bus `SOUR:DECT:PRAM:PRES`

RAMP TIME Input value for the rise and fall time of the envelope at the beginning or end of a slot. The time is set in units of the symbol period.

IEC/IEEE-bus `SOUR:DECT:PRAM:TIME 1.5`

RAMP FUNCTION Selection of shape of rising and falling ramp for envelope control.

LIN	Linear ramp function.
COS	Cosine function. A more favorable spectrum than that of the LIN function is obtained.

IEC/IEEE-bus `SOUR:DECT:PRAM:SHAP LIN`

RAMP DELAY Input value for a shift of the envelope characteristic to the modulated signal. A positive value causes a delay of the envelope. The values are set in units of the symbol length.

IEC/IEEE-bus `SOUR:DECT:PRAM:DEL 0.1`

RISE OFFSET Input value for a positive or negative offset of the rising ramp of the envelope at the beginning of a slot.

IEC/IEEE-bus `SOUR:DECT:PRAM:ROFF -1`

FALL OFFSET Input value for a positive or negative offset of the falling ramp of the envelope at the end of a slot.

IEC/IEEE-bus `:SOUR:DECT:PRAM:FOFF 1`

SLOT ATTENUATION

Input value for the level attenuation in dB of all active slots whose SLOT LEVEL was set to ATTEN. The slots whose level is to be attenuated are defined in the menu SELECT SLOT.

IEC/IEEE-bus command `:SOUR:DECT:SLOT:ATT 40 DB`

TIMING ADJUSTMENT SIMULATION

Input value for the number of bits used for simulating "Timing Adjust" in DECT instruments. Each 35th frame is extended (positive input values) or shortened (negative values) by the number of bits set.

IEC/IEEE-bus command `:SOUR:DECT:SIM:TADJ -1`

JITTER SIMULATION

Input value for the number of bits used for jitter simulation. A jitter is simulated by advancing the selected number of bits for even-numbered frames in time whereas ordinary timing is generated for odd-numbered frames.

IEC/IEEE-bus command `:SOUR:DECT:SIM:JITT 1`

PREAMBLE

Selection of the preamble type for full and double slot.

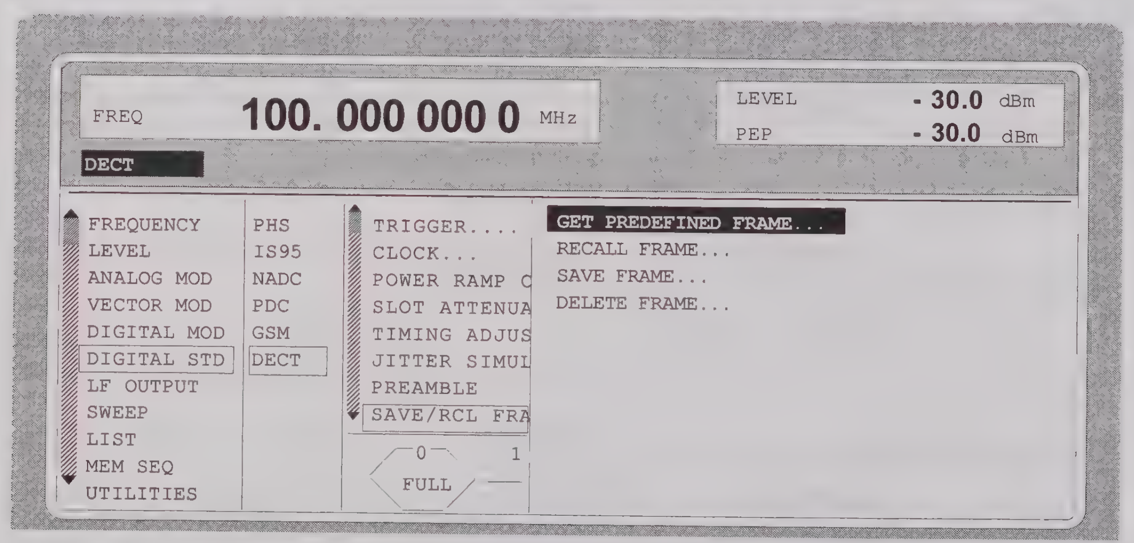
NORMAL The preamble field has a length of 16 bits.

PROLONGED The preamble field has a length of 32 bits.

IEC/IEEE-bus command `:SOUR:DECT:PRE NORM`

SAVE/RCL FRAME...

Opens a window for saving and loading a frame configuration. Loading a frame affects all parameters that can be set under SELECT SLOT.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

PEP - 30.0 dBm

DECT
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
PHS
IS95
NADC
PDC
GSM
DECT
TRIGGER....
CLOCK...
POWER RAMP C
SLOT ATTENUA
TIMING ADJUS
JITTER SIMUL
PREAMBLE
SAVE/RCL FRA
0
1
FULL
GET PREDEFINED FRAME...
RECALL FRAME...
SAVE FRAME...
DELETE FRAME...

Fig. 2-210 Menu DIGITAL STD - DECT - SAVE/RCL FRAME, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SAVE/RCL FRAME...) **GET PREDEFINED FRAME...**

Loads a factory-set frame configuration.

DNFULL	All downlink slots numbered 0 to 11 are active as full slots.
UPFULL	All uplink slots numbered 12 to 23 are active as full slots.
FULL_0	Only slot no. 0 is active as a full slot.
DOUB_0	Slots no. 0 and 1 are active as double slots.

IEEE `SOUR:DECT:FLIS:PRED:LOAD "name"`

RECALL FRAME...

Loads a frame configuration saved by the user.

IEC/IEEE-bus `SOUR:DECT:FLIS:LOAD "name"`

SAVE FRAME...

Saves a user-defined frame configuration.

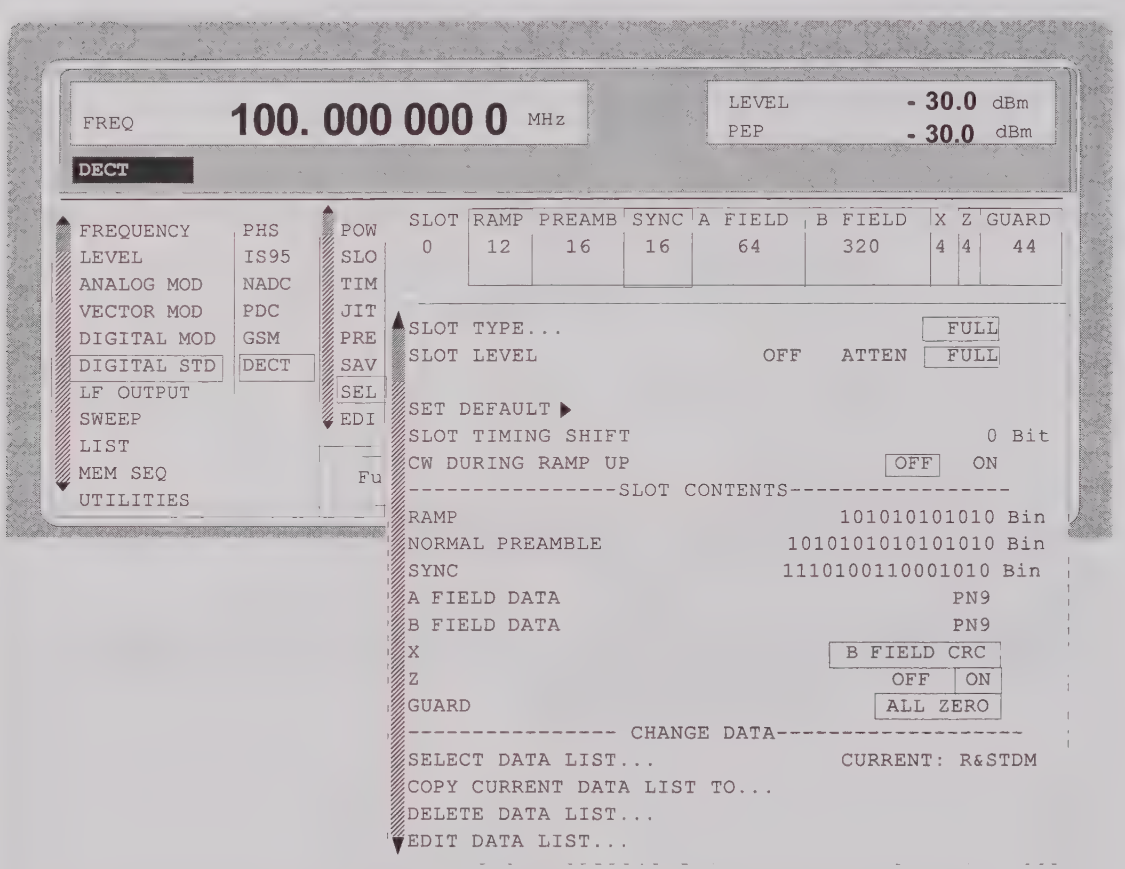
IEC/IEEE-bus `SOUR:DECT:FLIS:STOR "name"`

DELETE FRAME...

Deletes a frame configuration saved by the user.

IEC/IEEE-bus `SOUR:DECT:FLIS:DEL "name"`

SELECT SLOT... Selection of one of 24 possible slots. When selecting the slot, a window is opened in which the data contents belonging to this slot can be defined.
12 slots are available for uplink and downlink, respectively. They are labeled UP<i> and DN<i> in the display where i varies from 0 to 23. Up to 12 of the 24 slots can be active at the same time.
If the cursor is placed onto a slot in the diagram, the SLOT LEVEL may be modified by pressing one of the unit keys (toggle function).



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm
PEP - 30.0 dBm

SLOT	RAMP	PREAMB	SYNC	A FIELD	B FIELD	X	Z	GUARD
0	12	16	16	64	320	4	4	44

```
SLOT TYPE...                                    FULL
SLOT LEVEL                   OFF    ATTEN       FULL

SET DEFAULT ▶
SLOT TIMING SHIFT                               0 Bit
CW DURING RAMP UP                         OFF    ON
----------------SLOT CONTENTS-----------------
RAMP                               101010101010 Bin
NORMAL PREAMBLE                1010101010101010 Bin
SYNC                           1110100110001010 Bin
A FIELD DATA                                PN9
B FIELD DATA                                PN9
X                                   B FIELD CRC
Z                                      OFF   ON
GUARD                                  ALL ZERO
---------------- CHANGE DATA-------------------
SELECT DATA LIST...             CURRENT: R&STDM
COPY CURRENT DATA LIST TO...
DELETE DATA LIST...
EDIT DATA LIST...
```

Fig. 2-211 Menu DIGITAL STD - DECT - SELECT SLOT, SMIQ equipped with Modulation Coder SMIQB20 and Data Generator SMIQB11

(SELECT SLOT) **SLOT TYPE...** Opens a window for selecting the slot type.

FULL	Full slot configuration for simulating a basic R32 physical channel. IEEE-bus `:SOUR:DECT:SLOT2:TYPE FULL`
DOUBLE	Double slot configuration for simulating a high capacity R80 physical channel. IEEE-bus `:SOUR:DECT:SLOT2:TYPE DOUB`
ALL_DATA	SLOT type for test purposes with arbitrarily programmable data contents. IEEE-bus `:SOUR:DECT:SLOT2:TYPE ADAT`

(SELECT SLOT)

SLOT LEVEL

Selection of the level for the selected slot.

OFF Maximum attenuation

IEEE bus `:SOUR:DECT:SLOT2:LEV OFF`

FULL The level corresponds to the value indicated on the SMIQ LEVEL display.

IEEE bus `:SOUR:DECT:SLOT2:LEV FULL`

ATTEN The level is attenuated by the value set under SLOT ATTENUATION.

IEEE bus `:SOUR:DECT:SLOT2:LEV ATT`

Note: *If the cursor is placed onto a slot in the diagram, the SLOT LEVEL may be modified by pressing one of the unit keys (toggle function).*

SET DEFAULT ►

Resets the subsequent parameters to factory-set default values.

IEC/IEEE-bus command `SOUR:DECT:SLOT8:PRES`

SLOT TIMING SHIFT

Input value for the number of bits used for simulating a faulty slot timing. The active slot is shifted in time by the selected number of bits. A positive input value delays the slot, a negative value advances the slot in time.

IEEE bus command `:SOUR:DECT:SLOT2:STSH 1`

CW DURING RAMP UP

Switches the modulation during the rise of the power ramp on or off.

ON Before the preamble starts, the modulation is switched off so that only the unmodulated carrier is generated.

OFF Before the preamble starts, the modulation is switched on and the data bits in the ramp field are modulated.

IEEE bus command `:SOUR:DECT:SLOT2:RAMP:CW ON`

RAMP

Input value for the 12-bit data field RAMP in binary format. This data field is not defined in the DECT standard. It is used to define the data contents during the rise of the power ramp. A series of data representing the natural extension of the preamble bits in forward direction is set by default.

IEEE `:SOUR:DECT:SLOT2:RAMP:DATA #B111111111111`

NORMAL PREAMBLE

Input value for the NORMAL PREAMBLE in binary format. According to the DECT standard, the default setting for this 16-bit data field is different for uplink and downlink slots. This data field is not available for slot type ALL_DATA.

IEEE `:SOUR:DECT:SLOT2:PRE:DATA #B1010...`

(SELECT SLOT)

PROLONGED PREAMBLE

Input value for the PROLONGED PREAMBLE in binary format. According to the DECT standard, the default setting for this 32-bit data field is different for uplink and downlink slots. This data field is not available for slot type ALL_DATA.

IEEE `:SOUR:DECT:SLOT2:PRE:PROL:DATA #B1010...`

SYNC

Input value for the 16-bit data field SYNC in binary format. According to the DECT standard, the default setting for this 32-bit data field is different for uplink and downlink slots. This data field is not available for slot type ALL_DATA.

IEEE `:SOUR:DECT:SLOT2:SYNC #B1001...`

A FIELD DATA

Selection of the data source for the A FIELD. The 64-bit A data field is only displayed for the slot types FULL and DOUBLE.

PN.. PRBS data according to CCITT V52 or Rec. 0.151 with period lengths between 2^9-1 and 2^{23}-1.

IEEE bus `:SOUR:DECT:SLOT3:AFI PN15`

DLIST Data from a programmable list.

IEEE bus `:SOUR:DECT:SLOT3:AFI DLIS "name"`
`:SOUR:DECT:SLOT3:AFI DLIS`

SERDATA Data fed in via the entry SER DATA.

IEEE-bus `:SOUR:DECT:SLOT3:AFI SDAT`

B FIELD DATA

Selection of the data source for the B FIELD. The B data field is only displayed for the slot types FULL and DOUBLE. Its length is 320 bits for full slot and 800 bits for double slot.

PN.. PRBS data according to CCITT V52 or Rec. 0.151 with period lengths between 2^9-1 and 2^{23}-1.

IEE-bus `:SOUR:DECT:SLOT3:BFI PN15`

DLIST Data from a programmable list.

IEEE-bus `:SOUR:DECT:SLOT3:BFI DLIS "name"`
`:SOUR:DECT:SLOT3:BFI DLIS`

SERDATA Data fed in via the entry SER DATA.

IEEE-bus `:SOUR:DECT:SLOT3:BFI SDAT`

(SELECT SLOT)

DATA

Selection of the data source for the 424-bit data field DATA. The B data field is only displayed for the slot type ALL_DATA.

PN..	PRBS data according to CCITT V52 or Rec. 0.151 with period lengths between 2^9-1 and 2^{23}-1. IEEE-bus `:SOUR:DECT:SLOT3:DATA PN15`
DLIST	Data from a programmable list. IEEE-bus `:SOUR:DECT:SLOT3:DATA DLIS "name"` `:SOUR:DECT:SLOT3:DATA DLIS`
SERDATA	Data fed in via the entry SER DATA. IEEE-bus `:SOUR:DECT:SLOT3:DATA SDAT`

X

This 4-bit data field can not be edited. It contains a CRC (Cyclic Redundancy Code) generated from the data in the B data field according to the DECT standard. This data field is not available for slot type ALL_DATA.

Z

Switches the Z field on and off. This function is available for the slot types FULL and DOUBLE.

ON	The CRC (Cyclic Redundancy Code) generated for the X field is repeated in the 4-bit data field Z.
OFF	The Z field is not activated. The guard field is extended by 4 bits.

IEEE-bus command `:SOUR:DECT:SLOT2:ZFI ON`

GUARD

This data field can not be edited and contains zero data. At the beginning of the GUARD field, the power ramp of the active slot starts falling.

SELECT DATA LIST...

Opens a window for selecting a stored data list or for generating a new list.

COPY CURRENT DATA LIST TO...

Stores the current data list under a different name.

DELETE DATA LIST...

Deletes a data list.

EDIT DATA LIST...

Opens a window for editing a data list bit-by-bit. The available storage capacity and the length of the current list is displayed in parameters FREE and LEN (see also Section List Editor).

COPY	Copies a list range
FILL	Fills the range with filler pattern
INSERT	Inserts a list range at a different position of the list
DELETE	Deletes a list range
EDIT/VIEW	Edits or views the list

2.20 Digital Standard GPS

When equipped with the options Modulation Coder (SMIQB20), Data Generator (SMIQB11) and Digital Standard GPS (SMIQB51), SMIQ can generate the signal of a GPS satellite. Section 2.20.1 provides an introductory overview of the Global Positioning System (GPS). Section 2.20.2 then describes the associated control functions. For more detailed information on generating GPS signals, refer to section 2.20.3.

2.20.1 Description of Global Positioning System (GPS)

The Global Positioning System consists of several satellites circling the earth in low orbits. The position of a receiver on the earth can be determined by carrying out delay measurements of at least four signals emitted by different satellites.

Being transmitted on a single carrier frequency, the signals of the individual satellites can be distinguished by means of correlation (Gold) codes (with GPS, these codes are known as C/A codes). Information on time and satellite orbit is contained in the navigation data emitted by each satellite. In this case, the C/A codes are used as spreading codes (see CDMA) for the navigation data.

Each GPS satellite emits an individual navigation data sequence spread by the C/A code assigned to it at the carrier frequency L1 = 1.57542 GHz. A GPS signal of this type (see Table 2-33 GPS system parameters), which allows basic receiver function tests to be carried out, can be generated by SMIQ for one satellite.

Table 2-33 GPS system parameters

Carrier frequency	1.57542 GHz
Signal level, after antenna	Approx. –125 dBm to [1] and [2], depending on receive conditions
Doppler shift	-10 kHz to +10 kHz settable
Symbol rate (C/A code)	1.023 Msps
C/A codes	1 to 37 settable, 1023 chips per C/A code
Modulation	BPSK
Information data rate (navigation data)	50 Hz
Frame structure of navigation data	25 frames consisting of 5 subframes where 1 subframe consists of 10 words, 1 word consists of 30 data bits, 1 data bit consists of 20460 C/A code chips.

Use of navigation data
The C/A code used is fundamental to the simulation of GPS signals. The C/A code can be set from 1 to 37 in the operating menu and specifies the satellite to be simulated.

In addition to this, navigation data play an extremely important role, since they are essential for calculating the positions of the four satellites, which are the minimum prerequisite for localization purposes. However, even if only one satellite is available (as simulated by SMIQ), pseudo navigation data can be used to check the decoding of navigation information (such as GPS time, almanac and ephemeris) in addition to the recognition of the C/A code.

Current almanac data can be downloaded via the Internet. Once converted into navigation data via PC application software available on the Rohde & Schwarz homepage (www.rohde-schwarz.com, Application Note 1GP46, AlmanacUploader), the data can be transferred to SMIQ as a data list. This ensures that any navigation data records can be used for GPS signal generation in SMIQ.

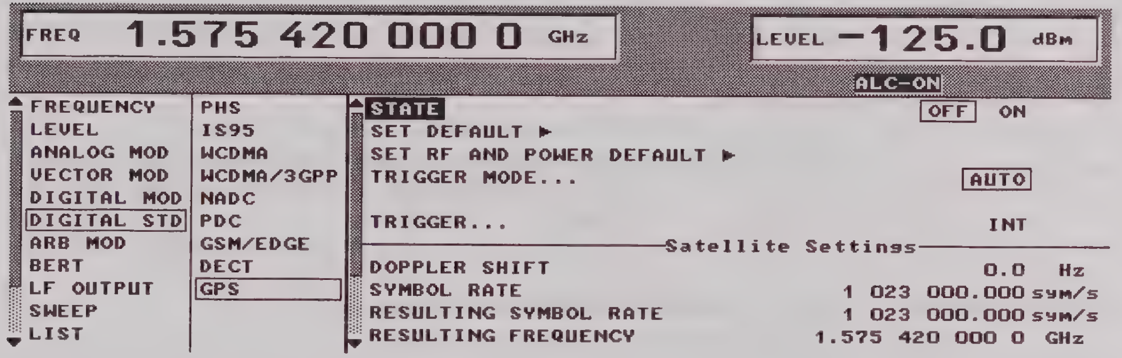
For more detailed information on the content and frame structure of navigation data, as well as C/A code generation, refer to the following documentation.

References:

[1] Interface Control Document ICD-GPS-200.
[2] Global Positioning System Standard Positioning Service Signal Specification, 2nd Edition, 1995.

2.20.2 GPS Menu

The following graphics show the menu for generating a GPS signal for one satellite. The individual menu items are explained below:



FREQ 1.575 420 000 0 GHz
LEVEL -125.0 dBm
STATE OFF ON
SET DEFAULT ▶
SET RF AND POWER DEFAULT ▶
TRIGGER MODE... AUTO
TRIGGER... INT
DOPPLER SHIFT 0.0 Hz
SYMBOL RATE 1 023 000.000 sym/s
RESULTING SYMBOL RATE 1 023 000.000 sym/s
RESULTING FREQUENCY 1.575 420 000 0 GHz

Fig. 2-212 DIGITAL STD - GPS menu

STATE Switch-on/off of modulation - digital standard GPS. If vector modulation, digital modulation or another digital standard were switched on, they are automatically switched off.

STATE = ON starts the calculation and output of the GPS signal on the basis of the current settings. There are no significant delays until the signal is output, since the signal is generated in realtime.

IEC/IEEE-bus command : `SOUR:GPS:STAT ON`

Note: *When a parameter is changed (exception: DOPPLER SHIFT and SYMBOL RATE), STATE is automatically switched to OFF to ensure that the set values and the output signal are consistent. Once all the values have been changed, STATE can manually be set to ON again. This can be done using the menu parameter STATE or the MOD ON/OFF key below the rollkey.*

SET DEFAULT ▶ Sets the default setting for GPS:

- The DOPPLER SHIFT is set to 0 kHz.
- The set SYMBOL RATE is positioned to 1.023 Msps.
- The C/A CODE 1 is used.
- PATTERN is used as NAVIGATION DATA SOURCE.
- The PATTERN is set to 1010101010101010.
- All TRIGGER settings are set to default values.

IEC/IEEE-bus command `:SOUR:GPS:PRES:STAN`

SET RF AND POWER DEFAULT ▶

Sets the RF default setting for GPS:

- The frequency FREQ is set to the GPS carrier frequency L1 of 1.57542 GHz.
- The output level LEVEL is set to –125.0 dBm.

IEC/IEEE-bus command `:SOUR:GPS:PRES:RF`

TRIGGER MODE

Configuration of autorun control of generated GPS signal.

AUTO — The GPS signal calculated in realtime is immediately output and cyclically repeated. Trigger events are ignored.

IEC/IEEE-bus command `:SOUR:GPS:SEQ AUTO`

RETRIG — The GPS signal calculated in realtime is immediately output and cyclically repeated. A trigger event causes a restart from the first navigation data bit.

IEC/IEEE-bus command `:SOUR:GPS:SEQ RETR`

ARMED_AUTO — Only a trigger event causes a start of the GPS signal from the first navigation data bit. Further trigger events are ignored.

IEC/IEEE-bus command `:SOUR:GPS:SEQ AAUT`

ARMED_RETRIG — Only a trigger event causes a start of the GPS signal from the first navigation data bit. Any other trigger event causes a restart.

IEC/IEEE-bus command `:SOUR:GPS:SEQ ARET`

EXECUTE TRIGGER ▶

Executes a trigger event to start the GPS signal.

IEC/IEEE-bus command `:TRIG:DM:IMM`

TRIGGER...

Opens a window for selecting the trigger source, configuring trigger output signals, and setting the delay of an external trigger signal.

TRIGGER SOURCE

Selects the trigger source.

EXT — The GPS signal is started from the first navigation bit with the active edge of an external trigger signal. The polarity, trigger threshold, and input impedance of the TRIGIN input can be changed in the DIGITAL MOD - EXT INPUTS menu.

INT — A trigger event is manually started by EXECUTE TRIGGER.

IEC/IEEE-bus command `:SOUR:GPS:TRIG:SOUR EXT`

EXT TRIGGER DELAY

Sets the number of chips by which an external trigger signal is delayed before it starts the GPS signal. This is used to set up synchronization with the DUT or other units.

IEC/IEEE-bus command
`:SOUR:GPS:TRIG:DEL 3`

TRIGGER...

EXT RETRIGGER INHIBIT

Sets the number of chips by which a restart is delayed after a trigger event. If the TRIGGER MODE RETRIG was selected, every further trigger signal restarts the GPS signal. This restart can be inhibited for the number of chips that have been entered.

Example:
Entry of 20460 chips (corresponds to a navigation data bit) causes every further trigger signal to be ignored for the duration of 20460 chips after the last trigger event.

IEC/IEEE-bus command `:SOUR:GPS:TRIG:INH 20460`

TRIGGER OUT 1/2

Selects the signals for outputs TRIGOUT 1 and TRIGOUT 2 of the PARDATA connector.

C/A_CODE	Marker signal for every C/A-code sequence (1023 chips). IEC/IEEE-bus command: `:SOUR:GPS:TRIG:OUTP1 CODE`
NAVBIT	Marker signal for every navigation data bit (20460 chips). IEC/IEEE-bus command: `:SOUR:GPS:TRIG:OUTP1 NBIT`
NAVWORD	Marker signal for every navigation data word (30 bits). IEC/IEEE-bus command: `:SOUR:GPS:TRIG:OUTP1 NWOR`
SUBFRAME	Marker signal for every navigation subframe (corresponds to 10 words). IEC/IEEE-bus command: `:SOUR:GPS:TRIG:OUTP1 SFR`
FRAME	Marker signal for every navigation frame (corresponds to 5 subframes). IEC/IEEE-bus command: `:SOUR:GPS:TRIG:OUTP1 FRAM`

TRIGGER OUT 1/2 POL

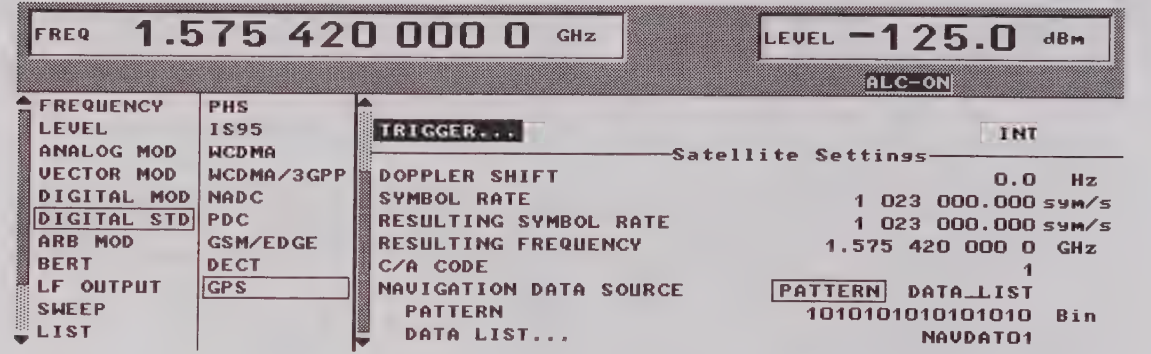
Selects the polarity of the signals at the TRIGOUT 1 and TRIGOUT 2 outputs of the PARDATA connector.

IEC/IEEE-bus command `:SOUR:GPS:OUTP2:POL POS`

TRIGGER OUT 1/2 DELAY

Enters the number of chips by which the selected trigger output signal is delayed.

IEC/IEEE-bus command `:SOUR:GPS:OUTP2:DEL 0`



```
FREQ   1.575 420 000 0 GHz                 LEVEL  -125.0 dBm

TRIGGER...                                              INT
---------------------Satellite Settings---------------------
DOPPLER SHIFT                                     0.0   Hz
SYMBOL RATE                            1 023 000.000 sym/s
RESULTING SYMBOL RATE                  1 023 000.000 sym/s
RESULTING FREQUENCY                    1.575 420 000 0 GHz
C/A CODE                                             1
NAVIGATION DATA SOURCE              PATTERN  DATA_LIST
  PATTERN                           1010101010101010  Bin
  DATA LIST...                               NAVDATO1
```

Fig. 2-213 DIGITAL STD - GPS menu

DOPPLER SHIFT

Sets the desired Doppler shift of the GPS signal. This affects the symbol rate used as well as the frequency shift of the carrier signal.

IEC/IEEE-bus command `:SOUR:GPS:DSH 0`

SYMBOL RATE

Sets the basic value of the symbol rate for zero Doppler, i.e. without Doppler shift. This value is used to calculate the actual symbol rate as a function of the Doppler shift set. At a Doppler shift of 0 Hz, the values of SYMBOL RATE and RESULTING SYMBOL RATE (see below) are identical.

IEC/IEEE-bus command `:SOUR:GPS:SRAT 1023000.0`

RESULTING SYMBOL RATE

Displays the physical symbol rate currently used. The value is obtained from the set frequency, the Doppler shift and the set basic symbol rate for zero Doppler according to the following equation:

$$f_{Symbol_{resulting}} = f_{Symbol_{ZeroDoppler}} * \left(1 + \frac{f_{Doppler}}{FREQ}\right)$$

IEC/IEEE-bus command `:SOUR:GPS:CURR:SRAT?`

RESULTING FREQUENCY

Displays the physical carrier frequency currently output. The value is obtained from the sum of the set frequency under FREQ and Doppler shift according to the following equation:

$$f = FREQ + f_{Doppler}$$

IEC/IEEE-bus command `:SOUR:GPS:CURR:FREQ?`

C/A CODE

Sets the C/A code for spreading the navigation data. This clearly defines the simulated GPS satellite.

IEC/IEEE-bus command `:SOUR:GPS:CODE 1`

NAVIGATION DATA SOURCE

Selects the navigation data source. Either simple bit patterns (see PATTERN) or more complex data lists (see DATA LIST) with GPS-specific navigation data (ephemeris and almanac), for example, can be selected.

IEC/IEEE-bus command `:SOUR:GPS:DATA PATT`

PATTERN	Sets the bit pattern to be used as navigation data source provided that PATTERN has been selected as NAVIGATION DATA SOURCE. IEC/IEEE-bus command `:SOUR:GPS:DATA:PATT #H1234,16`
DATA LIST	Selects the data list to be used as navigation data source provided that DATA_LIST has been selected as NAVIGATION DATA SOURCE . IEC/IEEE-bus command `:SOUR:GPS:DATA:DLIS ″name″`

2.20.3 Instructions for Generating GPS Signals

This section provides background information for generating GPS signals and is intended to facilitate generating signals with specific characteristics as well as user prompting.

1. Generating a simple GPS signal

After the default values have been set, a cyclically repetitive GPS signal is generated for one satellite by switching STATE to ON. Changing the C/A CODE setting enables the satellite to be simulated to be selected from 1 to 37. The associated signal level is set under LEVEL.

Note: *To avoid any damage to connected receivers, the user must ensure that the signal level used is not too high. A downstream attenuator pad must be connected to generate output levels below -144 dBm.*

Signals generated in this way can be recognized by a GPS receiver. However, since there are no real navigation data on the C/A code (as standard, a bit pattern with up to 16 characters is used as the navigation data source), only the signal level of the simulated satellite can be measured and displayed by the receiver.

A signal of this type is usually sufficient for performing simple function tests. It should be noted, however, that the receiver to be tested may have to be switched to a special test mode, since signals without correct navigation data are often not indicated (on a display, for example).

2. GPS signals with pseudo navigation data

Switching NAVIGATION DATA SOURCE over to DATA_LIST enables any data lists to be selected on SMIQ and used as navigation data. Real GPS navigation data with ephemeris and almanac information as generated with the AlmanacUploader PC tool from Rohde & Schwarz and transmitted to SMIQ, for example would be appropriate. The program can be downloaded from the Rohde & Schwarz Internet page (www.rohde-schwarz.com) under Application Notes 1GP46.

When pseudo-real navigation data with a length of 37500 bits (i.e. with a total signal time of 12.5 minutes) are used, for example, satellite-specific information on position and time is transmitted, which can be recognized by the receiver. In addition to information on the received satellite, almanac and ephemeris data can usually be output, thereby enabling the decoding of navigation data to be checked.

Note: *The GPS signal is cyclically repeated as a function of the TRIGGER MODE. When navigation data from data lists are used, this causes the transmitted GPS time to be set to its initial value for each new cycle.*

3. Doppler-shifted GPS signals

For enhanced receiver characteristics checking, a Doppler shift of the type that occurs with real GPS signals can also be simulated. The relevant change to the symbol rate of the C/A code is carried out automatically. The currently valid values for Doppler-shifted carrier frequency and symbol rate are displayed under RESULTING FREQUENCY and RESULTING SYMBOL RATE.

The simulation of Doppler-shifted GPS signals can be used to check the receiver characteristics under more realistic conditions than with zero Doppler. In contrast to the real system, however, the set Doppler frequency is fixed and must eventually be changed manually.

4. GPS signals with modified carrier frequency and symbol rate

Additional settings for carrier frequency and symbol rate can be made to perform more comprehensive receiver tests. However, these settings may result in signals that do not conform to GPS.

The basic carrier frequency without Doppler shift can be freely set under FREQ. The actual frequency output physically is yielded by the following equation

$$f = FREQ + f_{Doppler}$$

and is displayed under RESULTING FREQUENCY.

The symbol rate used for zero Doppler is set under SYMBOL RATE. Taking into account a Doppler shift, the actual symbol rate is determined by the following equation

$$f_{Symbol_{resulting}} = f_{Symbol_{ZeroDoppler}} * \left(1 + \frac{f_{Doppler}}{FREQ}\right)$$

and is given under RESULTING SYMBOL RATE.

Selecting the appropriate parameters enables the characteristics of the receiver (such as dependence of receiver synchronization on the symbol rate/carrier frequency ratio, etc.) to be easily checked.

2.21 Arbitrary Waveform Generator ARB

The option SMIQB60, a two-channel ARB generator, is an integrated I/Q modulation source of the SMIQ. Thus, arbitrary modulation signals such as COFDM, multicarrier or noise can be generated. The software WinIQSIM enables the calculation of modulation signals and the loading of these signals into the instrument. Furthermore, signals which were calculated by a mathematical program such as Matlab may be transferred.

2.21.1 Function

Conventional ARB generators substantially consist of an output memory with series-connected D/A converter and analog filter. Due to the limited number of filters and their steepness the sampling rate often has to be set considerably higher than required by Nyquist's theorem, in order for the aliasing effects to be sufficiently suppressed by the analog filter. Fig. 2-214 shows these ratios for a 1-MHz sinewave signal. If a filter provides a cutoff-frequency of 11 MHz, a sampling rate of at least 12 MHz must be selected to make sure that the aliasing effects are suppressed sufficiently.

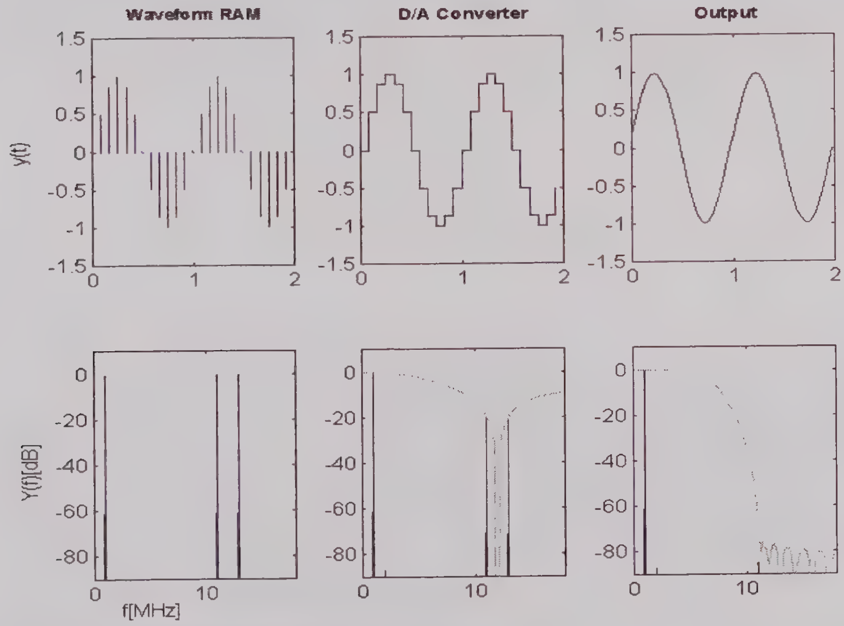


Fig. 2-214 Signal flow of ARB generator

This conception has been considerably improved in the option SMIQB60. The modulation signal is interpolated to a higher sampling rate by means of a very steep-edged, digital interpolation filter and is then output. The interpolation rate is set by the SMIQB60 automatically such that aliasing of the interpolated sampling rate is suppressed by the analog filter. This procedure offers the following advantages to the user:

1. Oversampling has to be selected such that the bandwidth of the interpolation filter exceeds that of the modulation signal. The following equation applies:

   ov * int_bw >= mod_bw,

   ov == Oversampling,

   int_bw == 0.375, standardized bandwidth of the interpolation filter,

   mod_bw == bandwidth of the modulation signal standardized to symbol rate

   The following value is thus obtained for the digital standard W-CDMA with the baseband filter √cos, $\alpha = 0.22$:

   $$\text{mod\_bw} = \frac{(1+\alpha)}{2} = 0.61, \Rightarrow \text{ov} >= \frac{0.61}{0.375} = 1.63.$$

2. Due to the reduced oversampling, the duration of the signal increases with constant number of sampling values. Accordingly, the number of sampling values decreases with constant signal duration. Usually, with conventional ARB generators, the minimum oversampling is limited to 4. Thus, provided that the above parameters of the W-CDMA system apply, the 512 ksamples of the SMIQB60 correspond to 4/1.63 * 512 = 1256 kSamples memory in a conventional ARB generator.

The block diagram in Fig. 2-215 roughly illustrates the structure of the ARB generator.

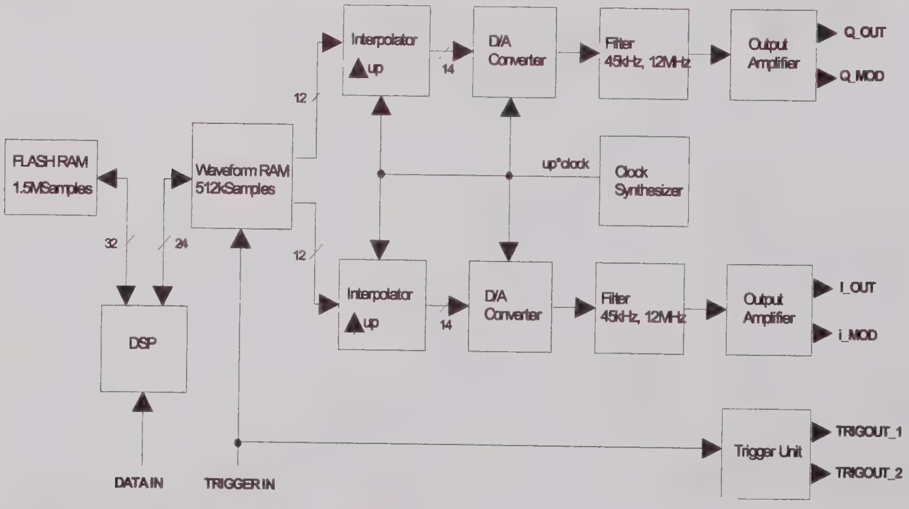

Interpolator
up
14
D/A Converter
Filter 45kHz, 12MHz
Output Amplifier
Q_OUT
Q_MOD
12
up*clock
Clock Synthesizer
FLASH RAM 1.5MSamples
Waveform RAM 512kSamples
32
24
12
Interpolator
up
14
D/A Converter
Filter 45kHz, 12MHz
Output Amplifier
I_OUT
I_MOD
DSP
Trigger Unit
TRIGOUT_1
TRIGOUT_2
DATA IN
TRIGGER IN

Fig. 2-215 Block diagram SMIQB60

The I/Q data are loaded by the host computer via the DATA IN interface to the DSP which passes them into a non-volatile FLASH RAM. The latter is organized in 22 blocks of 64ksamples, each. At least one block is occupied by each waveform. The I/Q data are subject to a convolution filter which limits the signal bandwidth. This is required for further processing.

If a waveform is selected, the I/Q data are loaded into the output memory. They are convoluted by a correction filter, which compensates all errors in the path to the I/Q modulator. In particular, the Si frequency response of the D/A converter as well as the frequency responses of the analog filter on the SMIQB60 and of the I/Q filter of the option SMIQB47 are corrected. Fig. 2-216 illustrates the further signal flow in the frequency and time domains.

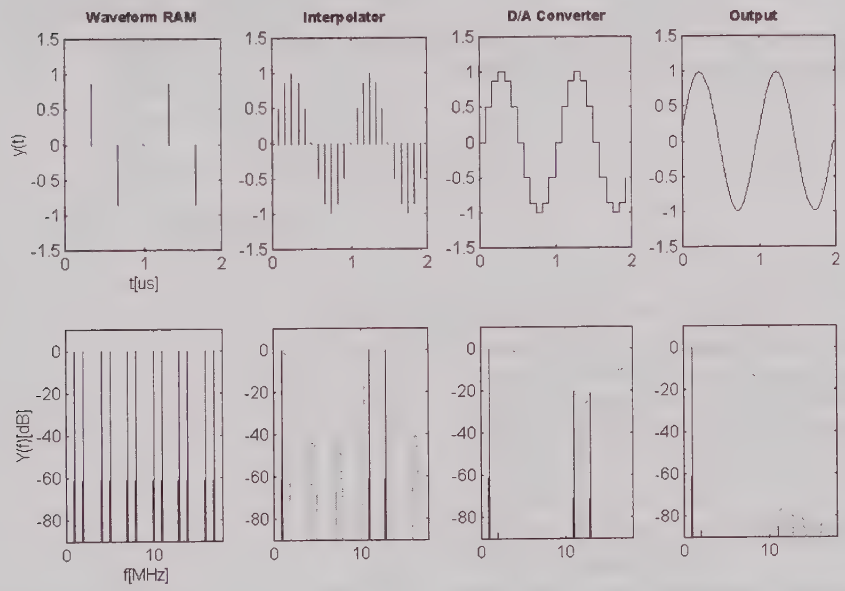


Fig. 2-216 Signal flow SMIQB60

A 1-MHz sinewave signal with fa = 3 MHz is sampled by way of example. The sampling procedure causes aliasing to occur in the frequency range (n*3 ± 1) MHz. The following interpolation filter increases the sampling rate by the factor 4. This corresponds to a correct reconstruction of the additional sampling values in the time domain. Due to the sample-and-hold process at the output of the subsequent D/A converter, a stair-step signal is generated in the time domain which corresponds to a SI-weighting in the frequency domain. The analog filter compensates for the aliasing of the interpolated sampling rate, the output signal is a sinewave signal in the time domain. SMIQB60 automatically sets the interpolation rate such that aliasing is suppressed by the analog filter.

The sampling rate is increased internally by the factor 4, in order to avoid errors with CCDF determination of the waveform with low sampling rate.

The absolute value of the I/Q output signal is 0.5 Vs at 50 Ω (= 0 dB) in Normal mode. This is the nominal output of the I/Q modulator. The output level may be varied in Manual mode between -6 dB and 3 dB in order to optimize the ACP in various channel offsets, e.g. The range above 0 dB is not specified, signal frequencies above 10 MHz may lead to a limitation.

The internal calibration of the SMIQB60, which is performed automatically with calibration of the vector modulation, corrects offset and gain errors to a minimum.

The trigger facilities of the SMIQB60 are similar to those of the data generator SMIQB11 (see Digital Modulation). The trigger generator consists of programmable counters which generate a periodic sequence with a pulse duty ratio of On Time/Off Time with settable start delay. E.g., to generate a slot trigger for a W-CDMA signal with a chip rate of 3.84 MHz, the following values are set:

t_{Slot} = Slot time
t_{Chip} = Chip time
t_a = Sampling time

$t_{Slot} = 625\mu s = 2400\ t_{Chip},\ t_{Chip} = ov * t_a,\ ov = 2, \Rightarrow t_{Chip} = 4800 * t_a$

On Time = 100 (for example) => Off Time = 4700.

The trigger signals are time-synchronous with the I/Q output signals. WinIQSIM supports the generation of predefined trigger signals (see Use of WinIQSIM Software).

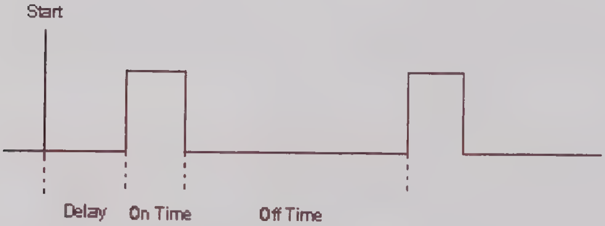
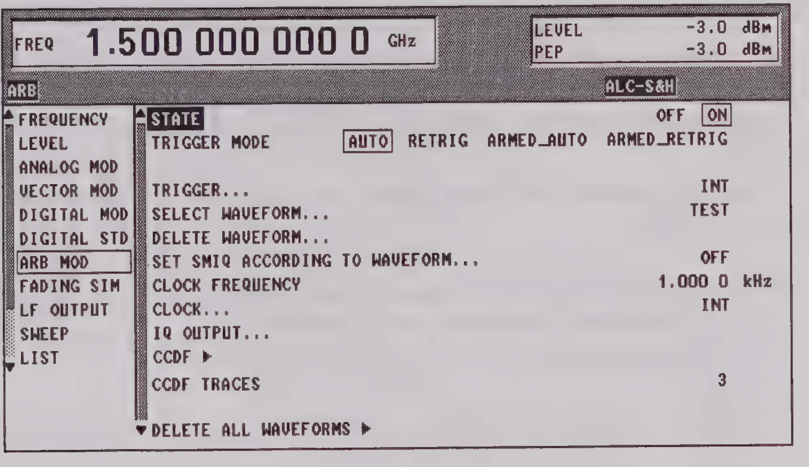


Fig. 2-217 Trigger signals SMIQB60

2.21.1.1 Use of WinIQSIM

The SMIQB60 is supported by WinIQSIM starting with version 3.30. Waveforms can be loaded via the IEC/IEEE bus into the FLASH memory, an individual operating menu allows for setting numerous SMIQ parameters. It must be noted that settings of WinIQSIM cannot be read from the waveform. WinIQSIM provides predefined settings for bit and symbol clock for the generation of trigger signals, slot and frame trigger and the restart signal for the BERT (SMIQB21). Si and filter compensation is not supported by WinIQSIM since these functions are carried out directly on the SMIQB60. Waveforms which contain these corrections may also be loaded in the SMIQB60. This is recognized and the internal correction filters are modified accordingly.

2.21.2 ARB MOD Menu



FREQ 1.500 000 000 0 GHz

LEVEL -3.0 dBm

PEP -3.0 dBm

STATE OFF ON

TRIGGER MODE AUTO RETRIG ARMED_AUTO ARMED_RETRIG

TRIGGER... INT

SELECT WAVEFORM... TEST

DELETE WAVEFORM...

SET SMIQ ACCORDING TO WAVEFORM... OFF

CLOCK FREQUENCY 1.000 0 kHz

CLOCK... INT

IQ OUTPUT...

CCDF ▸

CCDF TRACES 3

DELETE ALL WAVEFORMS ▸

Fig. 2-218 ARB MOD menu

STATE

Switching on/off ARB function. The state is indicated in the status line.

ON Loads the selected waveform in the output RAM and sets the module according to the current settings. This may last a few seconds depending on the length of the selected waveform. If vector modulation, digital modulation or digital standard were switched on, they are switched off automatically.

IEC/IEEE bus command `:ARB:STAT ON`

OFF Modulation is switched off, output of ARB waveform stops.

IEC/IEEE bus command `:ARB:STAT OFF`

TRIGGER MODE

Configuration of run control of ARB mode.

AUTO The calculated waveform (sequence) is output immediately and repeated in cycles. Trigger events are ignored.

IEC/IEEE bus command `:ARB:SEQ AUTO`

RETRIG Cyclic repetition of output. A trigger event initiates a restart.

IEC/IEEE bus command `:ARB:SEQ RETR`

ARMED_AUTO Only a trigger event initiates a start. Subsequent trigger events are ignored.

IEC/IEEE bus command `:ARB:SEQ AAUT`

ARMED_RETRIG Like ARMED_AUTO. Each following trigger event initiates a new start.

IEC/IEEE bus command `:ARB:SEQ ARET`

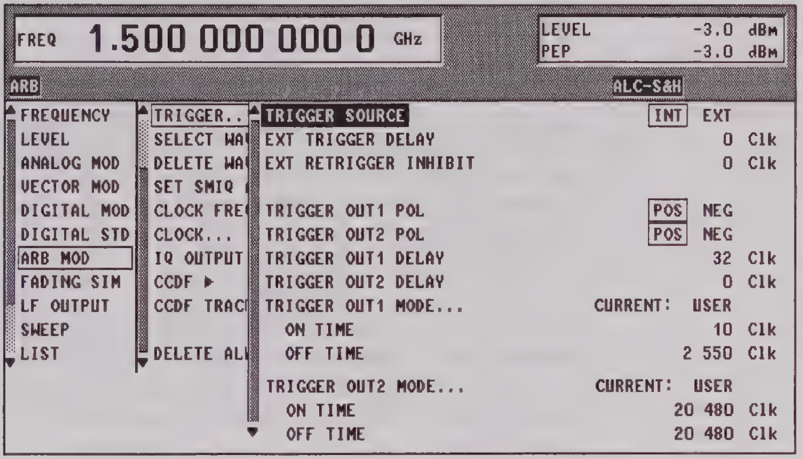
EXECUTE TRIGGER ➢

Initiates a trigger to start the ARB sequence (not displayed with TRIGGER MODE = AUTO).

IEC/IEEE bus command `:TRIG:DM:IMM`

TRIGGER...	Opens a window for setting the trigger facilities and the trigger outputs.
SELECT WAVEFORM...	Opens a window for selection of an ARB waveform and for display of its parameters.
DELETE WAVEFORM...	Opens a window for selection of an ARB waveform to be deleted.
SET SMIQ ACCORDING TO WAVEFORM...	Opens a window for configuration of the automatic setting of SMIQ parameters.
CLOCK FREQUENCY	Input value for the sample clock. If the clock is supplied by an external source, the applied frequency must be entered here. IEC/IEEE bus command `:ARB:CLOC 4.096MHz`
CLOCK...	Opens a window for selection of the clock source and delay of the clock signal.
IQ OUTPUT...	Opens a window for entry of the IQ level and mode.
CCDF ➢	Display of the Complementary Cumulative Distribution Function of the waveform loaded.
CCDF TRACES	Entry of the number of CCDFs. 1 to 3 may be selected.
DELETE ALL WAVEFORMS ➢	All waveforms are deleted (after being confirmed by the user).

2.21.2.1 ARB MOD - TRIGGER Menu


ARB
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
FADING SIM
LF OUTPUT
SWEEP
LIST
TRIGGER..
SELECT WA
DELETE WA
SET SMIQ
CLOCK FRE
CLOCK...
IQ OUTPUT
CCDF ▶
CCDF TRAC
DELETE AL

FREQ 1.500 000 000 0 GHz
LEVEL -3.0 dBm
PEP -3.0 dBm

TRIGGER SOURCE INT EXT
EXT TRIGGER DELAY 0 Clk
EXT RETRIGGER INHIBIT 0 Clk
TRIGGER OUT1 POL POS NEG
TRIGGER OUT2 POL POS NEG
TRIGGER OUT1 DELAY 32 Clk
TRIGGER OUT2 DELAY 0 Clk
TRIGGER OUT1 MODE... CURRENT: USER
ON TIME 10 Clk
OFF TIME 2 550 Clk
TRIGGER OUT2 MODE... CURRENT: USER
ON TIME 20 480 Clk
OFF TIME 20 480 Clk

Fig. 2-219 ARB MOD - TRIGGER... menu

TRIGGER... Opens a window for setting the trigger facilities and the trigger outputs.

TRIGGER SOURCE Configuration of trigger source.

INT Manually via EXECUTE TRIGGER or via software.
IEC/IEEE bus command `:ARB:TRIG:SOUR INT`

EXT Triggering with active edge at input socket.
IEC/IEEE bus command `:ARB:TRIG:SOUR EXT`

EXT TRIGGER DELAY Delays the start of sequence by the indicated samples after occurrence of an external trigger event. The resolution is 2 samples with clock rates (CLOCK) above 20 MHz (otherwise, 1 sample).
IEC/IEEE bus command `:ARB:TRIG:DEL 234`

EXT RETRIGGER INHIBIT Retriggering is inhibited until the sequence has reached the indicated sample.
IEC/IEEE bus command `:ARB:TRIG:INH 345`

TRIGGER OUT1(2) POL Signal polarity at the trigger outputs.

POS active state: positive voltage
IEC/IEEE bus command
`:ARB:TRIG:OUTP1:POL POS`

NEG active state: 0 or lower voltage
IEC/IEEE bus command
`:ARB:TRIG:OUTP2:POL NEG`

TRIGGER OUT1(2) DELAY Delay of the supplied trigger signals by the set number of samples.
IEC/IEEE bus command `:ARB:TRIG:OUTP:DEL 765`

(TRIGGER...) **TRIGGER OUT1(2) MODE**

The user may select one of the displayed modes for generation of the trigger output signals.
Depending on the system, WinIQSIM can calculate clock signals for BIT, SYMBOL, SLOT, FRAME and the restart signal (sequence) and transmit them together with the waveform. These modes set the counters for ON TIME and OFF TIME (see below). The names are defined as modes by WinIQSIM, stored in the waveform where they can be selected. The maximum number of defined counters is 4. It may occur that only the SEQUENZ and USER modes can be provided since the entirety of modes is not always available for all systems.

BIT_CLK	edge at beginning of each bit
SYMB_CLK	edge at beginning of each modulation symbol
SLOT_CLK	edge at beginning of each slot
FRAM_CLK	edge at beginning of each frame
SEQUENZ	edge at beginning of each sequence (may, e.g., be used as restart signal for BERT)
PULSE	pulse defined by WinIQSIM user
USER	The user defines a pulse by means of the parameters for ON and OFF TIME.

IEC/IEEE bus command `:ARB:TRIG:OUTP:MODE USER`

....ON TIME

Length of the active state of the output trigger signals indicated as number of samples. Can be modified only, if MODE = USER.
This time is repeated after OFF TIME has run off.

IEC/IEEE bus command `:ARB:TRIG:OUTP:ONT 3789`

....OFF TIME

Length of the non-active state of the output trigger signals indicated as number of samples. Can be modified only, if MODE = USER.

IEC/IEEE bus command `:ARB:TRIG:OUTP2:OFFT 639`

2.21.2.2 ARB MOD - SELECT WAVEFORM Menu

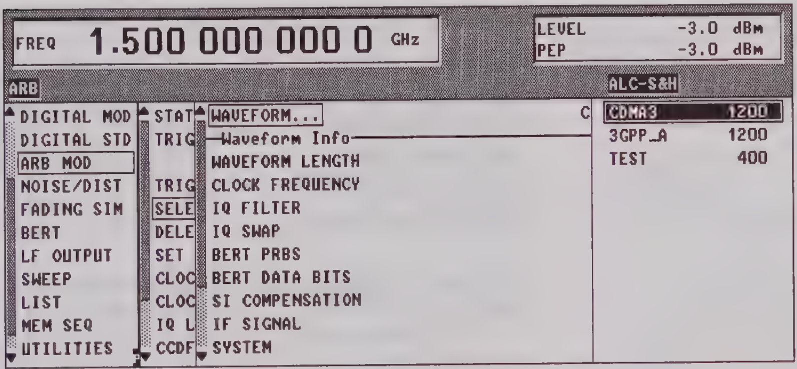
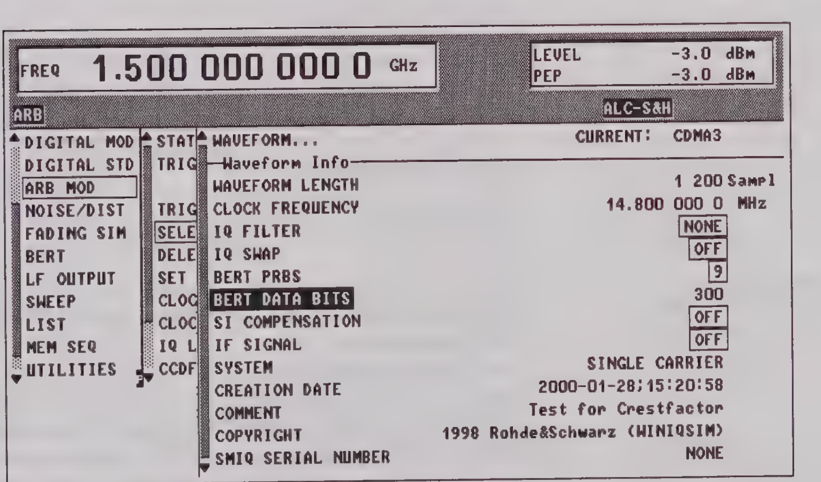

FREQ 1.500 000 000 0 GHz
LEVEL -3.0 dBm
PEP -3.0 dBm
ARB
ALC-S&H
DIGITAL MOD
DIGITAL STD
ARB MOD
NOISE/DIST
FADING SIM
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
STAT
TRIG
TRIG
SELE
DELE
SET
CLOC
CLOC
IQ L
CCDF
WAVEFORM...
C
Waveform Info
WAVEFORM LENGTH
CLOCK FREQUENCY
IQ FILTER
IQ SWAP
BERT PRBS
BERT DATA BITS
SI COMPENSATION
IF SIGNAL
SYSTEM
CDMA3 1200
3GPP_A 1200
TEST 400

Fig. 2-220 ARB MOD - SELECT WAVEFORM... menu

SELECT WAVEFORM... Opens a window for selection of an ARB waveform and for display of its parameters.

WAVEFORM A list of ARB waveforms is offered to the right. The length of the waveforms is indicated in samples. The user selects a waveform from this list, which then becomes the active waveform. Further information on this waveform is then provided.

IEC/IEEE bus command `:ARB:WAV:SEL 'TEST'`


FREQ 1.500 000 000 0 GHz
LEVEL -3.0 dBm
PEP -3.0 dBm
ARB
ALC-S&H
DIGITAL MOD
DIGITAL STD
ARB MOD
NOISE/DIST
FADING SIM
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
STAT
TRIG
TRIG
SELE
DELE
SET
CLOC
CLOC
IQ L
CCDF
WAVEFORM...
Waveform Info

CURRENT: CDMA3
WAVEFORM LENGTH 1 200 Sampl
CLOCK FREQUENCY 14.800 000 0 MHz
IQ FILTER NONE
IQ SWAP OFF
BERT PRBS 9
BERT DATA BITS 300
SI COMPENSATION OFF
IF SIGNAL OFF
SYSTEM SINGLE CARRIER
CREATION DATE 2000-01-28;15:20:58
COMMENT Test for Crestfactor
COPYRIGHT 1998 Rohde&Schwarz (WINIQSIM)
SMIQ SERIAL NUMBER NONE

Fig. 2-221 ARB MOD - WAVEFORM INFO menu

SELECT WAVEFORM... ----Waveform Info --------

These lines are for display purposes only, they cannot be modified. They are filled with values from the active waveform when opening the menu. After being confirmed by the user they are updated via (SELECT). Setting parameters are not set until this confirmation and errors are signaled with conflicts, when the menu is left via RETURN (see SET SMIQ ACCORDING TO WAVEFORM menu). IEC/IEEE bus commands have the form of queries.

SELECT WAVEFORM...	**WAVEFORM LENGTH**	Displays the number of samples contained in the waveform. IEC/IEEE bus command `:ARB:WAV:POIN?`
	CLOCK FREQUENCY	Provides the sample rate which the waveform has been generated for. This value is always entered as CLOCK FREQUENCY parameter of the ARB MOD menu with loading a waveform (independent of SET SMIQ ACCORDING TO WAVEFORM). IEC/IEEE bus command `:ARB:WAV:TAG? 'CLOCK'`
	IQ FILTER	Displays the setting used for calculation of the waveform in WinIQSIM. WinIQSIM sets this value to 0 (OFF), if the waveform is calculated for the SMIQ in particular. If, however, a compensation for an IQ filter is taken into account (for example when calculating the waveform for AMIQ), the compensation is automatically counted back by the SMIQ. Independently from this setting, however, a suitable IQ filter can be selected in the SMIQ. Compensation of the IQ filter is executed automatically by the SMIQ. The value of the waveform is used in the VECTOR MOD menu for the IQ filter if this is enabled in the SET SMIQ ACCORDING TO WAVEFORM menu. IEC/IEEE bus command `:ARB:WAV:TAG? 'LACP FILTER'`
	IQ SWAP	Indicates the setting used with the waveform calculation in WinIQSIM. The value is used in the VECTOR MOD menu for the IQ SWAP setting after enabling in the SET SMIQ ACCORDING TO WAVEFORM menu. IEC/IEEE bus command `:ARB:WAV:TAG? 'IQ SWAP'`
	BERT PRBS	Displays the type of PRBS used for the data in WinIQSIM, if the data have been prepared for a BER measurement. The setting is used in the BERT menu for setting the type of PRBS after clearing in the SET SMIQ ACCORDING TO WAVEFORM menu. A waveform cannot be used always for a BER measurement. If not, the display reads NONE. IEC/IEEE bus command `:ARB:WAV:TAG? 'PRBS LENGTH'`
	BERT DATA BITS	Indicates the number of data bits in a waveform, if the data have been prepared for a BER measurement by WinIQSIM. If the waveform cannot be used for a BER measurement, zero is displayed, here. IEC/IEEE bus command `:ARB:WAV:TAG? 'BERT DATABITS'`

(SELECT WAVEFORM...)	**SI COMPENSATION**	ON indicates that the SI compensation of WinIQSIM was taken into account with calculation of the waveform. Since the SMIQ, independent of that calculation, also takes into account the SI compensation, it is disabled in WinIQSIM, if the waveform is calculated particularly for the SMIQ. It is however counted back by the SMIQ, if it was taken into account e.g., with calculation of a waveform for the AMIQ. IEC/IEEE bus command `:ARB:WAV:TAG? 'SI COMPENSATED'`
	IF SIGNAL	ON indicates that the waveform was converted into an IF signal by WinIQSIM. This signal is not intended to be used as IQ vector. However, the user may derive it from the I-channel output and apply as IF signal. IEC/IEEE bus command `:ARB:WAV:TAG? 'IF_SIGNAL'`
	SYSTEM	Indicates the standard used to calculate the waveform in WinIQSIM. The following standards have been defined: Single Carrier, Multi Carrier, Multi Carrier Mixed Signal, 3GPP W-CDMA, IS-95, CDMA2000 and Import. IEC/IEEE bus command `:ARB:WAV:TAG? 'SYSTEM'`
	CREATION DATE	Displays the creation date of the waveform. Provides the user with information and is added automatically with calculation in WinIQSIM. IEC/IEEE bus command `:ARB:WAV:TAG? 'DATE'`
	COMMENT	Is provided for information purposes and may be entered as comment text in WinIQSIM. IEC/IEEE bus command `:ARB:WAV:TAG? 'COMMENT'`
	COPYRIGHT	Indicates whether the waveform was generated using the protected software WinIQSIM. IEC/IEEE bus command `:ARB:WAV:TAG? 'COPYRIGHT'`
	SMIQ SERIAL NUMBER	Is important, if the wave was generated in WinIQSIM with IQ FILTER = ON. In this case, filter coefficients have been used for compensation, which are best only for SMIQs with this serial number.

2.21.2.3 ARB MOD - DELETE WAVEFORM Menu

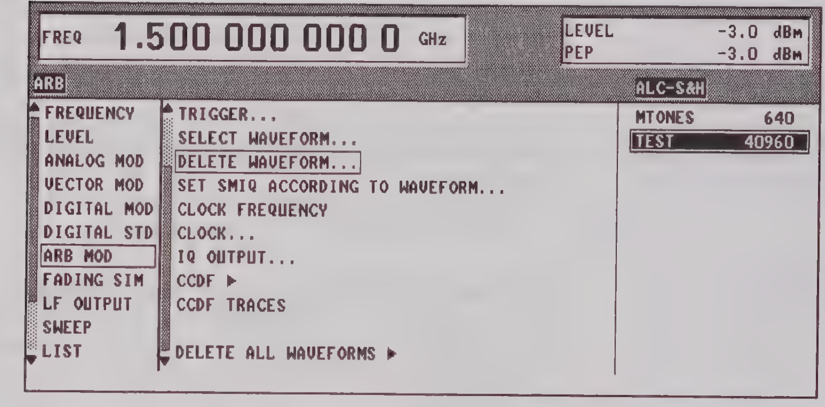

FREQ 1.500 000 000 0 GHz
LEVEL -3.0 dBm
PEP -3.0 dBm
ARB
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
FADING SIM
LF OUTPUT
SWEEP
LIST
TRIGGER...
SELECT WAVEFORM...
DELETE WAVEFORM...
SET SMIQ ACCORDING TO WAVEFORM...
CLOCK FREQUENCY
CLOCK...
IQ OUTPUT...
CCDF ▶
CCDF TRACES
DELETE ALL WAVEFORMS ▶
MTONES 640
TEST 40960

Fig. 2-222 ARB MOD - DELETE WAVEFORM... menu

DELETE WAVEFORM... Opens a window for selection of an ARB waveform to be deleted. A list of ARB waveforms including information on their lengths is offered to the right, where the user may select a waveform.

IEC/IEEE bus command `:ARB:WAV:DEL 'TEST'`

2.21.2.4 ARB MOD - SET SMIQ ACCORDING TO WAVEFORM Menu

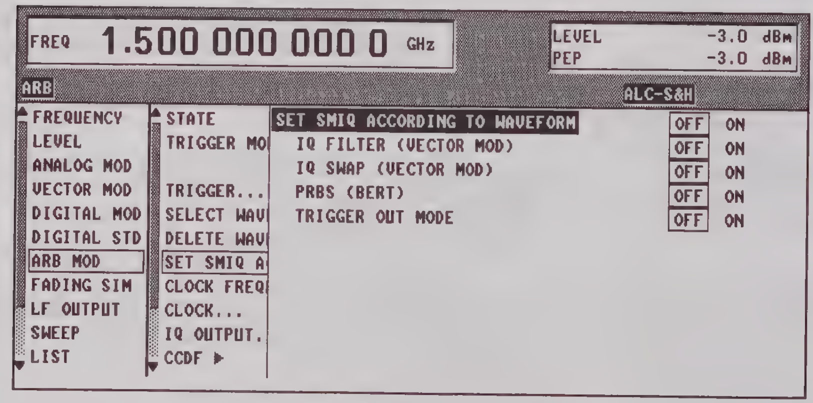

FREQ 1.500 000 000 0 GHz
LEVEL -3.0 dBm
PEP -3.0 dBm
ARB
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
FADING SIM
LF OUTPUT
SWEEP
LIST
STATE
TRIGGER MO
TRIGGER...
SELECT WAV
DELETE WAV
SET SMIQ A
CLOCK FREQ
CLOCK...
IQ OUTPUT.
CCDF ▶
SET SMIQ ACCORDING TO WAVEFORM OFF ON
IQ FILTER (VECTOR MOD) OFF ON
IQ SWAP (VECTOR MOD) OFF ON
PRBS (BERT) OFF ON
TRIGGER OUT MODE OFF ON

Fig. 2-223 ARB MOD - SET SMIQ ACCORDING TO WAVEFORM menu

SET SMIQ ACCORDING TO WAVEFORM... Opens a window for configuration of the automatic setting of SMIQ parameters.

SET SMIQ ACCORDING TO WAVEFORM

ON In any case, settings on the SMIQ are made based on the information from the waveform to be loaded.
IEC/IEEE bus command
`:ARB:ASET:STAT ON`

OFF No settings are made, even if the following configurations are set ON. CLOCK FREQUENCY in the ARB MOD menu is always set.
IEC/IEEE bus command
`:ARB:ASET:STAT OFF`

(SET SMIQ ACCORDING TO WAVEFORM...)

....IQ FILTER (VECTOR MOD)

The parameter IQ FILTER (filter of LACP Option SMIQB47) is set in the VECTOR MOD menu. If a waveform in WinIQSIM was compensated with the frequency responses of this filter, it should be switched on. A warning is output, if the waveform is compensated, however, this compensation does not apply for an SMIQ of this serial number or if the option is not fitted to the SMIQ.

ON Parameter is set
IEC/IEEE bus command
`:ARB:ASET:DM:IQF ON`

OFF Parameter is not set
IEC/IEEE bus command
`:ARB:ASET:DM:IQF OFF`

...IQ SWAP (VECTOR MOD)

The IQ SWAP parameter in the VECTOR MOD menu is set. If a waveform has been generated in WinIQSIM for this mode, it should be switched on. A warning is output, if the parameter is not set as indicated in the waveform.

ON Parameter is set
IEC/IEEE bus command
`:ARB:ASET:DM:IQSW ON`

OFF Parameter is not set
IEC/IEEE bus command
`:ARB:ASET:DM:IQSW OFF`

...PRBS (BERT)

The type of PRBS in the BERT menu is set. If a waveform has been generated in WinIQSIM with this PRBS, the latter should be selected with BER measurement. A warning is output, if the parameter is not set as indicated in the waveform.

ON Parameter is set
IEC/IEEE bus command
`:ARB:ASET:BERT:TYPE ON`

OFF Parameter is not set
IEC/IEEE bus command
`:ARB:ASET:BERT:TYPE OFF`

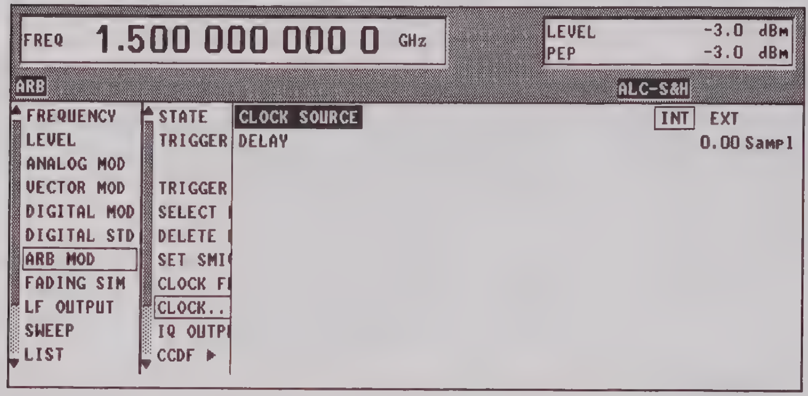
...TRIGGER OUT MODE

The parameters TRIGGER OUT1 MODE and TRIGGER OUT2 MODE in the ARB MOD TRIGGER... menu are set. Thus, the user can select the function of the SMIQ trigger outputs in WinIQSIM (see TRIGGER menu).

ON Parameter is set
IEC/IEEE bus command
`:ARB:ASET:TRIG:MODE ON`

OFF Parameter is not set
IEC/IEEE bus command
`:ARB:ASET:TRIG:MODE OFF`

2.21.2.5 ARB MOD - CLOCK... Menu


FREQ 1.500 000 000 0 GHz

LEVEL -3.0 dBm

PEP -3.0 dBm

CLOCK SOURCE INT EXT

DELAY 0.00 Sampl

ARB
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
FADING SIM
LF OUTPUT
SWEEP
LIST
STATE
TRIGGER
TRIGGER
SELECT
DELETE
SET SMI
CLOCK FI
CLOCK..
IQ OUTP
CCDF ▶

Fig. 2-224 ARB MOD - CLOCK... menu

CLOCK... Opens a window for selection of the clock source and delay of the clock signal.

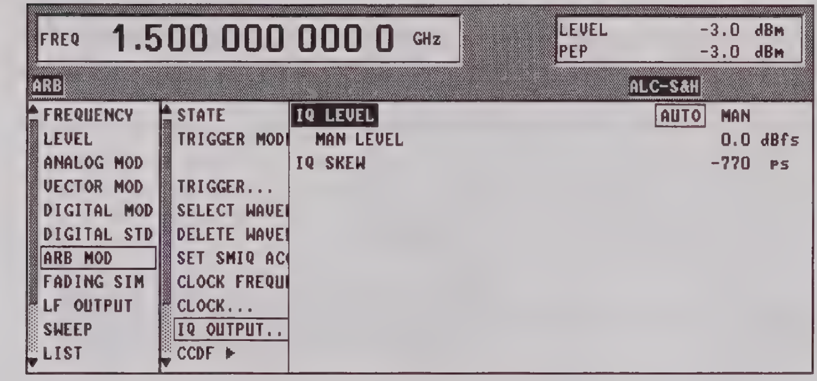
CLOCK SOURCE Defines the source for the sample clock.

INT Clock is generated internally.
IEC/IEEE bus command `:ARB:CLOC:SOUR INT`

EXT Clock must be applied at the socket.
IEC/IEEE bus command `:ARB:CLOC:SOUR EXT`

DELAY Delay of the modulation signal against the clock signal. This may be used, e.g., for synchronization with a second instrument, to obtain time-synchronous modulation signals of both instruments.
IEC/IEEE bus command `:ARB:CLOC:DEL 0.55`

2.21.2.6 ARB MOD - IQ OUTPUT... Menu


ARB
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
FADING SIM
LF OUTPUT
SWEEP
LIST
STATE
TRIGGER MOD
TRIGGER...
SELECT WAVE
DELETE WAVE
SET SMIQ AC
CLOCK FREQU
CLOCK...
IQ OUTPUT..
CCDF ▶

FREQ 1.500 000 000 0 GHz
LEVEL -3.0 dBm
PEP -3.0 dBm
IQ LEVEL AUTO MAN
MAN LEVEL 0.0 dBfs
IQ SKEW -770 ps

Fig. 2-225 ARB MOD - IQ OUTPUT... menu

IQ OUTPUT... Opens a window for entry of the IQ level and automatic level setting.

IQ LEVEL Defines the operating mode for setting the IQ level.

AUTO The level is set automatically to a maximum sum vector of 0.5 V for maximum input level of the IQ modulator. The subsequent level entry is not effective.

IEC/IEEE bus command
`:ARB:IQ:LEV:MODE AUTO`

MAN The level is set using the level entry below. The instrument data can not be guaranteed for entry values above 0 dB, since the output level is limited. However, the signal-to-noise ratio, e.g., may thus be increased.

IEC/IEEE bus command
`:ARB:IQ:LEV:MODE MAN`

MAN LEVEL Indicates the IQ level referred to maximum voltage.

Range: -6 to 3 dBfs

IEC/IEEE bus command `:ARB:IQ:LEV -2.5DB`

IQ SKEW Determines the delay between I and Q channel. Positive values delay I against Q.

IEC/IEEE bus command `:ARB:IQ:SKEW -250ps`

2.22 External Modulation Source AMIQ

AMIQ from Rohde&Schwarz is now available as an external source for I/Q modulation signals. The generated signals are provided by the unit at modulation outputs I and Q on the front panel. The signals can be fed to modulation inputs I and Q of SMIQ.
With vector modulation mode activated, SMIQ modulates (I/Q modulation) the modulation signals generated by AMIQ onto the RF carrier.

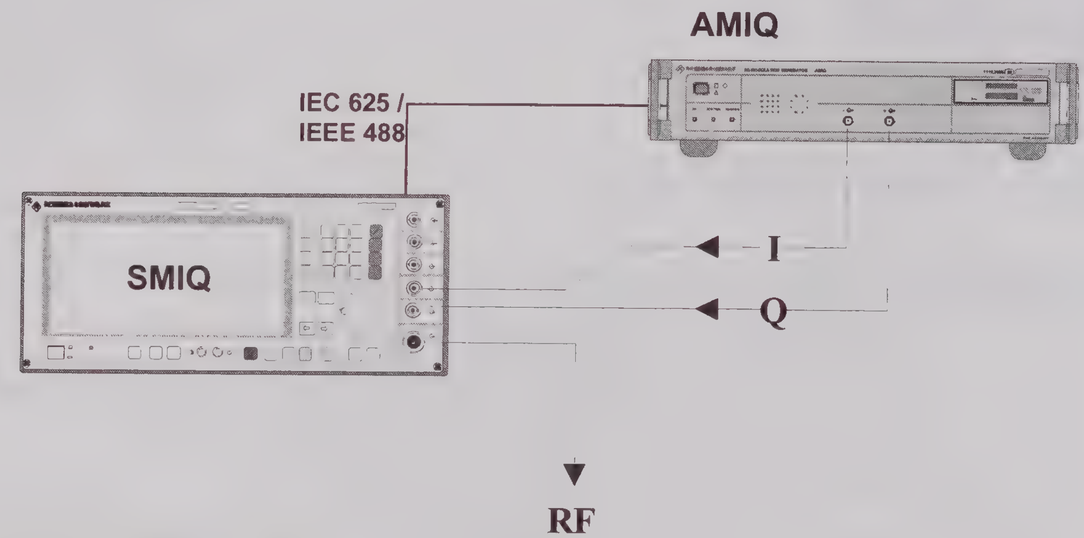


Fig. 2-226 Vector modulation with an external AMIQ

Menu VECTOR MOD provides access to settings required for vector modulation. Settings for generating the modulation signals are made in menu AMIQ CTRL. This menu allows to set the most important parameters of AMIQ via the remote-control interface IEC625/IEEE488. The IEC/IEEE-bus address of AMIQ is given in menu UTILITIES / SYSTEM / GPIB.

Note: *Menu AMIQ CTRL is masked out when SMIQ is delivered. It has to be switched on in menu UTILITIES / INSTALL.*

Complex signal data are created on the PC using Windows software WinIQSIM.

1. If WinIQSIM and AMIQ are linked via IEC/IEEE-bus or RS232 interface, signal data can be fully stored as waveform file on the hard disk of AMIQ. Signal data are then loaded from SMIQ to AMIQ by means of the following commands: SELECT WAVEFORM/EXECUTE BATCH ... DRIVE, HARDDISK and by selection of the file name.
2. If there is no remote-control connection between WinIQSIM and AMIQ, signal data up to 300.000 samples can be stored as waveform file on floppy. Signal data are then loaded from SMIQ to AMIQ by means of the following commands:
   SELECT WAVEFORM/EXECUTE BATCH ... DRIVE, FLOPPY.

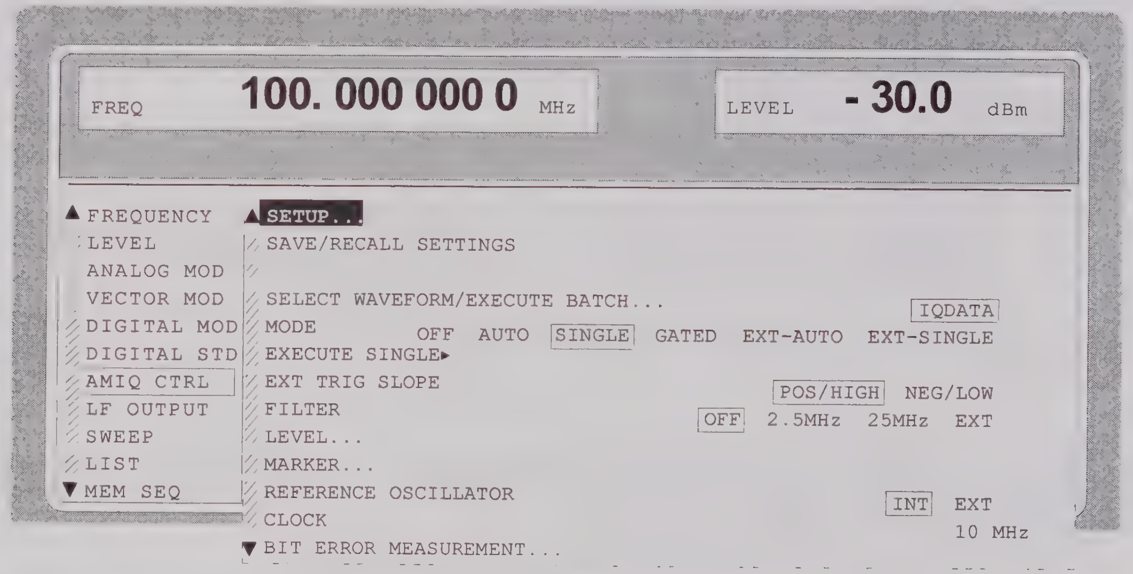
   A faster access is possible if signal data are stored as batch files on a floppy. The floppy is then inserted into AMIQ and the batch file is called up from SMIQ by means of the following commands:
   SELECT WAVEFORM/EXECUTE BATCH ...EXECUTE BATCH FROM FLOPPY.
   The signal data are stored as waveform file on the AMIQ hard disk under the name defined in WinIQSIM. After that, the waveform file can be loaded as described under 2.

For generation of waveform files for AMIQ see chapter on "Menu item AMIQ" in the WinIQSIM manual".

The system consisting of SMIQ as modulation coder, AMIQ as source for baseband signals and WinIQSIM for signal data configuration makes the generation of any complex, digitally modulated communication signals convenient and easy.

To remote control AMIQ via IEC625/IEEE488, SMIQ is configured as a system controller. In the normal mode, SMIQ is configured as a talker/listener.
In general, only one unit can be the system controller on the bus.
With menu AMIQ CTRL called up, SMIQ is automatically configured as the controller. If this is not possible since for example another unit (eg a PC) has already taken over the controller function, menu AMIQ CTRL will not be opened and an error message will be issued.
When menu AMIQ CTRL is closed, SMIQ will again switch to the talker/listener mode.

Menu selection: AMIQ CTRL



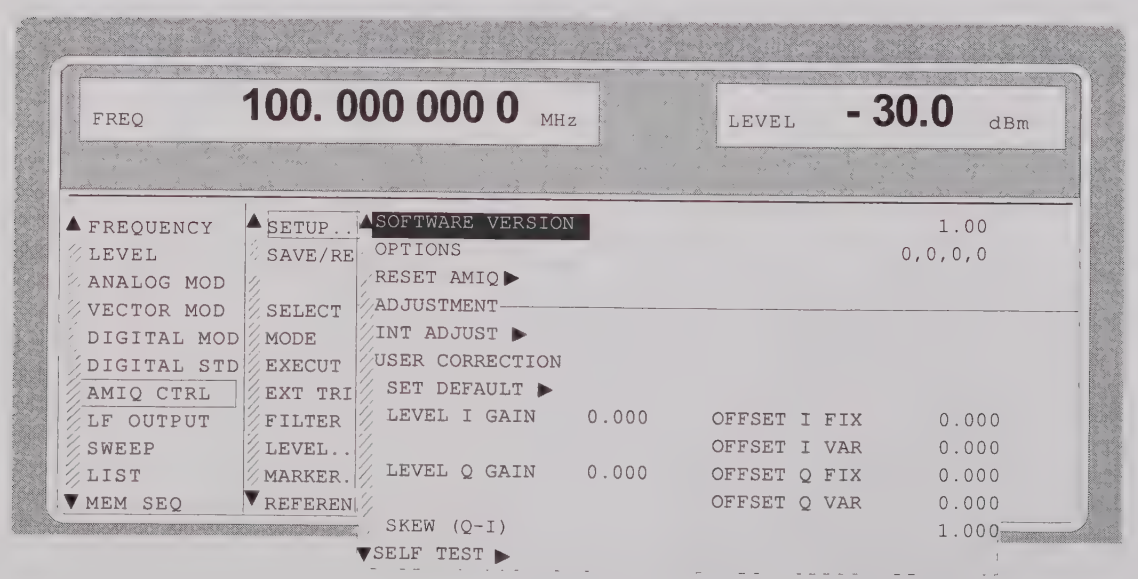
FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

SETUP...
SAVE/RECALL SETTINGS
SELECT WAVEFORM/EXECUTE BATCH... IQDATA
MODE OFF AUTO SINGLE GATED EXT-AUTO EXT-SINGLE
EXECUTE SINGLE▸
EXT TRIG SLOPE POS/HIGH NEG/LOW
FILTER OFF 2.5MHz 25MHz EXT
LEVEL...
MARKER...
REFERENCE OSCILLATOR INT EXT
CLOCK 10 MHz
BIT ERROR MEASUREMENT...

▲ FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
AMIQ CTRL
LF OUTPUT
SWEEP
LIST
▼ MEM SEQ

Fig. 2-227 Menu AMIQ CTRL (presetting depends on AMIQ)

SETUP... Opens a window to set the basic configuration of AMIQ.
The window offers functions to perform automatic adjustment, fine adjustment, selftest and to reset to a defined basic state.



FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

SOFTWARE VERSION 1.00
OPTIONS 0,0,0,0
RESET AMIQ ▶
ADJUSTMENT
INT ADJUST ▶
USER CORRECTION
SET DEFAULT ▶
LEVEL I GAIN 0.000
LEVEL Q GAIN 0.000
OFFSET I FIX 0.000
OFFSET I VAR 0.000
OFFSET Q FIX 0.000
OFFSET Q VAR 0.000
SKEW (Q-I) 1.000
SELF TEST ▶

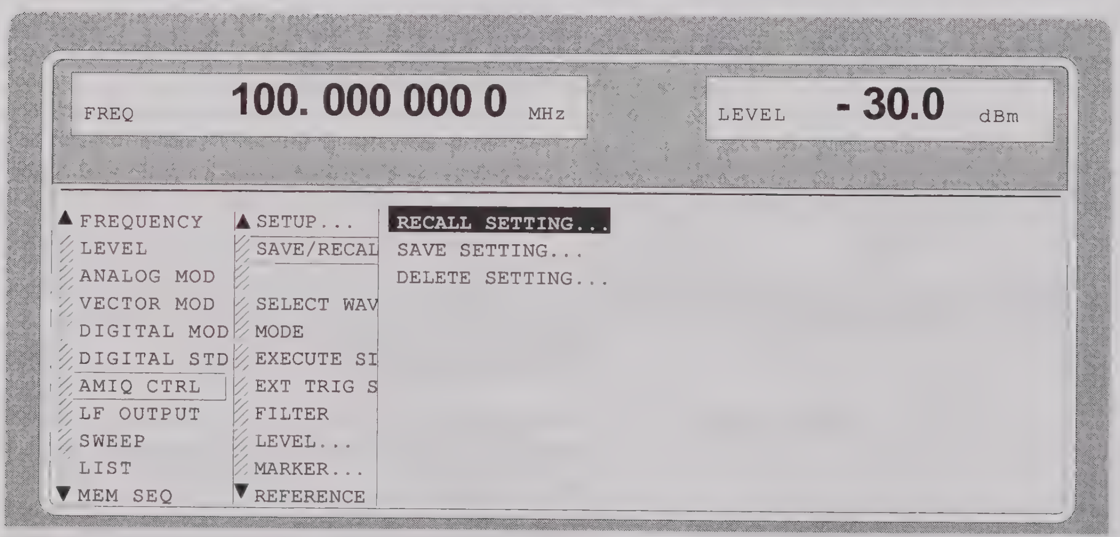
▲ FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
AMIQ CTRL
LF OUTPUT
SWEEP
LIST
▼ MEM SEQ
▲ SETUP...
SAVE/RE
SELECT
MODE
EXECUT
EXT TRI
FILTER
LEVEL..
MARKER.
▼ REFEREN

Fig. 2-228 Menu AMIQ CTRL -SETUP...

(SETUP...)	**SOFTWARE VERSION**	Display of the current AMIQ firmware version AMIQ.
	OPTIONS	Display of AMIQ options.
	RESET AMIQ ►	Resets AMIQ to a defined basic state.
	INT ADJUST ►	Starts automatic self-adjustment of AMIQ: – Level adjustment (I/Q) to 1.0 Volt when fully equipped – Offset adjustment (I/Q) to a minimum – Fine adjustment of reference oscillator to 10 MHz – Minimization of delay between I and Q channel
	USER CORRECTION	The user correction allows the fine adjustment of level and offset of the I and Q channel as well as the minimization of the delay between the channels of the complete system. The internal adjustment (INT ADJUST) is not taken into account.
	SET DEFAULT ►	Resets the user correction to the default values. This applies to the following parameters: – LEVEL I GAIN (*) – OFFSET I FIX (*) – OFFSET I VAR (**) – LEVEL Q GAIN (*) – OFFSET Q FIX (*) – OFFSET Q VAR (**) – SKEW (I-Q) * These parameters will only be effective provided that a fixed level of 0.5 V/50 Ohm has been assigned to the corresponding channel. For variable levels, the settings of these parameters are ineffective. A correction of the gain factor is possible when the level is entered. ** These parameters are only effective in case of variable levels and have no effect in case of fixed levels. The level is set in the submenu LEVEL... (LEVEL I/Q = 0.5V/50 Ohm or VAR).
	LEVEL I GAIN	Fine adjustment of gain factor for I channel in case of fixed level. Relative gain factors in the range ±10% can be entered.
	OFFSET I FIX	Fine adjustment of level offset for the I channel in case of fixed level. The relative factors ±1.0 correspond to a an offset detuning of ±30 mV.

(SETUP...)	**OFFSET I VAR**	Fine adjustment of level offset for the I channel in case of variable level. The offset detuning depends on the of the mechanical attenuator setting of AMIQ. See AMIQ manual, section SOURce - Hardware Settings.
	LEVEL Q GAIN	Fine adjustment of gain factor for the Q channel in case of fixed level.
	OFFSET Q FIX	Fine adjustment of level offset for the Q channel in case of fixed level.
	OFFSET Q VAR	Fine adjustment of level offset for the Q channel in case of variable level.
	SKEW (Q-I)	Defining the delay between the I and Q channel. Positive values delay I compared with Q. The delay can be modified by entering the relative factors ±1.0 in a range of approx. ±1 ns at a resolution of 10 ps.
	SELF TEST ▶	Triggers the internal AMIQ selftest which comprises the following: – memory test of the output memory. – control of components of analog hardware and check of signal paths by means of the built-in diagnostic A/D converter.

SAVE/RECALL SETTINGS...	Opens a window to save/load/delete a maximum number of 100 AMIQ setups.


FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

▲ FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
AMIQ CTRL
LF OUTPUT
SWEEP
LIST
▼ MEM SEQ
▲ SETUP...
SAVE/RECAL
SELECT WAV
MODE
EXECUTE SI
EXT TRIG S
FILTER
LEVEL...
MARKER...
▼ REFERENCE
RECALL SETTING...
SAVE SETTING...
DELETE SETTING...

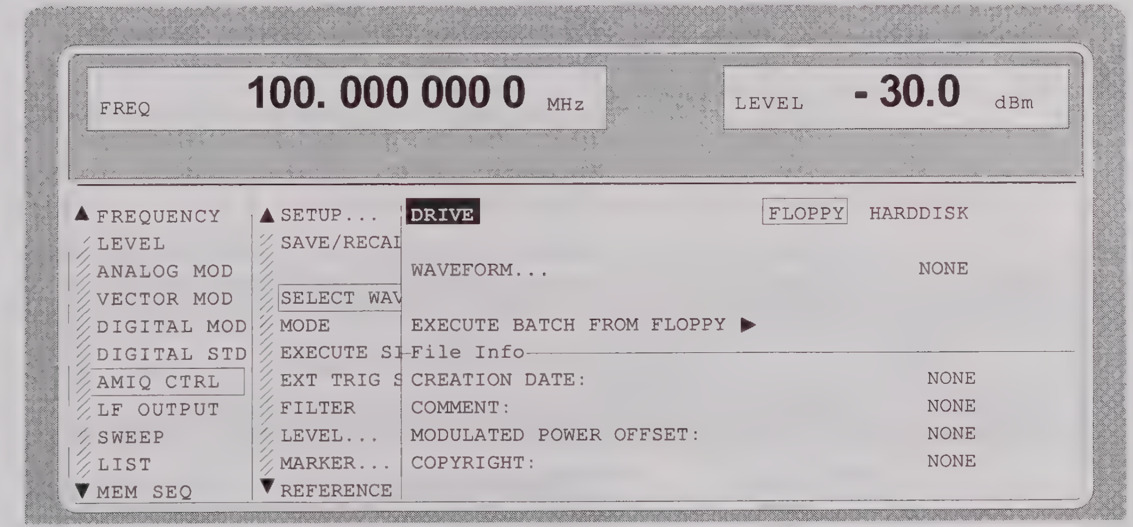
Fig. 2-229 Menu AMIQ CTRL -SAVE/RECALL SETTINGS...

(SAVE/RECALL SETTINGS...)	**RECALL SETTING...**	Selects and loads a stored AMIQ setup. The available setups are offered in a select window.

(SAVE/RECALL SETTINGS...)	**SAVE SETTING...**	Saves the current AMIQ setup. An already existing setup can be selected and overwritten in a select window. A new setup is created by selecting CREATE NEW SETTING ▶. SMIQ automatically offers SETTxx with xx being in the range from 0 to 99. This name can be modified any time as required.
	DELETE SETTING...	Deletes AMIQ setups. The available setups are offered in a select window.

SELECT WAVEFORM/ EXECUTE BATCH...

Opens a window to
- load signal data from diskette or hard disk into the AMIQ internal memory,
- transfer signal data generated by WinIQSIM to the AMIQ hard disk,
- display current signal data.


▲ FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
AMIQ CTRL
LF OUTPUT
SWEEP
LIST
▼ MEM SEQ
▲ SETUP...
SAVE/RECAL
SELECT WAV
MODE
EXECUTE SI
EXT TRIG S
FILTER
LEVEL...
MARKER...
▼ REFERENCE

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

DRIVE FLOPPY HARDDISK
WAVEFORM... NONE
EXECUTE BATCH FROM FLOPPY ▶
File Info
CREATION DATE: NONE
COMMENT: NONE
MODULATED POWER OFFSET: NONE
COPYRIGHT: NONE

Fig. 2-230 Menu AMIQ - SELECT WAVEFORM/EXECUTE BATCH...

(SELECT WAVEFORM/ EXECUTE BATCH...)	**DRIVE**	Selects storage medium with the signal data to be activated.
	DIRECTORY...	Selects the directory comprising the required signal data on the AMIQ hard disk. The directory structure is freely selectable and can be modified with WinIQSIM. SMIQ does not allow to change the directory structure. The menu item is only visible with DRIVE = HARDDISK.

(SELECT WAVEFORM/ EXECUTE BATCH...)	**WAVEFORM...**	Selects the signal data to be loaded. The data available on diskette or hard disk are offered in a select window. After selecting the signal data, the following actions are performed: 1. stopping the currently active signal output, 2. preparing the selected signal data, 3. further procedure is according to parameter MODE in main menu.
	SET SMIQ LEVEL ACCORDING TO WAVEFORM	Waveform files may contain information on LEVEL OFFSET and CREST FACTOR. If this function is active, these values are set automatically upon loading the waveform. If no waveform is loaded, they are set to zero.
	EXECUTE BATCH FROM FLOPPY ▶	Executes the batch file WINIQSIM.IEC generated by WinIQSIM from diskette. The signal data including the given directory structure are stored on the AMIQ hard disk. The directory structure is defined by the user during the generation of the diskette.
	FILE INFO	All menu entries displayed under FILE INFO are not editable. The file info (date, comment and origin as well as power offset of modulation) from the selected files is displayed.

MODE Configuration of trigger conditions for signal output. Any change in the trigger conditions resets the signal output and starts it again. Retriggering a running signal output is inhibited.

OFF	Stops the signal output, output of the idle signal.
AUTO	Continuous signal output, trigger events are ignored.
SINGLE	Prepares a single signal output. The idle signal is output first. Signal output is started via menu item EXECUTE SINGLE ▶. After completion of the signal output, the idle signal is output. External trigger events are ignored.
GATED	The level of the signal applied to the trigger input controls the output. If the level (HIGH/LOW) set under EXT TRIG SLOPE is applied, the signal output will be restarted and the signal will be output continuously. If the level set under EXT TRIG SLOPE is not applied, the output will be interrupted and the idle signal will be output.
EXT-AUTO	Continuous signal output. The idle signal is output first. The signal output is started via an external trigger event. Further trigger events are ignored. A signal slope whose polarity is configured in menu item EXT TRIG SLOPE (POS/NEG) is taken as a trigger event.
EXT-SINGLE	Prepares a single signal output. The idle signal is output first. The signal output is started via an external trigger event. After completion of the signal output, the idle signal is output until the next trigger event is obtained.

EXECUTE SINGLE ▶ Starts a single signal output. This menu item is displayed only with MODE = SINGLE.

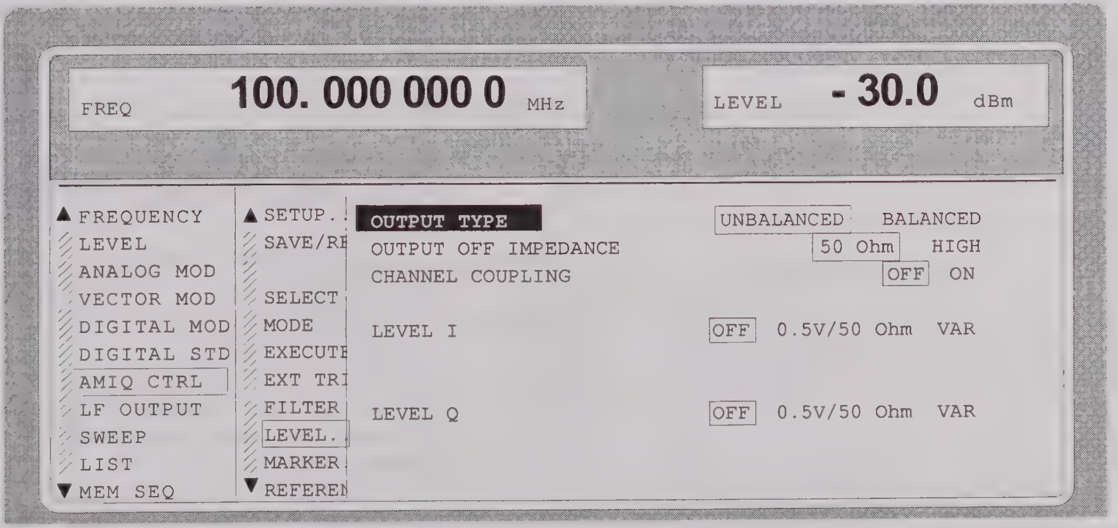
EXT TRIG SLOPE Selects the polarity of an external trigger signal at connector TRIG. This external trigger signal generates a trigger event. This setting has an effect on modes GATED (HIGH/LOW), EXT-AUTO and EXT-SINGLE (POS/NEG).

FILTER Configuration of reconstruction filters for signal paths I and Q.

OFF	Output of unfiltered I/Q signals
2.5 MHz	Loop in of the internal 2.5 MHz lowpass filter.
25 MHz	Loop in of the internal 25 MHz lowpass filter
EXT	Loop in of external bandpass filters for the I and Q channel. For a detailed specification of the filter see AMIQ manual, section Signal Inputs and Outputs.

Note: *With external filtering it has to be made sure that a filter is connected to both filter inputs I and Q on the rear of AMIQ. The two signal outputs can be switched off completely during external filtering provided that no filter is connected to the corresponding filter input.*

LEVEL... Opens a window to set the level for the two signal paths I and Q.


FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm
▲ FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
AMIQ CTRL
LF OUTPUT
SWEEP
LIST
▼ MEM SEQ
▲ SETUP.
SAVE/RE
SELECT
MODE
EXECUTE
EXT TRI
FILTER
LEVEL.
MARKER
▼ REFEREN

OUTPUT TYPE [UNBALANCED] BALANCED
OUTPUT OFF IMPEDANCE [50 Ohm] HIGH
CHANNEL COUPLING [OFF] ON
LEVEL I [OFF] 0.5V/50 Ohm VAR
LEVEL Q [OFF] 0.5V/50 Ohm VAR

Fig. 2-231 Menu AMIQ CTRL -LEVEL...

(LEVEL...)

OUTPUT TYPE

This command switches between ground-referred outputs I and Q (UNBalanced) and differential I and Q outputs (BALanced).

UNBalanced	The level indicated for LEVEL I/Q (0 V to 1 V) is the amplitude Vp of the inner conductor of BNC connectors I and Q referred to ground, measured on a terminating impedance of 50 W.
BALanced	The level indicated for LEVEL I/Q (0 V to 4 V) is the amplitude Vpp between the inner conductors of BNC connectors I and $\overline{I}$ as well as Q and $\overline{Q}$ with a high-impedance termination.

OUTPUT OFF IMPEDANCE

This command sets the impedance of the AMIQ outputs disconnected by LEVEL I/Q=OFF, with or without option AMIQ-B2 (differential outputs). The output impedance is indicated for both channels.
If the command LEVEL I/Q != OFF is activated when the AMIQ outputs are switched on, this is equivalent to presetting which will only have an effect when the AMIQ outputs are turned off.

50Ω	Corresponds to the setting LEVEL I/Q VAR = 0 V.
HIGH	The output connectors are isolated with a relay and become high impedance.

DIGITAL OUTPUT

Switching the digital outputs on or off (option AMIQ-B3).

CHANNEL COUPLING

Switches on/off coupling of level setting for the I and Q channel.

OFF	Separate setting of the two channels
ON	Combined setting of the two channels

LEVEL I/Q

Selection of level setting for the I/Q channel.

OFF	Switch-off of output
0.5V/50Ohm	Fixed setting of 1 V_{pp} into 50 Ohm
VAR	Variable setting of level

Note: *The fine setting of level and offset can be made in menu AMIQ CTRL - SETUP....*

LEVEL

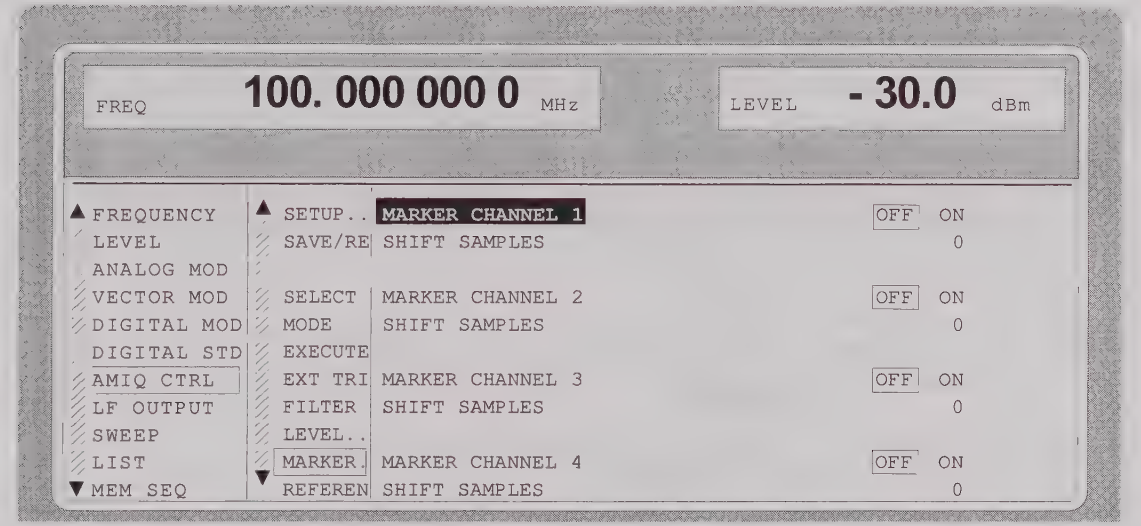
Variable setting of level for the I/Q channel. This menu item is displayed only with LEVEL I/Q = VAR.

(LEVEL) **BIAS**

DC offset in I/Q channel for the BALANCED setting. The indicated level remains the same with the I/Q output (LEVEL I/Q = OFF) turned off, provided the output impedance was prior set to 50 Ω with the command OUTPUT OFF IMPEDANCE.
If the output impedance is set to HIGH with the I/Q output off, the output connector is isolated with a relay and the BIAS setting has no effect.
(Only with option AMIQ-B2).

MARKER...

Opens a window to configure the four binary marker outputs of AMIQ (eg to control power ramping of SMIQ), see AMIQ operating manual, section Marker Outputs.



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

MARKER CHANNEL 1 OFF ON
SHIFT SAMPLES 0
MARKER CHANNEL 2 OFF ON
SHIFT SAMPLES 0
MARKER CHANNEL 3 OFF ON
SHIFT SAMPLES 0
MARKER CHANNEL 4 OFF ON
SHIFT SAMPLES 0

Fig. 2-232 Menu AMIQ CTRL - MARKER...

(MARKER...) **MARKER CHANNEL 1/2/3/4**

Configuration of marker output 1/2/3/4.

OFF	No marker output
ON	Marker output
SHIFT	Marker output displaced by SHIFT SAMPLES.

SHIFT SAMPLES

Number of samples by which the marker of the corresponding marker channel is displaced. Negative values cause the marker sequence to be started earlier while positive values make for a delay of the marker sequence.

REFERENCE OSCILLATOR

Setting the synchronization by a reference clock.

INT	Use of internally generated 10 MHz clock.
EXT	The frequency can be synchronized to an external clock of 10 MHz via the REF input of AMIQ.

Note: *The high-precision 10 MHz clock generated by the internal reference oscillator is always output at the REF output of AMIQ.*

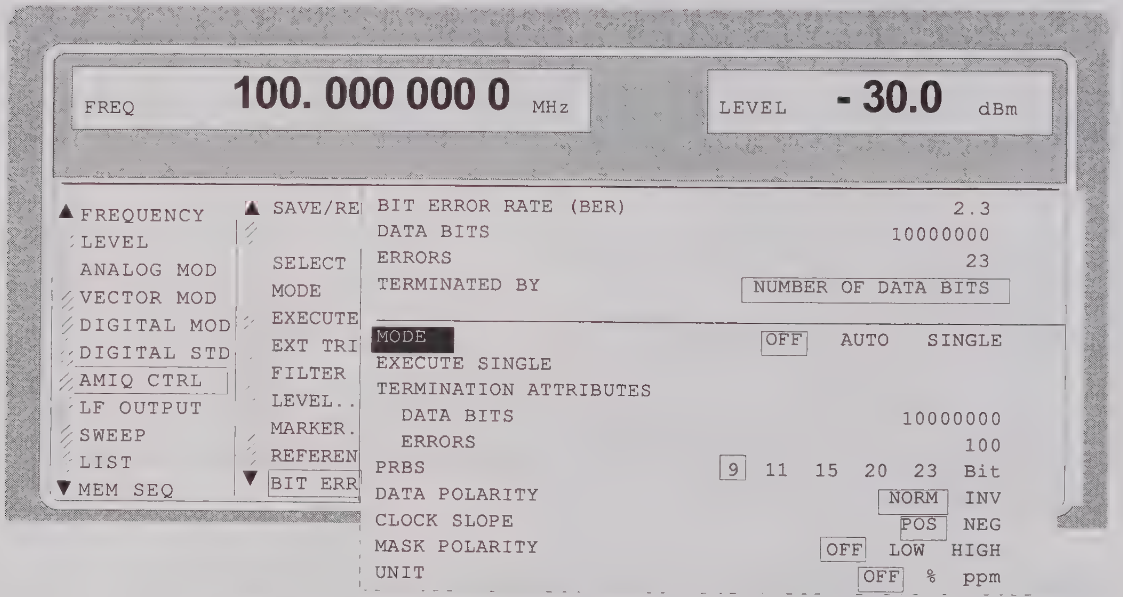
CLOCK Sets a clock rate, at which samples can be read out of the output buffer and transferred to the output connectors I and Q via the D/A converters.
Range: 5 MHz to 200 MHz

CLOCK SOURCE Selection of the clock signal source (model AMIQ03 or higher).

INT	The internal clock is used. This is the standard setting present upon switching on the unit or changing the clock.
EXT_SLOW	For external clock < 4 MHz
EXT_FAST	For external clock > 2 MHz

OUTPUT RESOLUTION Output resolution of the signal. The resolution set for output cannot be higher than for waveform generation.

BIT ERROR RATE TEST This menu enables configuration and performance of BER measurement.



FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

BIT ERROR RATE (BER) 2.3
DATA BITS 10000000
ERRORS 23
TERMINATED BY NUMBER OF DATA BITS

MODE OFF AUTO SINGLE
EXECUTE SINGLE
TERMINATION ATTRIBUTES
DATA BITS 10000000
ERRORS 100
PRBS 9 11 15 20 23 Bit
DATA POLARITY NORM INV
CLOCK SLOPE POS NEG
MASK POLARITY OFF LOW HIGH
UNIT OFF % ppm

Fig. 2-233 Menu AMIQ CTRL – BIT ERROR RATE TEST...

(BIT ERROR RATE TEST...)

BIT ERROR RATE (BER) Cyclical indication of the BER measured, ie the ratio of errors occurred to the number of data bits transmitted.

DATA BITS Number of data bits transmitted so far.

ERRORS Number of errors occurred so far.

TERMINATED BY The cause of the indicated measurement result is:

NUMBER OF DATA BITS	The specified number of data bits has been attained.
NUMBER OF ERRORS	The specified number of errors has been detected.
USER	User's manual interruption of the measurement
----	Measurement in progress

(BIT ERROR RATE TEST...)	**STATE**	Indication of activities on data and clock line as well as synchronization status.
	MODE	Setting the operating mode for the BER measurement. OFF Terminating measurement. AUTO Continuous BER measurement. If one or both termination criteria are met, a new measurement is initiated. SMIQ cyclically fetches the current measurement results from AMIQ. A measurement can take considerable time, depending on the settings made. During the first measurement, the intermediate results are displayed. For all further measurements, only the final results are shown (by AMIQ). The DATA BITS and ERRORS displayed are continuously updated and indicate the relative values referred to the current measurement. SINGLE Single BER measurement. SMIQ cyclically fetches the current measurement results from AMIQ while the measurement is being performed. If one of the termination criteria is met (TERMINATION ATTRIBUTES - DATA BITS and TERMINATION ATTRIBUTES - ERRORS), the measurement is terminated. A SINGLE measurement can only be initiated by selecting the EXECUTE SINGLE menu.
	EXECUTE SINGLE ►	Initiating a single BER measurement. This menu item is only shown if MODE = SINGLE has been set.
	TERMINATION ATTRIBUTES	**DATA BITS** Setting the termination criterion „Number of data bits transmitted“ **ERRORS** Setting the termination criterion „Maximum number of errors occurred“
	PRBS	Setting the data period length 9, 11, 15, 20, 23 bits.
	DATA POLARITY	Setting the polarity of the data bits. NORM Normal polarity INV Inverted polarity
	CLOCK SLOPE	Setting the polarity of the clock slopes. POS Rising slope NEG Falling slope
	ENABLE RESTART	Switching on or off the external restart.
	DATA ENABLE	Setting the polarity of the DUT's Data Valid signal. 3 possibilities: NOT_USED, LOW and HIGH.
	UNIT	Setting the unit for indicating the BIT ERROR RATE. OFF Indication without unit, decadic % Indication of relative frequency in per cent ppm Indication of relative frequency in thousandths

2.23 Bit Error Rate Test

Option SMIQB21 allows evaluation of the signal demodulated and decoded by the DUT. For this purpose, a PRBS-modulated data sequence (PRBS = Pseudo Random Binary Sequence) is sent to the DUT. This data sequence can be generated continuously (by means of option SMIQB20) or in sections (option SMIQB60), using a loaded waveform.
The PRBS data sequence is decoded by the DUT and sent to the SMIQ in the form of clock and data signals. The SMIQ synchronizes to the known PRBS sequence and counts the bit errors.

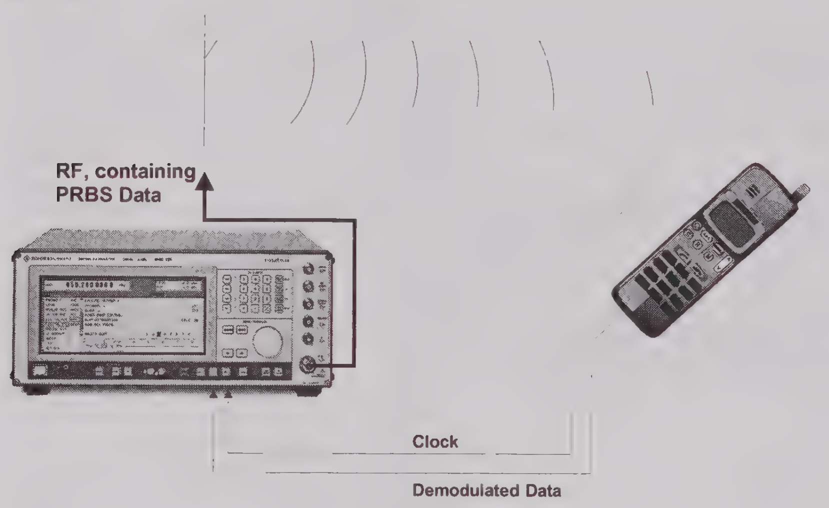


Fig. 2-234 BER Measurement

Termination of a measurement can be induced by various criteria. With option SMIQB21, measurements can be stopped either manually or because of one of the following two criteria: a user-defined number of data bits has been transmitted or a maximum number of errors has been detected.

By means of the RESTART function (eg for IQ signals that are not generated continuously and therefore do not contain integer multiples of PRBS sequences) BER results can be integrated onto the (partial) sequences. In this way, long BER measurements can also be performed on short IQ signals (which normally do not allow BER measurement because the PRBS sequence is not continuous).

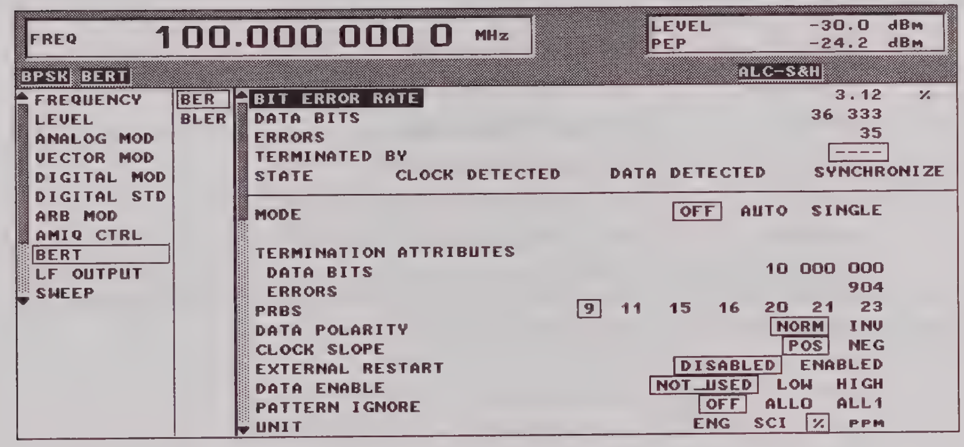
The BER measurement can also be performed separately (with data from another source).

In addition to testing PN bit sequences, the option may also be used to verify CRC checksums. This operating mode is described in section 2.23.2.

2.23.1 Bit Error Rate Measurement with PN Sequences (BER)

2.23.1.1 Operating Menu

In the operating menu, the configuration for the BER measurement is made and the results are displayed.



```
FREQ   100.000 000 0 MHz          LEVEL   -30.0 dBm
                                  PEP     -24.2 dBm

BIT ERROR RATE                                  3.12   %
DATA BITS                                     36 333
ERRORS                                            35
TERMINATED BY                                  ----
STATE       CLOCK DETECTED    DATA DETECTED    SYNCHRONIZE

MODE                               OFF   AUTO   SINGLE

TERMINATION ATTRIBUTES
 DATA BITS                                10 000 000
 ERRORS                                          904
PRBS                       9   11   15   16   20   21   23
DATA POLARITY                                 NORM   INV
CLOCK SLOPE                                    POS   NEG
EXTERNAL RESTART                     DISABLED    ENABLED
DATA ENABLE                       NOT_USED   LOW    HIGH
PATTERN IGNORE                         OFF   ALL0   ALL1
UNIT                                ENG   SCI   %    PPM
```

Fig. 2-235 Operating menu for BER measurement

In the upper section of the menu (display area), the measurement results are displayed together with the way in which they have been established, and additional information on the applied signal. In the lower section, settings can be made.

BIT ERROR RATE (BER) Cyclic display of the measured bit error rate, the ratio of detected errors to transmitted data bits.

DATA BITS Number of transmitted data bits.

ERRORS Number of detected errors.

TERMINATED BY The displayed measurement result was obtained as follows:

NUMBER OF DATA BITS	The defined number of data bits was reached.
NUMBER OF ERRORS	The defined number of errors was detected.
USER	The measurement was interrupted manually by the user.
----	Measurement in progress.

STATE

The status information described in the following tells the user whether the BER measurement is working. The STATE display signals the status of clock line, data line and synchronization. If the clock signal changes, CLOCK is indicated as status, otherwise NO CLOCK. The same applies to the data display DATA or NO DATA. NO DATA may be displayed because DATA ENABLE is not set to NOT USED or the data enable signal does not enable the data. SYNC indicates a successful attempt of PRBS synchronization by the analyzer. It is not before all three of CLOCK, DATA and SYNC are "active" that the measurement results are valid.

CLOCK	Change of clock signal.
NO CLOCK	No change of clock signal.
DATA	Change of data signal.
NO DATA	No change of data signal.
SYNC	Successful synchronization to PRBS.
NO SYNC	No synchronization.

The seven parameters listed above (data bits, error bits, error rate, attribute "terminated", attribute CLOCK, attribute DATA, attribute SYNC) are jointly polled by the IEC/IEEE bus query `:BERT:RES?` .

MODE

Setting for the BER measurement.

OFF	Terminate measurement.
AUTO	Continuous measurement of bit error rate. If one or both criteria for termination are fulfilled, a new measurement is initiated automatically. Depending on the settings made, a measurement may take considerable time. During the first measurement, intermediate results are displayed. For the measurements that follow, only the final results are shown. The parameters DATA BITS and ERRORS are continuously updated and indicate the relative values for the measurement in progress.
SINGLE	Single measurement of bit error rate. During the measurement, the SMIQ cyclically displays the updated values for rate, number of errors and number of data bits. If any one of the termination criteria (TERMINATION ATTRIBUTES [DATA BITS] and TERMINATION ATTRIBUTES [ERRORS]) is reached, the measurement is stopped. A single measurement is initiated only if the menu item EXECUTE SINGLE is selected.

IEC/IEEE-bus command `:BERT:STAT ON`
`:BERT:SEQ SING`

EXECUTE SINGLE↗

Initiates a single BER measurement. This menu item is only displayed if MODE = SINGLE is set.

IEC/IEEE-bus command `:TRIG:BERT:IMM`

TERMINATION ATTRIBUTES

DATA BITS	Setting the termination criterion "Number of transmitted data bits". IEC/IEEE-bus command `:BERT:SET:MCO <1>`
ERRORS	Setting the termination criterion "Maximum number of detected errors". IEC/IEEE-bus command `:BERT:SET:MERR <1>`

PRBS

Setting the period length of data: 9, 11, 15, 16, 20, 21, 23 bits.

Note: Data inversion for PRBS15 and PRBS23, which is contained in the standard, is performed automatically when the PRBS is selected. DATA POLARITY remains unaffected.

IEC/IEEE-bus command `:BERT:SET:TYPE PRBS9`

DATA POLARITY

Setting the data bit polarity.

NORM	Normal polarity
INV	Inverted polarity

IEC/IEEE-bus command `:BERT:SET:DATA NORM`

CLOCK SLOPE

Setting the clock edge polarity.

POS	Rising edge
NEG	Falling edge

IEC/IEEE-bus command `:BERT:SET:CLOC RIS`

EXTERNAL RESTART

Activating/deactivating an external restart of the BER measurement.

Note: *An enabled RESTART works always independently of PATTERN IGNORE or DATA ENABLE.*

DISABLED	Deactivated
ENABLED	Activated

IEC/IEEE-bus command `:BERT:SET:REST INT`

DATA ENABLE

Setting the polarity of the DUT's DATA ENABLE signal.

Note: *If the data are not enabled, the execution of the BER measurement is stopped completely. The identification circuit for PATTERN IGNORE as well as the PRBS generator of the BER measurement wait as long as data are not enabled. If the data are enabled, the measurement is continued.*

NOT USED	Irrespective of the data enable signal applied, all data at the BERT data input are used for the BER measurement.
LOW	Only the data applied at the BERT data input with a low-level data enable signal are used for the BER measurement.
HIGH	Only the data applied at the BERT data input with a high-level data enable signal are used for the BER measurement.

IEC/IEEE-bus command `:BERT:SET:DEN OFF`

PATTERN IGNORE

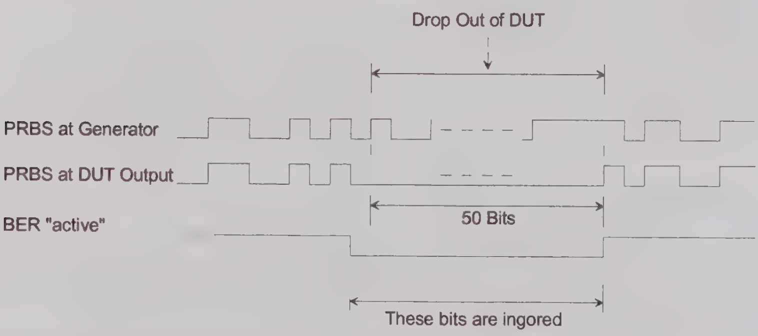
In the event of a so-called frame error (eg an error detected in the check sum), some mobile radio standards communicate a complete "0" or "1" frame to the BERT instead of the detected frame data. This is to signal to the BERT that this frame is not to be used for the BER measurement.

OFF	PATTERN IGNORE is not active.
ALL 1	Bit sequences consisting of 31 or more subsequent "1" data are not used (ie ignored) for the BER measurement.
ALL 0	Bit sequences consisting of 31 or more subsequent "0" data are not used (ie ignored) for the BER measurement.

IEC/IEEE-bus command `:BERT:SET:IGN OFF`

***Notes**:*

- *While ignoring the bits, the PRBS generator for the BER measurement keeps running. Following the "0" or "1" sequence, the BER measurement is continued as if the ignored sequence had contained PRBS data.*
- *If PATTERN IGNORE is switched on, synchronization time is 32 bits longer.*
- *If PATTERN IGNORE and RESTART are active at the same time, the presence of the restart signal causes the measurement to stop **immediately**. None of the 32 bits within the PATTERN IGNORE detector is evaluated.*
- *If DATA ENABLE and PATTERN IGNORE are active at the same time, DATA ENABLE is given priority, ie bits that are not enabled are not examined for "0" or "1" sequences.*



Example 50 bits were set to "0" by the DUT. These 50 bits plus the preceding "0" are ignored in the measurement.

UNIT

Setting the unit for displaying the bit error rate.

OFF	No unit, decade values
%	Relative frequency in percent
ppm	Relative frequency in parts per million

IEC/IEEE-bus command `:BERT:UNIT OFF`

2.23.1.2 Signal Path and Waveform

See also chapter 1, section "Option SMIQB21", Connector.

Test setup

The signal is computed using one of the modulation sources listed above and then output by the SMIQ and sent to the DUT (device under test). The latter demodulates the source bits contained and returns them to the SMIQ together with a transfer clock. In the SMIQ, the data bits are checked for errors. The total of the transmitted bits and the faulty bits are counted. The quotient of error bits/total bits is the BER.

PRBS data

To be able to detect faulty bits in a BER measurement, the data generation polynomial must be known. Data are calculated with the aid of so-called pseudo-random binary sequences (PRBS). These are quasi-random bit sequences which are repeated according to the selected polynomial.

An advantage of the PRBS data is that the bit error detector has only to know the polynomial but not the total sequence. Furthermore, the analysis can be started anywhere in the bit stream, ie the bit-stream source and the analyzer need not be synchronized.

Transfer clock

Should the DUT not provide a transfer clock, the bit clock can be taken from the output connector PAR-DATA for the generation of the signal using option SMIQB11 (DGEN). If AMIQ is used as the IQ source, a marker channel may be programmed as clock output. However, the clock-to-data ratio is to be observed.

2.23.1.3 Test Method

Generation of PRBS data

PRBS data are generated with the aid of a shift register with feedback points determined by the polynomial. A random start status yields one subsequent state. The start status and therefore the subsequent status occur only once in the whole sequence.

Feedback of data stream

If the shift register is filled with a data sequence at the beginning of a measurement and the register is then switched from "filling" to "feedback", the register generates the same data sequence as it expects to receive. Faulty bits can thus be identified and counted by comparing the received data to the results obtained from the shift register.

This method has the advantage that the analyzer can be used separately from signal generation (logically and with respect to time). Consequently, delays caused by the DUT, the use of other PRBS sources and transmission over long distances with spatially separated transmitter and receiver, do not cause any problems.

Errors in start status

If a bit error is already present in the start status (faulty bits are not detected during filling), the shift register starts from an incorrect position in the data sequence. As a result all subsequent states will be faulty. Since, statistically, every second bit is faulty, the BER will be about 50%. In this case the measurement is started again automatically, without the user realizing.

BER measurement with uninterrupted repetition of the random sequence

The (non-integrating) BER measurement operates on the basis of random sequences that are run continuously. These sequences are either generated continuously or by cyclic output of a stored sequence, with the number of encoded bits being an integer multiple of the length of the random sequence. The length of the random sequence is 2 to the power of the degree of the polynomial less 1, ie PRBS9 has a length of 511 (2^9 equals 512 less 1).

This type of BER measurement is selected by either setting External Restart to DISABLED or by issuing the IEC/IEEE-bus command `:BERT:SET:REST INT`. The CLOCK and DATA lines are sufficient for this measurement.

The data to be analyzed are interrupted by other data

The data bits contain data like sync, preambles, other channels, etc in addition to the PRBS data. To mark the data to be evaluated, a DAT ENABle signal has to be sent with the data. Either this DAT ENABle signal is generated by the DUT or an additional channel of the IQ source is used (eg marker channel of AMIQ).

The BER measurement in the SMIQ has to be adjusted for the use of a DAT ENABle signal; this is done by setting the required polarity in the menu or by means of IEC/IEEE-bus command (`:BERT:SET:DEN Low|High`). Data Enable = high means that data are only counted by the DUT and subjected to the BER measurement if the data enable input is set to 1.

BER measurement with interrupted random sequence - integrating BER measurement

Due to the type of data, oversampling and the finite memory length of the SMIQ (option SMIQB60) or the AMIQ, it may happen that the generated random sequence is not cyclically repeated at the memory wrap-around but has a break at this point. In a normal BER measurement operating exclusively with CLOCK and DATA signals, a break of this kind would lead to approx. 50% faulty bits due to the loss of synchronization.

A random sequence with a break can be processed by means of the **integrating BER measurement,** it is switched on in the menu under EXTERNAL RESTART (or by the remote-control command `BERT:SETup:RESTart EXTern)`. The BER measurement is to be stopped in time and then restarted at the beginning of the data sequence. The measurement is stopped and started via a signal at the RES input (pin 9 of SUB-D connector): A transition from logic 0 to 1 causes a partial result to be generated and the measurement to be stopped. A transition from 1 to 0 starts the measurement for the next sub-interval. This measurement is synchronized anew.
This input is best connected to an AMIQ marker channel or the SMIQ trigger output, with a single 1 encoded both at the start and at the end of the data sequence. This causes the BER measurement to stop briefly at memory wrap-around (the break) and start again. The partial results are integrated.

When the signals are interrupted by other data (eg preambles), these data will normally cause bit errors. The BER measurement for these data can then be interrupted using the data enable input .

In the integrating BER measurement, partial BER results are added up (controlled by a signal at the RES input) until the predefined total number of data or error bits is attained or exceeded.

Note: *Since the test hardware can be programmed flexibly, other bit error measurement techniques such as comparison with the output pattern, masking of certain time and data ranges, are also possible. In this matter, please contact your nearest R&S representative.*

PRBS Polynomials

For generating and testing the PRBS, a shift register with feedback is used. The feedback depends on the type of polynomial used. The sequence length of a generator is the result of $2^n - 1$, n being the degree of the polynomial.

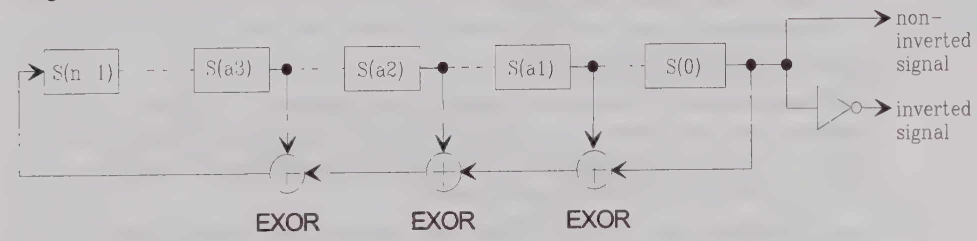


Fig. 2-236 PRBS polynomials

PN generator	N	a1	a2	a3	Output	Acc. to standard
PN9	9	4	-	-	non-inverted	ITU-T Rec. O.153 Fascicle IV.4
PN11	11	2	-	-	non-inverted	ITU-T Rec. O.152 Fascicle IV.4
PN15	15	1	-	-	inverted	ITU-T Rec. O.151 Fascicle IV.4
PN16	16	5	3	2	non-inverted	--
PN20	20	3	-	-	non-inverted	ITU-T Rec. O.153 Fascicle IV.4
PN21	21	2	-	-	non-inverted	--
PN23	23	5	-	-	inverted	ITU-T Rec. O.151 Fascicle IV.4

Measurement Result, Accuracy, Measurement Time

Value range

Measurement results for the bit error rate (ie the quotient of error bits and total bits) are usually found between 10^{-2} and 10^{-9}. This means that a large number of bits may have to be checked before a faulty bit is detected. Because of the large number of bits involved the measurement time is usually very long.

Since 32-bit-wide counters are used for the total bits and the error bits, the maximum measurement time is 4.29×10^9 bits.

Statistics

The BER measurement measures statistical bit errors, ie errors which do not occur at regular intervals but at random. Although a single measurement determines the exact number of errors in the measured interval, a statistically reliable BER can only be obtained when a sufficient number of errors occurs in the observed interval. Only this ensures that the single BER measurement result approaches the true error rate with high probability .

Termination criteria

To keep the measurement time short with low and high bit error rates, two termination criteria have been defined in the SMIQ for the BER measurement.

- Criterion 1: Total number of bits
  The measurement is terminated when the specific number of data bits is reached. Due to this criterion the BER measurement is reliably stopped after the specified number of bits even if no error or only a few errors were detected; the measurement result is not statistically reliable (few bit errors).
- Criterion 2: Number of errors

  The measurement is terminated when the specified number of bit errors is detected. With this criterion, the measurement is rapidly terminated when high bit error rates occur. Since a great number of errors is counted, the measurement result is statistically reliable.

The two criteria are used together. The criterion which finally yields a valid result is indicated in the results screen ("Measurement terminated by...").

Interruption of measurement

At the end of a measurement, the restart of a new one is delayed until the first measurement result has been queried with `:BERT:RES?`. The resulting brief measurement interruption is irrelevant because the subsequent measurement will be synchronized within 24 data bits.

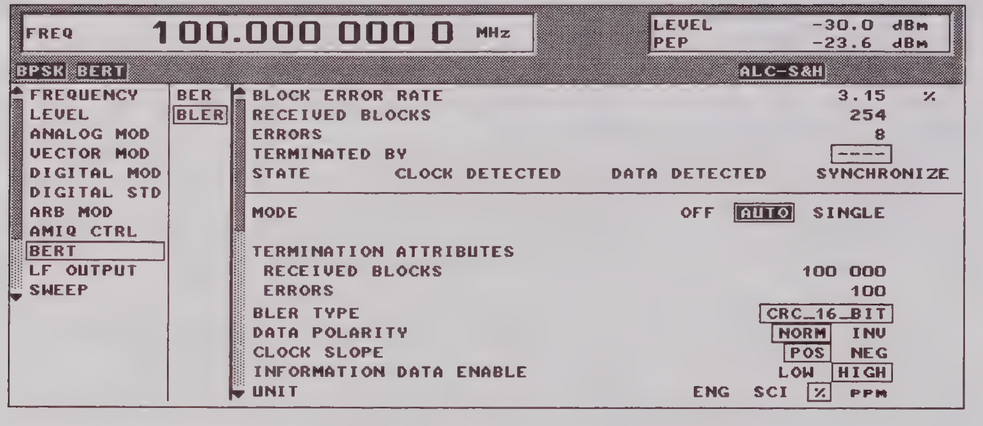
Possible Problems with BER Measurement and Related Solutions

Fault	Possible cause	Fault description/remedy
BER measurement does not synchronize	No signals received from DUT or the signal level is not correct.	➢ Check displays for activity at BER measurement inputs. If a status is displayed (Clock, Data, Sync) for a line, there is activity.
	The selected PRBS is not correct.	Normally, the PRBS on which the data are based is used as the default setting. If the PRBS is changed, the BER measurement cannot synchronize to the data (because the polynomial is not correct).
	A wrong clock edge is used, which violates setup or hold times.	➢ Check the bit clock signal, the data signal and the data enable signal, if any, on an oscilloscope. The fault may also be caused by reflections on the clock line, which switch the data signal twice into the BER measurement, eg on lines without termination. The SMIQ input is **not** terminated.
	Incorrect polarity of data signal (or data enable signal).	In this case the PRBS cannot synchronize. Note that an inversion of the output signal specified for some cases by the PRBS standard is performed automatically upon PRBS selection. Manual inversion of the data signal is therefore not required.
	A bit error occurs during synchronization (nine data bits with PRBS9)	The BER measurement is started at a wrong position so that about 50% of the subsequent data bits are identified as faulty.
No clock received from DUT	When testing RF components, clock recovery is not available. An external clock is however required for clocking the data during the BER measurement	Instead of a clock recovery circuit, the bit clock at the PAR-DATA connector of the SMIQ may be possibly used as a replacement. This is possible if DGEN (SMIQB11) is used as a data source. However, this bit clock is not possible with all modulation types. Also, the delay between data and clock has to be observed.
Measured BER too high	The data are clocked with the wrong edge and/or the eye aperture of the data is not optimally met.	➢ Check the clock/data relationship by means of an oscilloscope and set optimum timing. .
	BER measurement does not synchronize	If data that are not cyclically continued (ie when a break occurs at the memory wrap-around), the measurement will identify about 50% of the bits as faulty after the wrap-around. Make sure the measurement is started optimally at the beginning of the sequence by means of a signal on the REStart line (see: "BER measurement with interrupted random sequence – integrating BER measurement" in section 2.23.1.3, "Test Method").

2.23.2 Block Error Rate Measurement (BLER)

2.23.2.1 Operating Menu

In the operating menu, the BLER measurement is configured and the results are displayed.


FREQ 100.000 000 0 MHz
LEVEL -30.0 dBm
PEP -23.6 dBm
BPSK BERT
ALC-S&H
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
ARB MOD
AMIQ CTRL
BERT
LF OUTPUT
SWEEP
BER
BLER

BLOCK ERROR RATE 3.15 %
RECEIVED BLOCKS 254
ERRORS 8
TERMINATED BY ----
STATE CLOCK DETECTED DATA DETECTED SYNCHRONIZE
MODE OFF AUTO SINGLE
TERMINATION ATTRIBUTES
RECEIVED BLOCKS 100 000
ERRORS 100
BLER TYPE CRC_16_BIT
DATA POLARITY NORM INV
CLOCK SLOPE POS NEG
INFORMATION DATA ENABLE LOW HIGH
UNIT ENG SCI % PPM

Fig. 2-1 Operating menu for BLER measurement

In the upper section of the menu (display area), the measurement results are displayed, the way in which they have been obtained, and additional information on the applied signal. In the lower section, settings can be made.

BLOCK ERROR RATE (BLER) Cyclic display of measured block error rate, the ratio of detected errors to transmitted data blocks.

IEC/IEEE bus `:BLER:RES?` `Return value 3 ( of 7)`

RECEIVED BLOCKS Number of transmitted data blocks.

IEC/IEEE bus `:BLER:RES?` `Return value 1 ( of 7)`

ERRORS Number of detected errors.

IEC/IEEE bus `:BLER:RES?` `Return value 2 ( of 7)`

TERMINATED BY The displayed measurement result was obtained as follows:

NUMBER OF RECEIVED BLOCKS	The defined number of data blocks was reached.
NUMBER OF ERRORS	The defined number of errors was detected.
USER	The measurement was manually aborted by the user.
----	Measurement in progress.

IEC/IEEE bus `:BLER:RES?` `Return value 4 ( of 7)`

STATE

The status information described in the following tells the user whether the BLER measurement functions correctly. The STATE display signals the status of clock line, data line and synchronization. If the clock signal changes, CLOCK is indicated as status, otherwise NO CLOCK. The same applies to the DATA or NO DATA display. SYNCHRONIZE indicates a successful attempt of synchronization by the CRC tester. Only when all three displays (CLOCK, DATA and SYNCHRONIZE) are "active" will the measurement results become valid.

CLOCK	Change of clock signal.
NO CLOCK	No change of clock signal.
DATA	Change of data signal.
NO DATA	No change of data signal.
SYNCHRONIZE	Successful synchronization to CRC.
NO SYNC	No synchronization.

The seven parameters listed above (data blocks, error blocks, block error rate, attribute "terminated", attribute CLOCK, attribute DATA, attribute SYNC) are queried together with the IEC/IEEE bus query `:BERT:RES?`.

MODE

Setting the BLER measurement mode.

OFF	Terminate measurement.
AUTO	Continuous measurements of block error rate. If one or both termination criteria are fulfilled, a new measurement is started automatically. Depending on the settings made, a measurement may take considerable time. During the first measurement, intermediate results are displayed. For all subsequent measurements, only the final results are displayed. The parameters RECEIVED BLOCKS and ERRORS are continuously updated and indicate the relative values for the measurement in progress.
SINGLE	Single measurement of block error rate. During the measurement, the SMIQ cyclically displays the current values for rate, number of errors and data blocks. If any one of the termination criteria (TERMINATION ATTRIBUTES [RECEIVED BLOCKS] and TERMINATION ATTRIBUTES [ERRORS]) is reached, the measurement is terminated. A SINGLE measurement can only be triggered when the menu item EXECUTE SINGLE is selected.

IEC/IEEE-bus command `:BLER:STAT ON`
`:BLER:SEQ SING`

EXECUTE TRIGGER↗

Triggers a single BLER measurement. This menu item is only displayed when MODE = SINGLE is selected.

IEC/IEEE-bus command `:TRIG:BLER:IMM`

TERMINATION ATTRIBUTES

RECEIVED BLOCKS

Setting of termination criterion "Number of received data blocks".

IEC/IEEE-bus command `:BLER:SET:MCO <1>`

ERRORS

Setting of termination criterion "Maximum number of detected errors".

IEC/IEEE-bus command `:BLER:SET:MERR <1>`

BLER TYPE

Displays the CRC type. Only the CRC_16_BIT type is supported at present.

IEC/IEEE-bus command `:BLER:SET:TYPE?`

DATA POLARITY

Setting the data bit polarity.

NORM	Normal polarity
INV	Inverted polarity

IEC/IEEE-bus command `:BLER:SET:DATA[:POL] NORM`

CLOCK SLOPE

Setting the clock edge polarity.

POS	Rising edge
NEG	Falling edge

IEC/IEEE-bus command `:BLER:SET:CLOC[:POL] RIS`

INFORMATION DATA ENABLE

Setting the polarity of the DUT's DATA ENABLE signal.

LOW	The bits received at low level of the data enable signal are interpreted as information bits. The bits received at high level of the data enable signal are interpreted as checksum bits.
HIGH	The bits received at high level of the data enable signal are interpreted as information bits. The bits received at low level of the data enable signal are interpreted as checksum bits.

IEC/IEEE-bus command `:BLER:SET:DEN LOW`

UNIT

Setting the unit for BLOCK ERROR RATE display.

ENG	Display in exponential notation, exponent -3 or -6
SCI	Display in scientific notation, i.e. normalized to one character ahead of the decimal point
%	Display of relative frequency in percent
ppm	Display of relative frequency in parts per million

IEC/IEEE-bus command `:BLER:UNIT ENG`

2.23.2.2 CRC Polynomial

CCITT CRC 16 : $G(x) = x^{16} + x^{12} + x^{5} + x^{1}$ is the only CRC polynomial currently supported.
At the beginning of information data (marked by the edge of the DATA ENABLE signal) the shift register is initialized with 0. All information bits are then shifted through the shift register. The CRC component is then read into a second register and compared bit by bit with the result of the calculation.

2.23.2.3 Measurement Result, Accuracy, Measurement Time

Range of values

The measurement results for the block error rate (i.e. the quotient of erroneous blocks and total blocks) are normally between 10^{-2} and 10^{-4}. This means that a large number of blocks may have to be tested before an erroneous block occurs. Because of the large number of blocks involved the measurement time is usually very long. Since 32-bit-wide counters are used for the total number of blocks and the error bits, the maximum measurement time is 4.29×10^{9} blocks.

Statistics

The BLER measurement measures statistical block errors, i.e. errors which do not occur at regular intervals but at random. Although a single measurement determines the exact number of errors in the measured interval, a statistically reliable BLER can only be obtained when a sufficient number of errors occurs in the observed interval. Only this ensures that the single BLER measurement result approaches the true error rate with high probability.

Termination criteria

To keep the measurement time short for low and high block error rates, two termination criteria have been provided in the SMIQ for the BLER measurement.

- Criterion 1: Total number of blocks
  The measurement is terminated when the specified number of data blocks is checked. Due to this criterion the BLER measurement is reliably terminated after the specified number of blocks even if no error or only a few errors were detected; the measurement result may not be statistically reliable (if few block errors occur).
- Criterion 2: Number of errors

  The measurement is terminated when the specified number of block errors has occurred. With this criterion, the measurement is rapidly terminated when the block error rate is high. Since a great number of errors has been counted, the measurement result is statistically reliable.

The two criteria are used together. The criterion which finally yields a valid result is displayed under ("Measurement terminated by...").

Interruption of measurement

After termination of a measurement, the restart of a new one is delayed until the first measurement result has been queried with `:BLER:RES?`.

2.23.2.4 Possible BLER Measurement Problems and Solutions

Fault	Possible cause	Fault description/remedy
BLER measurement does not synchronize	No signals received from DUT or the signal level is not correct.	➢ Check activity at BLER measurement inputs in the display. A status display (Clock, Data, Sync) signals activity on the respective line.
	A wrong clock edge is used, which violates setup or hold times.	➢ Check the bit clock signal, the data signal and the DATA ENABLE signal, if any, on an oscilloscope. The fault may also be caused by reflections on the clock line, which clock the data signal twice into the BLER measurement, e.g. if lines are not terminated. The SMIQ input is **not** terminated.
	Incorrect polarity of data signal (or DATA ENABLE signal).	In this case the CRC tester cannot synchronize.
No clock received from DUT	When testing RF components, clock recovery may not be available. An external clock is however required for clocking the data during the BLER measurement.	The bit clock at the PAR DATA connector of the SMIQ may be used instead of a clock recovery circuit. This is possible if DGEN (SMIQB11) is used as a data source. However, this bit clock is not available with all modulation types. Also, the delay between data and clock has to be taken into account.
Measured BLER too high	The data is switched with the wrong clock edge and/or the eye pattern of the data is not optimally met.	➢ Check the clock/data relationship by means of an oscilloscope and optimize the timing.

2.24 Noise Generator and Distortion Simulator

The noise generator and the distortion simulator (Option SMIQB17) perform the following two functions:

- Addition of noise to the SMIQ output level. Since the C/N can be finely varied, different reception conditions can be realistically simulated.
- Simulation of TWTA (traveling wave tube amplifier) distortion of satellite. The signal received by the satellite is strongly distorted and thus more difficult to demodulate than an undistorted signal. Real reception conditions can be simulated with the distortion simulator and the receivers can thus be tested realistically.

The noise generator outputs an AWGN signal (Additive White Gaussian Noise) , ie the noise power ratio is Gaussian-distributed and the noise signal is added to the signal.

Distortion is performed via AM/AM conversion and AM/PM conversion. The respective characteristics can be loaded or modified via IEC/IEEE bus.

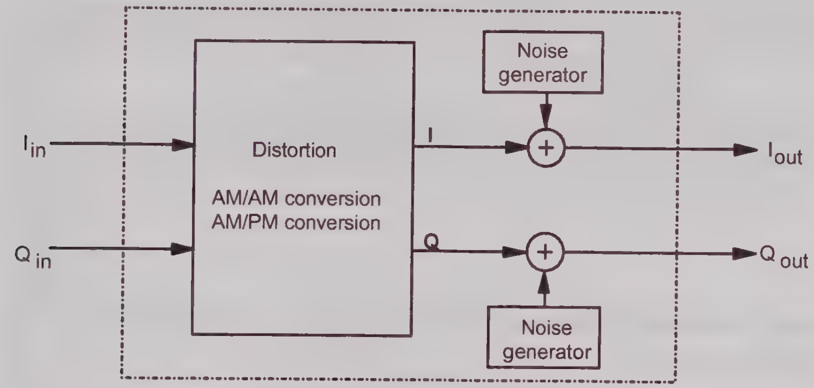

Noise generator
I
Iin
Distortion
AM/AM conversion
AM/PM conversion
+
Iout
Q
Qin
+
Qout
Noise generator

Fig. 2-237 Block diagram of noise generator and distortion simulator

The noise generator and distortion simulator are independent functional units and can be operated separately. They exclusively use baseband signals I and Q. Both external I/Q signals and I/Q signals internally generated by the modulation coder can be distorted and superimposed with noise. The noise generator can also be switched on if internal and external modulation are switched off. The unmodulated carrier is then superimposed with the noise signal.

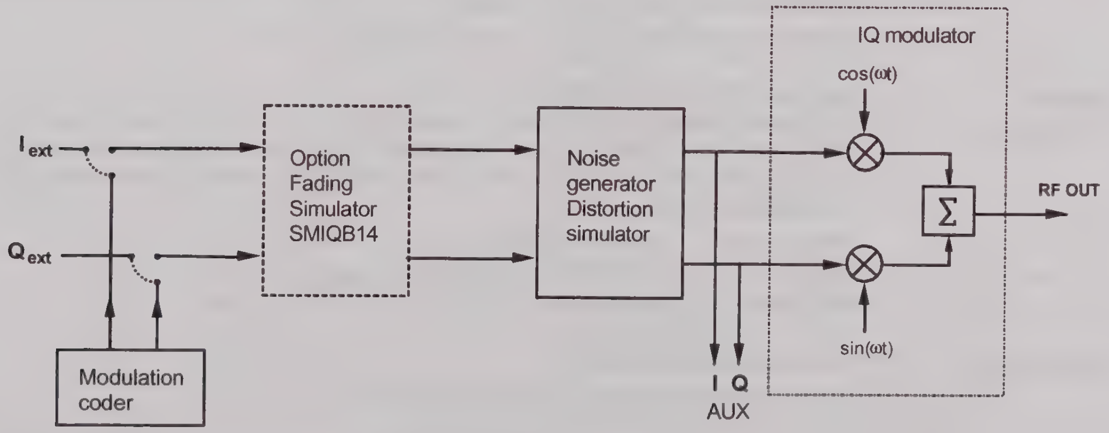

IQ modulator
cos(ωt)
Iext
Qext
Option Fading Simulator SMIQB14
Noise generator Distortion simulator
Σ
RF OUT
sin(ωt)
Modulation coder
I Q
AUX

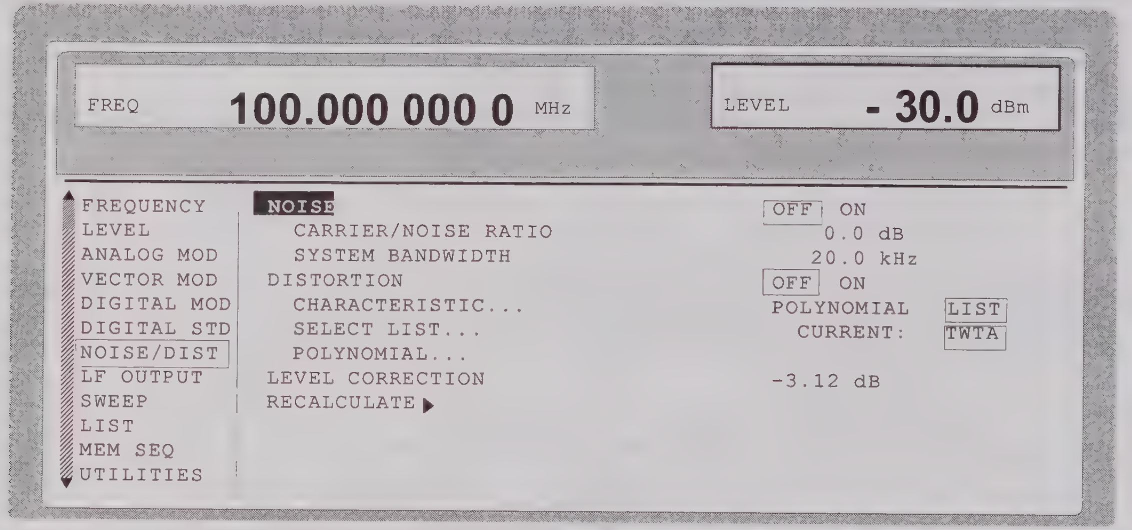
Fig. 2-238 Noise generator and distortion simulator in the SMIQ

2.24.1 Setting NOISE/DIST Menu

The NOISE/DIST menu comprises all the settings of the noise generator and the distortion simulator. For calibration of the noise generator and the distortion simulator, see Chapter 2, Section Calibration VECTOR MOD.

Note: Functions NOISE/DIST and BB-AM cannot be set at the same time and switch off mutually.

Menu selection: NOISE/DIST



```
FREQ     100.000 000 0  MHz               LEVEL     - 30.0  dBm

FREQUENCY     NOISE                               OFF  ON
LEVEL           CARRIER/NOISE RATIO                 0.0 dB
ANALOG MOD      SYSTEM BANDWIDTH                  20.0 kHz
VECTOR MOD    DISTORTION                          OFF  ON
DIGITAL MOD     CHARACTERISTIC...                 POLYNOMIAL    LIST
DIGITAL STD     SELECT LIST...                      CURRENT:    TWTA
NOISE/DIST      POLYNOMIAL...
LF OUTPUT     LEVEL CORRECTION                    -3.12 dB
SWEEP         RECALCULATE ▶
LIST
MEM SEQ
UTILITIES
```

Fig. 2-239 Menu NOISE/DIST (presetting)

NOISE

Switch on/off of noise source. The noise source is an AWGN (Additive White Gaussian Noise) signal.

IEC/IEEE-bus commands `:SOUR:NOIS ON`

CARRIER/NOISE RATIO

Input value of the carrier-to-noise ratio. Setting range is 5.0 to 30 dB. When changing the CARRIER/NOISE RATIO, the noise power is changed as well but the carrier power remains unchanged. The carrier power is the power displayed in the header under LEVEL.

IEC/IEEE-bus command `:SOUR:NOIS:SNR 10 dB`

SYSTEM BANDWIDTH

Input value of system bandwidth.
The system bandwidth is the HF-bandwidth by which the noise power is calculated. The bandwidth of the generated noise can only be set in steps. The set bandwidth is to be 1.4 times the system bandwidth at minimum and 10 MHz at maximum.

Setting range: 10 kHz to 10 MHz;
Resolution: three digits

IEC/IEEE-bus command `:SOUR:NOIS:BAND 1.23 MHz`

DISTORTION

Switch on/off of distortion.

IEC/IEEE-bus command `:SOUR:DIST ON`

CHARACTERISTIC... Switchover between distortion data from the polynomial and list. Under polynomial the distortion data are calculated from the coefficients entered into the polynomial menu. Under list they are calculated according to the characteristic transferred via the IEC/IEEE-bus and selected under SELECT LIST.

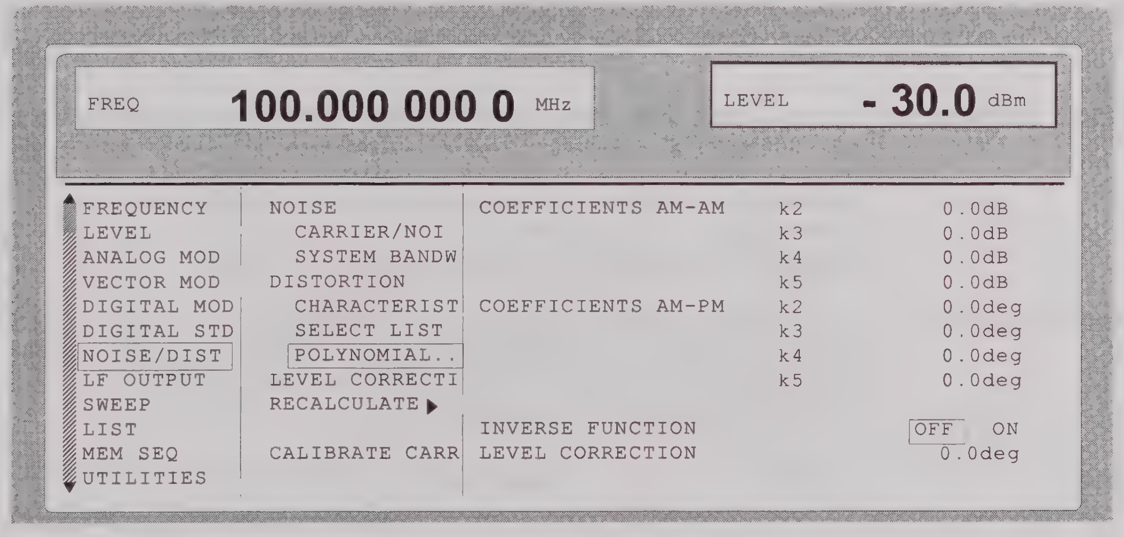
IEC/IEEE-bus command `:SOUR:DIST:MODE POLY | DATA`

SELECT LIST... Opens a window for selecting the distortion characteristics. Several distortion characteristics can be stored in the memory at the same time.

IEC/IEEE-bus command `:SOUR:DIST:DATA:SEL "TWTA"`

POLYNOMIAL... Opens a window for entering the polynomial parameters.

Menu selection: NOISE/DIST - POLYNOMIAL...


FREQ 100.000 000 0 MHz
LEVEL - 30.0 dBm

COEFFICIENTS AM-AM	k2	0.0dB
	k3	0.0dB
	k4	0.0dB
	k5	0.0dB
COEFFICIENTS AM-PM	k2	0.0deg
	k3	0.0deg
	k4	0.0deg
	k5	0.0deg
INVERSE FUNCTION		OFF ON
LEVEL CORRECTION		0.0deg

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
NOISE/DIST
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
NOISE
CARRIER/NOI
SYSTEM BANDW
DISTORTION
CHARACTERIST
SELECT LIST
POLYNOMIAL..
LEVEL CORRECTI
RECALCULATE ▶
CALIBRATE CARR

Fig. 2-240 Menu NOISE/DIST - POLYNOMIAL...

(POLYNOMIAL...)

COEFFICIENT AM-AM Entry of polynomial coefficients k2 to k5 for the AM-AM distortion in dB. The polynomial has the following form:
Aout = Ain + n2*Ain^2 + n3*Ain^3 + n4*Ain^4 + n5*Ain^5
with n<i> = 10^(k<i>/20) - 1, i = 2, 3, 4, 5
Normalization: Aout_n = 0 ... 1, Ain = 0...1
The coefficient n0 is always 0 and n1 is always 1.
Value range: -10.0 to +10.0 dB

IEEE-bus `:SOUR:DIST:POLY:AMAM:K2 -2 DB`

COEFFICIENT AM-PM Entry of polynomial coefficients k2 to k5 for the AM-PM distortion in degrees. The polynomial has the following form:
Pout = Pin + k2*Ain^1 + k3*Ain^2 + k4*Ain^3 + k5*Ain^4
The coefficients K0 and K1 are always 0.
Value range: - 60.0 to + 60.0 degrees.

IEEE-bus `:SOUR:DIST:POLY:AMPM:K3 -45 DEG`

(POLYNOMIAL...)	**INVERSE FUNCTION**	Compensation of an amplifier connected after the SMIQ, the coefficients entered correspond to the measured distortion of the amplifier. **OFF** The above equations are applicable. **ON** AM-AM distortion: inverse function of Aout_n (Ain), AM-PM distortion: Pout = Pin - k2*Aout_n(Ain)^1 - k3*Aout_n(Ain)^2 - k4*Aout_n(Ain)^3 - k5*Aout_n(Ain)^4 IEEE-bus `:SOUR:DIST:POLY:IFUN ON`
	LEVEL CORRECTION	Entry of the level correction for the polynomial. The value entered is active and is displayed in the main menu after selection of the polynomial and calling up RECALCULATE. It can be edited only here. Value range: -20 dB to +6 dB IEEE-bus `:SOUR:DIST:POLY:LEV:CORR -10 DB`

LEVEL CORRECTION Display of level correction to correct the output level so that the attenuation or gain of the distortion simulator can be compensated. For the polynomial the level correction can be entered into submenu POLYNOMIAL. The level correction for the lists can only be transferred via the IEC/IEEE bus.

Value range: -20 dB to +6 dB

IEC/IEEE-bus command `:SOUR:DIST:DATA:LEV:CORR -3.12 DB`

RECALCULATE ▶ Distortion data are active since as they are transferred into the module. This is necessary when the available characteristic has been overwritten via the IEC/IEEE bus or when a polynomial parameter has been modified.

IEC/IEEE-bus command `:SOUR:DIST:REC`

2.24.2 Loading New Distortion Characteristics

The SMIQ is supplied with a preset distortion characteristic that corresponds to the typical characteristic of a traveling wave tube amplifier in WorldSpace satellites. The name of the characteristic set as standard is TWTA (Traveling Wave Tube Amplifier). Other user-defined distortion characteristics can also be stored in the SMIQ. If several characteristics are stored, they can be selected under SELECT CHARACTERISTIC... .

A new characteristic is formed by the interpolation points of AM/AM and AM/PM conversion. Entry is possible via IEC/IEEE bus. The SMIQ determines the complete characteristic based on these interpolation points by cubic spline interpolation. The factory-set characteristics (TWTA) of AM/AM and AM/PM conversion are shown in the following figures. The continuous line indicates the interpolated characteristic. The circles show the reference points. The input values are on the x-axis, the output values on the y-axis.

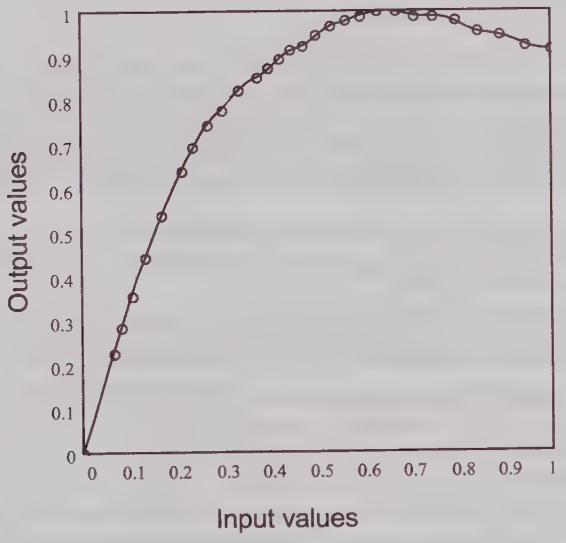


Fig. 2-241 AM/AM conversion

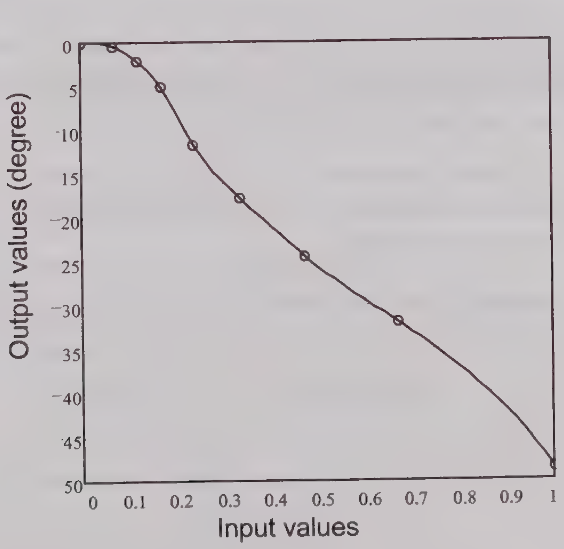


Fig. 2-242 AM/PM conversion

The two characteristics are formed by 2 data fields, the x-axis and the y-axis. 4 data fields therefore have to be loaded for a new distortion characteristic. The minimum number of interpolation points that can be entered for a characteristic is 5, the maximum number is 30. The limit values for the data fields are as follows:

Input values (x-axis) of AM/AM conversion:	-100 dB to 0 dB
Output values (y-axis) of AM/AM conversion:	-100 dB to 0 dB
Input values (x-axis) of AM/PM conversion:	-100 dB to 0 dB
Output values (y axis) of AM/PM conversion:	-180° to +180°

2.24.3 Level Correction of the Distortion Simulator

The level correction influences the level if digital modulation is switched on (not with VECTOR MOD ON) and the distortion simulator is active.

For the rms level set as LEVEL to appear at the RF output, the level control has to compensate for the attenuation or gain of the distortion characteristic. For this purpose, level correction (-20 dB to 6 dB) stored under the previously selected name is transmitted via IEC/IEEE-bus command. If this characteristic is active, the output level is increased or decreased by the level-correction value. The value by which the level is increased or decreased is indicated under LEVEL CORRECTION.

As the attenuation/gain of the distortion characteristic is dependent on the type of input signal being dealt with, the level correction applies only to one particular type of digital modulation (modulation type, filter type and filter parameters) and fading setting.
For the characteristic TWTA, for instance, which is supplied as standard, -3.12 dB is stored as level correction. This value only applies to "WorldSpace Modulation" (QPSK, SQR-COS/0.4).

For the determination of level correction through measurement, the parameter is at first set to 0 dB via IEC/IEEE bus. The desired type of digital modulation is then set, and the characteristic TEST (linear characteristic supplied, LEVEL CORRECTION = 0 dB) activated, followed by the new characteristic. The level difference of the two characteristics is measured at the RF output by means of an RF level meter. The level correction for the new characteristic is then adjusted to the level difference measured via IEC/IEEE bus. If the level difference is measured again, the result should be 0 dB ± 0.1 dB.

As theoretically the characteristic gain (negative level correction) cannot exceed the crest factor of the modulation used, the warning "Warning 426 Absolute value of level correction > crest factor of Digital Mod;" is displayed if the magnitude of the negative level correction exceeds the crest factor. The correction is also restricted to the crest factor when the level is set, so LEVEL and PEP are identical.

Step-by-step instruction to enter a new distortion characteristic via IEC/IEEE bus:

1. **Enter the name of a new characteristic**

   IEC/IEEE-bus command

   ```
   :SOUR:DIST:DATA:SEL "TWTA1"
   ```

   The characteristic is listed under a freely selectable name (max. 8 characters) in the select menu that comprises different characteristics.

2. **Enter the data field for input values (x-axis) of AM/AM conversion in dB**

   IEC/IEEE-bus command

   ```
   :SOUR:DIST:DATA:AMB -23.5,-21.5,
   -19.5,-17.5,-15.5,-13.5,-12.5,-11.5,
   -10.5,-9.5,-8.5,-8,-7.5,-7,-6.5,-6,
   -5.5,-5,-4.5,-4,-3.5,-3,-2.5,-2,-1.5,
   -1,-0.5,0
   ```

3. **Enter the data field for output values (y-axis) of AM/AM conversion in dB**

   IEC/IEEE-bus command

   ```
   :SOUR:DIST:DATA:AM -12.9,-10.9,-9,
   -7.1,-5.4,-3.9,-3.2,-2.6,-2.2,-1.7,
   -1.4,-1.2,-1,-0.8,-0.7,-0.5,-0.3,
   -0.2,-0.1,0,0,-0.1,-0.1,-0.2,-0.4,
   -0.5,-0.7,-0.8
   ```

4. **Enter the data field for input values (x-axis) of AM/PM conversion in dB**

   IEC/IEEE-bus command

   ```
   :SOUR:DIST:DATA:PMB -23.5,-18.5,
   -15.5,-12.5,-9.5,-6.5,-3.5,0
   ```

5. **Enter the data field for output values (y-axis) of AM/PM conversion in degrees**

   IEC/IEEE-bus command

   ```
   :SOUR:DIST:DATA:PM 0,-2.1,-5,-11.6,
   -17.7,-24.4,-31.9,-48.6
   ```

6. **Enter the level correction**

   IEC/IEEE-bus command

   ```
   :SOUR:DIST:DATA:LEV:CORR -3.12
   ```

7. **Data transmission to the module**

   IEC/IEEE-bus command

   ```
   :SOUR:DIST:REC
   ```

Note: *The values of the above example correspond to the TWTA characteristic that is supplied.*

2.24.4 Calculation of the Distortion Characteristic from Polynomial Equations

A characteristic can be defined by entering polynomial coefficients in submenu POLYNOMIAL instead of transferring reference values via the IEC/IEEE bus. The characteristic is calculated and loaded from the four polynomial coefficients for AM-AM and AM-PM using the equations specified under section 2.24.1. An IEC/IEEE-bus transfer is not required in this case.

It is possible to compensate the distortion of an amplifier connected after the SMIQ using the INVERSE FUNCTION.
The characteristic entered via polynomial coefficients should be identical to the distortion characteristic of the amplifier.

The level correction described in section 2.24.3 can be directly entered in the POLYNOMIAL menu. If the polynomial is activated, this value can be displayed and set in the NOISE/DIST menu under LEVEL CORRECTION. The determination by measurement of the level correction is performed as described in section 2.24.3.

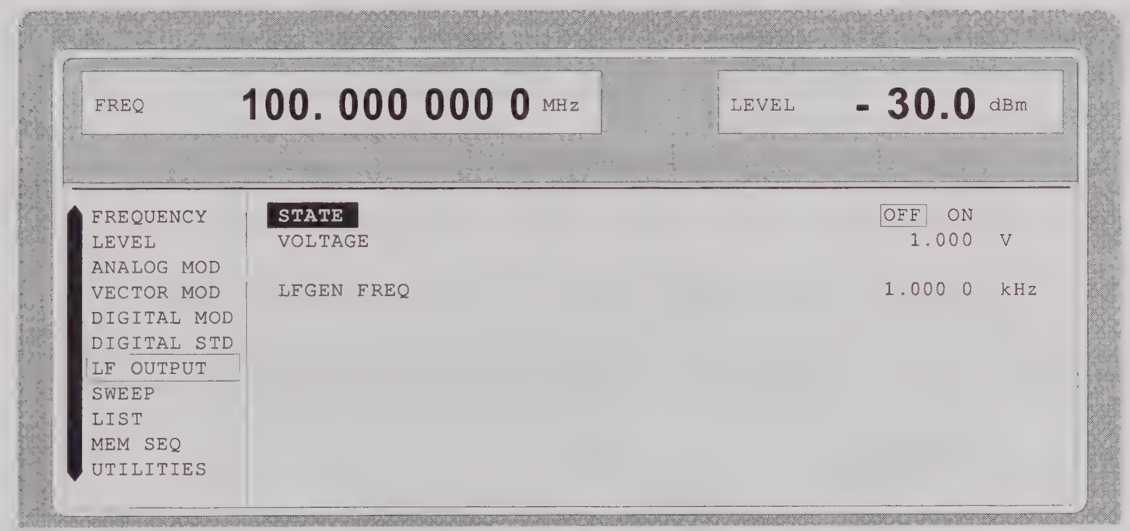
2.25 LF Output

The internal LF generator is available as a signal source for the LF output.

Menu LF OUTPUT offers access to the settings of the LF output.

Notes: - *An alteration of the frequency of the internal modulation generator in the LF-output menu automatically effects the modulation for which the generator is selected as modulation source.*
- *The SWEEP function of LF generator can be activated in menu SWEEP-LF-GEN.*

Menu selection: LF OUTPUT



FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

STATE OFF ON
VOLTAGE 1.000 V
LFGEN FREQ 1.000 0 kHz

Fig. 2-243 Menu LF OUTPUT (preset setting)

STATE Switching on/off the LF output. Parameter STATE has no influence on the modulation settings.
IEC/IEEE-bus command `:OUTP2 ON`

VOLTAGE Input value of the output voltage of the LF output. The input is effected in the form of a peak voltage.
IEC/IEEE-bus command `:OUTP2:VOLT 1V`

LFGEN FREQ Input value of the frequency of internal modulation generator.
IEC/IEEE-bus command `:SOUR2:FREQ 1kHz`

2.26 Sweep

The SMIQ offers a digital step-by-step sweep for parameters:

- RF frequency
- LF frequency
- RF level

Setting a sweep is effected in five basic steps which are shown in the following example, the setting of a frequency sweep:

1. Set sweep range (START and STOP or CENTER and SPAN).
2. Select linear or logarithmic sequence (SPACING).
3. Set step width (STEP) and dwell time (DWELL).
4. Activate marker if desired (MARKER).
5. Switch on sweep (MODE set to AUTO, SINGLE or STEP).

2.26.1 Setting the Sweep Range (START, STOP, CENTER and SPAN)

The sweep range of the RF sweep can be entered in two different ways. Either by entering the START and STOP value or by entering CENTER and SPAN. Please observe that the two parameter sets influence one another. The influence is exerted in the following way:

START frequency altered:	STOP	=	unaltered
	CENTER	=	(START + STOP)/2
	SPAN	=	(STOP - START)
STOP frequency altered:	START	=	unaltered
	CENTER	=	(START + STOP)/2
	SPAN	=	(STOP - START)
CENTER frequency altered	SPAN	=	unaltered
	START	=	(CENTER - SPAN/2)
	STOP	=	(CENTER + SPAN/2)
	CENTER	=	unaltered
	START	=	(CENTER - SPAN/2)
	STOP	=	(CENTER + SPAN/2)

2.26.2 Selecting the Sweep Run (SPACING LIN, LOG)

The sweep run, linear or logarithmic, can be selected using SPACING. For the RF and LF sweep, a linear or logarithmic run is possible. For level sweep, only the logarithmic run is possible.

With the logarithmic sweep, step width STEP is equal to a constant fraction of the present setting. The logarithmic step width is entered in unit % with RF or LF sweep, in unit dB with level sweep.

2.26.3 Operating Modes (MODE)

The following sweep operating modes are available:

AUTO

Sweep from the starting point to the stop point, with automatic restart at the starting point. If another sweep operating mode was activated prior to the AUTO operating mode, continuation is made from the current sweep setting.

IEC/IEEE-bus commands:

RF sweep:	LF sweep:	Level sweep:
`SOUR:FREQ:MODE SWE`	`SOUR2:FREQ:MODE SWE`	`SOUR:POW:MODE SWE`
`SOUR:SWE:MODE AUTO`	`SOUR2:SWE:MODE AUTO`	`SOUR:SWE:POW:MODE AUTO`
`TRIG:SOUR AUTO`	`TRIG2:SOUR AUTO`	`TRIG:SOUR AUTO`

SINGLE

Single run from the starting point to the stop point. If SINGLE is selected, the run is not started yet. Function EXECUTE SINGLE SWEEP ► to be executed, which can be used to start the run, is displayed below the MODE line.

IEC/IEEE-bus commands:

RF sweep:	LF sweep:	Level sweep:
`SOUR:FREQ:MODE SWE`	`SOUR2:FREQ:MODE SWE`	`SOUR:POW:MODE SWE`
`SOUR:SWE:MODE AUTO`	`SOUR2:SWE:MODE AUTO`	`SOUR:SWE:POW:MODE AUTO`
`TRIG:SOUR SING`	`TRIG2:SOUR SING`	`TRIG:SOUR SING`

STEP

Step-by-step, manual run within the sweep limits. Activating STEP stops a running sweep and the cursor wraps to the indication value of CURRENT. The sweep run can now be controlled upwards or downwards in discrete steps using the rotary knob or the numeric keys.

IEC/IEEE-bus commands:

RF sweep:	LF sweep:	Level sweep:
`SOUR:FREQ:MODE SWE`	`SOUR2:FREQ:MODE SWE`	`SOUR:POW:MODE SWE`
`SOUR:SWE:MODE STEP`	`SOUR2:SWE:MODE STEP`	`SOUR:SWE:POW:MODE STEP`
`TRIG:SOUR SING`	`TRIG2:SOUR SING`	`TRIG:SOUR SING`

EXT-SINGLE

Single run from the starting point to the stop point as in the case of SINGLE, but triggered by an external trigger signal.

IEC-bus short commands:

RF sweep:	LF sweep:	Level sweep:
`SOUR:FREQ:MODE SWE`	`SOUR2:FREQ:MODE SWE`	`SOUR:POW:MODE SWE`
`SOUR:SWE:MODE AUTO`	`SOUR2:SWE:MODE AUTO`	`SOUR:SWE:POW:MODE AUTO`
`TRIG:SOUR EXT`	`TRIG2:SOUR EXT`	`TRIG:SOUR EXT`

EXT-STEP Step-by-step run by means of the external trigger signal. Each trigger event triggers a single step.

IEC-bus short commands:

RF sweep:	LF sweep:	Level sweep:
`SOUR:FREQ:MODE SWE`	`SOUR2:FREQ:MODE SWE`	`SOUR:POW:MODE SWE`
`SOUR:SWE:MODE STEP`	`SOUR2:SWE:MODE STEP`	`SOUR:SWE:POW:MODE STEP`
`TRIG:SOUR EXT`	`TRIG2:SOUR EXT`	`TRIG:SOUR EXT`

OFF The sweep operating mode is switched off.

IEC-bus short commands:

RF sweep:	LF sweep:	Level sweep:
`SOUR:FREQ:MODE CW`	`SOUR2:FREQ:MODE CW`	`SOUR:POW:MODE CW`

2.26.4 Trigger Input

An external signal at the rear input triggers the sweep in the EXT-SINGLE and EXT-STEP operating modes. The polarity of the active trigger edge can be set in menu UTILITIES - AUX I/O EXT TRIG SLOPE .

2.26.5 Sweep Outputs

Outputs X-AXIS, BLANK and MARKER are available at the rear of the instrument to control and trigger oscilloscopes or XY recorders.

X_AXIS With sweep switched on, this output supplies a voltage ramp of 0 to 10 V for the X-deflection of an oscilloscope or an XY recorder.

BLANK This output supplies a signal (0V/5V) to trigger and blank an oscilloscope or for the PEN LIFT control of an XY recorder. The polarity and the period of the signal can be set under UTILITIES - AUX I/O - BLANK POLARITY and - BLANK TIME.

MARKER This output becomes active when the sweep run has reached the mark. The MARKER signal can be used for the brightness control of an oscilloscope. Up to three marks can be set in order to mark certain positions in the sweep run. The polarity of the signal can be set in menu UTILITIES - AUX I/O - MARKER POLARITY. The period of the active signal is equal to the dwell time (DWELL) of a step.

Signal examples:

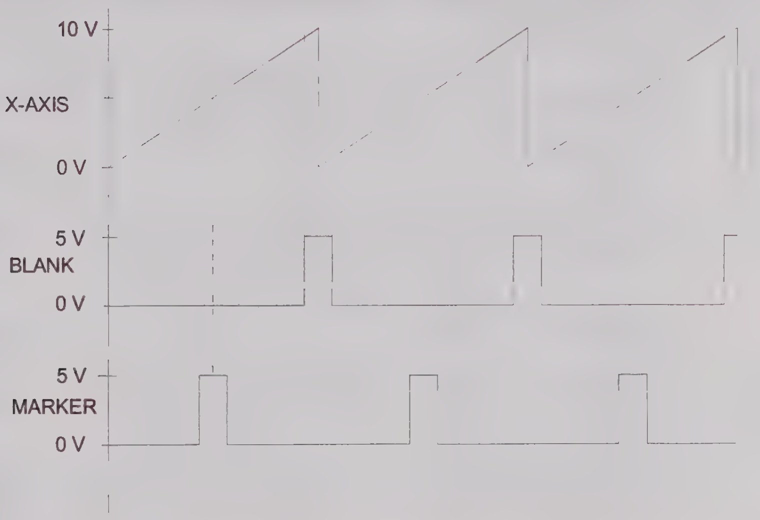

10 V
X-AXIS
0 V
5 V
BLANK
0 V
5 V
MARKER
0 V

Fig. 2-244 Signal example sweep: MODE = AUTO, BLANK TIME = NORMAL

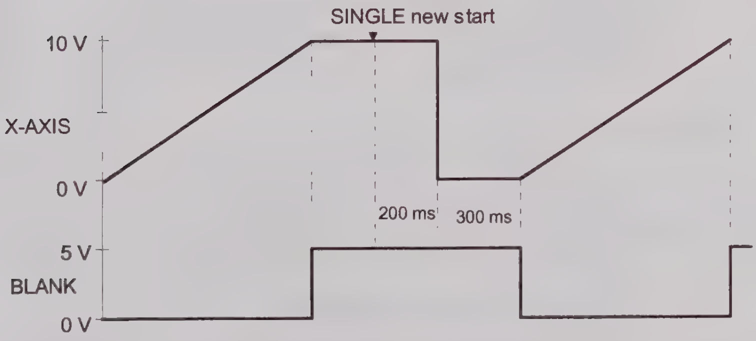
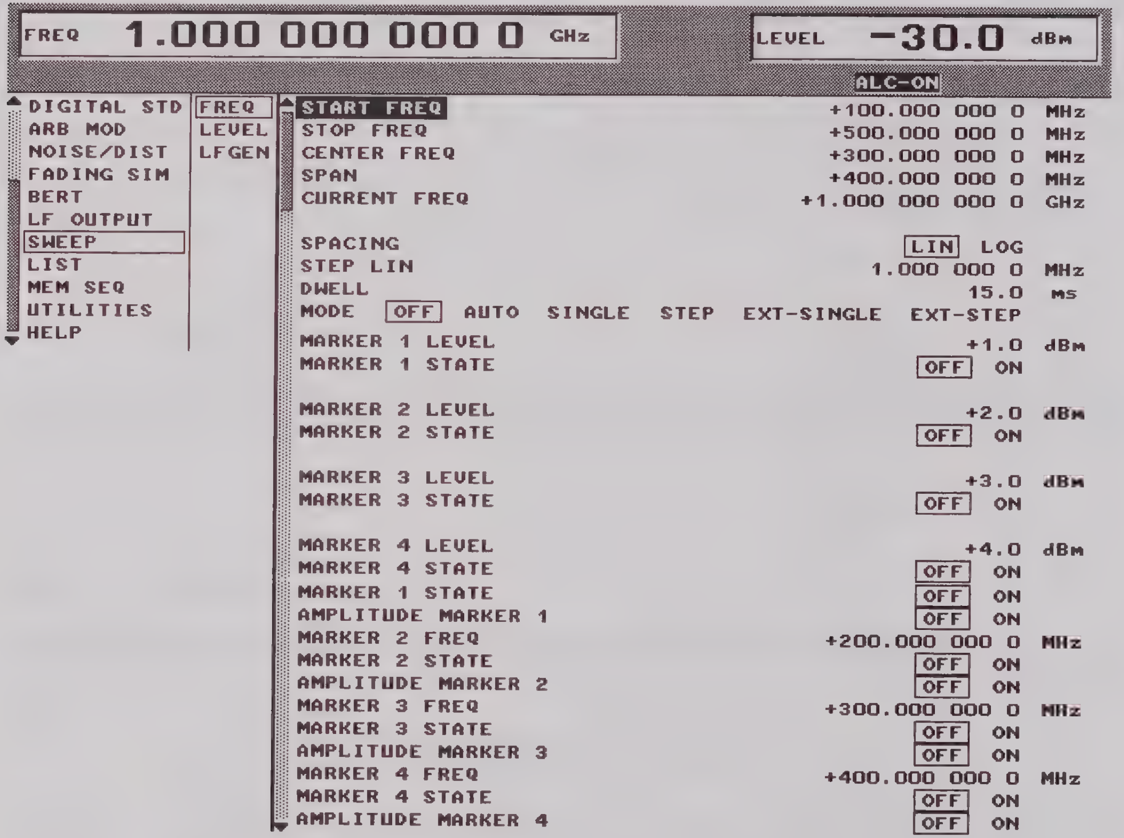

SINGLE new start
10 V
X-AXIS
0 V
200 ms
300 ms
5 V
BLANK
0 V

Fig. 2-245 Signal example sweep: MODE = SINGLE, BLANK TIME = LONG

2.26.6 RF Sweep

Menu SWEEP - FREQ offers access to settings for RF sweep.

Menu selection: SWEEP - FREQ



```
FREQ   1.000 000 000 0 GHz                    LEVEL  -30.0 dBm
                                                      ALC-ON
DIGITAL STD | FREQ  | START FREQ                       +100.000 000 0  MHz
ARB MOD     | LEVEL | STOP FREQ                        +500.000 000 0  MHz
NOISE/DIST  | LFGEN | CENTER FREQ                      +300.000 000 0  MHz
FADING SIM  |       | SPAN                             +400.000 000 0  MHz
BERT        |       | CURRENT FREQ                   +1.000 000 000 0  GHz
LF OUTPUT   |       |
SWEEP       |       | SPACING                                [LIN] LOG
LIST        |       | STEP LIN                           1.000 000 0  MHz
MEM SEQ     |       | DWELL                                     15.0  ms
UTILITIES   |       | MODE  [OFF]  AUTO  SINGLE  STEP  EXT-SINGLE  EXT-STEP
HELP        |       | MARKER 1 LEVEL                            +1.0  dBm
                    | MARKER 1 STATE                      [OFF]  ON

                    | MARKER 2 LEVEL                            +2.0  dBm
                    | MARKER 2 STATE                      [OFF]  ON

                    | MARKER 3 LEVEL                            +3.0  dBm
                    | MARKER 3 STATE                      [OFF]  ON

                    | MARKER 4 LEVEL                            +4.0  dBm
                    | MARKER 4 STATE                      [OFF]  ON
                    | MARKER 1 STATE                      [OFF]  ON
                    | AMPLITUDE MARKER 1                  [OFF]  ON
                    | MARKER 2 FREQ                   +200.000 000 0  MHz
                    | MARKER 2 STATE                      [OFF]  ON
                    | AMPLITUDE MARKER 2                  [OFF]  ON
                    | MARKER 3 FREQ                   +300.000 000 0  MHz
                    | MARKER 3 STATE                      [OFF]  ON
                    | AMPLITUDE MARKER 3                  [OFF]  ON
                    | MARKER 4 FREQ                   +400.000 000 0  MHz
                    | MARKER 4 STATE                      [OFF]  ON
                    | AMPLITUDE MARKER 4                  [OFF]  ON
```

Fig. 2-246 Menu SWEEP - FREQ

START FREQ Input value of the starting frequency.
IEC/IEEE-bus command `:SOUR:FREQ:STAR 100MHz`

STOP FREQ Input value of the stop frequency.
IEC/IEEE-bus command `:SOUR:FREQ:STOP 500MHz`

CENTER FREQ Input value of the center frequency.
IEC/IEEE-bus command `:SOUR:FREQ:CENT 300MHz`

SPAN Input value of the span.
IEC/IEEE-bus command `:SOUR:FREQ:SPAN 100MHz`

CURRENT FREQ Indication of the current frequency value.
Operating mode STEP: Input value of the frequency.

STEP LIN (LOG)	Input value of the step width. Depending on whether SPACING LIN or LOG is selected, STEP LIN or STEP LOG is displayed. IEC/IEEE-bus command `:SOUR:SWE:STEP:LIN 1MHz`
DWELL	Input value of the dwell time per step. IEC/IEEE-bus command `:SOUR:SWE:DWEL 10ms`
SPACING	Input value of the dwell time per step. IEC/IEEE-bus command `SOUR:SWE:SPAC LIN`

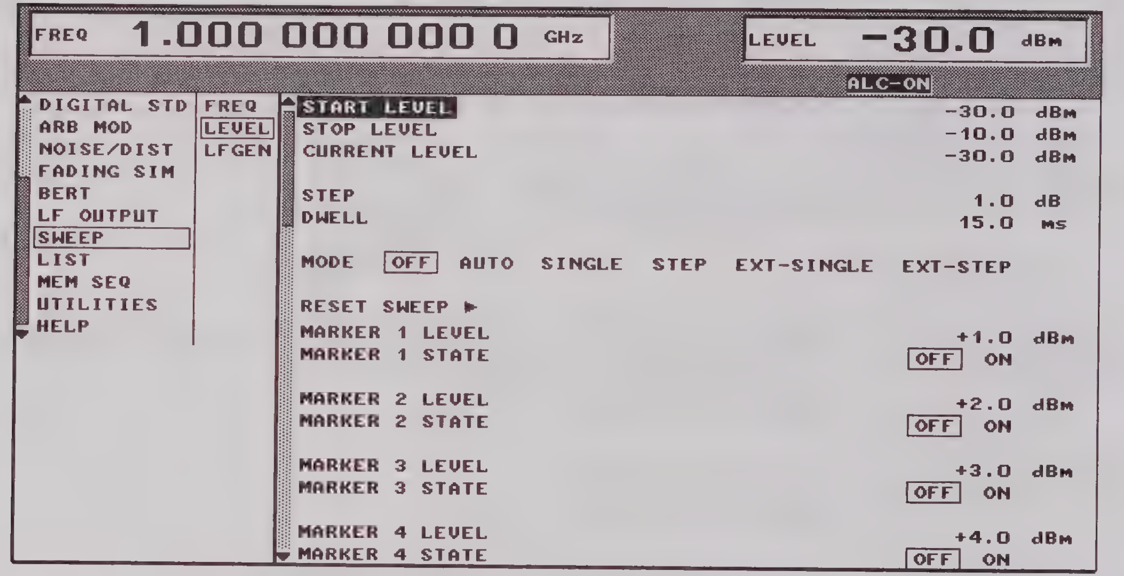
MODE	Selection of the sweep operating mode.
	IEC/IEEE-bus commands `:SOUR:FREQ:MODE SWE;`
	`:SOUR:SWE:MODE AUTO;`
	`:TRIG:SOUR SING`

EXECUTE SINGLE SWEEP ▶	Starts a single sweep run. This action to be executed is only indicated and is only effective if MODE SINGLE has been selected. IEC/IEEE-bus command `:TRIG`
RESET SWEEP ▶	Sets the starting frequency. IEC/IEEE-bus command `:ABOR`
MARKER 1 FREQ **MARKER 2 FREQ** **MARKER 3 FREQ** **MARKER 4 FREQ**	Input value of the frequency for the marker selected IEC/IEEE-bus command `:SOUR:MARK1:FREQ 100MHz`
MARKER 1 STATE **MARKER 2 STATE** **MARKER 3 STATE** **MARKER 4 STATE**	Switching on/off the marker selected IEC/IEEE-bus command `:SOUR:MARK1 OFF`
AMPLITUDE MARKER1 **AMPLITUDE MARKER 2** **AMPLITUDE MARKER 3** **AMPLITUDE MARKER 4**	Switching on/off the amplitude marker selected OFF Input value of the frequency for the marker selected ON Amplitude marker is switched on. On reaching the mark the output level is reduced by 1 dB. IEC/IEEE-bus command `:SOUR:MARK1:AMPL OFF`

2.26.7 LEVEL Sweep

Menu SWEEP - LEVEL offers access to settings for LEVEL sweep.

Menu selection: SWEEP - LEVEL



FREQ 1.000 000 000 0 GHz

LEVEL -30.0 dBm

START LEVEL -30.0 dBm

STOP LEVEL -10.0 dBm

CURRENT LEVEL -30.0 dBm

STEP 1.0 dB

DWELL 15.0 ms

MODE [OFF] AUTO SINGLE STEP EXT-SINGLE EXT-STEP

RESET SWEEP ▶

MARKER 1 LEVEL +1.0 dBm

MARKER 1 STATE [OFF] ON

MARKER 2 LEVEL +2.0 dBm

MARKER 2 STATE [OFF] ON

MARKER 3 LEVEL +3.0 dBm

MARKER 3 STATE [OFF] ON

MARKER 4 LEVEL +4.0 dBm

MARKER 4 STATE [OFF] ON

Fig. 2-247 Menu SWEEP - LEVEL

START LEVEL	Input value of the starting level. IEC/IEEE-bus command `:POW:STAR -30dBm`
STOP LEVEL	Input value of the stop level. IEC/IEEE-bus command `:SOUR:POW:STOP -10dBm`
CURRENT LEVEL	Indication of the current level. Operating mode STEP: Input value of the level.
STEP	Input value of the step width. IEC/IEEE-bus command `:SOUR:SWE:POW:STEP 1dB`
DWELL	Input value of the dwell time per step IEC/IEEE-bus command `:SOUR:SWE:POW:DWEL 15ms`

MODE	Selection of the sweep operating mode.
	IEC/IEEE-bus commands `:SOUR:POW:MODE SWE;`
	`:SOUR:SWE:POW:MODE AUTO;`
	`:TRIG:SOUR SING`

EXECUTE SINGLE SWEEP ▶	Starts a single sweep run. This action to be executed is only indicated and is only effective if MODE SINGLE is selected. IEC/IEEE-bus command `:TRIG`

RESET SWEEP ►

Sets the starting level.

IEC/IEEE-bus command `:ABOR`

MARKER 1 LEVEL
MARKER 2 LEVEL
MARKER 3 LEVEL
MARKER 4 LEVEL

Input value of the level for the marker selected.

IEC/IEEE-bus command `:SOUR:MARK1:PSW:POW 0dBm`

MARKER 1 STATE
MARKER 2 STATE
MARKER 3 STATE
MARKER 4 STATE

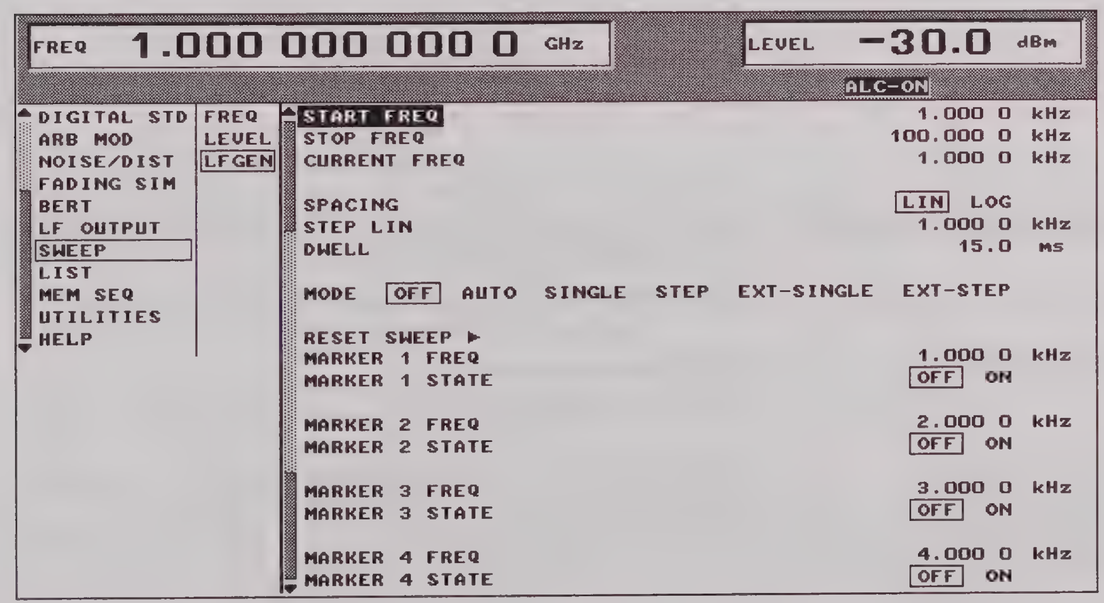
Switching on/off the marker selected.

IEC/IEEE-bus command `:SOUR:MARK1:PSW OFF`

2.26.8 LF Sweep

Menu SWEEP - LF GEN offers access to settings for LF sweep.

Menu selection: SWEEP - LF GEN


DIGITAL STD
ARB MOD
NOISE/DIST
FADING SIM
BERT
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
HELP
FREQ
LEVEL
LFGEN
ALC-ON

FREQ 1.000 000 000 0 GHz
LEVEL -30.0 dBm

START FREQ 1.000 0 kHz
STOP FREQ 100.000 0 kHz
CURRENT FREQ 1.000 0 kHz

SPACING LIN LOG
STEP LIN 1.000 0 kHz
DWELL 15.0 ms

MODE OFF AUTO SINGLE STEP EXT-SINGLE EXT-STEP

RESET SWEEP ►
MARKER 1 FREQ 1.000 0 kHz
MARKER 1 STATE OFF ON

MARKER 2 FREQ 2.000 0 kHz
MARKER 2 STATE OFF ON

MARKER 3 FREQ 3.000 0 kHz
MARKER 3 STATE OFF ON

MARKER 4 FREQ 4.000 0 kHz
MARKER 4 STATE OFF ON

Fig. 2-248 Menu SWEEP - LF GEN

START FREQ	Input value of the starting frequency. IEC/IEEE-bus command `:SOUR2:FREQ:STAR 100kHz`
STOP FREQ	Input value of the stop frequency. IEC/IEEE-bus command `:SOUR2:FREQ:STOP 50kHz`
CURRENT FREQ	Indication of the current frequency value. Operating mode STEP: Input value of the frequency.
STEP	Input value of the step width. IEC/IEEE-bus command `:SOUR2:SWE:STEP:LIN 1kHz`
DWELL	Input value of the dwell time per step. IEC/IEEE-bus command `:SOUR2:SWE:DWEL 15ms`
SPACING	Selection of the sweep run, linear or logarithmic. IEC/IEEE-bus command `:SOUR2:SWE:SPAC LIN`

MODE	Selection of the sweep operating mode.
	IEC/IEEE-bus command `:SOUR2:FREQ:MODE SWE`
	`:SOUR2:SWE:MODE AUTO`
	`:TRIG2:SOUR SING`

EXECUTE SINGLE SWEEP ▶	Starts a single sweep run. This action to be executed is only indicated and is only effective if MODE SINGLE is selected. IEC/IEEE-bus command `:TRIG`
RESET SWEEP ▶	Sets the starting frequency. IEC/IEEE-bus command `:ABOR`
MARKER 1 FREQ **MARKER 2 FREQ** **MARKER 3 FREQ** **MARKER 4 FREQ**	Input value of the frequency for the marker selected. IEC/IEEE-bus command `:SOUR2:MARK1:FREQ 1kHz`
MARKER 1 STATE **MARKER 2 STATE** **MARKER 3 STATE** **MARKER 4 STATE**	Switching on/off the marker selected. IEC/IEEE-bus command `:SOUR2:MARK1 OFF`

2.27 LIST Mode

A sequence of predefined frequency and level points is executed in the LIST mode, similar as in a sweep. Differently from the sweep, however, a list with freely selectable pairs of values (frequency and level) can be generated. The specified range of the frequency comprises the entire adjustable frequency range of the instrument. The specified range of the level covers a 20-dB range. If the permissible variation range is exceeded, the level error increases.

Caution: *After the generation or change of a list in the LIST mode, function LEARN has to be started to ensure that the new settings are transferred to the hardware (IEC-bus short command:* `LIST:LEAR`*.*

Table 2-33 LIST mode; Example of a list

Index	Frequency	Level
0001	100 MHz	0 dBm
0002	575 MHz	13 dBm
0003	235 MHz	7 dBm
0100	333 MHz	5 dBm
:	:	:

Up to 10 lists can be created. The total amount of possible pairs of values including all lists may maximally be 2000. I.e., a list may have 2000 entries at the most, or less if several lists have been created.

Each list is identified by a separate name and selected via this name. A detailed description how to process the lists can be found in Section 2.2.11, List Editor.

2.27.1 Operating Modes (MODE)

The following LIST-operating modes are available:

AUTO Run from the beginning to the end of the list with automatic restart at the beginning. If another mode was activated prior to the AUTO operating mode, continuation is made from the current index.

IEC/IEEE-bus commands:

```
:SOUR:FREQ:MODE LIST
:SOUR:LIST:MODE AUTO
:TRIG:LIST:SOUR AUTO
```

SINGLE Single run from the beginning to the end of the list. If SINGLE is selected, the run is not yet started. Function EXECUTE SINGLE LIST ▶ to be executed, which can be used to start the run, is displayed below the MODE line.

IEC/IEEE-bus commands:

```
:SOUR:FREQ:MODE LIST
:SOUR:LIST:MODE AUTO
:TRIG:LIST:SOUR SING
```

STEP Step-by-step manual processing of the list. Activating STEP stops a list running and the cursor wraps to the indication value of CURRENT INDEX. The list can now be controlled upwards or downwards in discrete steps using the rotary knob or the numeric keys.

IEC/IEEE-bus commands:

```
:SOUR:FREQ:MODE LIST
:SOUR:LIST:MODE STEP
:TRIG:LIST:SOUR SING
```

EXT-SINGLE Single run from the beginning to the end of the list as with SINGLE, but triggered by an external trigger signal.

IEC/IEEE-bus commands:

```
:SOUR:FREQ:MODE LIST;
:SOUR:LIST:MODE AUTO
:TRIG:LIST:SOUR EXT
```

EXT-STEP Step-by-step run by means of the external trigger signal. Each trigger event triggers a single step.

IEC/IEEE-bus commands:

```
:SOUR:FREQ:MODE LIST
:SOUR:LIST:MODE STEP
:TRIG:LIST:SOUR EXT
```

HOP Step-by-step run by means of the internal trigger signal of the data generator (see also Section 'Internal Modulation Data and Control Signals from Lists' and Section 'Menu DIGITAL STANDARD - GSM'). Each trigger event triggers a single step.

IEC/IEEE-bus commands:

```
:SOUR:FREQ:MODE LIST
:SOUR:LIST:MODE STEP
:TRIG:LIST:SOUR HOP
```

OFF Operating mode LIST is switched off.

IEC/IEEE-bus command:

```
:SOUR:FREQ:MODE CW
```

Note: *The minimum step time of 1 ms must not be violated in modes EXT-STEP and HOP either. With fading switched on, the minimum step time is increased to 3 ms, in case of Lognormal fading it is increased to 50 ms.*

2.27.2 Inputs/Outputs

TRIGGER input and BLANK output are available at the rear of the instrument for synchronization with other instruments.

TRIGGER An external signal at this input triggers the LIST mode in operating modes EXT-SINGLE and EXT-STEP. The polarity of the active trigger edge can be set in the UTILITIES - AUX I/O - EXT TRIG SLOPE menu.

BLANK This output supplies a signal (0 V/5 V) to blank the settling process by means of pulse modulation or AM. The signal can also be used to synchronize other instruments. The polarity of the signal can be set in the UTILITIES - AUX I/O - BLANK POLARITY menu.

MARKER At the first step of the LIST mode, this output provides an approx. 200 µs trigger signal immediately after blanking. At small DWELL times, this signal can be used for an accurate synchronization to trigger other devices and shows the first stable output frequency. The delay to the fed-in signal at the TRIGGER input for EXT-SINGLE or EXT-STEP is 1.5 to 2 ms and has a jitter of 0.5 ms.

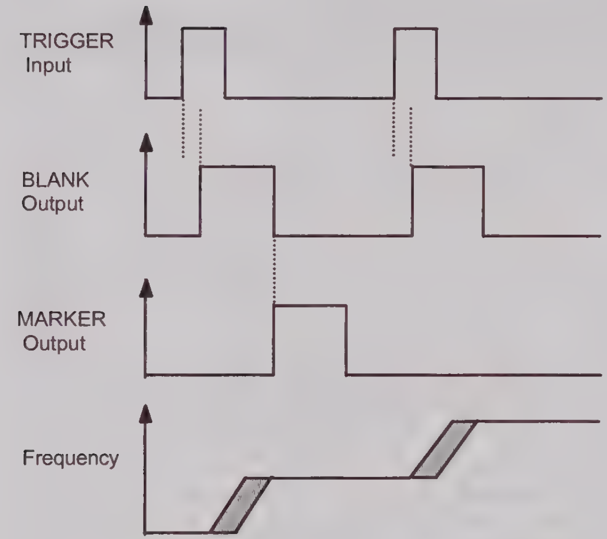
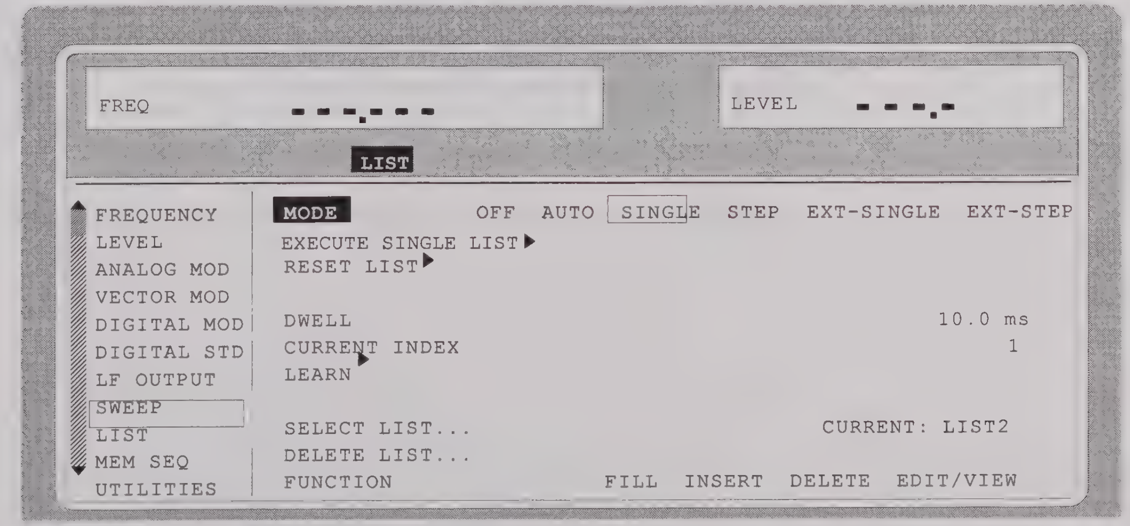

TRIGGER Input
BLANK Output
MARKER Output
Frequency

Fig. 2-249 Signal example LIST mode: MODE = EXT-STEP

The LIST menu offers access to settings for the LIST mode.

Menu selection: LIST


FREQ
LEVEL
LIST
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES

```
MODE                OFF  AUTO  SINGLE  STEP  EXT-SINGLE  EXT-STEP
EXECUTE SINGLE LIST▶
RESET LIST▶

DWELL                                                   10.0 ms
CURRENT INDEX                                              1
LEARN▶

SELECT LIST...                              CURRENT: LIST2
DELETE LIST...
FUNCTION                   FILL  INSERT  DELETE  EDIT/VIEW
```

Fig. 2-250 Menu LIST - OPERATION page

MODE

Selection of the operating mode.

IEC/IEEE-bus commands :
```
:SOUR:FREQ:MODE LIST;
:SOUR:LIST:MODE AUTO;
:TRIG:LIST:SOUR SING
```

EXECUTE SINGLE LIST ▶

Starts a single run of a list. This menu option is only visible if MODE SINGLE is selected.

IEC/IEEE-bus command : `:TRIG:LIST`

RESET LIST ▶

Sets the starting point.

IEC/IEEE-bus command `:ABOR:LIST`

DWELL

Input value of the dwell time per step.

IEC/IEEE-bus command `:SOUR:LIST:DWEL 10ms`

CURRENT INDEX

Indication of the current list index. Setting value of the current list index in the STEP operating mode.

LEARN ▶

Starts the LEARN function. All value pairs of the active list are subsequently set by the instrument with the current additional parameters, and the hardware setting data are stored.

Caution: *This function must be called after every creating and altering the list (or the remaining setting data). The list must be learned as well after temperature changes or after calling internal calibration routines, as all parameters usually controlled are replaced by stored values in the LIST mode.*

IEC/IEEE-bus command `:SOUR:LIST:LEAR`

SELECT LIST...

Selection of a list or creation of a new list (cf. Section 2.2.11, List Editor).

IEC/IEEE-bus command `:SOUR:LIST:SEL 'LIST2'`

DELETE LIST...

Deletion of a list (cf. Section 2.2.11, List Editor).

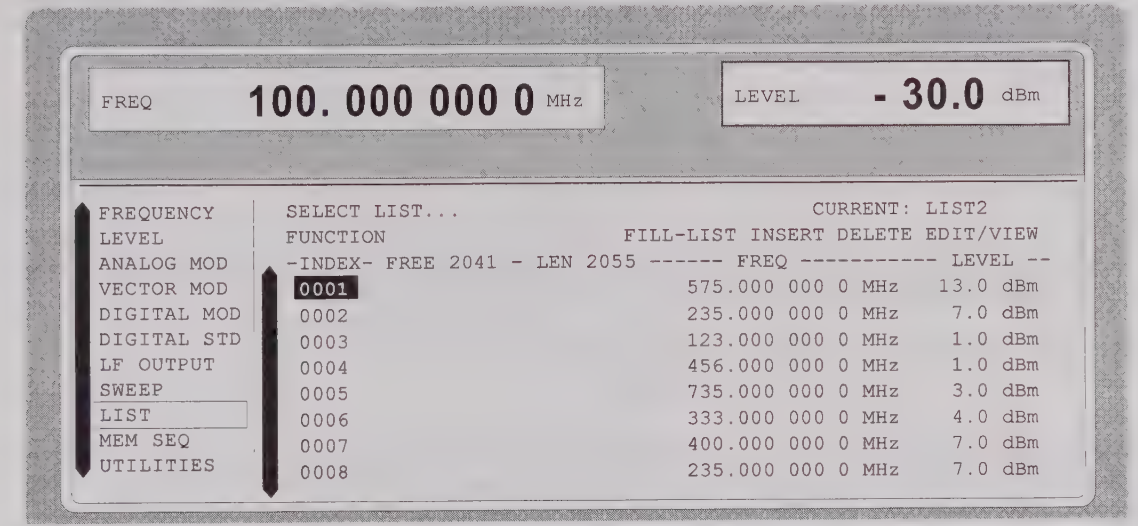
IEC/IEEE-bus command `:SOUR:LIST:DEL  'LIST1'`

FUNCTION

Selection of the editor functions to process a list (cf. Section 2.2.11, List Editor).

IEC/IEEE `:SOUR:LIST:FREQ 100MHz, 1.2GHz; POW 0dBm, 6dBm`

The second page of the LIST menu, the EDIT page is automatically activated if one of the editor functions of line FUNCTION is selected. The list which is displayed as CURRENT LIST in the SELECT LIST line is shown.


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES

```
FREQ        100. 000 000 0 MHz                LEVEL      - 30.0 dBm

SELECT LIST...                                     CURRENT: LIST2
FUNCTION                            FILL-LIST INSERT DELETE EDIT/VIEW
-INDEX- FREE 2041 - LEN 2055 ------ FREQ ----------- LEVEL --
 0001                          575.000 000 0 MHz   13.0 dBm
 0002                          235.000 000 0 MHz    7.0 dBm
 0003                          123.000 000 0 MHz    1.0 dBm
 0004                          456.000 000 0 MHz    1.0 dBm
 0005                          735.000 000 0 MHz    3.0 dBm
 0006                          333.000 000 0 MHz    4.0 dBm
 0007                          400.000 000 0 MHz    7.0 dBm
 0008                          235.000 000 0 MHz    7.0 dBm
```

Fig. 2-251 Menu List - EDIT page

INDEX	Index of the list.
FREE	Indication of the list entries still vacant.
LENGTH	Length of the current list.
FREQ	Parameter: Frequency.
LEVEL	Parameter: Level; specified range 20 dB.

2.28 Memory Sequence

In the memory-sequence operating mode the instrument automatically services a list with stored instrument settings. Memory locations 1 to 50, which are loaded using SAVE and whose stored settings are called either separately using RECALL or automatically and subsequently in the SEQUENCE mode, are available.

The list is continuously serviced from the beginning to the end with a continual index. The order of the memories to be passed through is arbitrary. Each setting can be assigned a freely selectable dwell time. The dwell time determines the duration of the setting, its minimal value is 50 ms, its maximal value 60 sec.

The list is divided up into 3 columns for list index, memory location number (Memory) and dwell time (Dwell). The beginning of the list has index 1.

Table 2-35 MEMORY SEQUENCE; Example of a list

Index	Memory	Dwell
001	09	50.0 ms
002	02	50.0 ms
003	01	75.0 ms
004	10	75.0 ms
...	...	...

Up to 10 sequence lists can be created. The total number of possible list elements is maximally 256. I.e., a list can have 256 entries at the most, or less if several lists have been created.

Each list is identified by a separate name and selected via this name. A detailed description how to process the lists can be found in Section 2.2.4, List Editor.

Note: *Frequently changing the level in the operating mode MEMORY SEQUENCE can stress the mechanically switched attenuator. The attenuator is also actuated when AM is switched on or off. For this reason we recommend that you make use of the non-interrupting level setting as much as possible and that you use the setting AM 0% instead of switching AM off.*

Operating Modes (MODE)

The following operating modes are available:

AUTO

Run from the beginning to the end of the list with automatic restart at the beginning. If another mode was activated prior to the AUTO operating mode, continuation is made from the current index.

IEC/IEEE-bus command:
```
:SYST:MODE MSEQ;
:SYST:MSEQ:MODE AUTO
:TRIG:MSEQ:SOUR AUTO
```

SINGLE

Single run from the beginning to the end of the list. If SINGLE is selected, the run is not yet started. Below the MODE line, function EXECUTE SINGLE SEQUENCE ▶ to be executed is displayed which can be used to start the run.

IEC/IEEE-bus command
```
:SYST:MODE MSEQ;
:SYST:MSEQ:MODE AUTO
:TRIG:MSEQ:SOUR SING
```

STEP

Step-by-step manual processing of the list. Activating STEP stops an automatic run and the cursor wraps to the indication value of CURRENT INDEX. The list can now be passed through upwards or downwards step by step using the rotary knob.

IEC/IEEE-bus command
```
:SYST:MODE MSEQ;
:SYST:MSEQ:MODE STEP
:TRIG:MSEQ:SOUR SING
```

EXT-SINGLE

Single run from the beginning to the end of the list as with SINGLE, but triggered by an external trigger signal.

IEC/IEEE-bus command
```
:SYST:MODE MSEQ;
:SYST:MSEQ:MODE AUTO
:TRIG:MSEQ:SOUR EXT
```

EXT-STEP

Step-by-step run using the external trigger signal. Each trigger event triggers a single step.

IEC/IEEE-bus command
```
:SYST:MODE MSEQ;
:SYST:MSEQ:MODE STEP
:TRIG:MSEQ:SOUR EXT
```

OFF

Step-by-step run using the external trigger signal. Each trigger event triggers a single step.

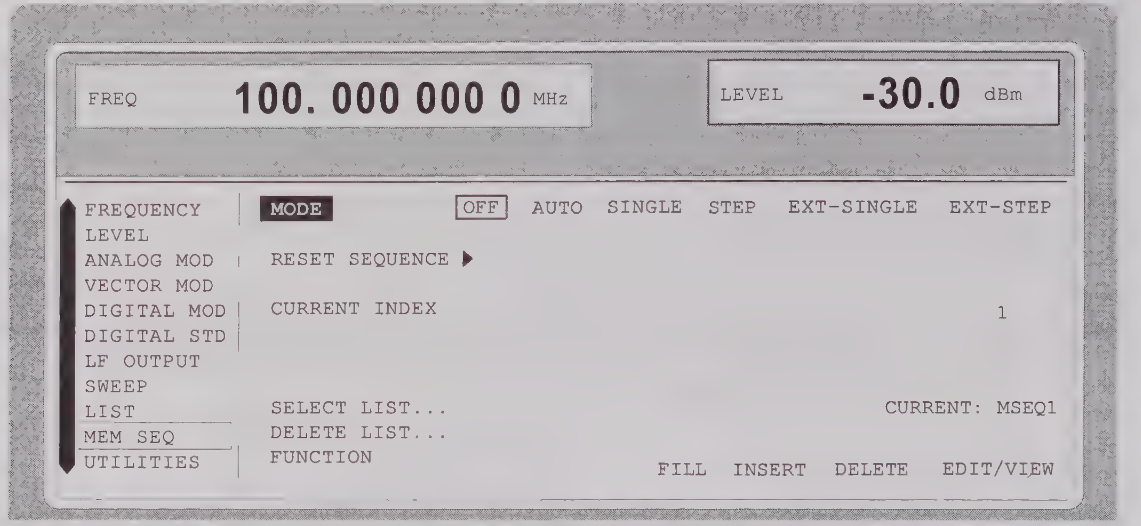
IEC/IEEE-bus command
```
:SYST:MODE FIX
```

External Trigger

An external signal at the rear input TRIGGER triggers the MEMORY SEQUENCE in the EXT-SINGLE and EXT-STEP operating modes. The polarity of the active trigger edge can be set in the UTILITIES - AUX I/O - EXT TRIG SLOPE menu.

Menu MEM SEQ with the two menu pages OPERATION and EDIT offers access to the memory-sequence operating mode.

Menu selection: MEM SEQ



FREQ 100. 000 000 0 MHz
LEVEL -30.0 dBm

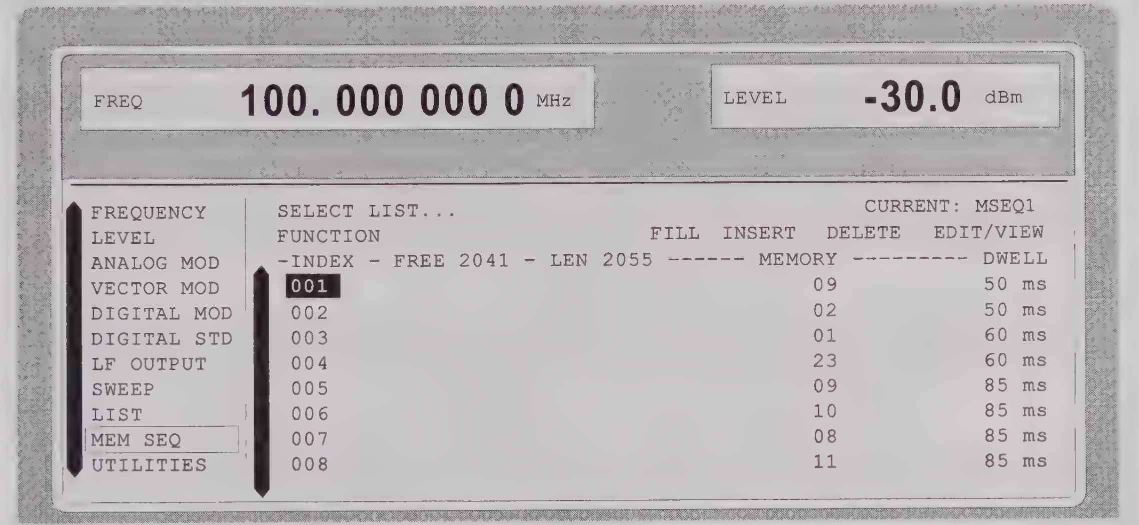
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES

MODE OFF AUTO SINGLE STEP EXT-SINGLE EXT-STEP
RESET SEQUENCE ▶
CURRENT INDEX 1
SELECT LIST... CURRENT: MSEQ1
DELETE LIST...
FUNCTION FILL INSERT DELETE EDIT/VIEW

Fig. 2-252 Menu MEM SEQ -OPERATION-page (preset setting)

MODE	Selection of the operating mode; setting the operating mode regards various command systems at the IEC bus (cf. above).
EXECUTE SINGLE SEQUENCE ▶	Starts the single run of a memory sequence. This menu option is only visible if MODE SINGLE is selected. IEC/IEEE-bus command `:TRIG:MSEQ`
RESET SEQUENCE ▶	Wrap to the beginning of the list. IEC/IEEE-bus command `:ABOR:MSEQ`
CURRENT INDEX	Indication of the current list index. Setting value of the current list index in the MODE STEP operating mode.
SELECT LIST...	Selection of a list or generation of a new list (cf. Section 2.2.4, List Editor). IEC/IEEE-bus command `:SYST:MSEQ:SEL "MSEQ1"`
DELETE LIST...	Deletion of a list (cf. Section 2.2.4, List Editor). IEC/IEEE-bus command `:SYST:MSEQ:DEL "MSEQ2"`
FUNCTION	Selection of the editor functions to process a list (cf. Section 2.2.4, List Editor). IEC/IEEE-bus command `:SYST:MSEQ 9,2,...;` `:SYST:MSEQ:DWEL 50ms, 50ms,.`

The second page of menu MEM SEQ, the EDIT page, is automatically activated if one of the editor functions of the FUNCTION line is selected. The list which is entered as CURRENT LIST in the SELECT LIST line is shown.


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SELECT LIST...
FUNCTION
FILL
INSERT
DELETE
EDIT/VIEW

FREQ 100. 000 000 0 MHz

LEVEL -30.0 dBm

CURRENT: MSEQ1

```
-INDEX - FREE 2041 - LEN 2055 ------ MEMORY --------- DWELL
 001                                  09            50 ms
 002                                  02            50 ms
 003                                  01            60 ms
 004                                  23            60 ms
 005                                  09            85 ms
 006                                  10            85 ms
 007                                  08            85 ms
 008                                  11            85 ms
```

Fig. 2-253 Menu MEM SEQ - EDIT page

INDEX	Index of the list.
FREE	Indication of the list entries still vacant.
LEN	Length of the current list.
MEMORY	Parameter: number of memory location; range 1 to 50.
DWELL	Parameter: dwell time; specified range 50 ms to 60 sec, step width 1 ms.

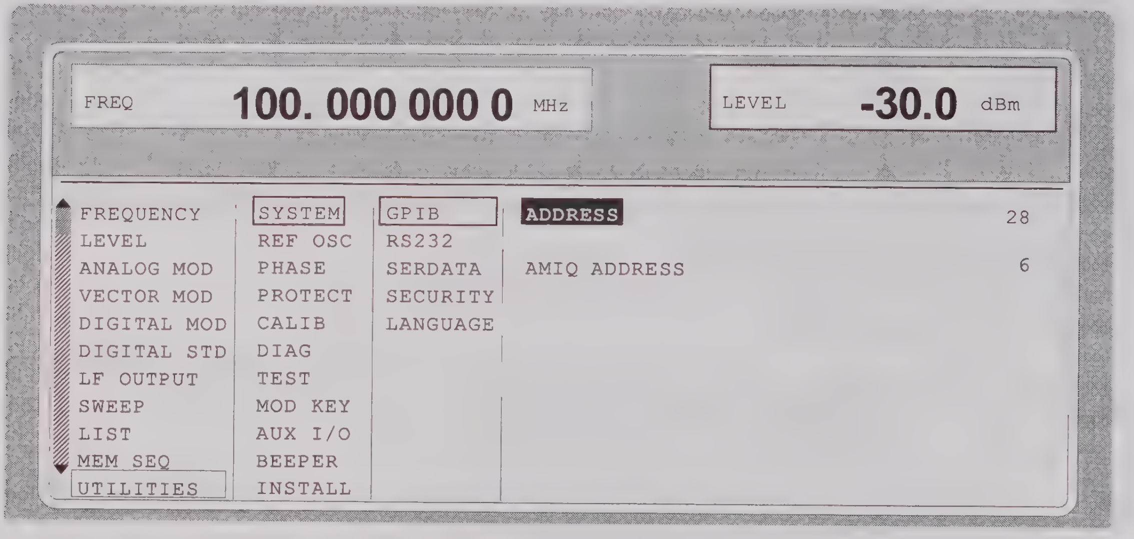
2.29 Utilities

The UTILITIES menu contains submenus for general functions which do not directly relate to the signal generation.

2.29.1 IEC-Bus Address (SYSTEM-GPIB)

Submenu SYSTEM-GPIB offers access to the remote-control address. The setting range is 0 to 30. At the point of delivery address 28 is set.

Menu selection: UTILITIES -SYSTEM -GPIB



FREQ 100. 000 000 0 MHz

LEVEL -30.0 dBm

ADDRESS 28

AMIQ ADDRESS 6

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
GPIB
RS232
SERDATA
SECURITY
LANGUAGE

Fig. 2-254 Menu UTILITIES -SYSTEM -GPIB

ADDRESS	Input value of the IEC-bus address IEC/IEEE-bus command `:SYST:COMM:GPIB:ADDR 28`
AMIQ ADDRESS	IEC/IEEE-bus address of AMIQ.

2.29.2 Parameter of the RS232 Interface (SYSTEM-RS232)

Submenu SYSTEM-RS232 offers access to the configuration of the RS-232 interface. The pin assignment of the interface corresponds to the pin assignment of a PC.

Menu selection: UTILITIES - SYSTEM - RS232

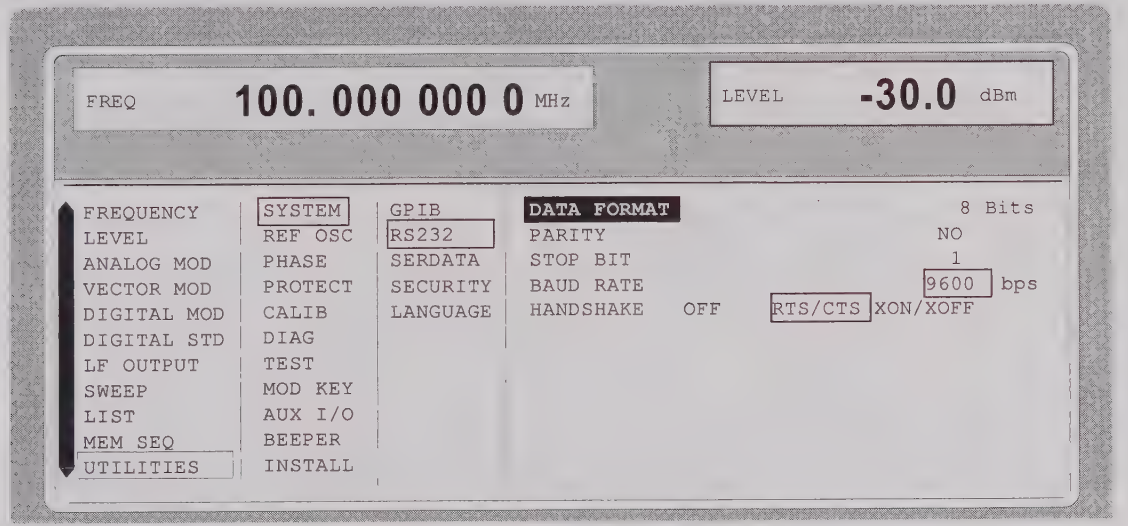

FREQ 100. 000 000 0 MHz
LEVEL -30.0 dBm
DATA FORMAT 8 Bits
PARITY NO
STOP BIT 1
BAUD RATE 9600 bps
HANDSHAKE OFF RTS/CTS XON/XOFF
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
GPIB
RS232
SERDATA
SECURITY
LANGUAGE

Fig. 2-255 Menu UTILITIES - SYSTEM - RS232

DATA FORMAT	Indication of the number of data bits. This value cannot be changed.
PARITY	Indication of parity. This value cannot be changed.
STOP BIT	Indication of the number of stop bits. This value cannot be changed.

BAUD RATE Selection of the baud rate.

IEC/IEEE-bus command `:SYST:COMM:SER:BAUD 9600`

HANDSHAKE Selection of the handshake.

OFF No handshake

IEC/IEEE-bus command `:SYST:COMM:SER:PACE NONE`
`:SYST:COMM:SER:CONT:RTS ON`

RTS/CTS Hardware handshake using the interface lines RTS and CTS. This mode always is to be preferred to XON/XOFF mode, if permitted by the configuration of the host computer.

IEC/IEEE-bus command `:SYST:COMM:SER:CONT:RTS RFR`

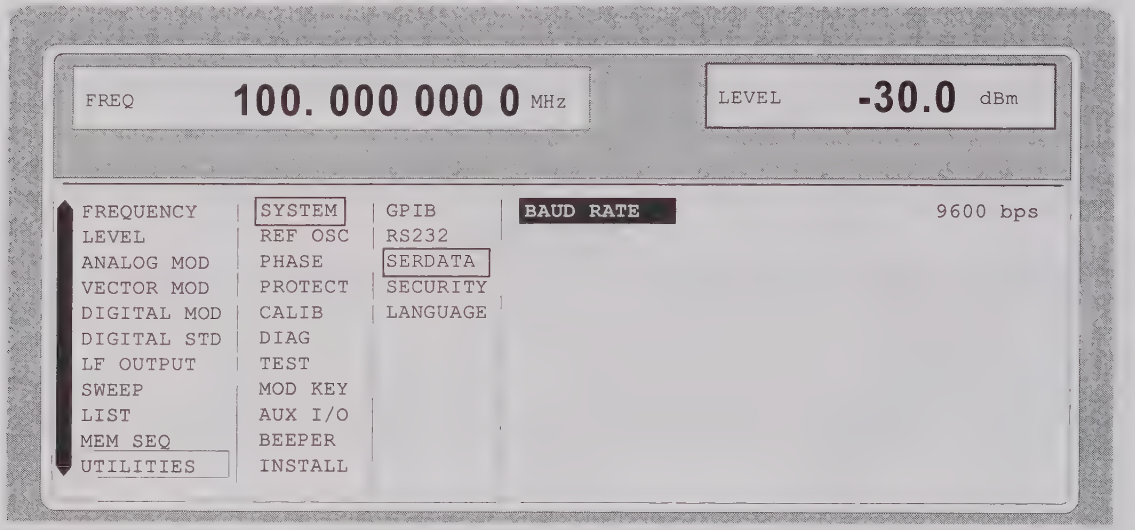
XON/XOFF Software handshake using the ASCII codes 11h <XON> and 13h <XOFF>. This mode is not recommended for binary data and for baud rates above 9600 baud.

IEC/IEEE-bus command `:SYST:COMM:SER:PACE XON`

2.29.3 Parameter of the SER DATA Input (SYSTEM-SERDATA)

Submenu SYSTEM-SERDATA offers access to the configuration of the SERDATA input.

Menu selection: UTILITIES - SYSTEM - SERDATA



FREQ 100. 000 000 0 MHz

LEVEL -30.0 dBm

BAUD RATE 9600 bps

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
GPIB
RS232
SERDATA
SECURITY
LANGUAGE

Fig. 2-256 Menu UTILITIES - SYSTEM - SERDATA

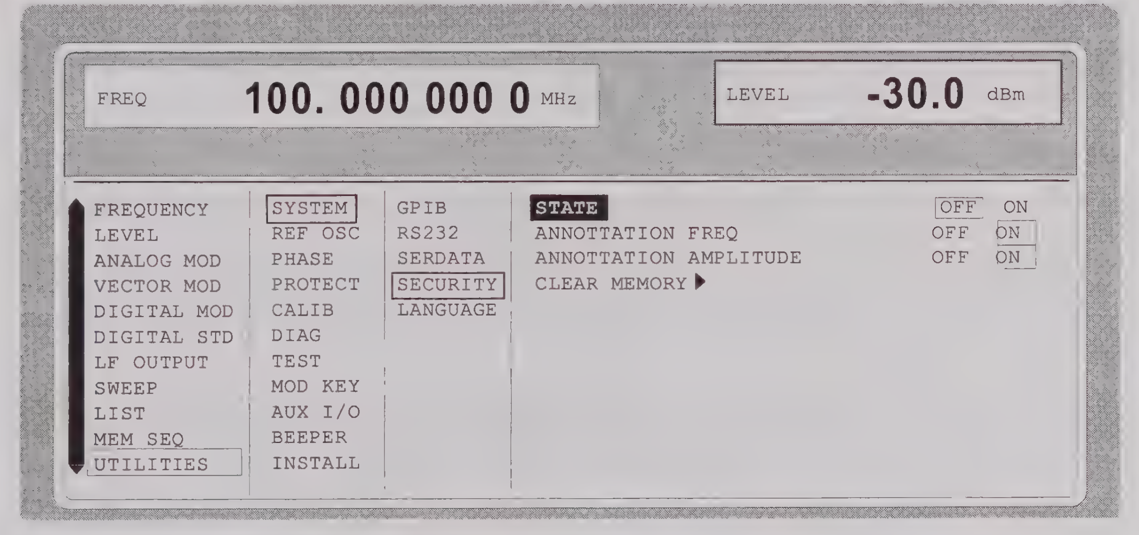
BAUD RATE Selection of the baud rate.

IEC/IEEE-bus command `:SYST:COMM:SDAT:BAUD 9600`

2.29.4 Suppressing Indications and Deleting Memories (SYSTEM-SECURITY)

For security interests, indications can be suppressed and memories deleted in the SYSTEM-SECURITY submenu.

Menu selection: UTILITIES - SYSTEM-SECURITY


FREQ
100. 000 000 0 MHz
LEVEL
-30.0 dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
GPIB
RS232
SERDATA
SECURITY
LANGUAGE
CLEAR MEMORY ▶

STATE OFF ON
ANNOTATION FREQ OFF ON
ANNOTATION AMPLITUDE OFF ON

Fig. 2-257 Menu UTILITIES - SYSTEM-SECURITY

STATE

Selection of the SECURITY state

ON Locks the suppression of the indications. Can only be set via IEC bus.

OFF Deactivates the interlock of the indication suppression. The preset state is set in the transition ON→OFF, and all data stored such as settings, with the exception of the DM lists are deleted. Can only be set via IEC bus.

IEC/IEEE-bus command `:SYST:SEC OFF`

ANNOTATION FREQ

OFF All frequency indications are suppressed.

ON The frequency setting is displayed..

IEC/IEEE-bus command `:DISP:ANN:FREQ ON`

ANNOTATION AMPLITUDE

OFF All level indications are suppressed.

ON The level setting is displayed.

IEC/IEEE-bus command `:DISP:ANN:AMPL ON`

CLEAR MEMORY ▶

Deletion of all data stored such as settings, user correction and list settings stored, with the exception of the DM lists.

For this action, two commands are necessary at the IEC bus:

IEC/IEEE-bus command `:SYST:SEC ON; SEC OFF`

2.29.5 Indication of the IEC-Bus Language (LANGUAGE)

Submenu UTILITIES-SYSTEM LANGUAGE indicates the IEC-bus language and the current SCPI version.

2.29.6 Reference Frequency Internal/External (REF OSC)

In the internal-reference operating mode, the internal reference signal at a frequency of 10 MHz is available at the REF socket (rear of the instrument).

Signal level: V_{eff} (EMF, sine) = 1 V.

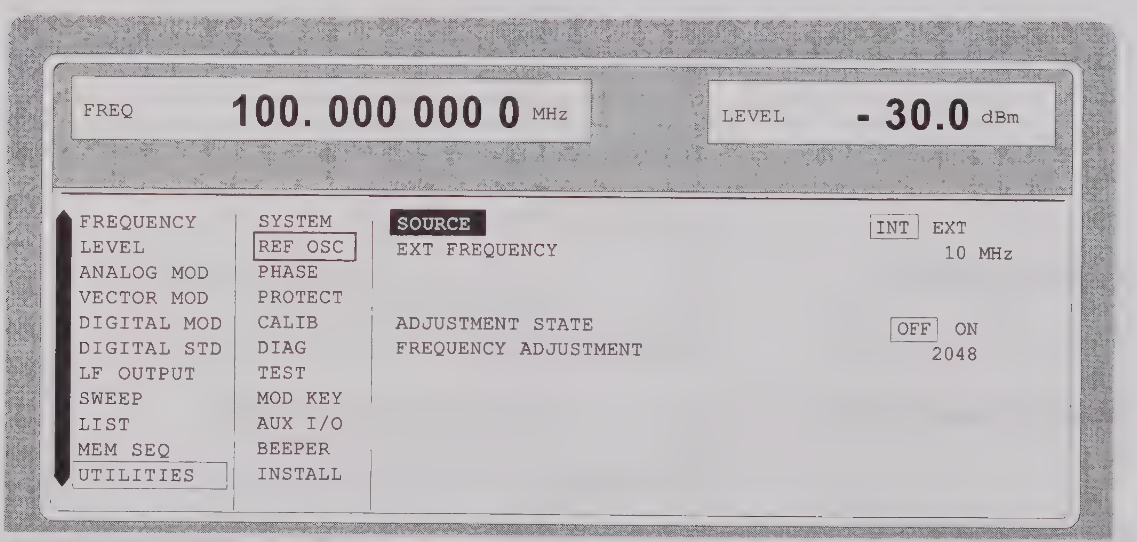
The frequency of the internal reference oscillator can be detuned via the EXT TUNE input (rear of the instrument). Input voltage range ±10 V, pulling range $\pm 1\times10^{-6}$.
The external detuning is possible in both states of the ADJUSTMENT STATE (ON or OFF) unless option SM-B1, reference oscillator OCXO, is fitted. If option SM-B1, reference oscillator OCXO, is fitted, the detuning via the TUNE input is only possible if the ADJUSTMENT STATE selection has been switched to ON in the UTILITIES-REF OSC menu.

In the external-reference operating mode, an external signal at a frequency of 1 MHz to 16 MHz (spacing 1 MHz) is to be fed into socket REF. The setting to external frequency is effected in the UTILITIES-REF OSC menu.

Signal level: V_{eff} = 0.1 to 2 V

The message "EXT REF" is displayed in the status line in the header field of the display in the external-reference operating mode.

Menu selection: UTILITIES - REF OSC



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

SOURCE INT EXT

EXT FREQUENCY 10 MHz

ADJUSTMENT STATE OFF ON

FREQUENCY ADJUSTMENT 2048

Fig. 2-258 Menu UTILITIES - REF OSC (preset setting)

SOURCE Selection of the operating mode.

INT	Internal-reference operating mode
EXT	External-reference operating mode

IEC/IEEE-bus command `SOUR:ROSC:SOUR INT`

EXT FREQUENCY

Input value of the external reference frequency (1 MHz to 16 MHz, spacing 1 MHz).

IEC/IEEE-bus command `SOUR:ROSC:EXT:FREQ 10E6`

ADJUSTMENT STATE

OFF	Tuning value of the internal reference frequency as calibrated (cf. menu UTILITIES-CALIB)
ON	Tuning value according to setting value FREQUENCY ADJUSTMENT. Option SM-B1, reference oscillator OCXO, is switched off. Only the standard reference oscillator is in operation.

IEC/IEEE-bus command `SOUR:ROSC:ADJ:STAT ON`

FREQUENCY ADJUSTMENT

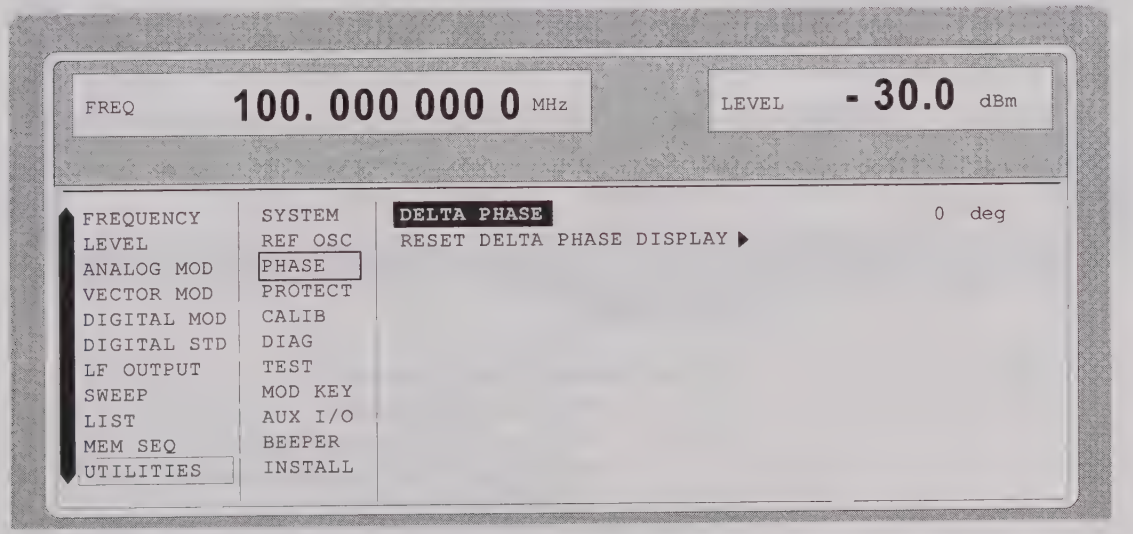
Input value in the range 0 to 4095 to set the internal reference frequency. Pulling range $\pm 4\times10^{-6}$

IEC/IEEE-bus command `SOUR:ROSC:ADJ:VAL 2048`

2.29.7 Phase of the Output Signal (PHASE)

Menu UTILITIES-PHASE offers access to the phase setting of the RF output signal with respect to a reference signal of the same frequency.

Menu selection: UTILITIES - PHASE



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

DELTA PHASE 0 deg

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
RESET DELTA PHASE DISPLAY ▶

Fig. 2-259 Menu UTILITIES - PHASE (preset setting)

DELTA PHASE

Setting value of the phase.

IEC/IEEE-bus command `:SOUR:PHAS 10 DEG`

RESET DELTA PHASE DISPLAY ▶

Sets the display of the DELTA PHASE to 0 without the phase of the output signal being influenced.

IEC/IEEE-bus command `:SOUR:PHAS:REF`

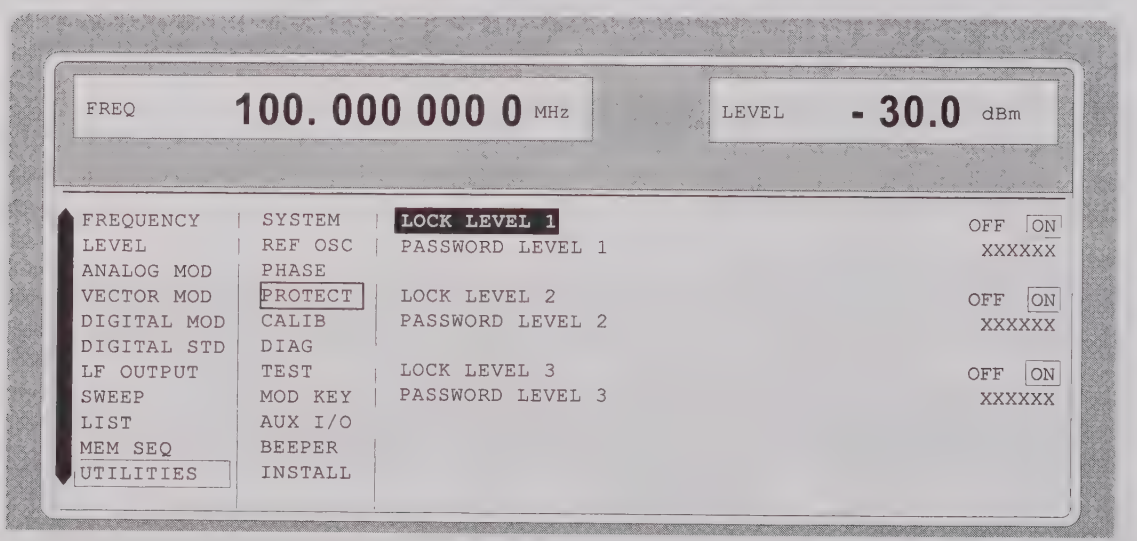
2.29.8 Password Input With Functions Protected (PROTECT)

The execution of calibrating and service functions is protected by a password. To unlock the lock-out, the correct password, a 6-digit number, has to be entered and then the [ENTER] key has to be pushed. After the instrument has been switched on, the lock-out is automatically activated.

Password 1	unlocks the lock-out for calibrations LEV PRESET and VCO SUM.
Password 2	unlocks the lock-out for calibration REF OSC.
Password 3	permits the input of the serial number and the value of the counter for POWER ON, operating hours and attenuator circuits.

Menu UTILITIES-PROTECT offers access to the unlocking of protected functions.

Menu selection: UTILITIES - PROTECT


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

LOCK LEVEL 1 OFF ON
PASSWORD LEVEL 1 XXXXXX

LOCK LEVEL 2 OFF ON
PASSWORD LEVEL 2 XXXXXX

LOCK LEVEL 3 OFF ON
PASSWORD LEVEL 3 XXXXXX

Fig. 2-260 Menu UTILITIES - PROTECT (preset setting)

LOCK LEVEL x

Activating/deactivating the lock-out.

ON	The lock-out is activated.
OFF	The cursor automatically wraps to the input of the password. After the password has been entered, the lock-out is deactivated.

IEC/IEEE-bus command `:SYST:PROT1 ON`

PASSWORD LEVEL x

Input of the password; termination with [ENTER] key.

IEC/IEEE-bus command `:SYST:PROT1 OFF, 123456`

2.29.9 Calibration (CALIB)

For servicing, the following menus offer access to calibrating routines and correction values:

UTILITIES - CALIB - ALL
VCO SUM
VECTOR MOD
LEV PRESET
REF OSC (cf. service manual)
LEVEL (cf. service manual)
ALC TABLE
LEV ATT
LFGEN

Internal calibration routines LEV PRESET and VCO SUM are protected by a password. They can only be executed if the lock-out in the UTILITIES - PROTECT menu has been unlocked. The password is PASSWORD LEVEL 1 = "123456".

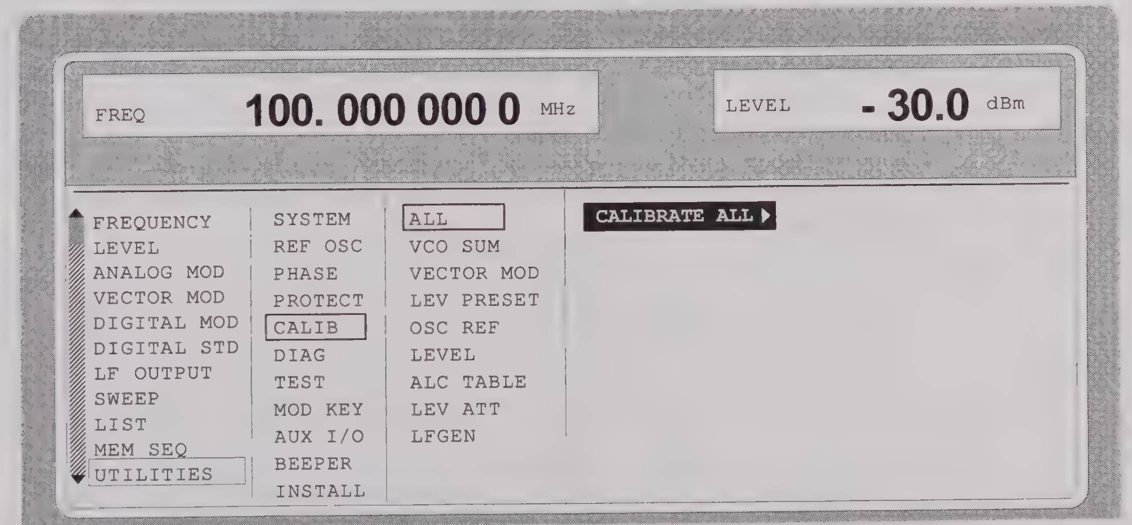
Caution: *Execute calibration routines only when the instrument has warmed up*

Calibration routines LEVEL and REF OSC are described in the service manual.

Calibration ALL

CALIB ALL triggers all internal calibrations which do not require any external measuring equipment. The calibrations with external measurements are described in the service manual.

Menu selection: UTILITIES - CALIB - ALL



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
ALL
VCO SUM
VECTOR MOD
LEV PRESET
OSC REF
LEVEL
ALC TABLE
LEV ATT
LFGEN
CALIBRATE ALL ▶

Fig. 2-261 Menu UTILITIES - CALIB - ALL

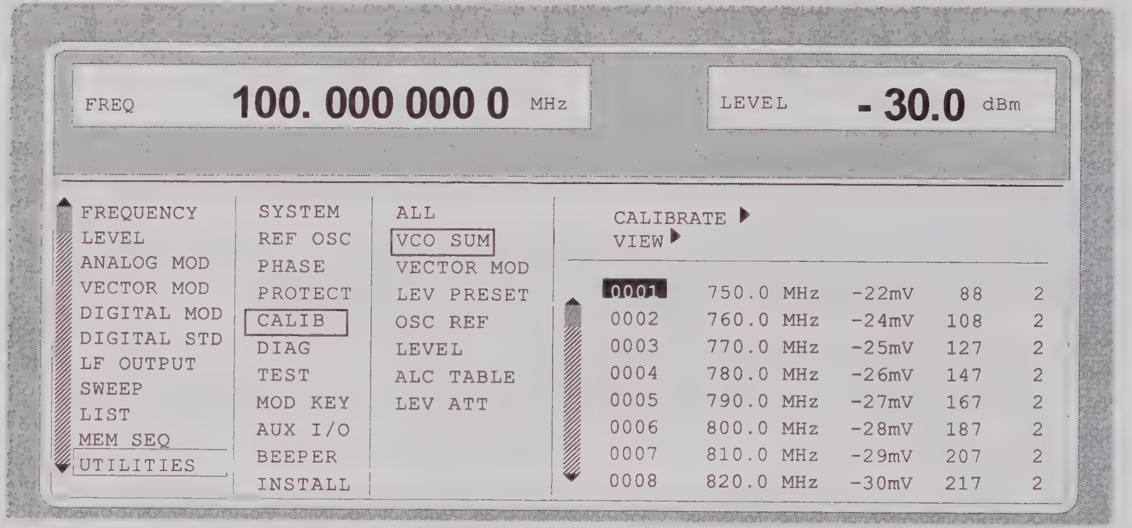
CALIBRATE ALL ▶ Triggers all internal calibrations.

IEC\IEEE-bus command `:CAL:ALL?`

Calibration VCO SUM

To synchronize the summing loop, the frequency the oscillator generates must be so close to the rated frequency that the phase control can lock in. This is effected by means of presetting values. The presetting values are stored in a table and can be renewed using internal calibration routine VCO SUM. The calibration routine needs only be executed after a data loss in the RAM or after an exchange of modules.

Menu selection: UTILITIES - CALIB - VCO SUM


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
ALL
VCO SUM
VECTOR MOD
LEV PRESET
OSC REF
LEVEL
ALC TABLE
LEV ATT
CALIBRATE ▶
VIEW ▶

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

0001	750.0 MHz	-22mV	88	2
0002	760.0 MHz	-24mV	108	2
0003	770.0 MHz	-25mV	127	2
0004	780.0 MHz	-26mV	147	2
0005	790.0 MHz	-27mV	167	2
0006	800.0 MHz	-28mV	187	2
0007	810.0 MHz	-29mV	207	2
0008	820.0 MHz	-30mV	217	2

Fig. 2-262 Menu UTILITIES - CALIB - VCO SUM

CALIBRATE ▶ Triggers the calibration for the VCO summing loop.

IEC/IEEE-bus command `:CAL:VSUM?`

VIEW ▶ Indication of the list of correction values.
The cursor wraps to index 1 of the list. The list can be executed using the rotary knob. This index can be obtained by entering the index value on the digit block.

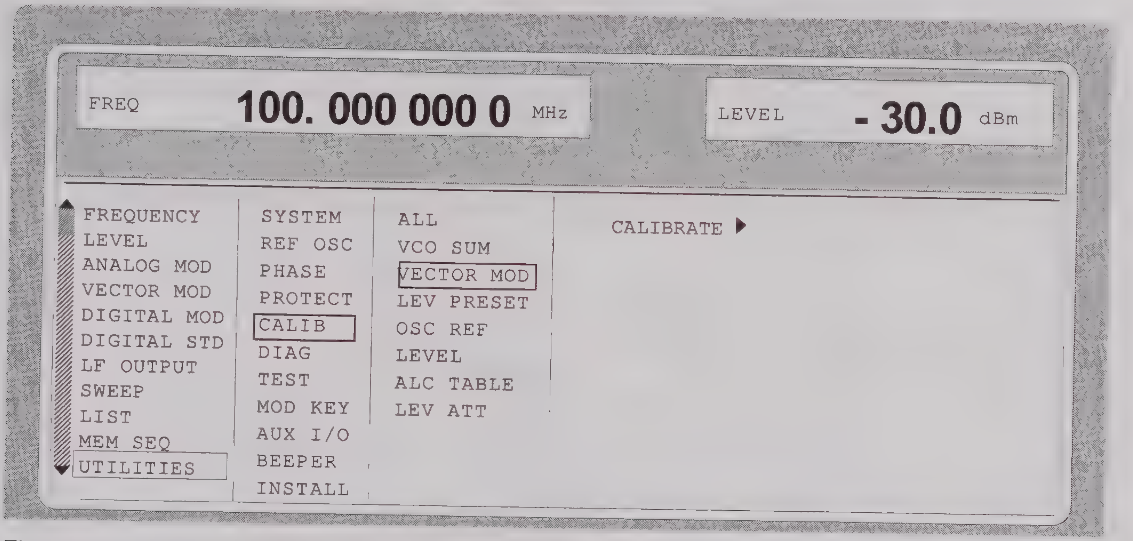
IEC/IEEE-bus command

```
:CAL:VSUM:OFFS?
:CAL:VSUM:DAC?
:CAL:VSUM:KOS?
```

Calibration VECTOR MOD

In order to obtain accurate and reproducible measurements, the I/Q modulator has to be calibrated. To do this the internal calibration routine VECTOR MOD is used for adjusting the residual carrier, I/Q imbalance and quadrature offset of the modulator. Calibration should be carried out prior to the measurement but after a warm-up time of approx. 1 h or in the case of temperature changes of more than 5° C.

Menu selectiom: UTILITIES - CALIB - VECTOR MOD



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
ALL
VCO SUM
VECTOR MOD
LEV PRESET
OSC REF
LEVEL
ALC TABLE
LEV ATT
CALIBRATE ▶

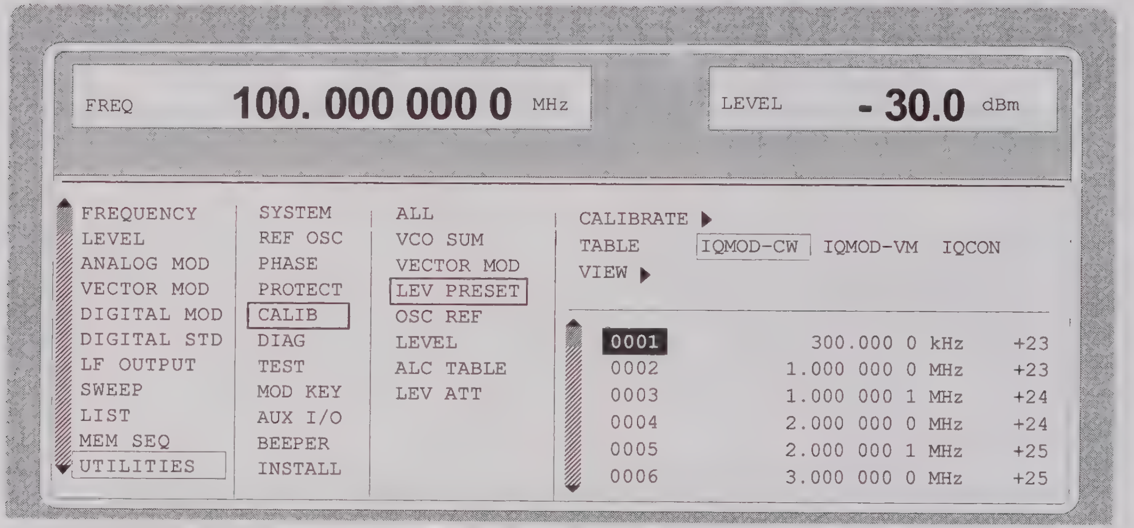
Fig. 2-263 Menu UTILITIES - CALIB - VECTOR MOD menu

CALIBRATE ▶ Triggers a calibration for the I/Q modulator, for FSIM, NDSIM and MCOD.
IEC/IEEE-bus command `:CAL:VMOD?`

Calibration LEV PRESET

The vector modulator is aligned in terms of residual carrier, IQ imbalance and quadrature. With options SMIQB10/B20 MCOD, SMIQB14/15 FSIM and SMIQB17 NDSIM provided, calibrations are called up for these modules too and the module offset and partly the IQ imbalance are calibrated. Since the calibrations are interdependent, calling up individual calibrations is not recommended. All calibrations are run internally. Calibration should be called up after a warm-up time of approx. 1 h or in case of a temperature change of more than 5°C .

Menu selection: UTILITIES - CALIB - LEV PRESET


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
ALL
VCO SUM
VECTOR MOD
LEV PRESET
OSC REF
LEVEL
ALC TABLE
LEV ATT
CALIBRATE ▶
VIEW ▶

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

TABLE IQMOD-CW IQMOD-VM IQCON

0001	300.000 0 kHz	+23
0002	1.000 000 0 MHz	+23
0003	1.000 000 1 MHz	+24
0004	2.000 000 0 MHz	+24
0005	2.000 000 1 MHz	+25
0006	3.000 000 0 MHz	+25

Fig. 2-264 Menu UTILITIES - CALIB - LEV PRESET

CALIBRATE ▶ Triggers the calibration for level preset.

IEC/IEEE-bus command `:CAL:LPR?`

The following parameters are only necessary for the indication of the correction values:

TABLE Selection of the correction values displayed by VIEW

IQMOD-CW	Correction values of module "IQ-Modulator", valid with CW mode
IQMOD-VM	Correction values of module "IQ-Modulator", valid with IQ mode
CONVERTER	Correction values of module "IQ-Converter"

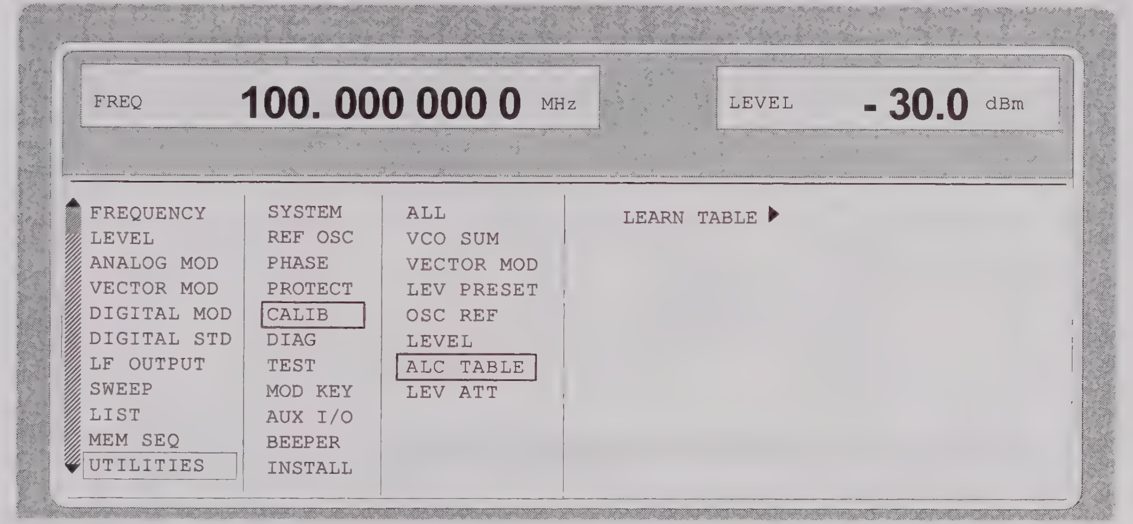
VIEW ▶ The cursor wraps to index 1 of the list. The list can be executed using the rotary knob. This index can be directly obtained by entering the index value on the digit block.

IEC/IEEE-bus command `:CAL:LPR:DATA?`

Calibration ALC TABLE

For vector modulation or digital modulation, SMIQ is operated with the internal level control switched off. In mode ALC MODE - SAMPLE & HOLD, the level is recalibrated for each level or frequency setting. In mode TABLE, the latter does not apply since the necessary input values are taken from a table. This table can be regenerated using LEARN TABLE ▶. This is required to attain an optimum level accuracy for the given operating temperature.

Menu selection: UTILITIES - CALIB - ALC TABLE



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
ALL
VCO SUM
VECTOR MOD
LEV PRESET
OSC REF
LEVEL
ALC TABLE
LEV ATT
LEARN TABLE ▶

Fig. 2-265 Menu UTILITIES - CALIB - ALC TABLE

LEARN TABLE ▶ Triggers the generation of the new correction value for the function LEVEL-ALC-ALC OFF MODE TABLE.

IEC/IEEE-bus command `:SOUR:POW:ALC:TABL?`

Calibration LEV ATT

For digital modulation (DIGITAL MOD and DIG STANDARD) and with the function LEV ATT switched on, the output level of SMIQ is reduced by a settable value in dB for a certain time (eg a slot). Calibration serves for attaining an optimum accuracy of level reduction.

Menu selection: UTILITIES - CALIB - LEV ATT

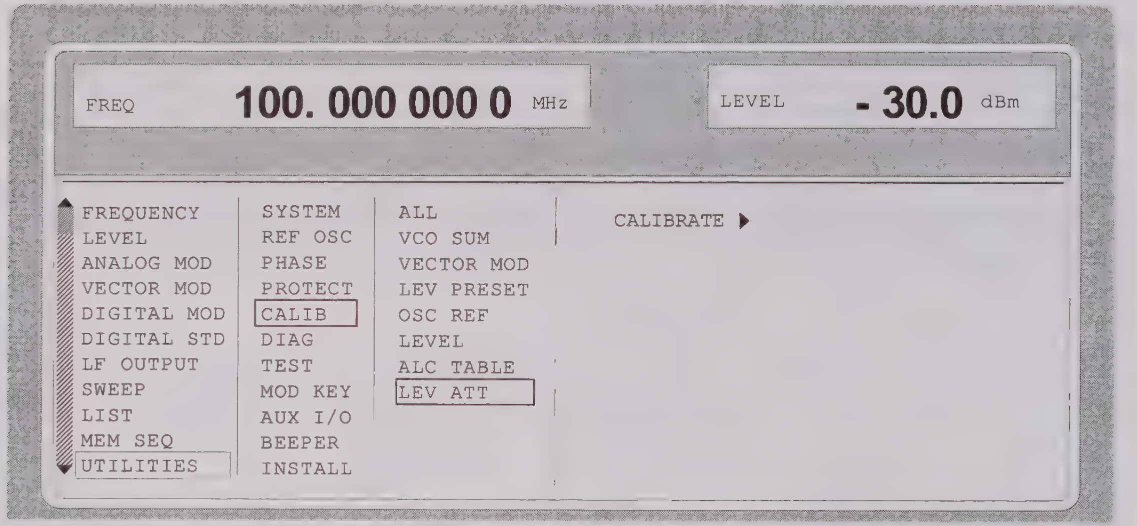

FREQ
100. 000 000 0
MHz
LEVEL
- 30.0
dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
ALL
VCO SUM
VECTOR MOD
LEV PRESET
OSC REF
LEVEL
ALC TABLE
LEV ATT
CALIBRATE ▶

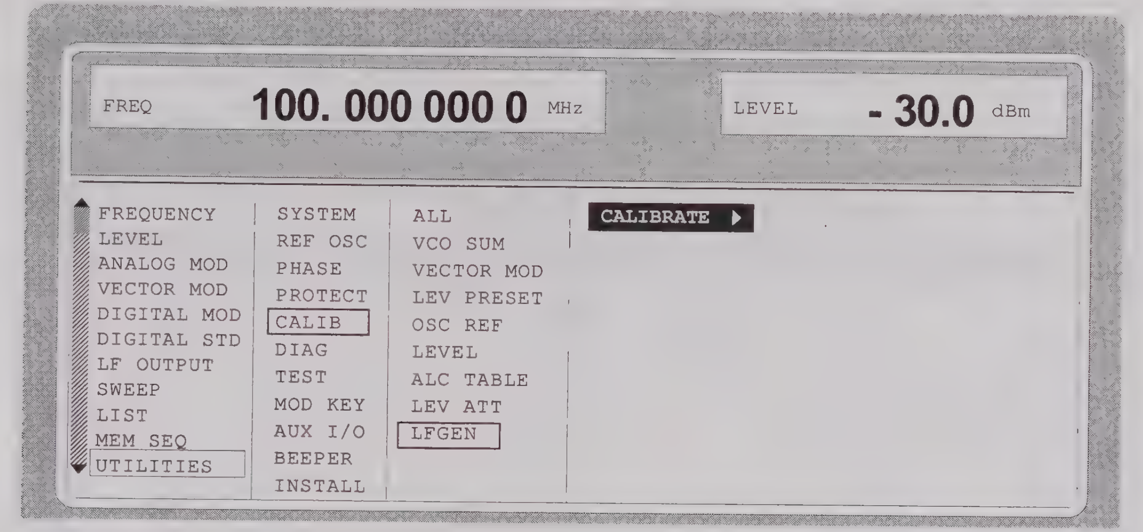
Fig. 2-266 Menu UTILITIES - CALIB - LEV ATT

CALIBRATE ▶	Triggers the calibration for the function LEV ATT. IEC/IEEE-bus command `:CAL:LATT?`

Calibration LFGEN

Calibrates the output level of the LF generator to 1.000 V. This calibration is only available with I/Q modulator IQMOD Var. 10 or higher.

Menu selection: UTILITIES – CALIB - LFGEN



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
ALL
VCO SUM
VECTOR MOD
LEV PRESET
OSC REF
LEVEL
ALC TABLE
LEV ATT
LFGEN
CALIBRATE ▶

Fig. 2-267 Menu UTILITIES - CALIB – LFGEN

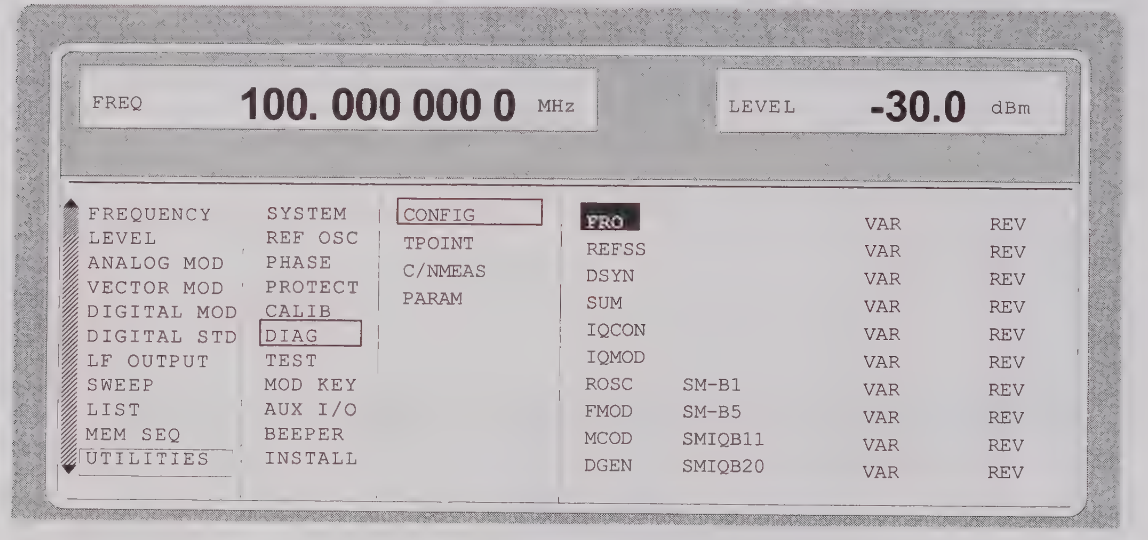
CALIBRATE	Triggers calibration for the LFGEN function. IEC/IEEE bus command `:CAL:LFG?`

2.29.10 Indications of Module Variants (DIAG-CONFIG)

For service purposes, the modules installed can be indicated with their variants and states of modification. Submenu DIAG-CONFIG offers access to the module indication.

IEC/IEEE-bus command `:DIAG:INFO:MOD?`

Menu selection: UTILITIES - DIAG - CONFIG


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
CONFIG
TPOINT
C/NMEAS
PARAM

FREQ 100. 000 000 0 MHz LEVEL -30.0 dBm

```
FRO                 VAR      REV
REFSS               VAR      REV
DSYN                VAR      REV
SUM                 VAR      REV
IQCON               VAR      REV
IQMOD               VAR      REV
ROSC    SM-B1       VAR      REV
FMOD    SM-B5       VAR      REV
MCOD    SMIQB11     VAR      REV
DGEN    SMIQB20     VAR      REV
```

Fig. 2-268 Menu UTILITIES - DIAG - CONFIG

2.29.11 Voltage Indication of Test Points (DIAG-TPOINT)

Submenu DIAG-TPOINT offers access to internal test points. If a test point is switched on, the voltage indication is displayed in a window in the header field. For greater detail, see service manual.

Menu selection: UTILITIES - DIAG - TPOINT

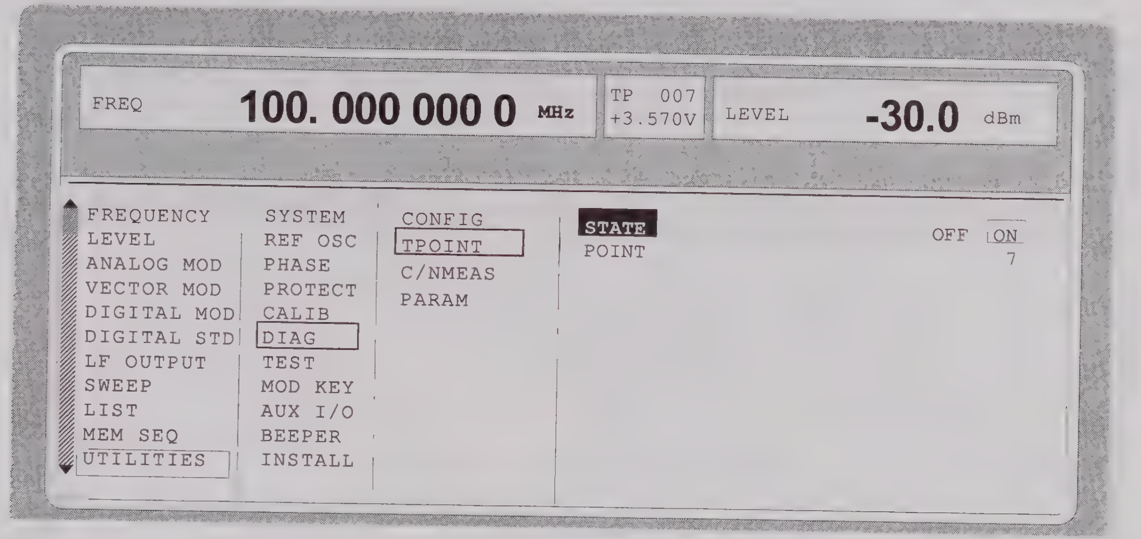

FREQ
100. 000 000 0 MHz
TP 007
+3.570V
LEVEL
-30.0 dBm
FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
CONFIG
TPOINT
C/NMEAS
PARAM
STATE
POINT
OFF ON
7

Fig. 2-269 Menu UTILITIES - DIAG - TPOINT

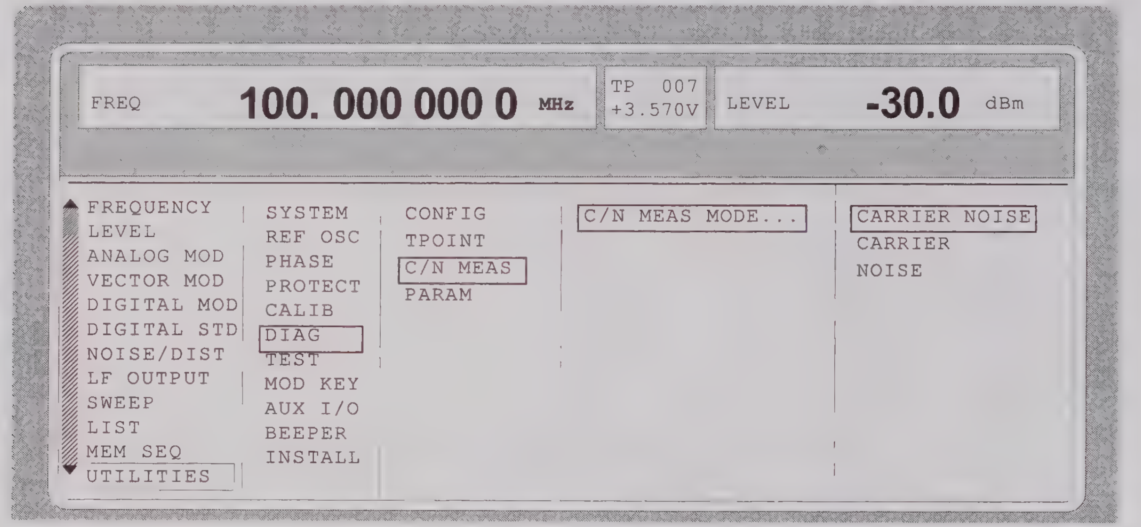
STATE Switching on/off the voltage indication in the header field.

POINT........... Input value of the test point.
IEC/IEEE-bus command `:DIAG:POINxx?`

2.29.12 Measurement of CARRIER/NOISE RATIO (DIAG-C/N MEAS)

The submenu DIAG - C/N MEAS can only be called with the option SMIQB17 (Noise generator and distortion simulator). For C/N measurements in servicing the generator can be set to the service modes as far are concerned. The carrier/noise ratio can then be easily determined this way. The submenu DIAG - C/N MEAS offers an access to these modes.

Menu selection: UTILITIES - DIAG - C/N MEAS



FREQ 100. 000 000 0 MHz

TP 007
+3.570V

LEVEL -30.0 dBm

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
NOISE/DIST
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
CONFIG
TPOINT
C/N MEAS
PARAM
C/N MEAS MODE...
CARRIER NOISE
CARRIER
NOISE

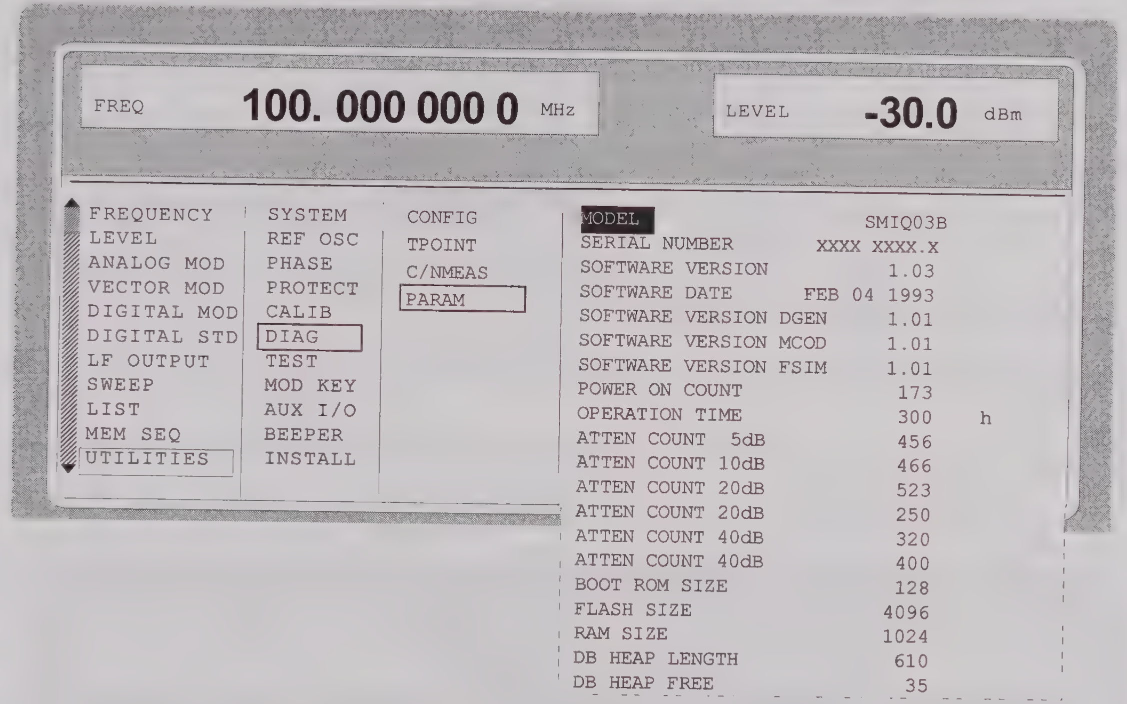
Fig. 2-270 Menu UTILITIES - DIAG - C/N MEAS

CARRIER NOISE	Noise and carrier signal IEC/IEEE-bus command	 `:DIAG:CNM:MODE CN`
CARRIER	Only carrier signal IEC/IEEE-bus command	 `:DIAG:CNM:MODE CARR`
NOISE	Only noise signal IEC/IEEE-bus command	 `:DIAG:CNM:MODE NOIS`

2.29.13 Indications of Service Data (DIAG-PARAM)

Submenu DIAG-PARAMETER offers access to different parameters such as serial number, software version, operating-hours counter and attenuator circuits.

Menu selection: UTILITIES - DIAG - PARAM


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL
CONFIG
TPOINT
C/NMEAS
PARAM

FREQ 100. 000 000 0 MHz

LEVEL -30.0 dBm

MODEL	SMIQ03B
SERIAL NUMBER	XXXX XXXX.X
SOFTWARE VERSION	1.03
SOFTWARE DATE	FEB 04 1993
SOFTWARE VERSION DGEN	1.01
SOFTWARE VERSION MCOD	1.01
SOFTWARE VERSION FSIM	1.01
POWER ON COUNT	173
OPERATION TIME	300 h
ATTEN COUNT 5dB	456
ATTEN COUNT 10dB	466
ATTEN COUNT 20dB	523
ATTEN COUNT 20dB	250
ATTEN COUNT 40dB	320
ATTEN COUNT 40dB	400
BOOT ROM SIZE	128
FLASH SIZE	4096
RAM SIZE	1024
DB HEAP LENGTH	610
DB HEAP FREE	35

Fig. 2-271 Menu UTILITIES - DIAG - PARAM

For IEC-bus commands, cf. Chapter 3, Section "DIAGnostic System" and Section "Common Commands; *IDN?".

2.29.14 Test (TEST)

(cf. Chapter 4, Section "Functional Test")

2.29.15 Assigning Modulations to the [MOD ON/OFF] Key (MOD-KEY)

The modulations can be switched on/off in the individual modulation menus and parallely by means of the [MOD ON/OFF] key.

For which modulations the [MOD ON/OFF] key is effective can be defined in the UTILITIES-MOD KEY menu. The key can either be effective for all modulations or for a selected one.

Function of the [MOD ON/OFF] key if effective for a type of modulation:

➢ Every pressing a key alters the state (ON or OFF) of the selected modulation.

Function of the [MOD ON/OFF] key if effective for all types of modulation (ALL):

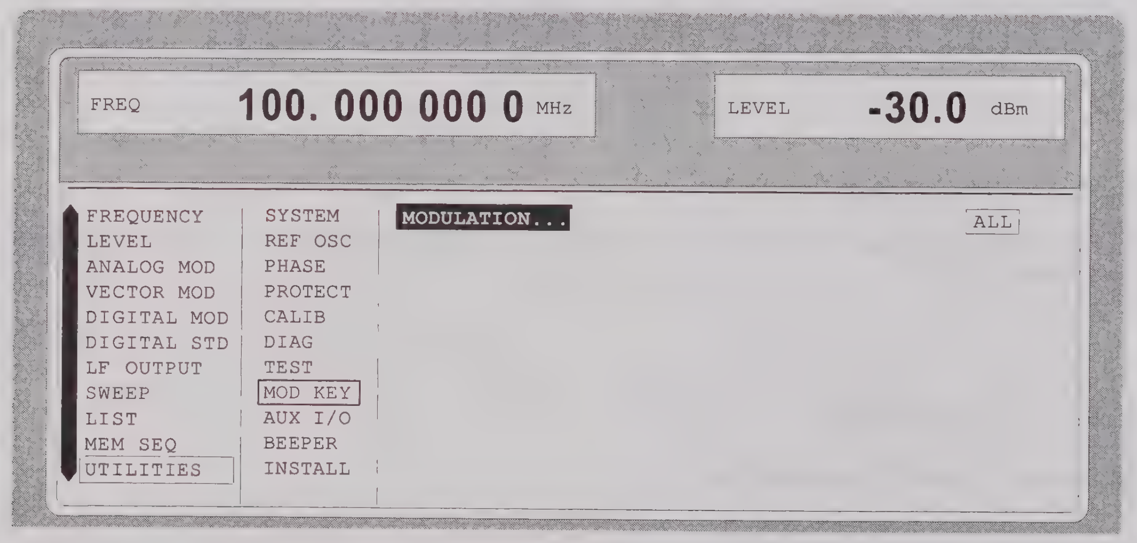
➢ If at least one modulation is switched on, pressing the [MOD ON/OFF] key switches the modulation/s off. Which modulations were switched on is stored.

If no modulation is switched on, pressing the [MOD ON/OFF] key switches on the modulations which were last switched off using the [MOD ON/OFF] key.

On switching on using the [MOD ON/OFF] key, the modulation sources are used as defined in the modulation menus.

Access to the selection of the modulation to be switched using the [MOD ON/OFF] key is possible in the UTILITIES-MOD KEY menu.

Menu selection: UTILITIES - MOD KEY


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL

FREQ 100. 000 000 0 MHz

LEVEL -30.0 dBm

MODULATION... ALL

Fig. 2-272 Menu UTILITIES - MOD KEY (preset setting)

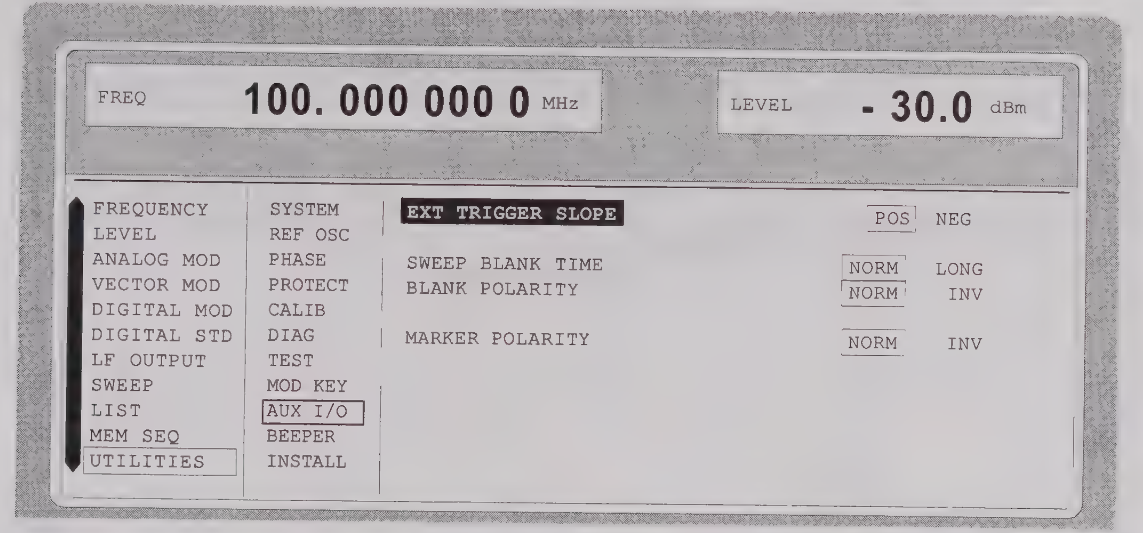
MODULATION Open a window for selection of the modulation for which the [MOD ON/OFF] key is to be effective. The following is to choose from: ALL, AM, BB-AM, FM, PM, PULSE, DM, VM, FSIM, DISTO, NOISE.

Note: *Preset switches off all modulations, sets the selection to ALL and stores AM 30%, AM SOURCE INT as default setting.*

2.29.16 Setting Auxiliary Inputs/Outputs (AUX-I/O)

Menu UTILITIES - AUX I/O offers access to settings for the TRIGGER input, BLANK output and MARKER output. Sections Sweep, LIST Mode and Memory Sequence provide further information.

Menu selection: UTILITIES - AUX I/O


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL

FREQ 100. 000 000 0 MHz
LEVEL - 30.0 dBm

EXT TRIGGER SLOPE POS NEG
SWEEP BLANK TIME NORM LONG
BLANK POLARITY NORM INV
MARKER POLARITY NORM INV

Fig. 2-273 Menu UTILITIES - AUX I/O

EXT TRIGGER SLOPE

Selection of the active edge of the external trigger signal.

POS The instrument triggers on the positive edge of the external signal.

NEG The instrument triggers on the negative edge of the external signal.

IEC/IEEE-bus command `:TRIG:SLOP POS`

SWEEP BLANK TIME

Selection of the blank duration.

NORM BLANK duration is set to the shortest duration possible.

LONG The BLANK duration is set for the PEN LIFT control of an XY recorder (approx. 500ms).

IEC/IEEE-bus command `:SOUR2:SWE:BTIM NORM`

BLANK POLARITY

Selection of the polarity for the blank signal.

NORM With active BLANK, the output signal is HIGH.

INV Polarity is inverted.

IEC/IEEE-bus command `:OUTP:BLAN NORM`

MARKER POLARITY

Selection of the polarity for the marker signal.

NORM The output signal is HIGH when the sweep cycle reaches the mark.

INV Polarity is inverted.

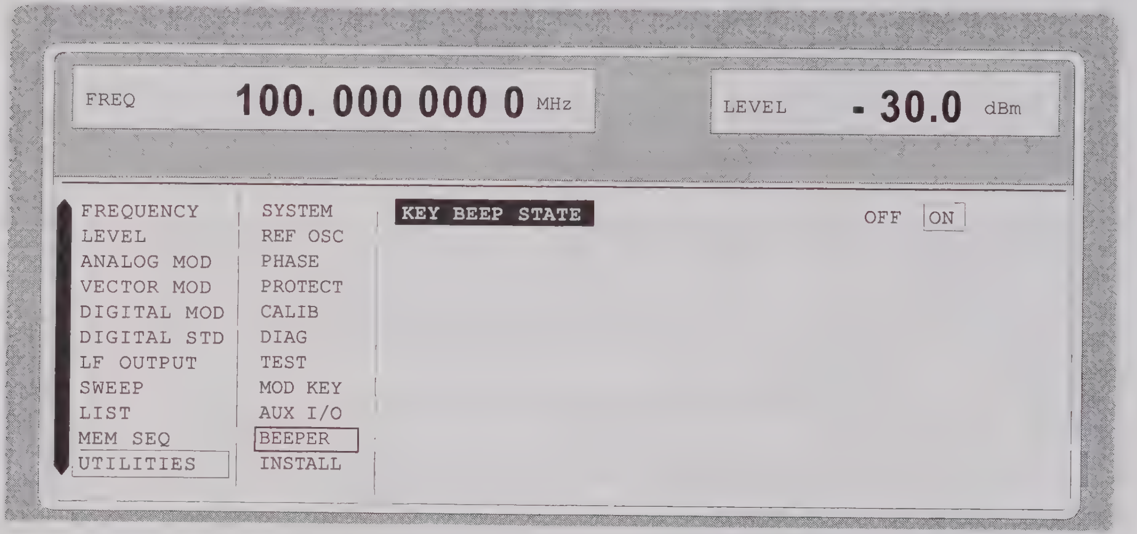
IEC/IEEE-bus command `SOUR:MARK:POL NORM`

2.29.17 Switching On/Off Beeper (BEEPER)

Menu UTILITIES-BEEPER offers access to the switching on/off of the beeper.

Note: *Preset does not alter the current state (ON or OFF).*

Menu selection: UTILITIES - BEEPER



FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

KEY BEEP STATE OFF ON

FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL

Fig. 2-274 Menu UTILITIES - BEEPER

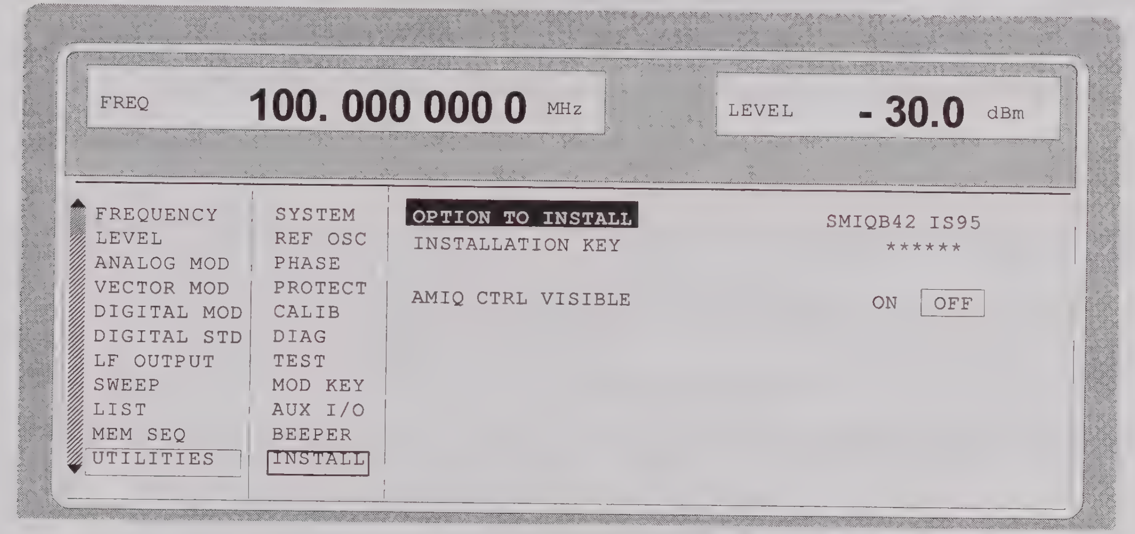
KEY BEEP STATE	Switching on/off the beeper	
	IEC-bus command	`:SYST:BEEP:STAT ON`

2.29.18 Installation of Software Option

Optionss are installed in the menu UTILITIES-INSTALL by means of a keyword. The keyword is part to the equipment supplied in case of a follow-up order.

Menu UTILITIES-INSTALL gives access to the keyword entry.

Menu selection: UTILITIES - INSTALL


FREQUENCY
LEVEL
ANALOG MOD
VECTOR MOD
DIGITAL MOD
DIGITAL STD
LF OUTPUT
SWEEP
LIST
MEM SEQ
UTILITIES
SYSTEM
REF OSC
PHASE
PROTECT
CALIB
DIAG
TEST
MOD KEY
AUX I/O
BEEPER
INSTALL

FREQ 100. 000 000 0 MHz

LEVEL - 30.0 dBm

OPTION TO INSTALL SMIQB42 IS95

INSTALLATION KEY ******

AMIQ CTRL VISIBLE ON OFF

Fig. 2-275 Menu UTILITIES - INSTALL, fitted with options

OPTION TO INSTALL	Selection of the option to be installed.
INSTALLATION KEY	Entry of the keyword; after entry, press key [ENTER].
AMIQ CTRL VISIBLE	Activates or deactivates menu AMIQ CTRL.

2.30 The Help System

The SMIQ has two help systems. On the one hand the context-sensitive help which is called by means of the HELP key and which gives information on the current menu. On the other hand, auxiliary texts can be selected according to headwords in alphabetical order by accessing menu HELP.

HELP Key

The yellow HELP key can be pressed at any point in time. The current setting menu is faded out and context-sensitive text faded in. The help panel can be exited by means of the RETURN key.

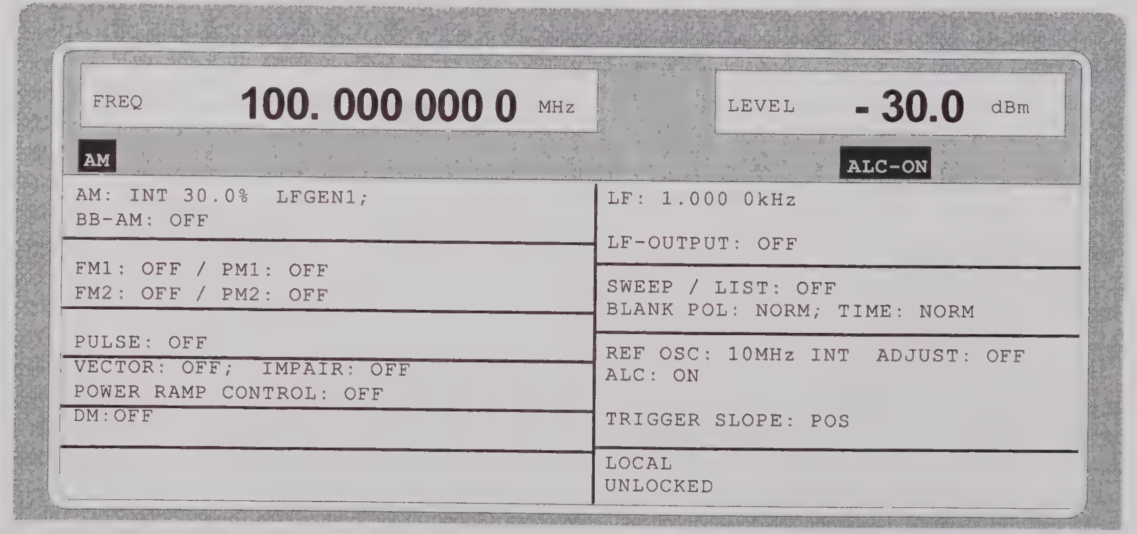
Menu HELP

After calling the help menu, access to all auxiliary texts is possible via an index. Operation is analog to menu operation.

- ➢ Set the menu cursor to the index desired using the shaft encoder.
- ➢ Press the [SELECT] key.
- ➢ The information for the index marked is displayed.
- ➢ Press the [RETURN] key to exit the menu.

2.31 Status

By means of a STATUS page, the SMIQ permits an overview over all settings of the instrument. The settings are displayed in an abbreviated form. The STATUS page is called by pressing the [STATUS] key. Return to the preceding menu is effected using the [RETURN] key.



```
FREQ    100. 000 000 0  MHz             LEVEL    - 30.0  dBm
AM                                                ALC-ON
AM: INT 30.0%  LFGEN1;           LF: 1.000 0kHz
BB-AM: OFF
                                 LF-OUTPUT: OFF
FM1: OFF / PM1: OFF
FM2: OFF / PM2: OFF              SWEEP / LIST: OFF
                                 BLANK POL: NORM; TIME: NORM
PULSE: OFF
VECTOR: OFF;  IMPAIR: OFF        REF OSC: 10MHz INT  ADJUST: OFF
POWER RAMP CONTROL: OFF          ALC: ON
DM:OFF
                                 TRIGGER SLOPE: POS

                                 LOCAL
                                 UNLOCKED
```

Fig. 2-276 Menu STATUS page

2.32 Error Messages

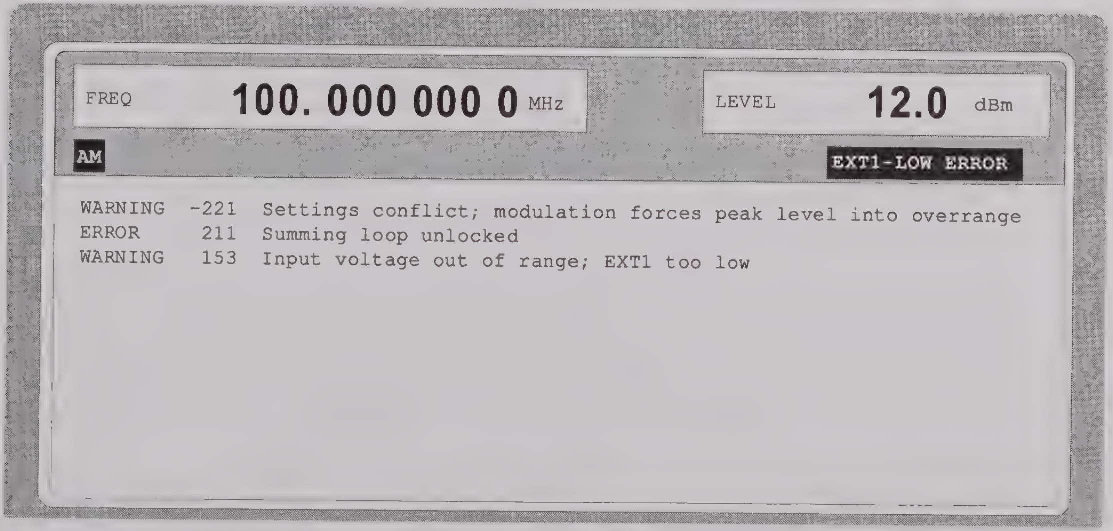
The SMIQ displays error and caution messages in a different manner, depending on how long, for a short period of time or permanently, the cause exists.

Short-term message	The short-term message is displayed in the status line. Part of it overwrites the status indications and disappears after approx. 2 seconds or in the case of a new entry. The instrument shows, e.g., short-term messages if the attempt is made to enter an overrange or if incompatible operating modes deactivate one another.
Long-term message	The long-term message is displayed in the status line by means of the message "WARNING" or "ERROR". Pressing the [ERROR] key calls the ERROR page in which the messages are entered. Several messages can be entered at the same time. The long-term message remains existing until there is no cause any more. The ERROR page is exited using the [RETURN] key. The instrument displays, e.g., the long-term message "ERROR" if there is a hardware error or "WARNING" if overrange settings have been made.

Notes: – *An error message "ERROR" does not necessarily point to a defect instrument. There are various operating states which can cause an ERROR message. E. g. if the instrument is set to external reference but no external reference is connected.*

– *Error 313 indicates the loss of calibration data and is also applicable in case of a cold start (key [PRESET] is pressed during switch-on). The calibration values can be restored with internal calibration routines. These routines are accessible via menu UTILITIES-CALIB (see section on calibration).*

The ERROR page offers access to long-term messages if the [ERROR] key is pressed.



FREQ 100. 000 000 0 MHz

LEVEL 12.0 dBm

AM

EXT1-LOW ERROR

```
WARNING  -221  Settings conflict; modulation forces peak level into overrange
ERROR     211  Summing loop unlocked
WARNING   153  Input voltage out of range; EXT1 too low
```

Fig. 2-277 ERROR page

A list of the possible error messages is to be found in annex B.

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